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(12) **United States Design Patent**
Ajmera et al.

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(54) **SET OF NAAN CHIPS**

- (71) Applicant: **FGF Brands Inc.**, Concord, Ontario (CA)
- (72) Inventors: **Ojus Ajmera**, Concord (CA); **Tejus Ajmera**, Concord (CA)
- (73) Assignee: **FGF BRANDS INC.**, Concord, Ontario (CA)
- (**) Term: **14 Years**
- (21) Appl. No.: **29/432,980**
- (22) Filed: **Sep. 24, 2012**

(30) **Foreign Application Priority Data**

- Mar. 23, 2012 (CA) 144990
- (51) **LOC (10) Cl.** **01-01**
- (52) **U.S. Cl.**
USPC **D1/128; D1/129**
- (58) **Field of Classification Search**
USPC D1/100-130, 199; D7/369.2, 388, 509, D7/523; D21/386, 489, 782; D25/113; 426/92, 94, 95, 99, 101, 138-139, 143, 426/279, 282, 283, 391, 514; D9/707; D8/367, 370; D11/1, 2, 3, 7, 11, 12, D11/13, 17, 22, 24, 35, 79; D17/20; 84/320-322, 327
See application file for complete search history.

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Primary Examiner — Garth Rademaker
Assistant Examiner — Katie S Mroczka
(74) *Attorney, Agent, or Firm* — Banner & Witcoff, Ltd.

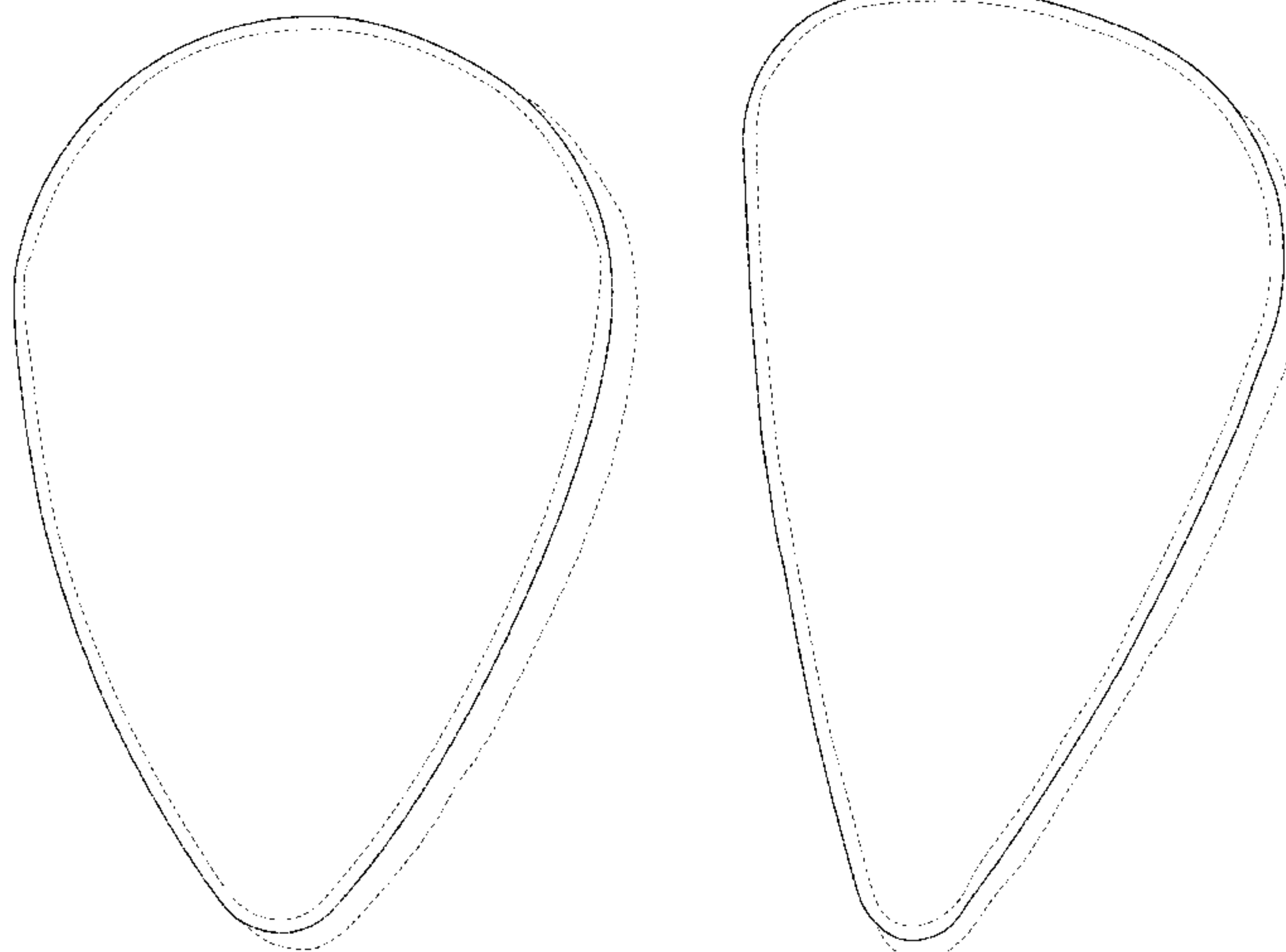
(57) **CLAIM**

The ornamental design for a set of naan chips, as shown and described.

DESCRIPTION

FIG. 1 is an oblique projection view of a first chip of a set of naan chips showing our new design;
FIG. 2 is an oblique projection view of a second chip of a set of naan chips;
FIG. 3 is an oblique projection view of a third chip of a set of naan chips;
FIG. 4 is an oblique projection view of a fourth chip of a set of naan chips;
FIG. 5 is an oblique projection view of a fifth chip of a set of naan chips;
FIG. 6 is an oblique projection view of a sixth chip of a set of naan chips;
FIG. 7 is an oblique projection view of a seventh chip of a set of naan chips; and,
FIG. 8 is an oblique projection view of an eighth chip of a set of naan chips.
The claimed design for a set of naan chips is directed to the combination of each of the eight naan chips of FIGS. 1-8. The broken lines in the interior portion of each naan chip illustrate boundary lines in which the boundary lines and the area defined by the boundary lines form no part of the claimed design. The broken line showing in FIGS. 1-8 showing the thickness of each naan chip illustrates portions of each naan chip which form no part of the claimed design.

1 Claim, 8 Drawing Sheets



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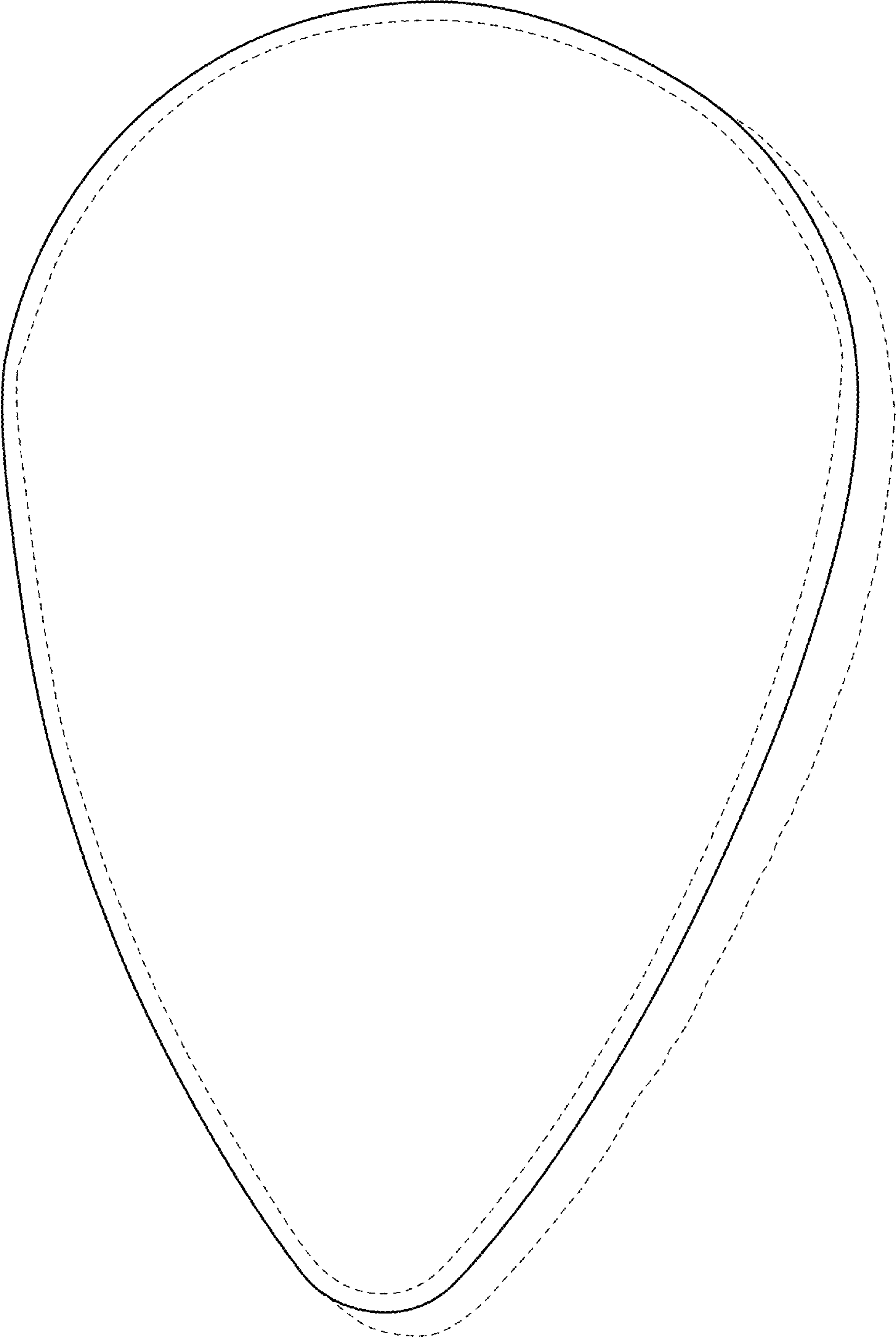


FIG. 1

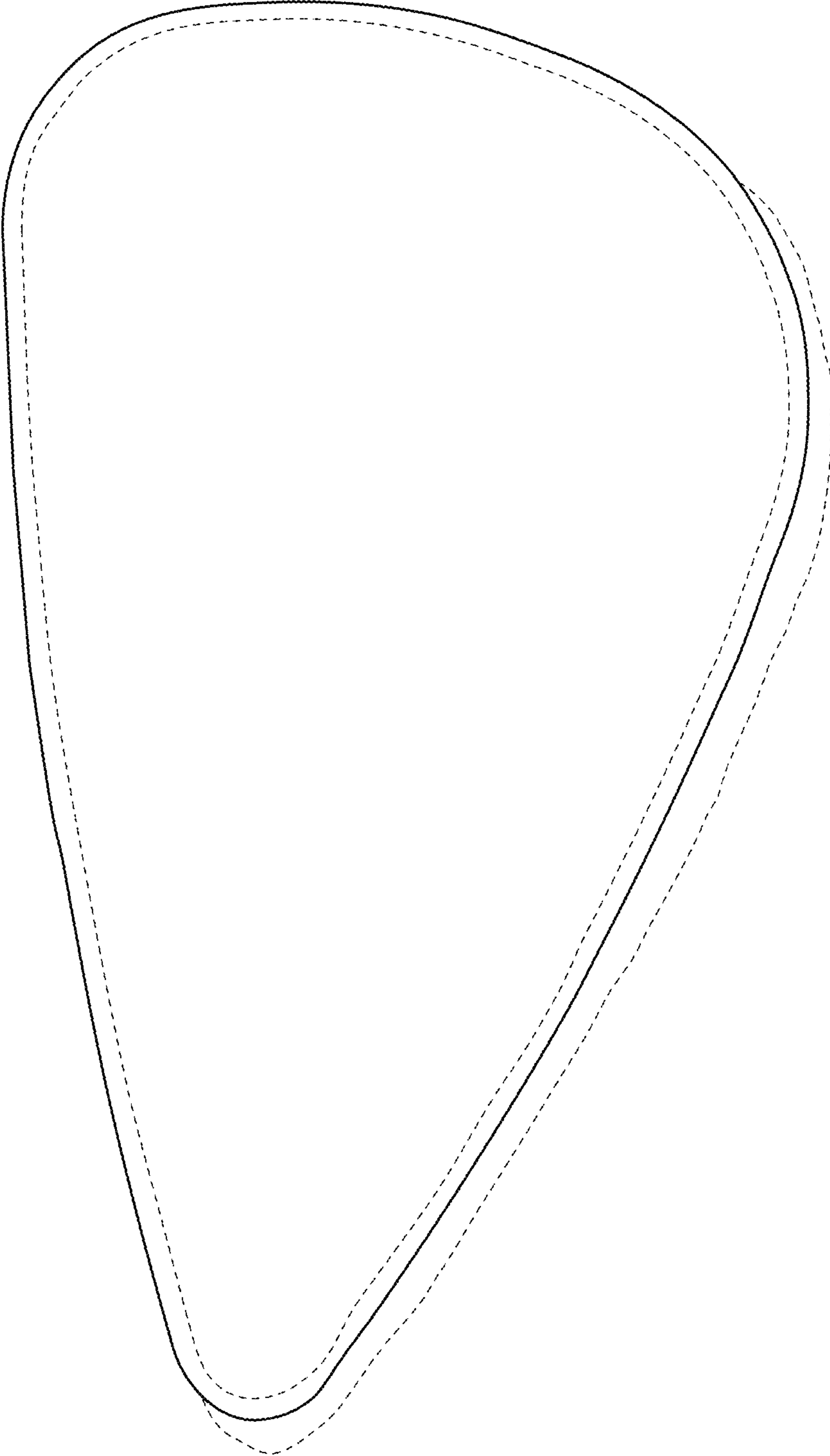


FIG. 2

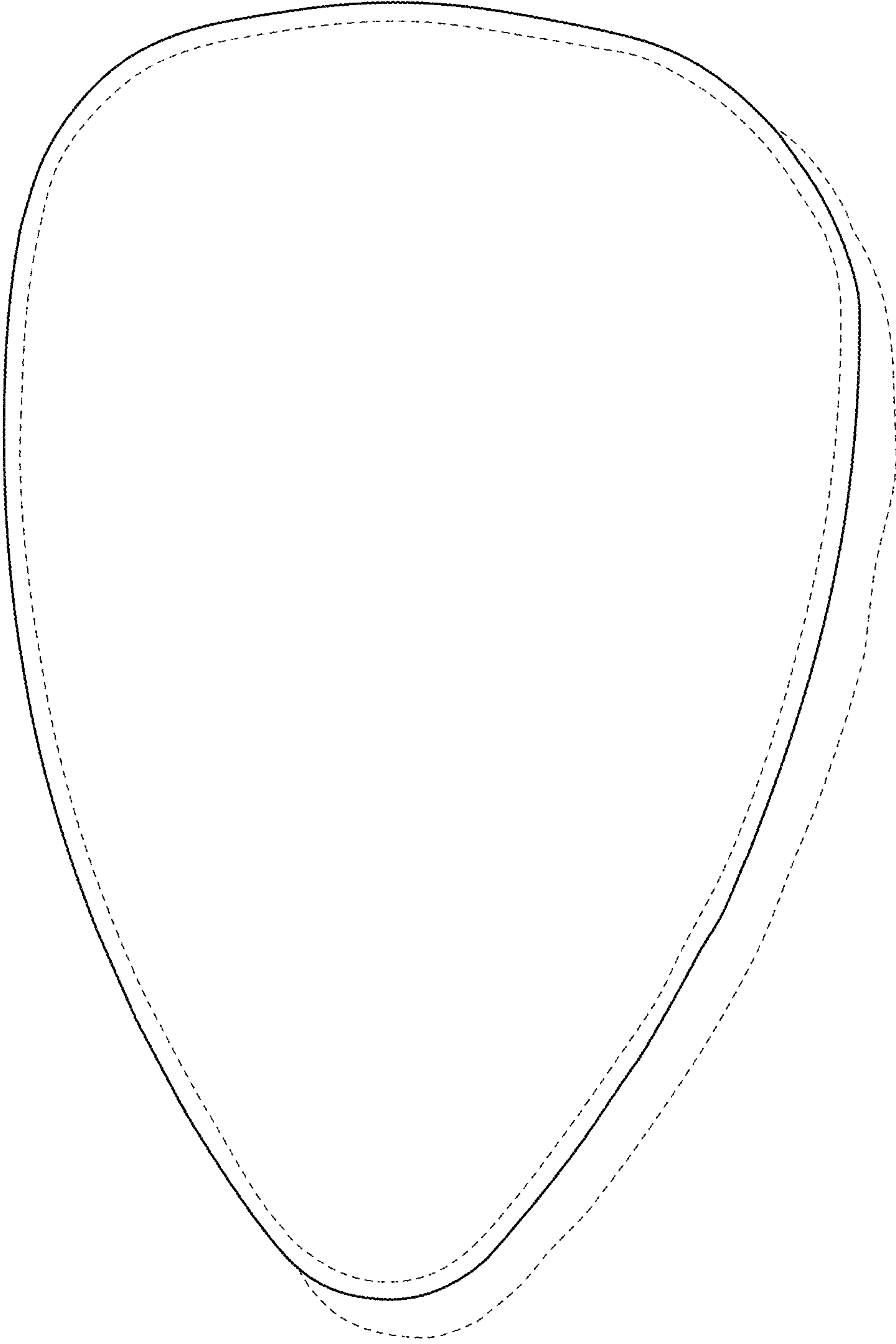


FIG. 3

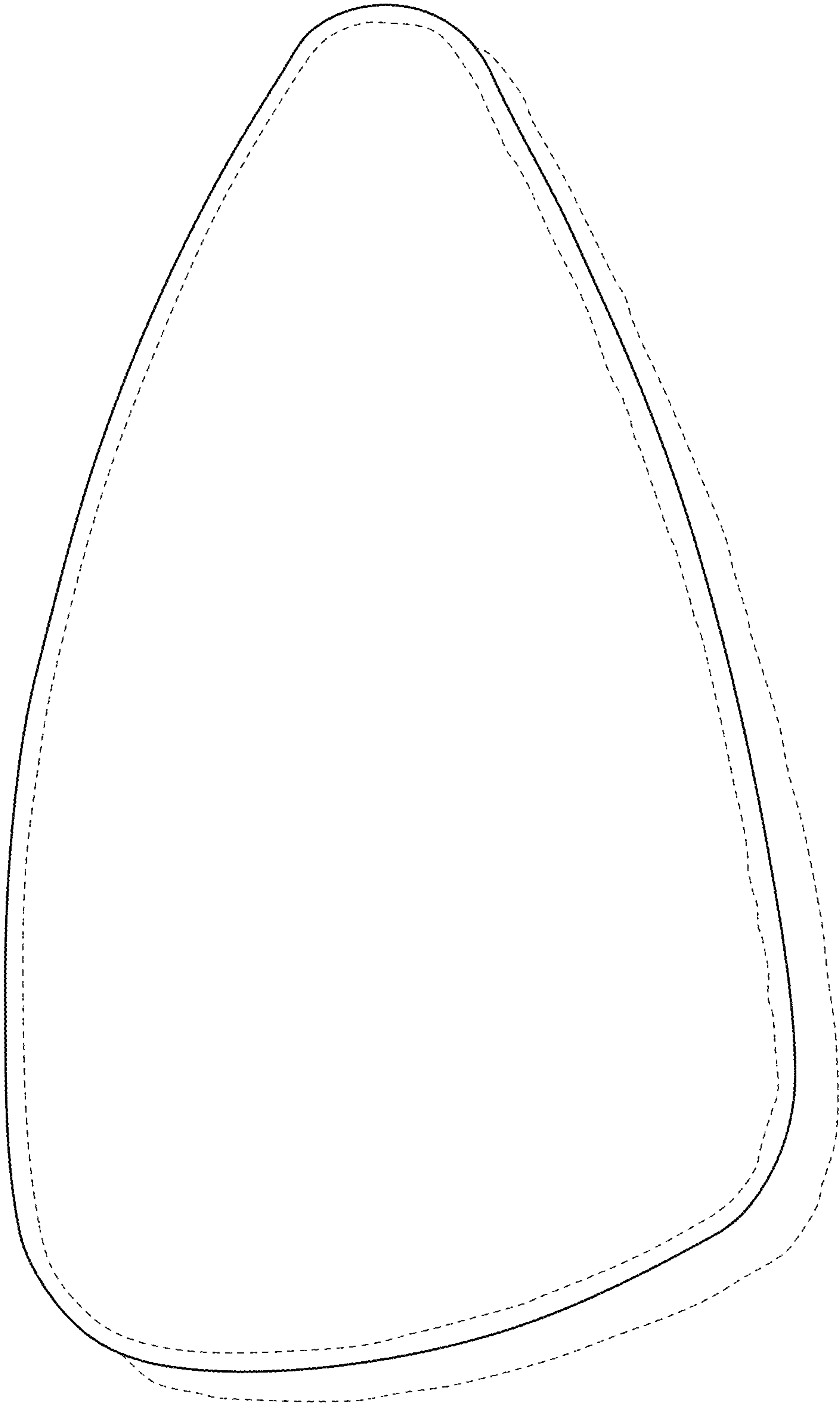


FIG. 4

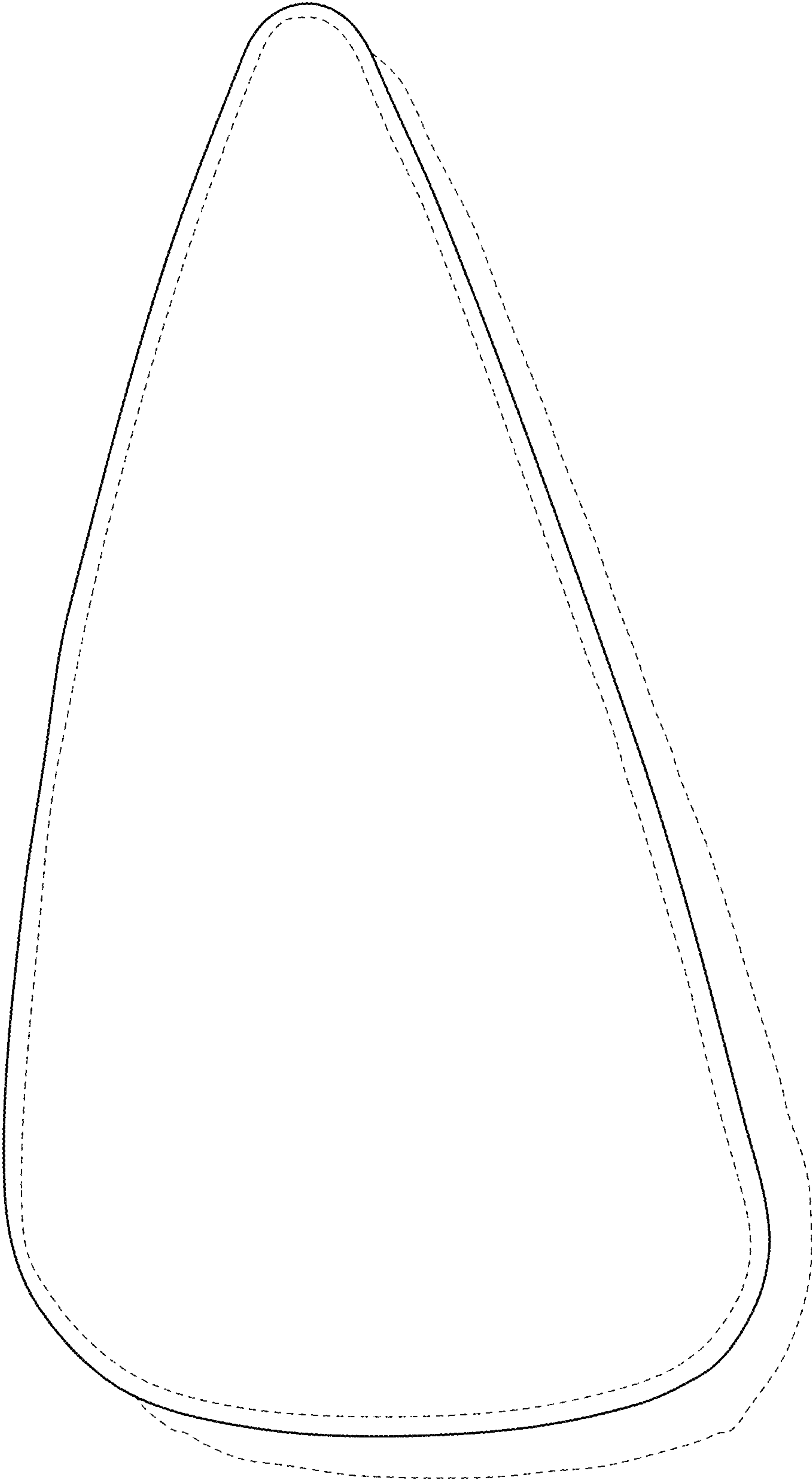


FIG. 5

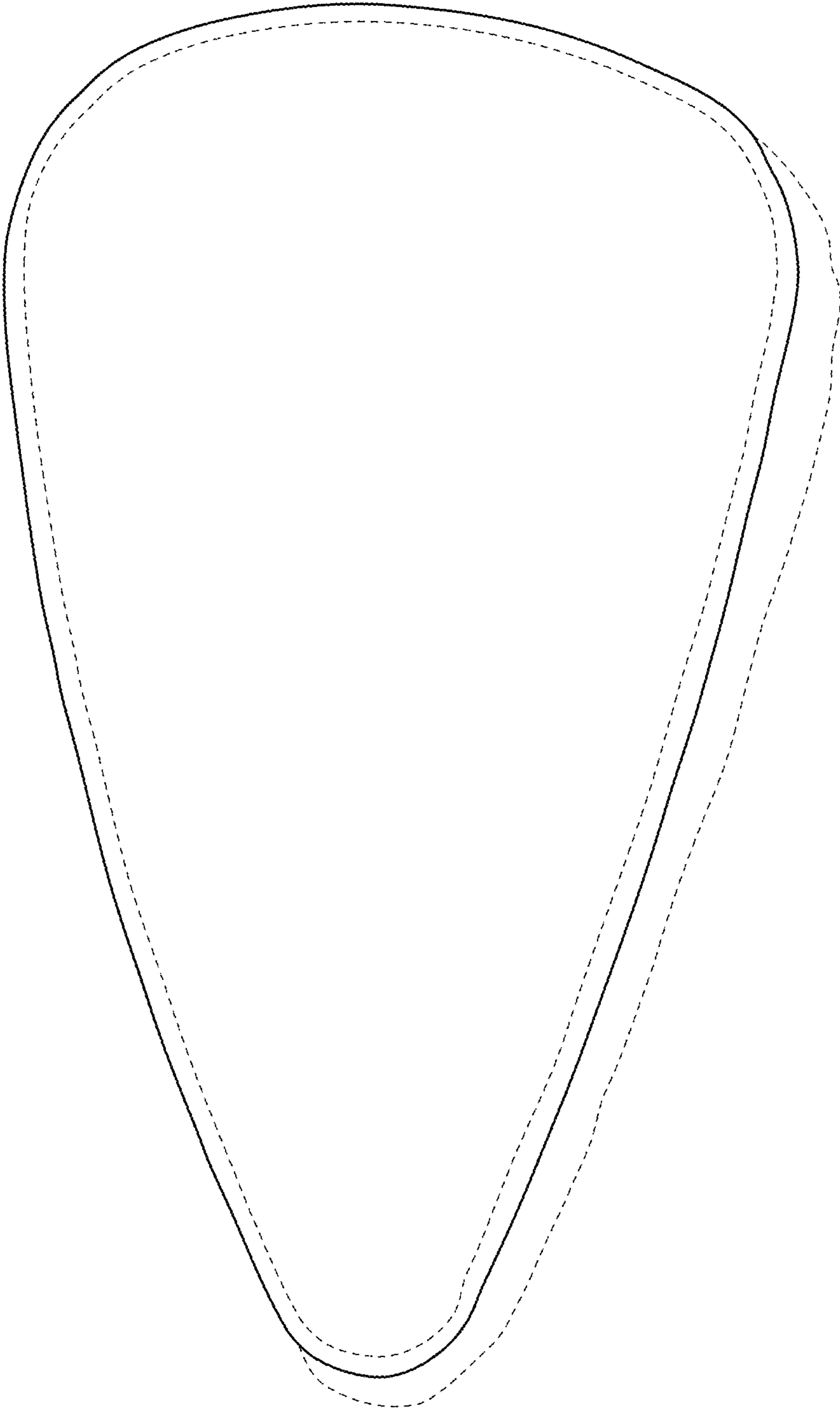


FIG. 6

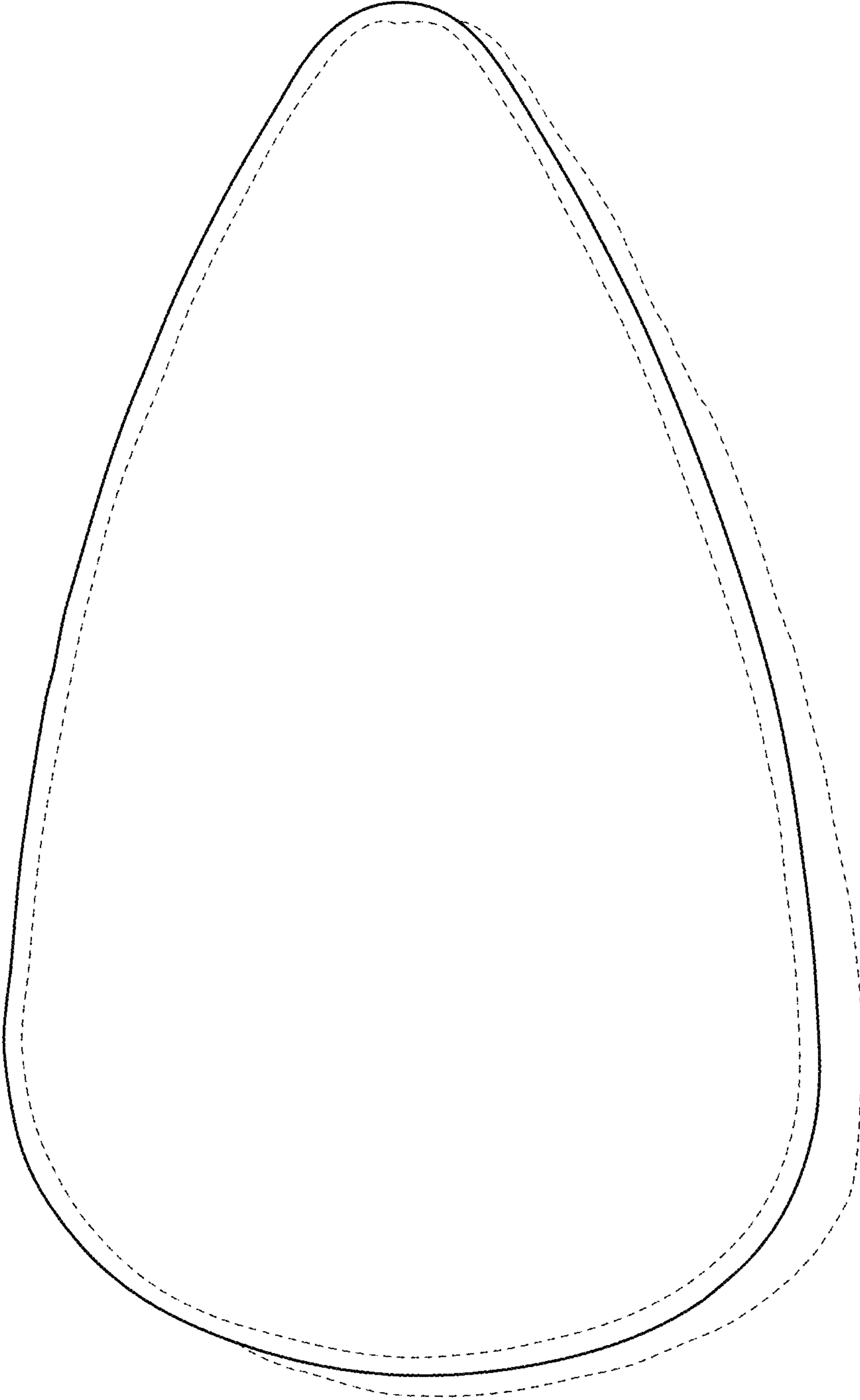


FIG. 7

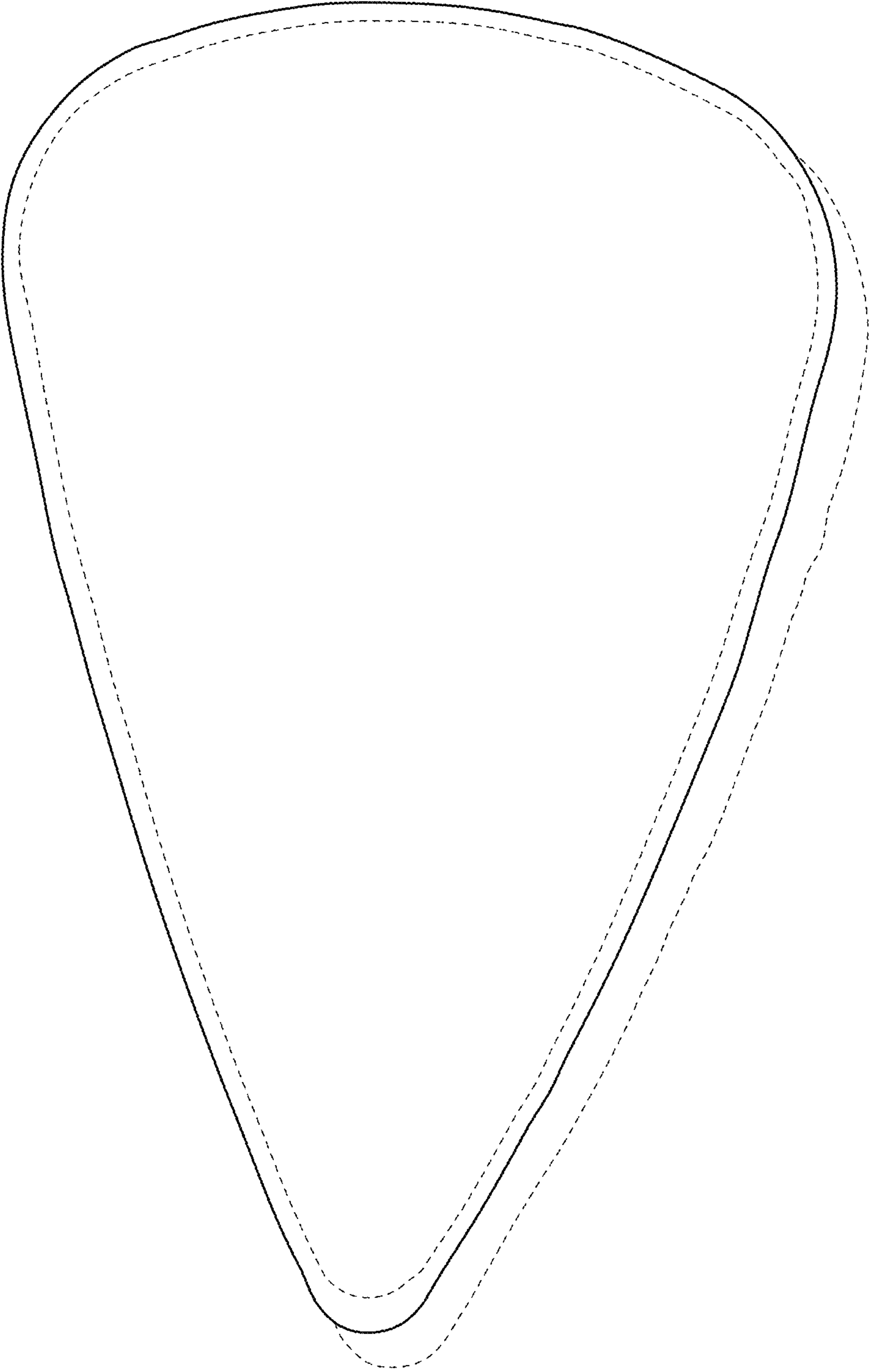


FIG. 8