



US00D760707S

(12) **United States Design Patent**
Walliser et al.

(10) **Patent No.:** **US D760,707 S**
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(54) **ELECTRONIC DEVICE**

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(**) Term: **14 Years**

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(51) **LOC (10) Cl.** **14-03**

(52) **U.S. Cl.**
USPC **D14/248**

(58) **Field of Classification Search**
USPC D14/138 G, 138 AD, 341, 138 R, 496, D14/203.1, 203.3, 203.5, 203.7, 248, 218, D14/250; 455/575.1, 556.2, 575.3, 575.4; D21/517, 329; 379/433.01, 433.04; 361/679.3, 679.56; D13/168
CPC G06F 1/1626; H04M 1/0266
See application file for complete search history.

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(57) **CLAIM**

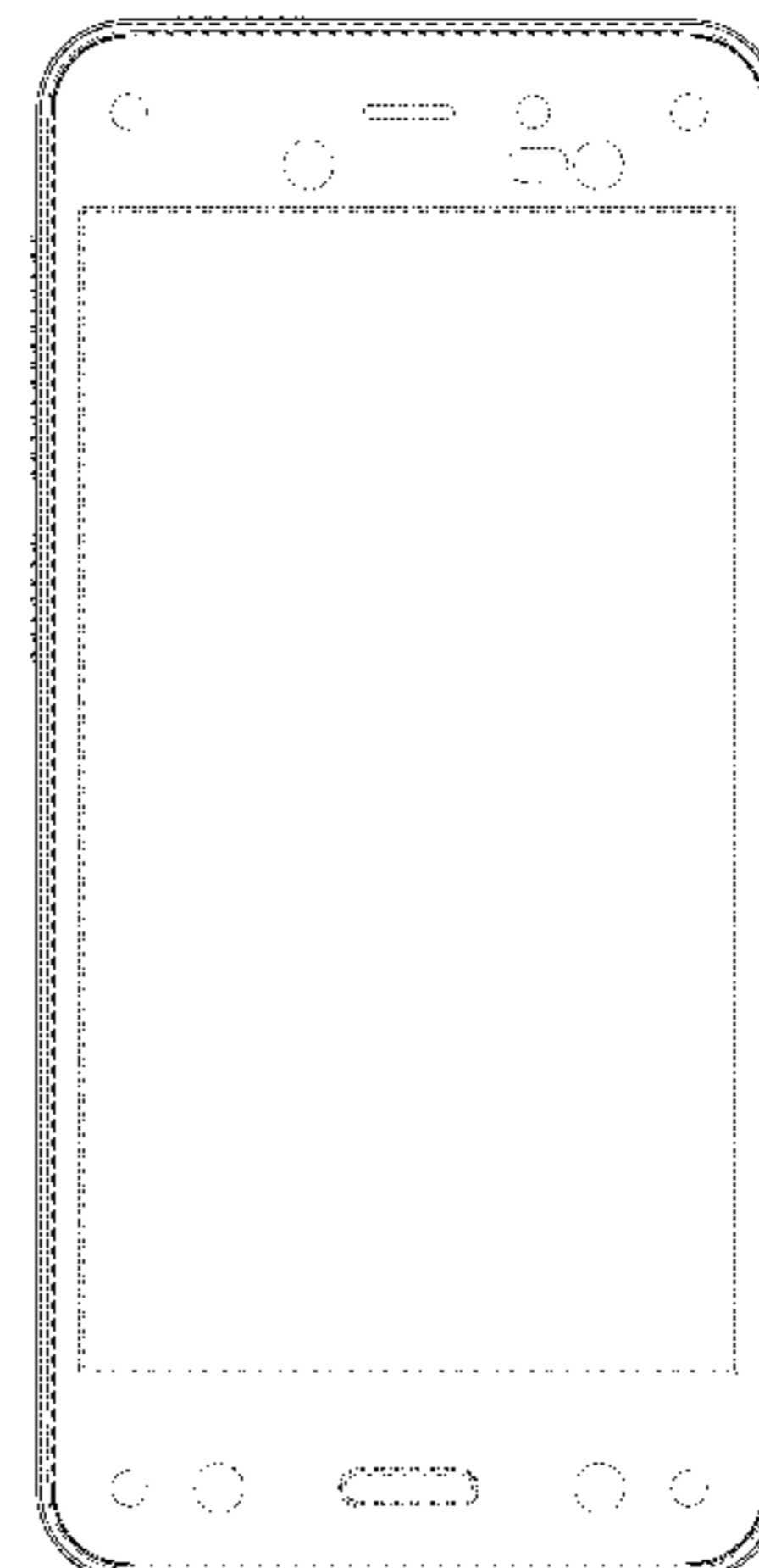
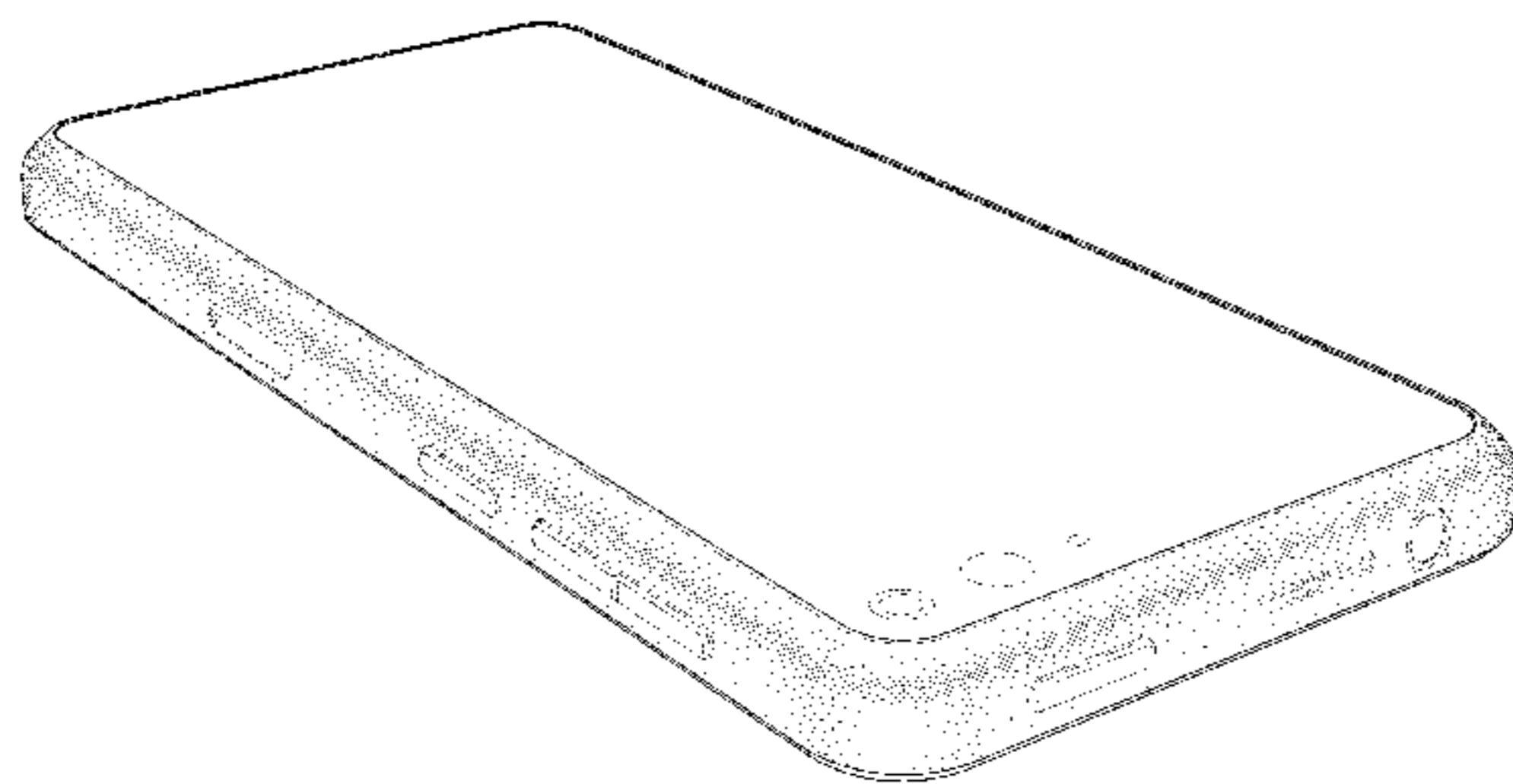
The ornamental design for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of an electronic device; FIG. 2 is another perspective view of the electronic device of FIG. 1; FIG. 3 is a top plan view of the electronic device of FIG. 1; FIG. 4 is a bottom plan view of the electronic device of FIG. 1; FIG. 5 is a left-side elevation view of the electronic device of FIG. 1; FIG. 6 is a right-side elevation view of the electronic device of FIG. 1; FIG. 7 is a back elevation view of the electronic device of FIG. 1; and, FIG. 8 is a front elevation view of the electronic device of FIG. 1.

The broken lines shown in the figures that represent openings through a surface, including the outermost broken lines that are immediately adjacent to the claimed surfaces around the sides and front and rear (in FIGS. 5-8), represent boundaries of the claim. The broken lines shown within broken line boundaries, and the broken lines shown outside the boundaries of the claim on the top and bottom surfaces, represent environment. The broken lines, the features shown in broken lines, and the unshaded top and bottom surfaces, form no part of the claimed design.

1 Claim, 6 Drawing Sheets



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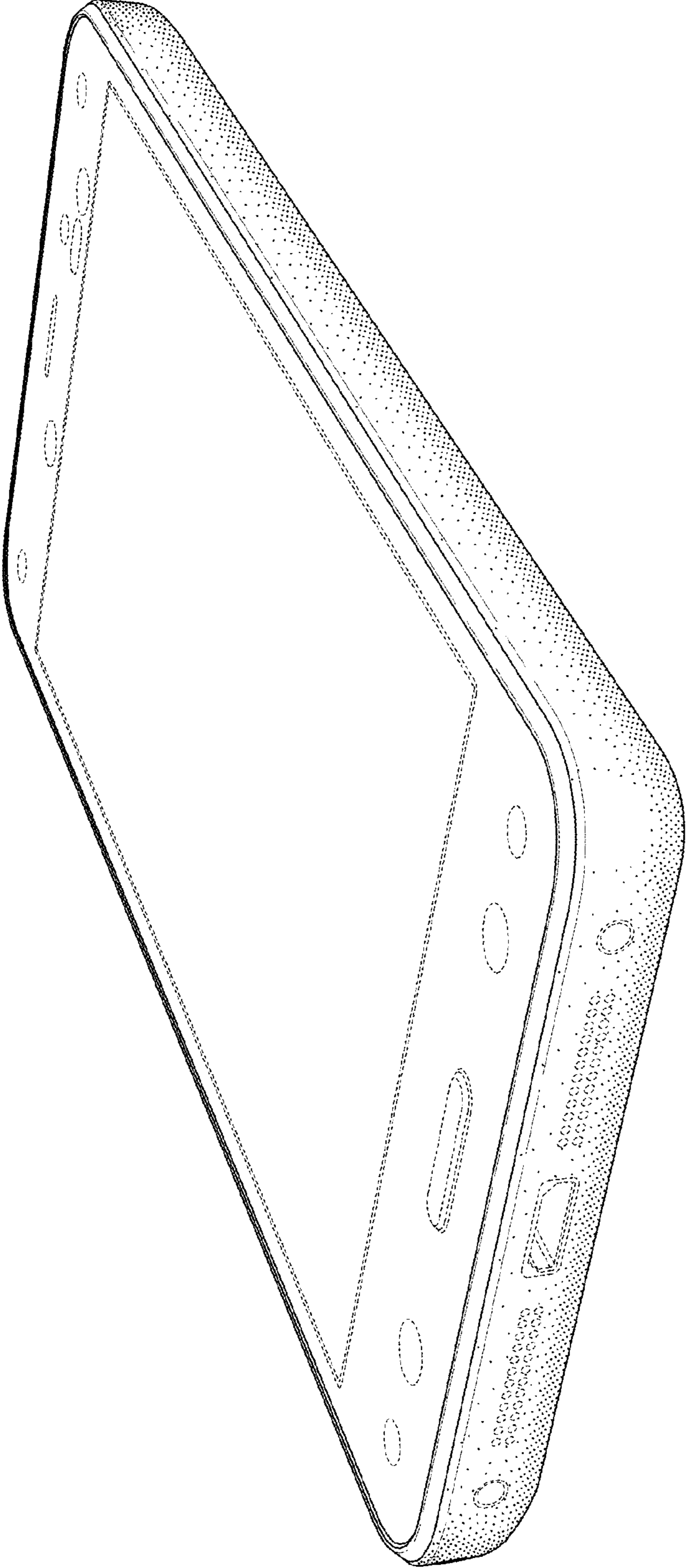


FIG. 1

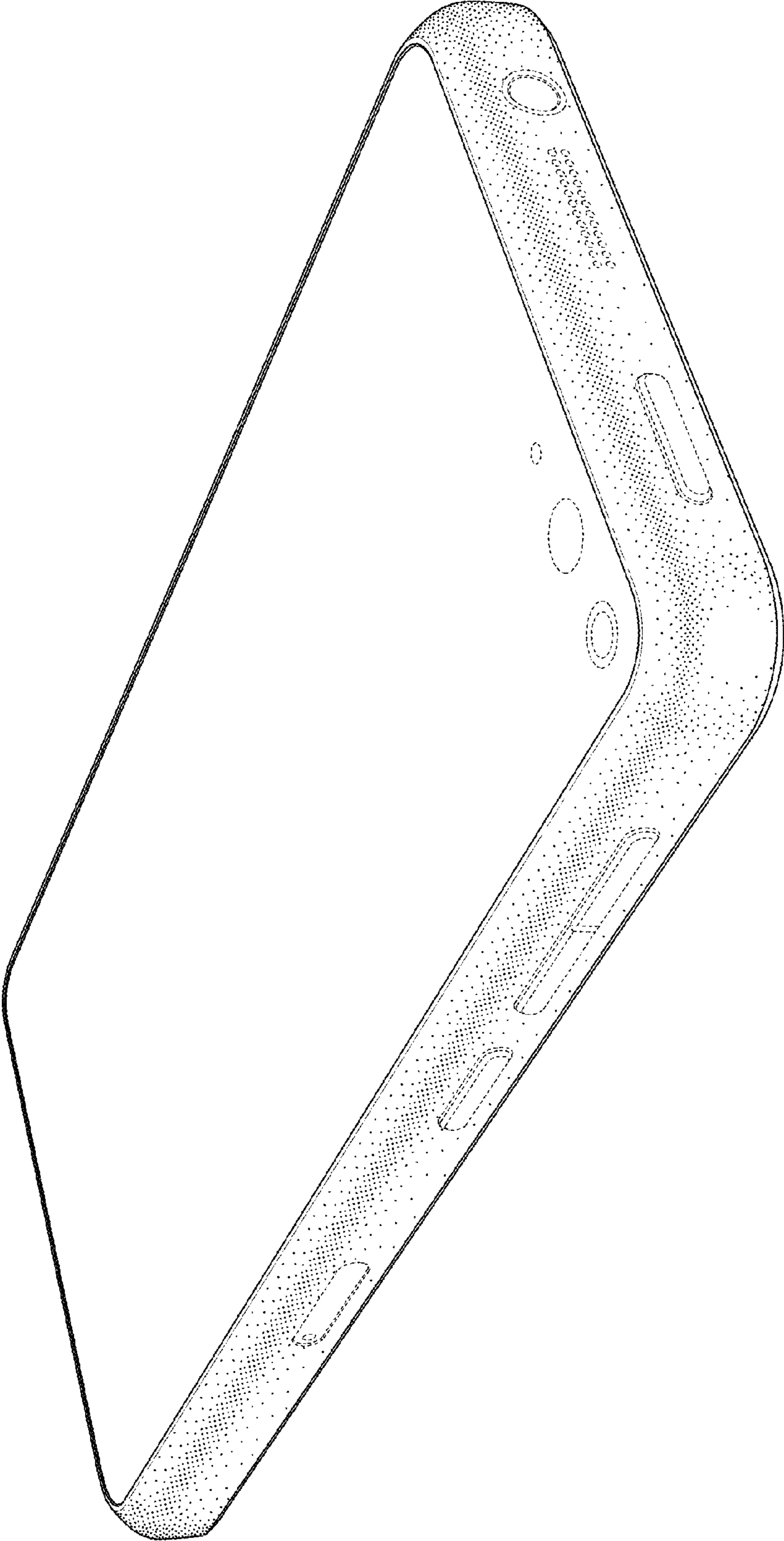


FIG. 2

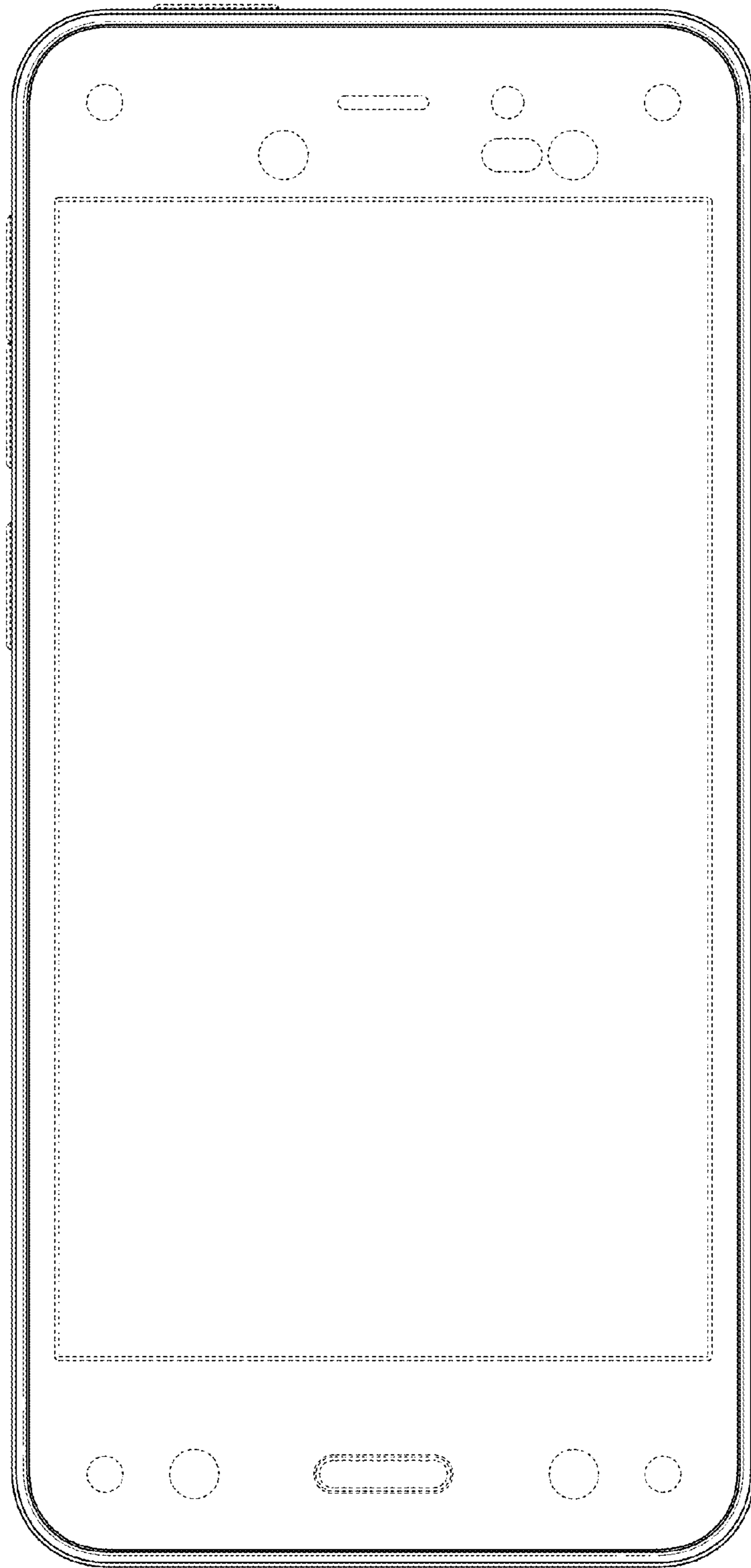


FIG. 3

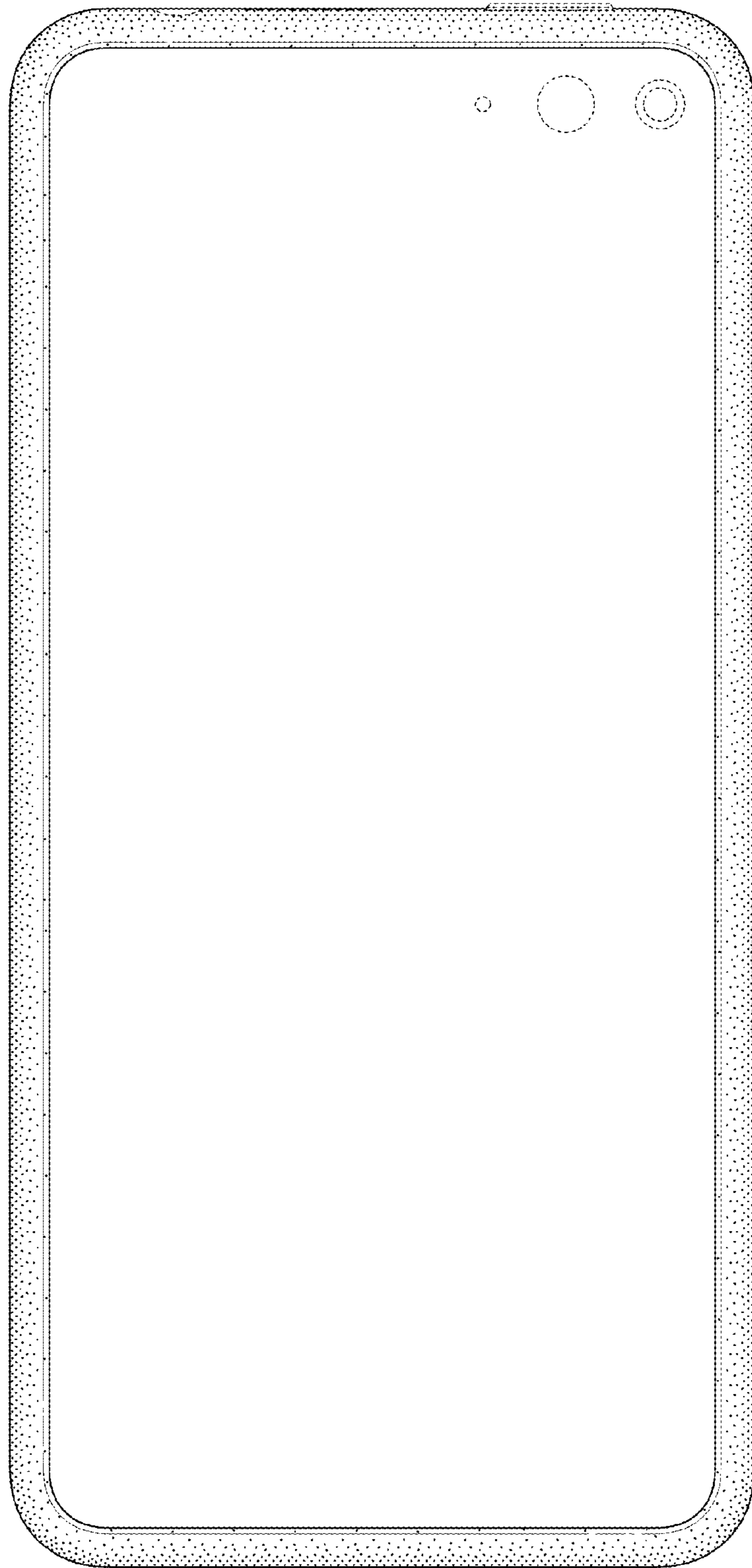


FIG. 4

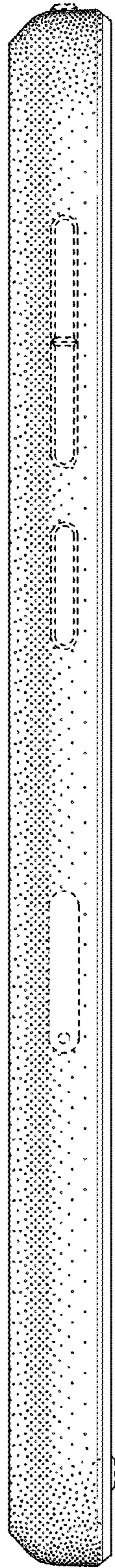


FIG. 5

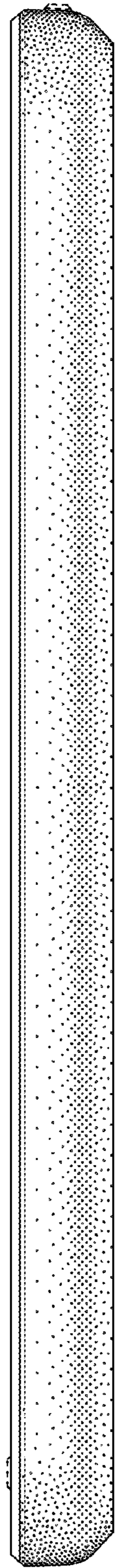


FIG. 6

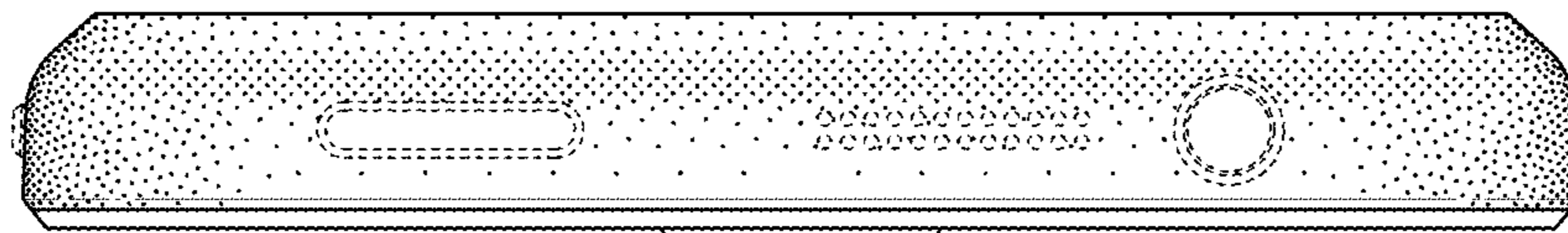


FIG. 7

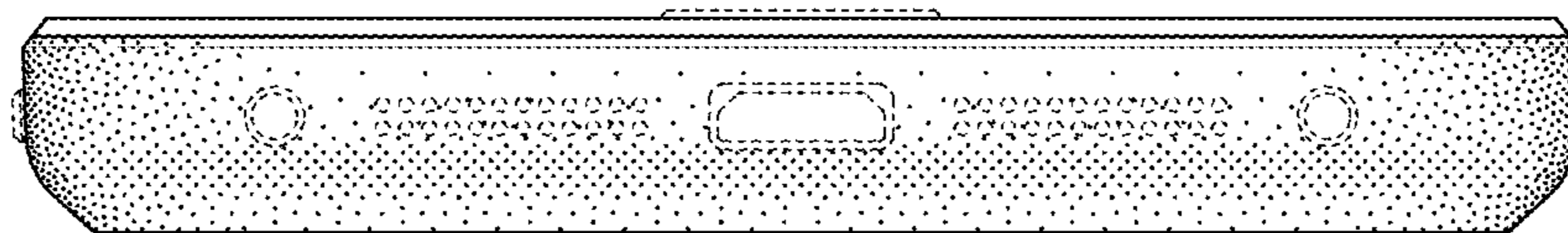


FIG. 8