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(12) **United States Design Patent**
Featherstone

(10) **Patent No.:** **US D758,970 S**
(45) **Date of Patent:** **** Jun. 14, 2016**

- (54) **EDGE-WOUND RESISTOR**
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(US)
- (73) Assignee: **Vishay Dale Electronics, LLC**,
Columbus, NE (US)
- (**) Term: **14 Years**
- (21) Appl. No.: **29/491,946**
- (22) Filed: **May 27, 2014**
- (51) **LOC (10) Cl.** **D13-03**
- (52) **U.S. Cl.**
USPC **D13/125**
- (58) **Field of Classification Search**
USPC D13/117, 123–125, 184, 199; 257/360;
338/267, 270, 278, 296, 334; 439/817
CPC H01C 1/084; H01C 3/00; H01C 3/06;
H01C 3/21; H01C 3/14
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for an edge-wound resistor, as shown and described.

DESCRIPTION

FIG. 1 is a top second end first side perspective view of an edge-wound resistor showing my new design;
FIG. 2 is a first end view of the edge-wound resistor shown in FIG. 1;
FIG. 3 is a second end view of the edge-wound resistor shown in FIG. 1;
FIG. 4 is a first side view of the edge-wound resistor shown in FIG. 1;
FIG. 5 is a second side view of the edge-wound resistor shown in FIG. 1;
FIG. 6 is a top view of the edge-wound resistor shown in FIG. 1; and,
FIG. 7 is a bottom view of the edge-wound resistor shown in FIG. 1.

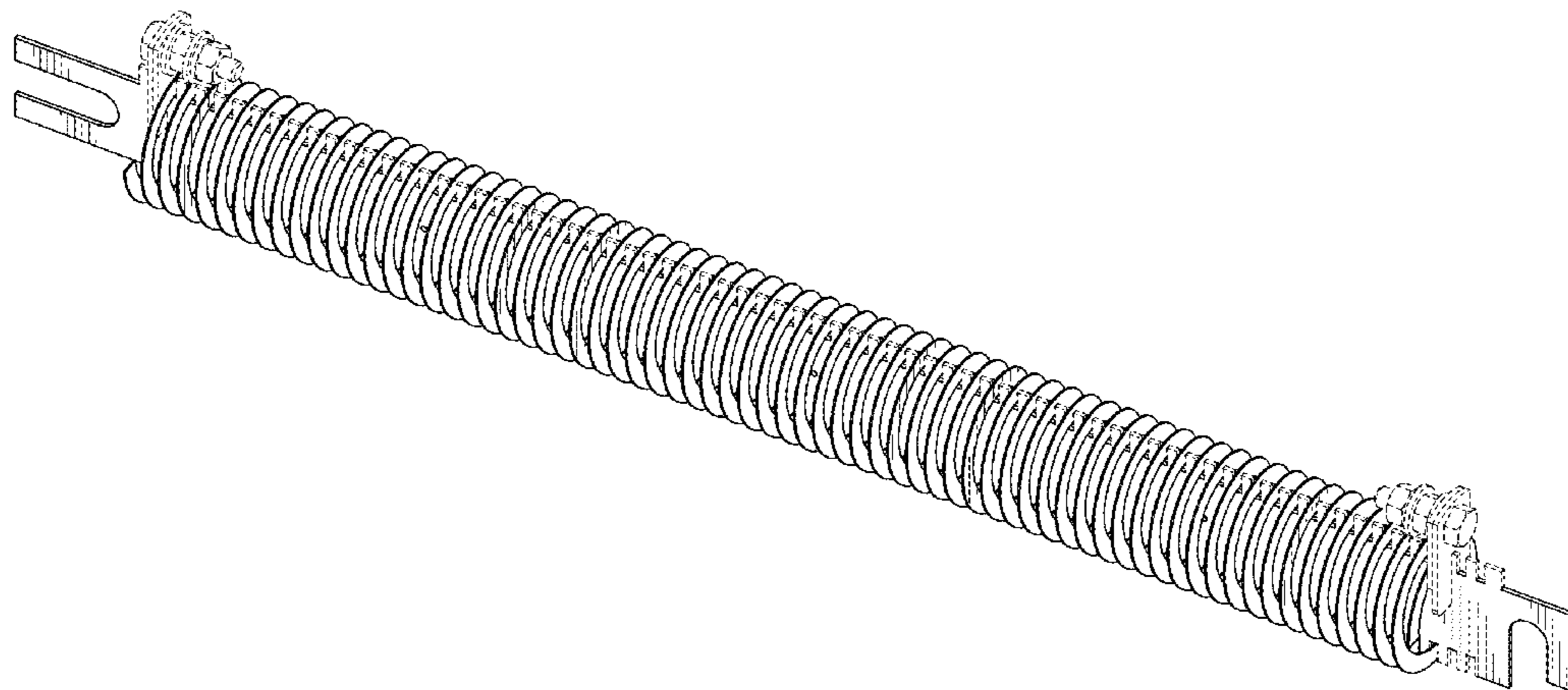
All broken lines appearing in the drawings are for illustrative purposes and/or represent unclaimed subject matter and form no part of the claimed design.

1 Claim, 3 Drawing Sheets

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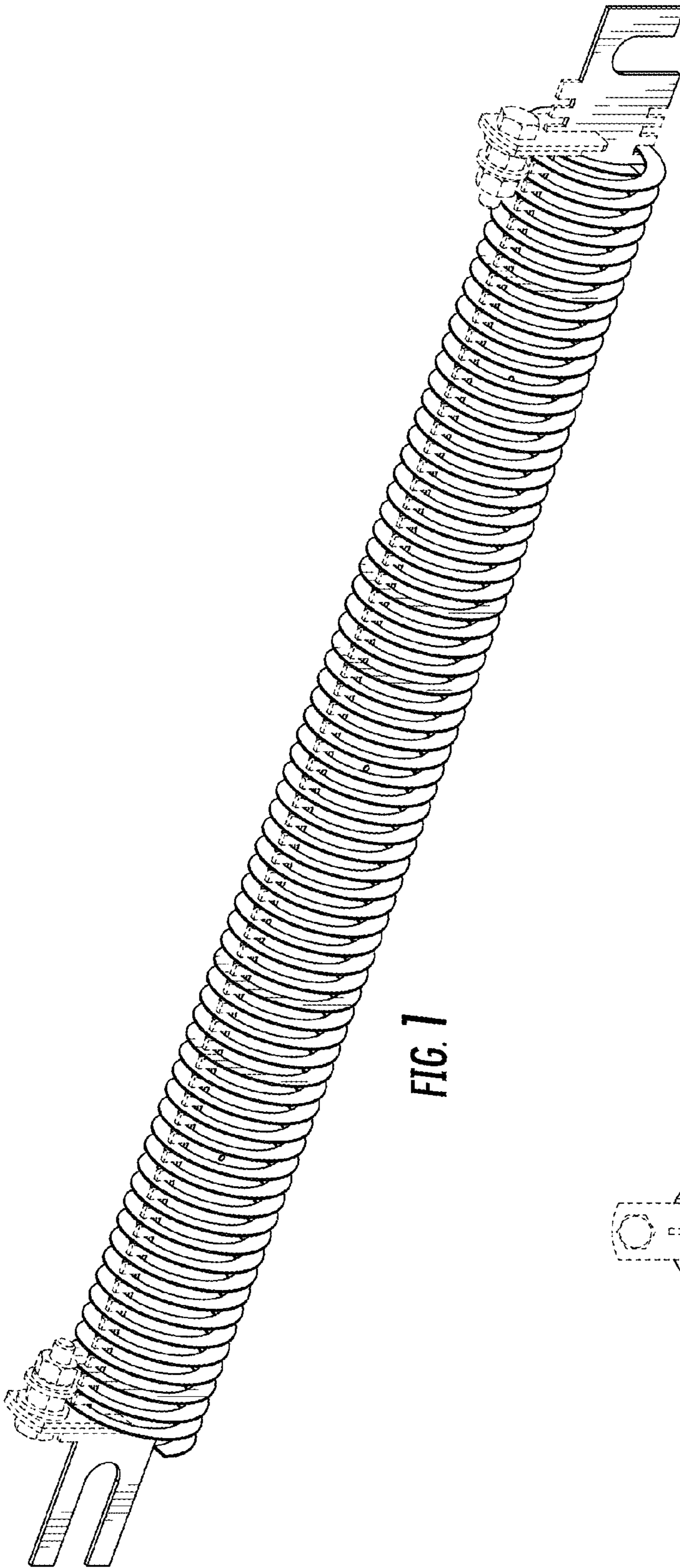


FIG. 1

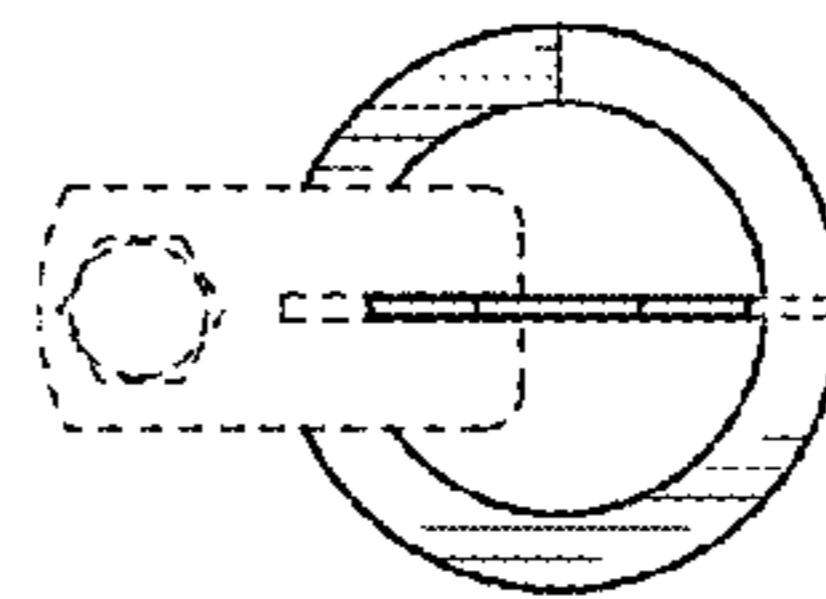


FIG. 2

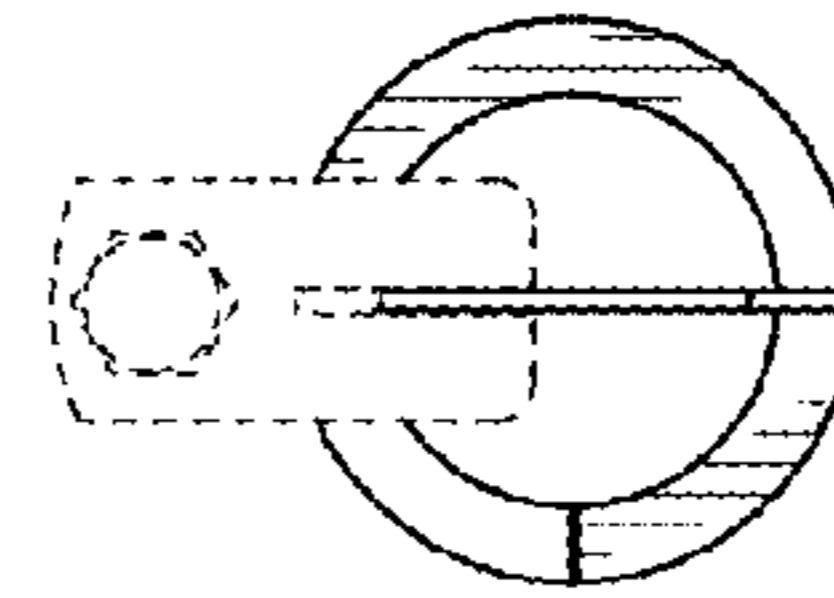


FIG. 3

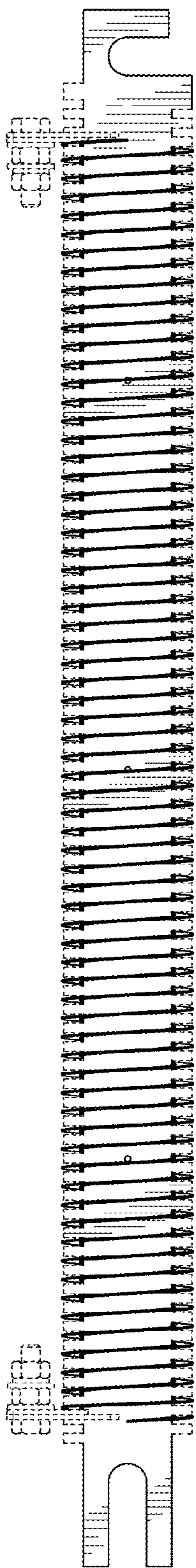


FIG. 4

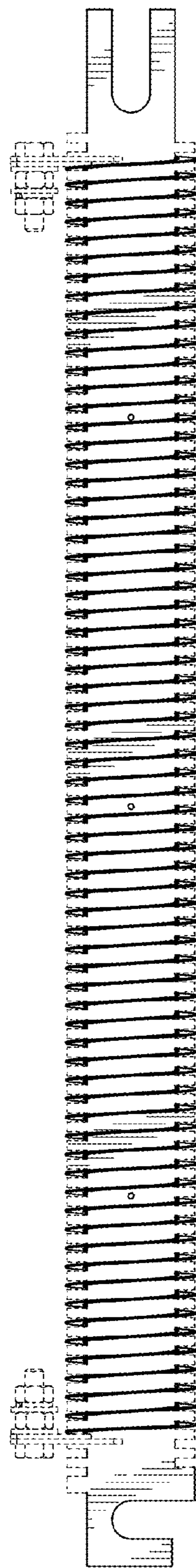


FIG. 5

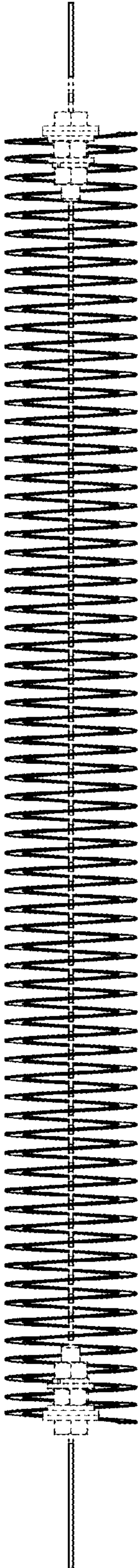


FIG. 6

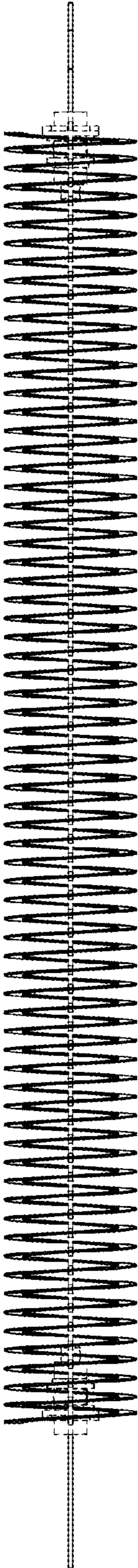


FIG. 7