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(12) **United States Design Patent**
Greiser

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(54) **HOUSING FOR ELECTRONIC CIRCUITRY**

(71) Applicant: **Beckhoff Automation GmbH, Verl (DE)**

(72) Inventor: **Frank Greiser, Rheda-Wiedenbrueck (DE)**

(73) Assignee: **BECKHOFF AUTOMATION GMBH, Verl (DE)**

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(52) **U.S. Cl.**
USPC **D13/162**

(58) **Field of Classification Search**

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D14/301, 302; 361/728, 729, 736

CPC G05B 19/00; G05B 19/04; G05B 19/0423;
H05K 7/14; H05K 7/1428; H05K 7/1464;
H05K 7/1465; H05K 7/1467; H05K 7/1468;
H05K 7/1469; H05K 7/1471; H05K 7/1474;
H05K 7/1481

See application file for complete search history.

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Primary Examiner — Selina Sikder

(74) *Attorney, Agent, or Firm* — Dorsey & Whitney LLP

(57) **CLAIM**

The ornamental housing design for electronic circuitry, as shown and described.

DESCRIPTION

FIG. 1 is an isometric view of a housing for electronic circuitry, presenting the top, front and left side.

FIG. 2 is an alternate isometric view of the housing for electronic circuitry as shown in FIG. 1, presenting the top, back and right side.

FIG. 3 is a top view of the housing for electronic circuitry as shown in FIG. 1.

FIG. 4 is a bottom view of the housing for electronic circuitry as shown in FIG. 1.

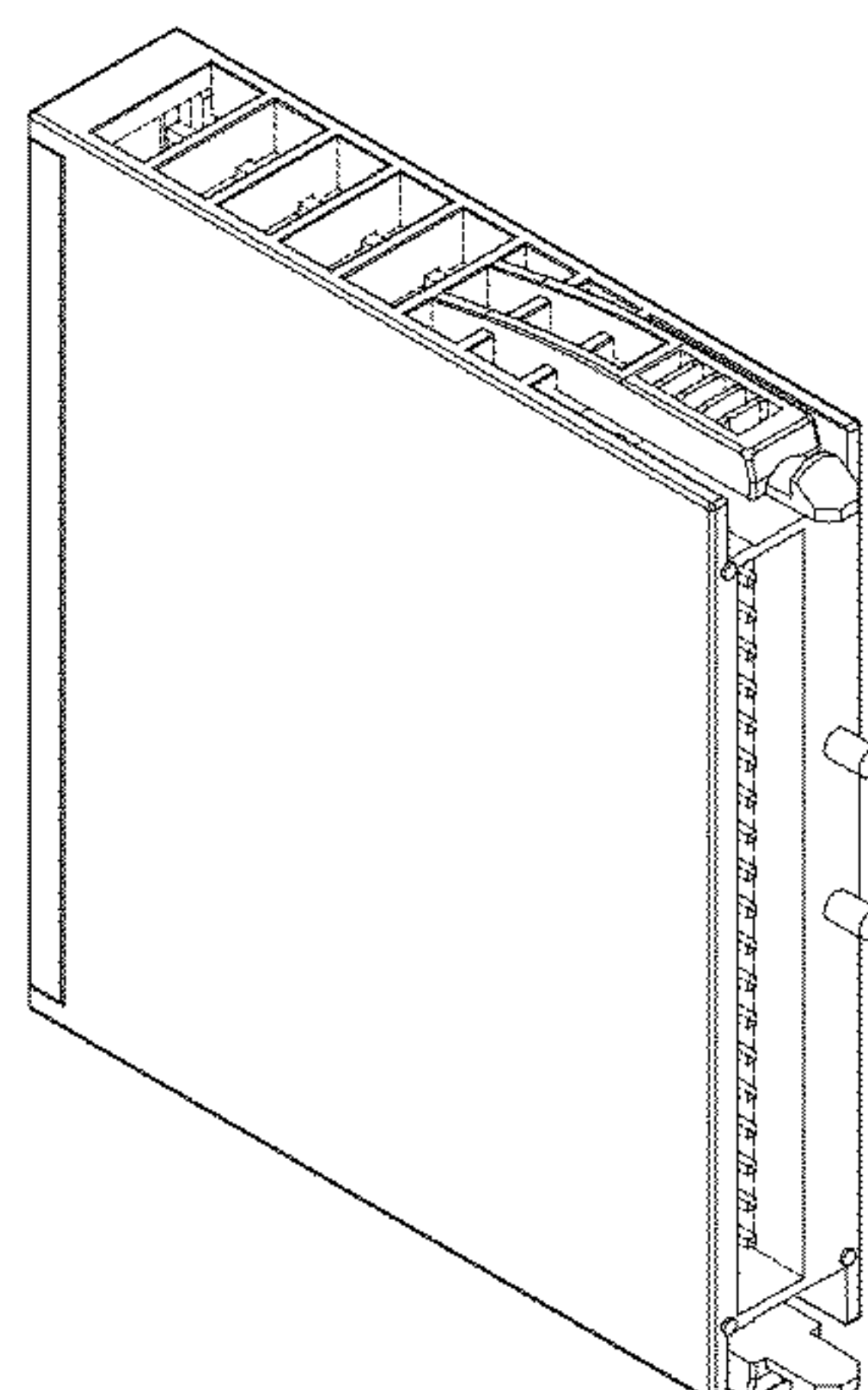
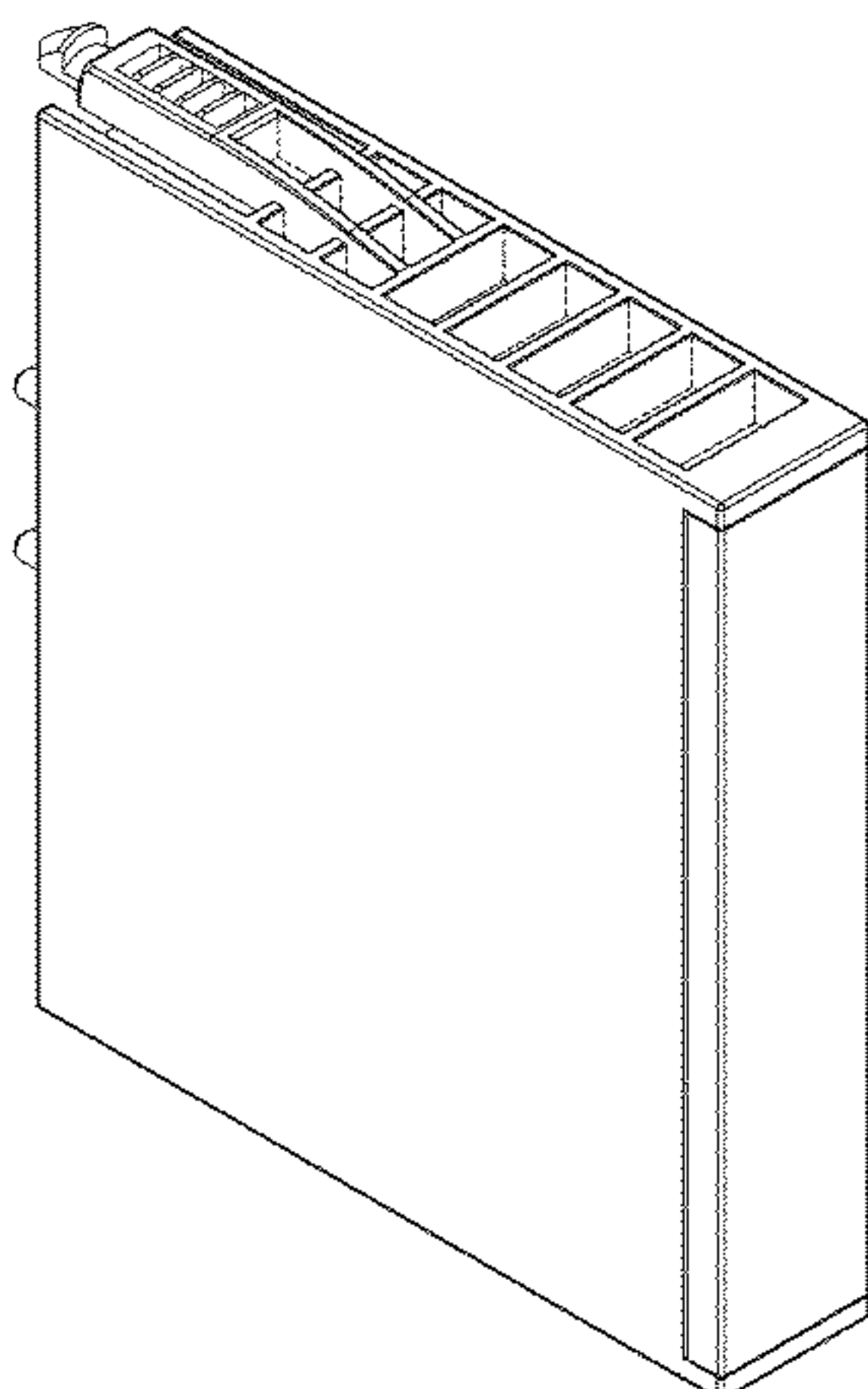
FIG. 5 is a back view of the housing for electronic circuitry as shown in FIG. 1.

FIG. 6 is a left side view of the housing for electronic circuitry as shown in FIG. 1; and,

FIG. 7 is a detail view of the housing for electronic circuitry, presenting portions of the back and right side as shown in FIG. 2.

The broken lines in the drawings show environmental structure and form no part of the claimed design.

1 Claim, 7 Drawing Sheets



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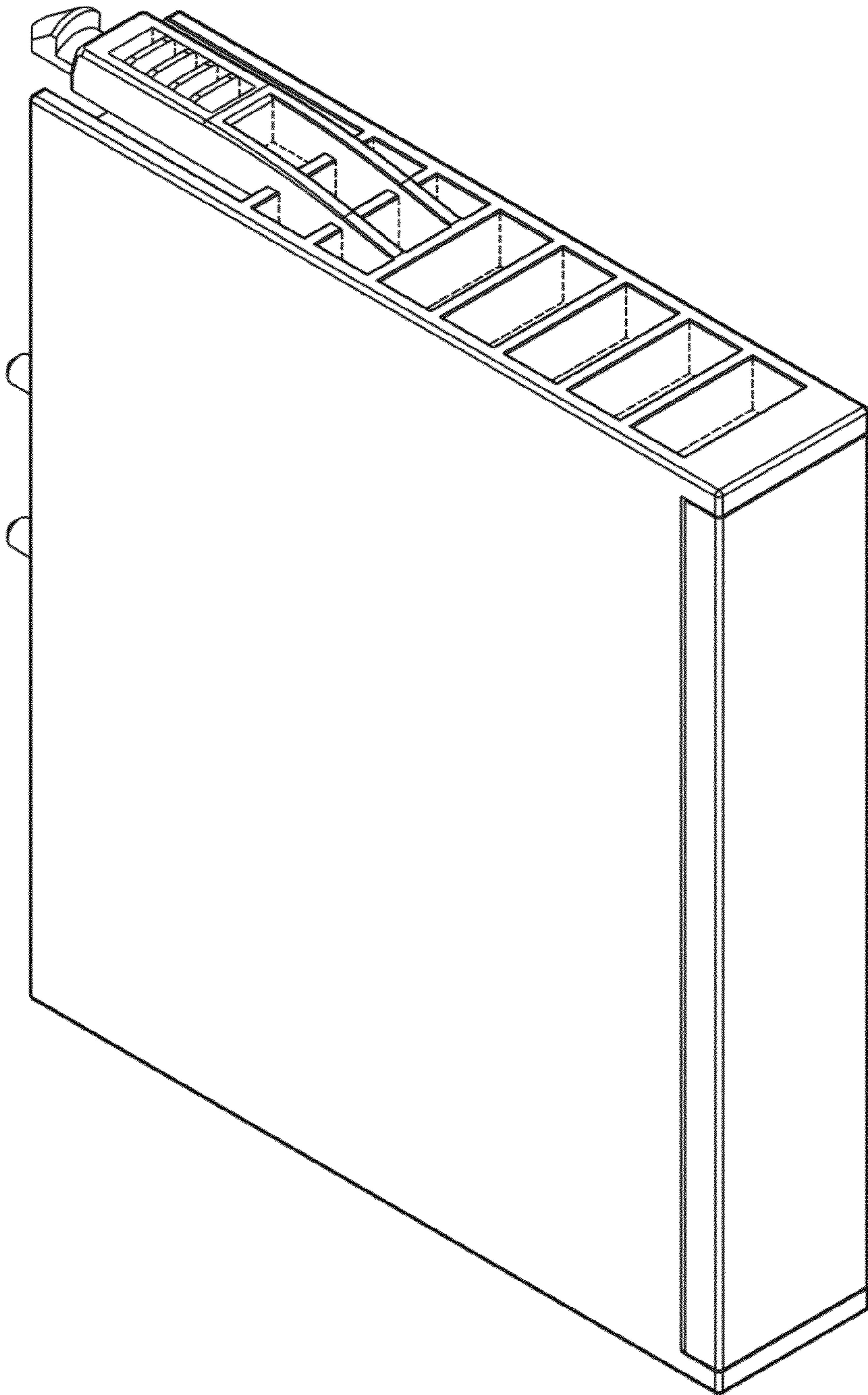


FIG. 1

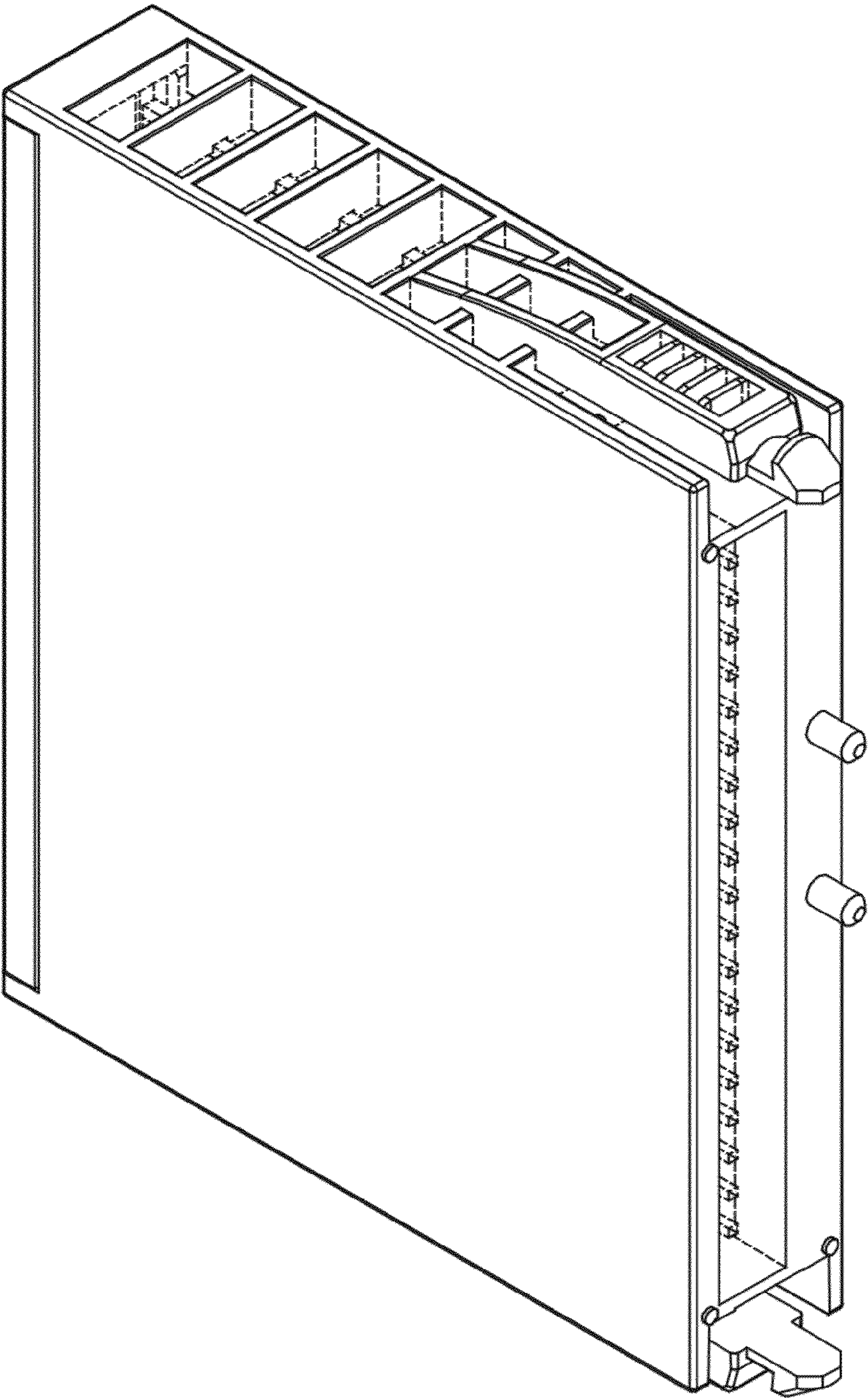


FIG. 2

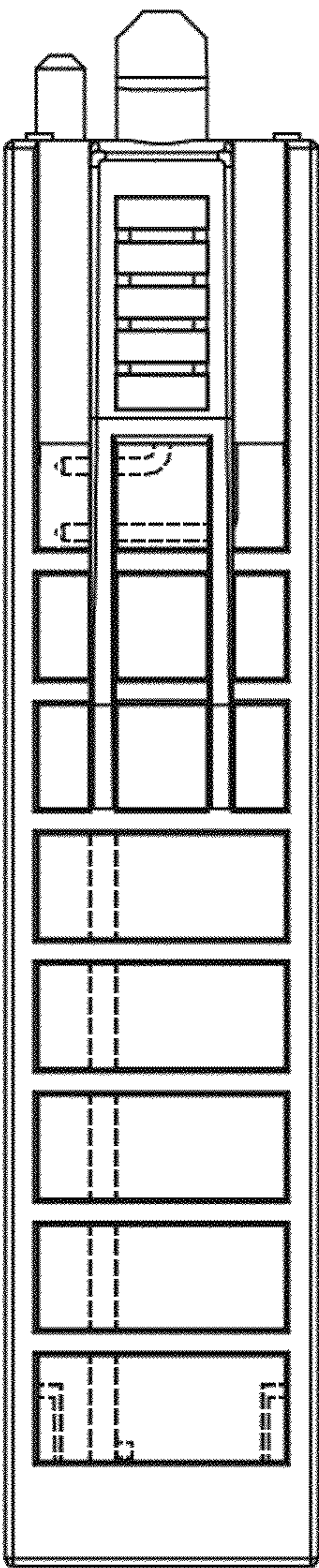


FIG. 3

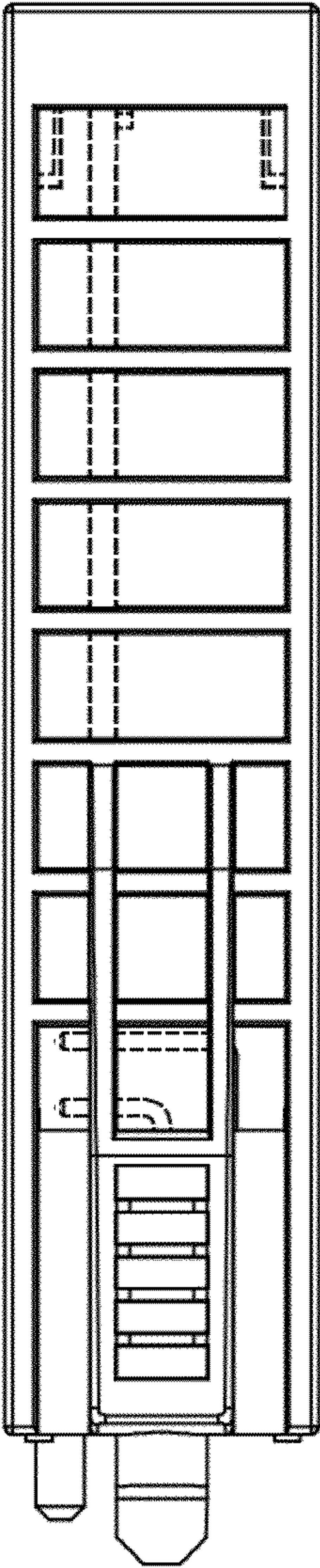


FIG. 4

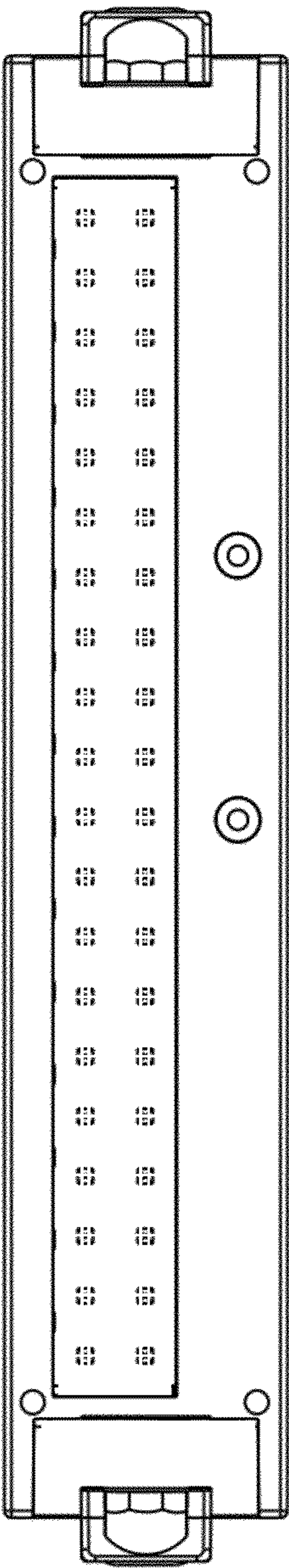


FIG. 5

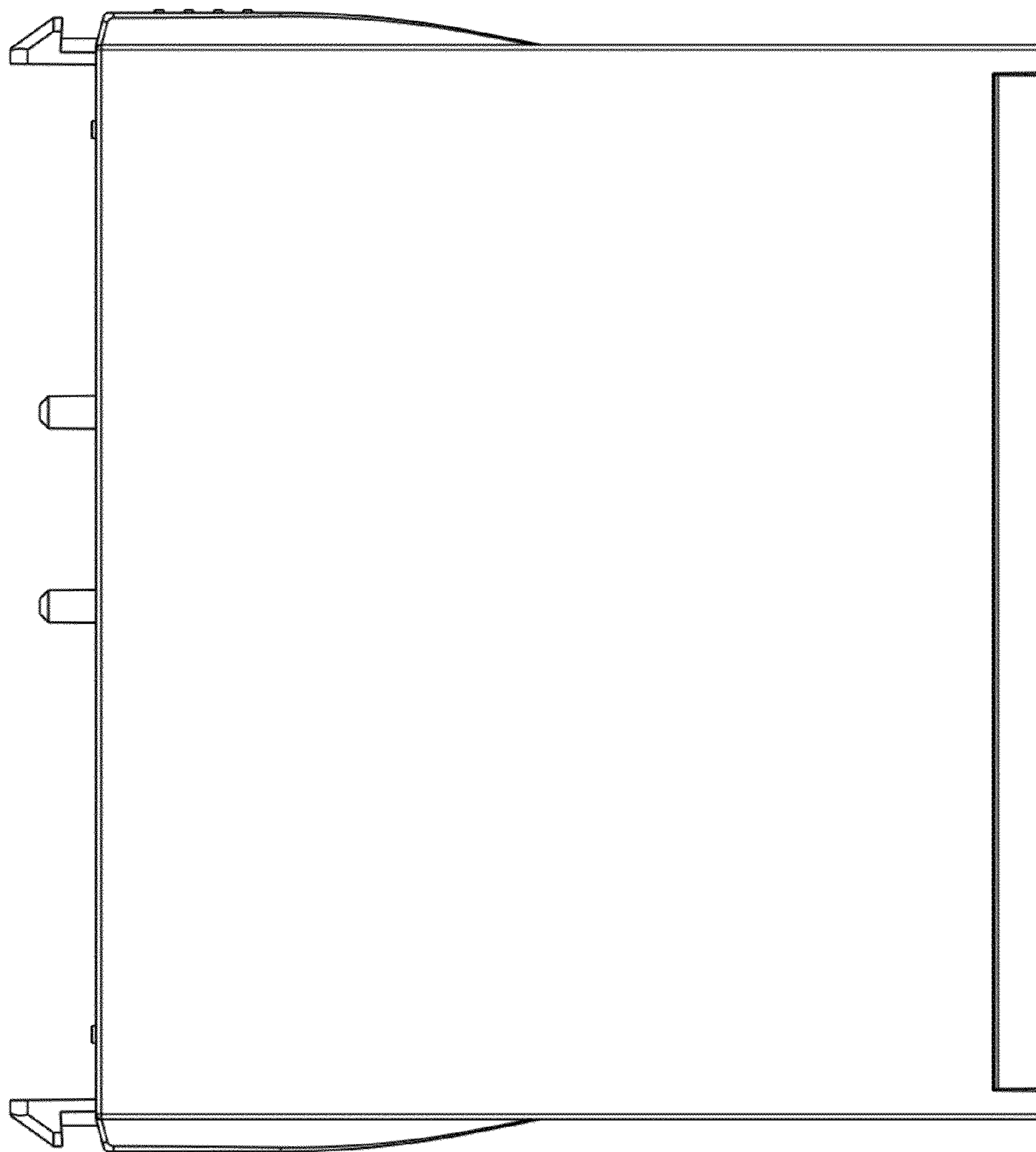


FIG. 6

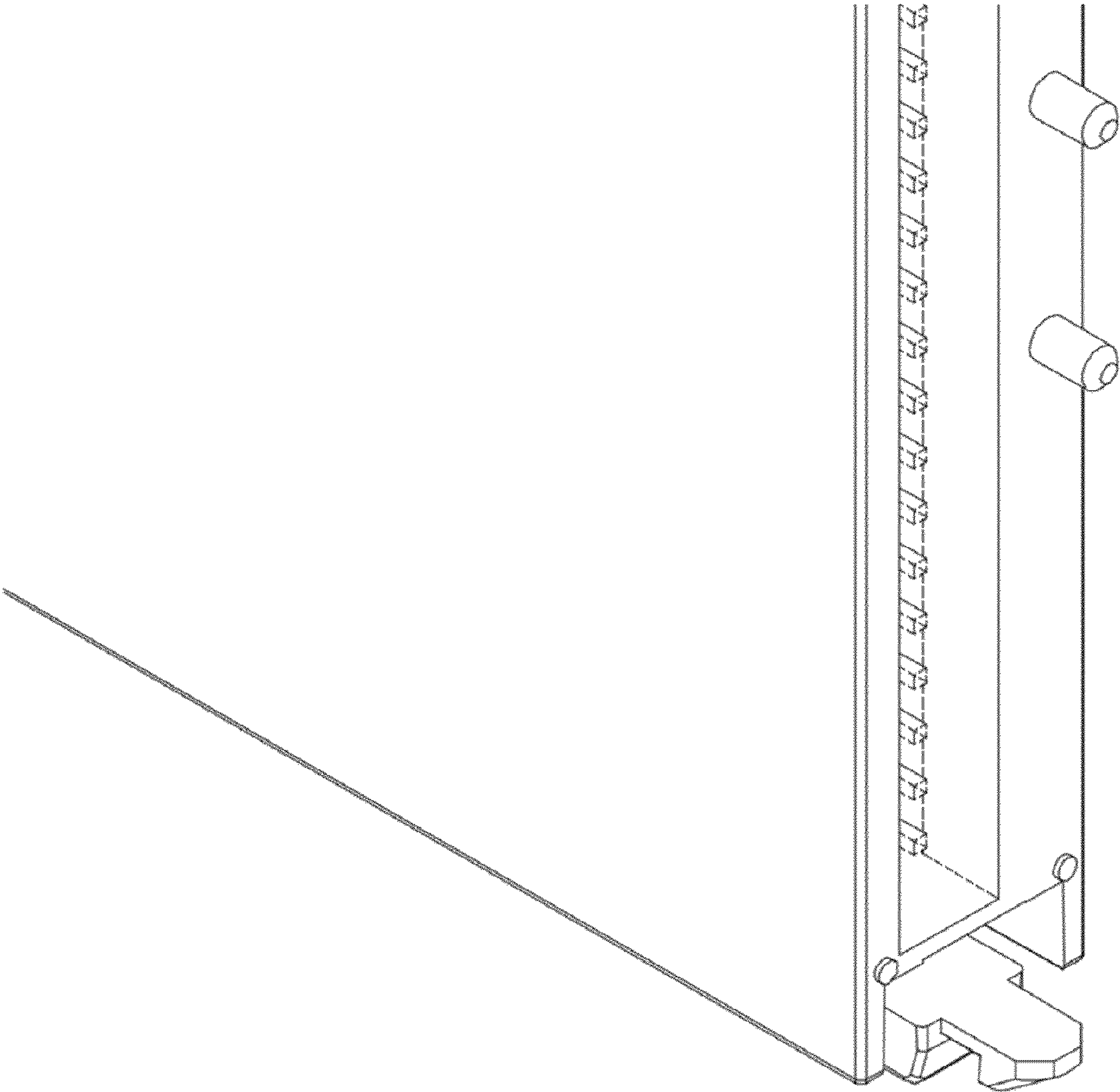


FIG. 7