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(54) **ELECTRONIC DEVICE**

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See application file for complete search history.

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(57) **CLAIM**

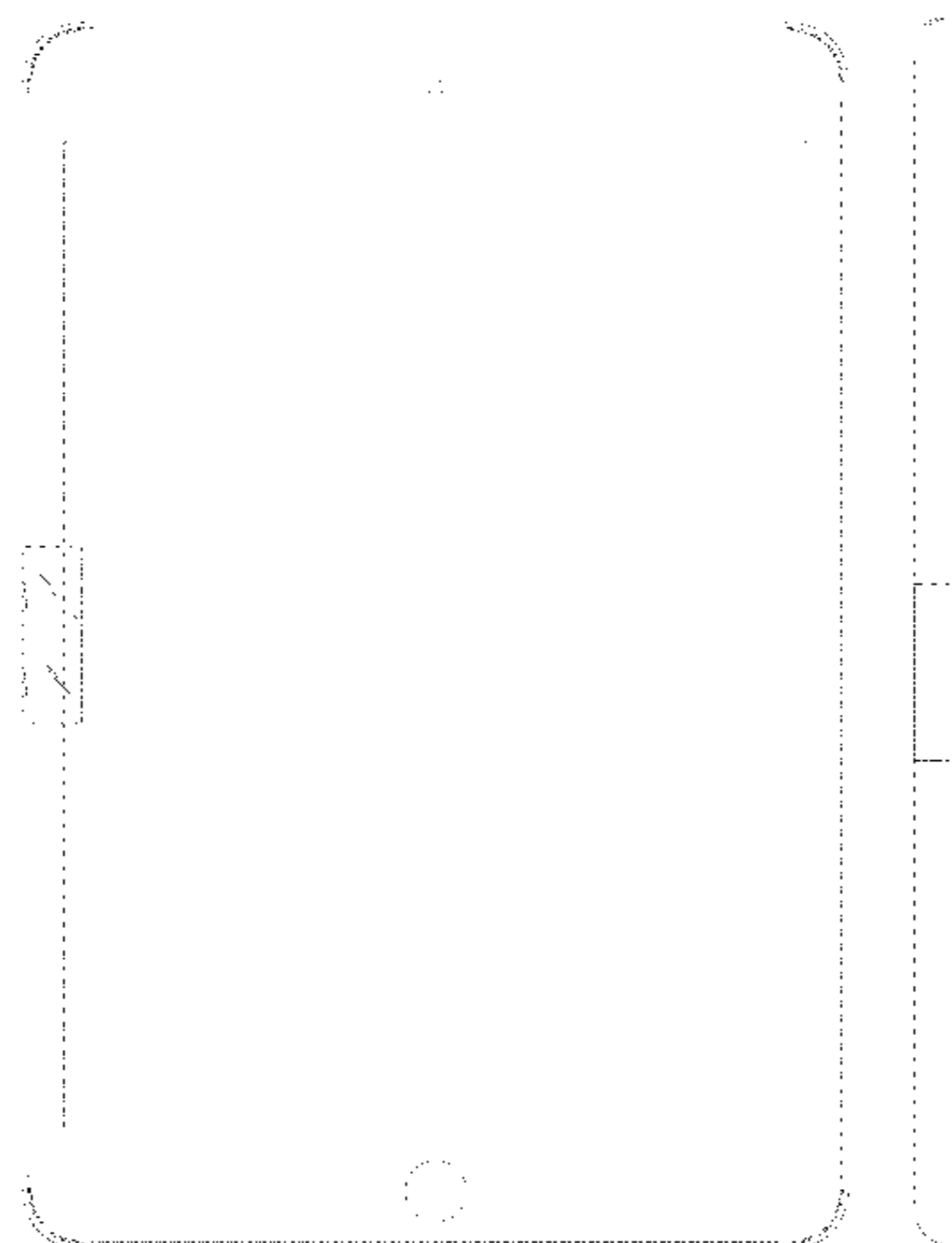
The ornamental design for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a bottom front perspective view of an electronic device showing our new design;
FIG. 2 is a bottom rear perspective view thereof;
FIG. 3 is a top rear perspective view thereof;
FIG. 4 is a front view thereof;
FIG. 5 is a rear view thereof;
FIG. 6 is a left side view thereof;
FIG. 7 is a right side view thereof;
FIG. 8 is a top view thereof; and,
FIG. 9 is a bottom view thereof.

The broken lines in the Figures show portions of the electronic device which form no part of the claimed design. The dot-dash lines in the Figures show a boundary and form no part of the claimed design.

1 Claim, 7 Drawing Sheets



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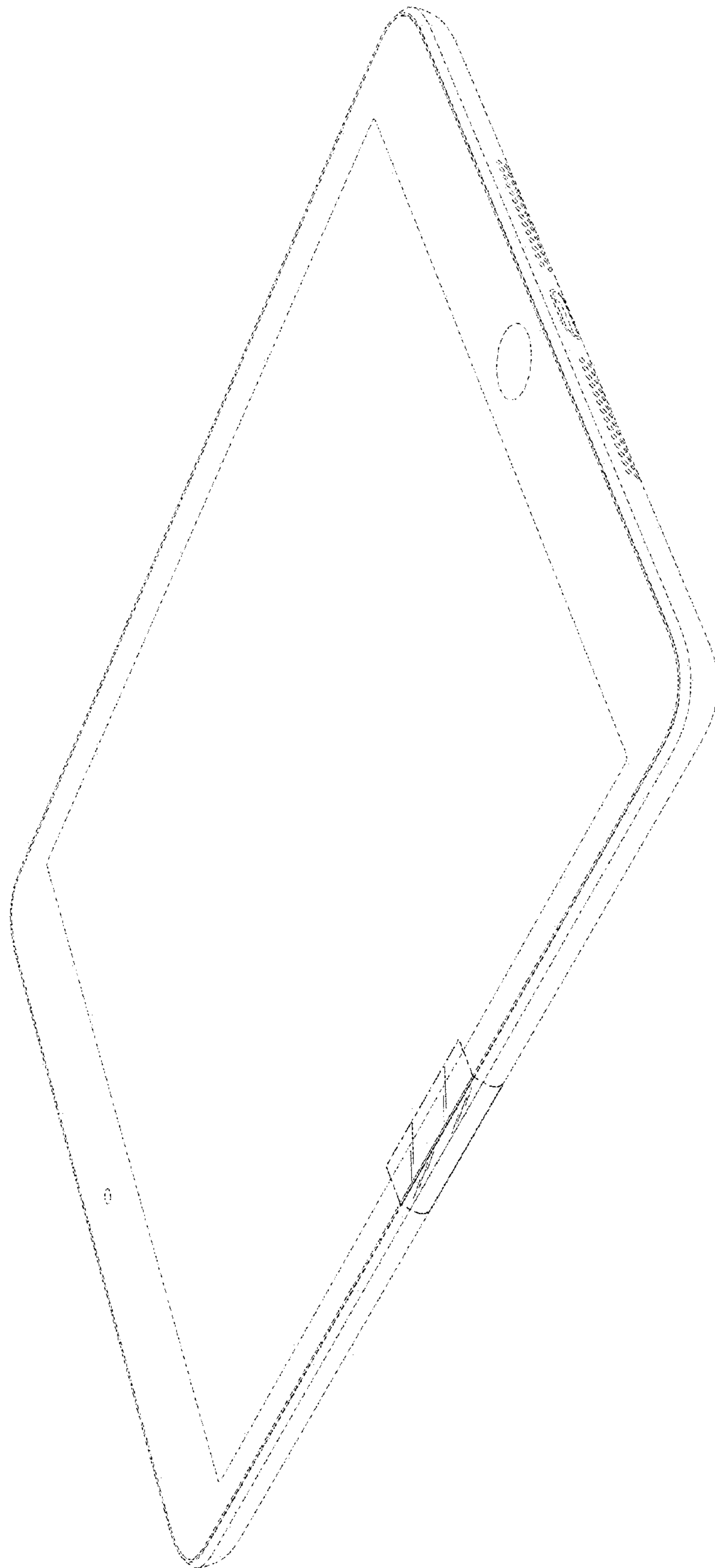


FIG. 1

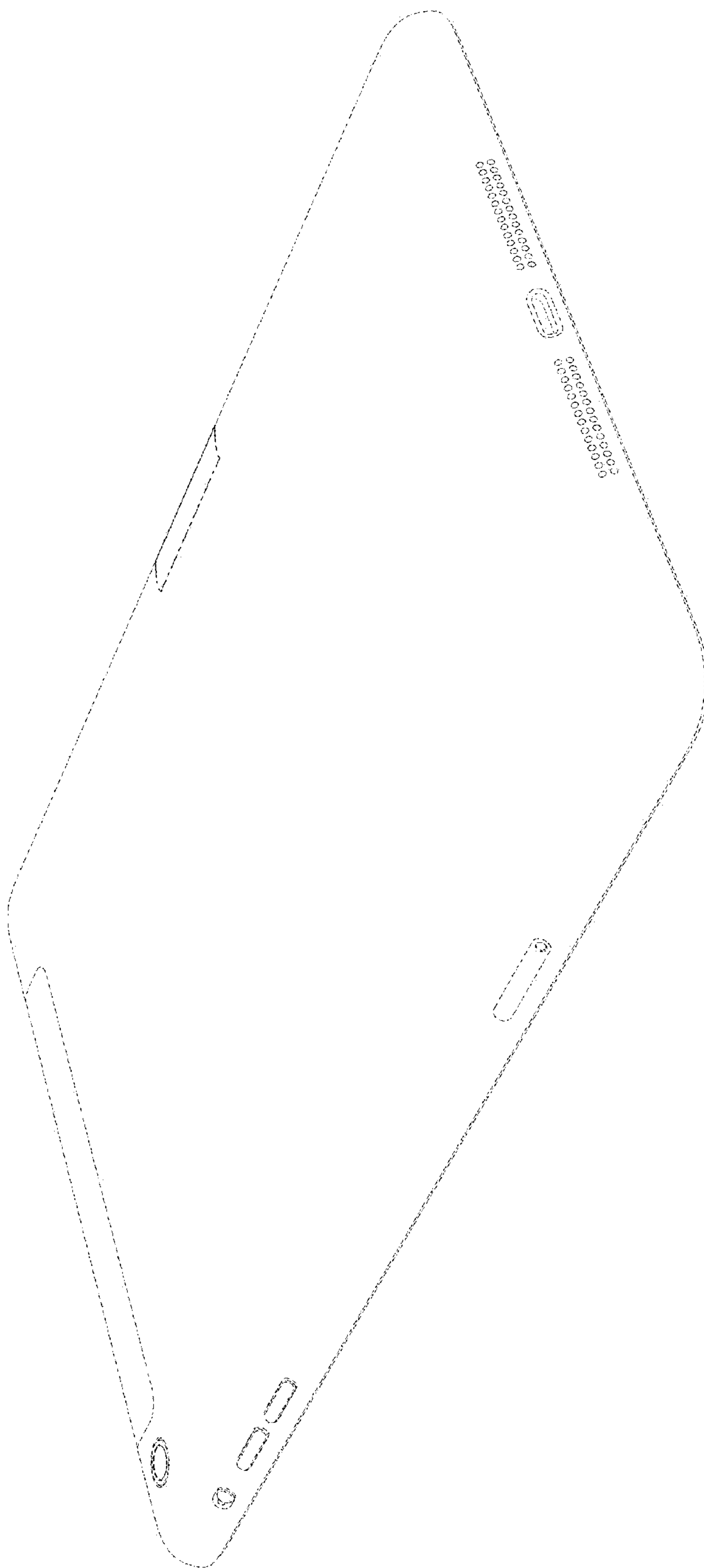


FIG. 2

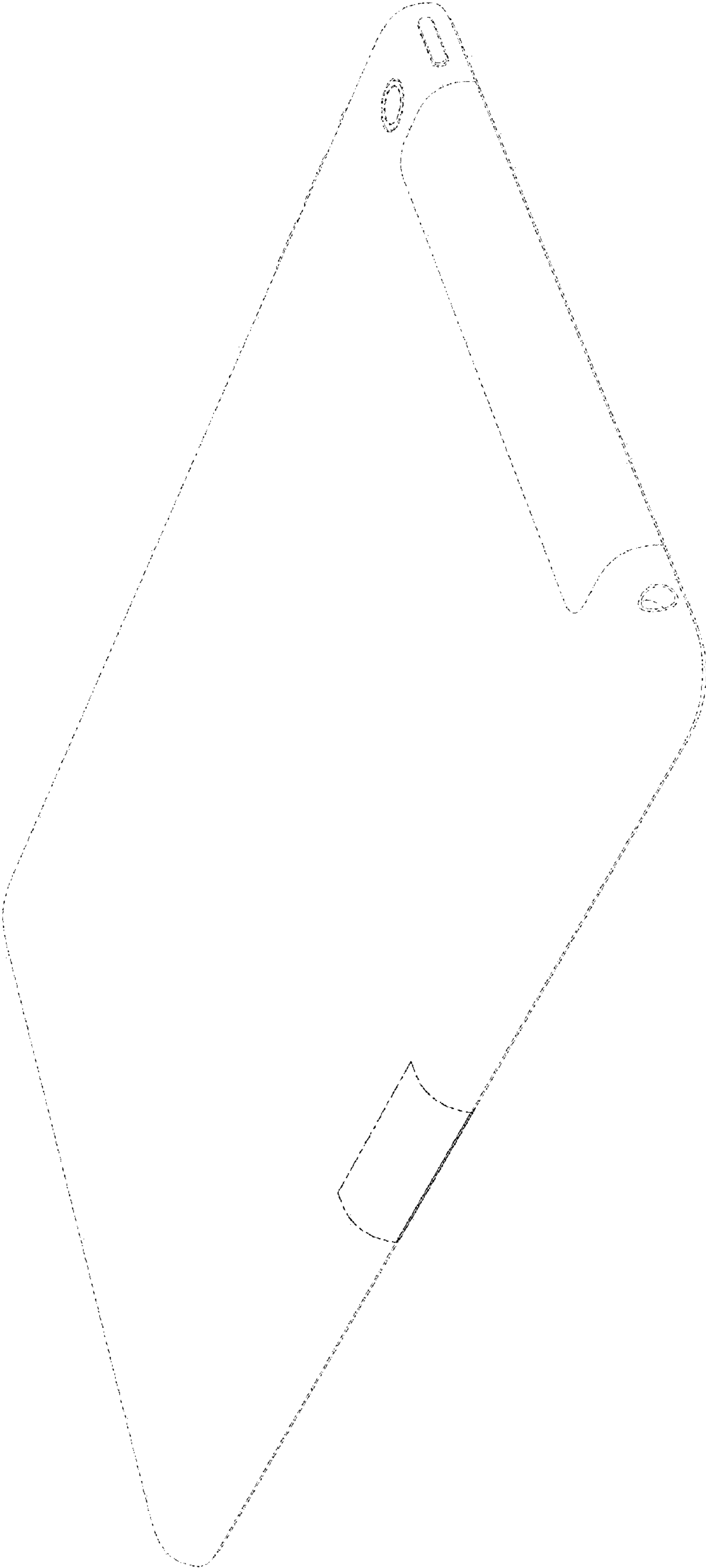


FIG. 3

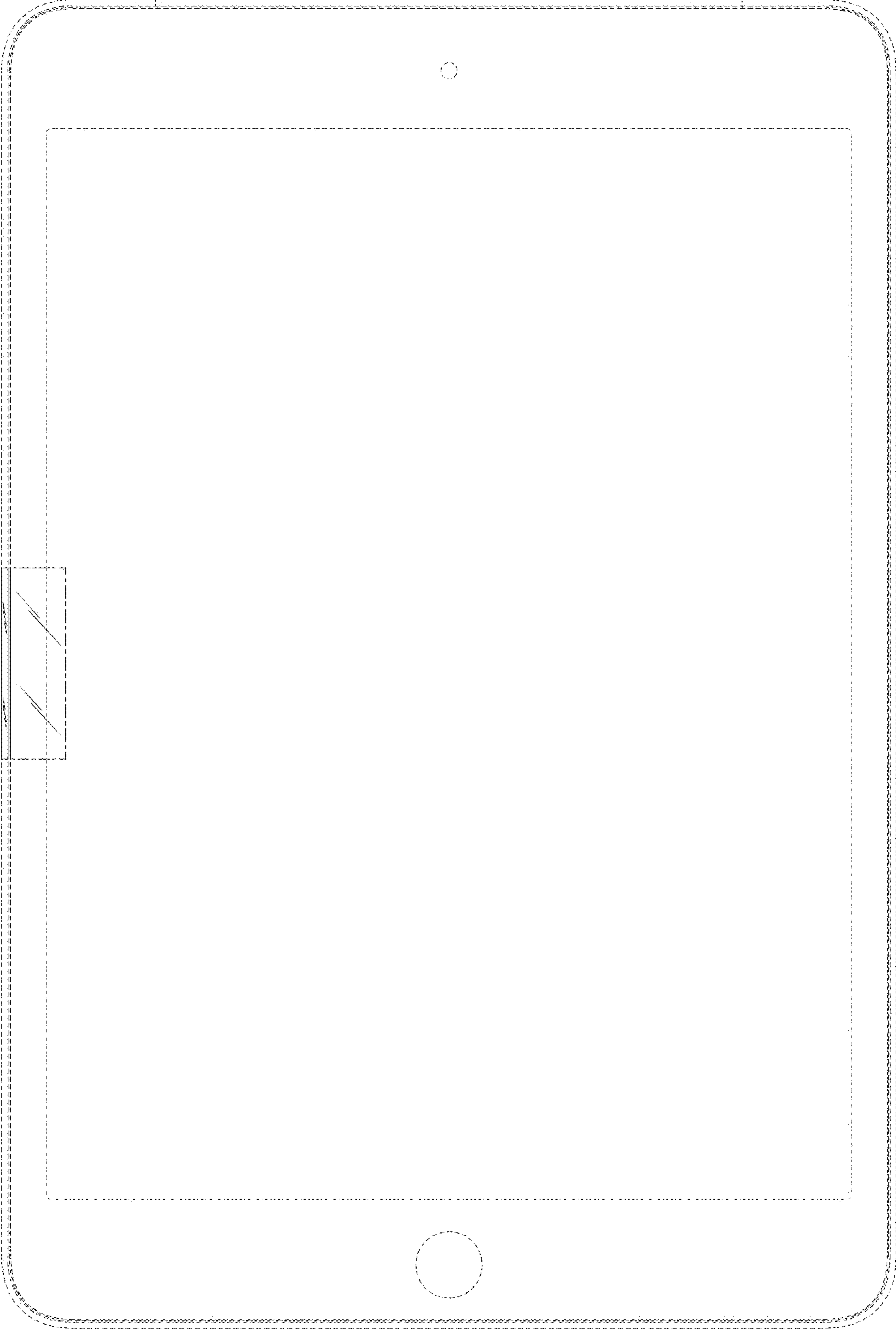


FIG. 4

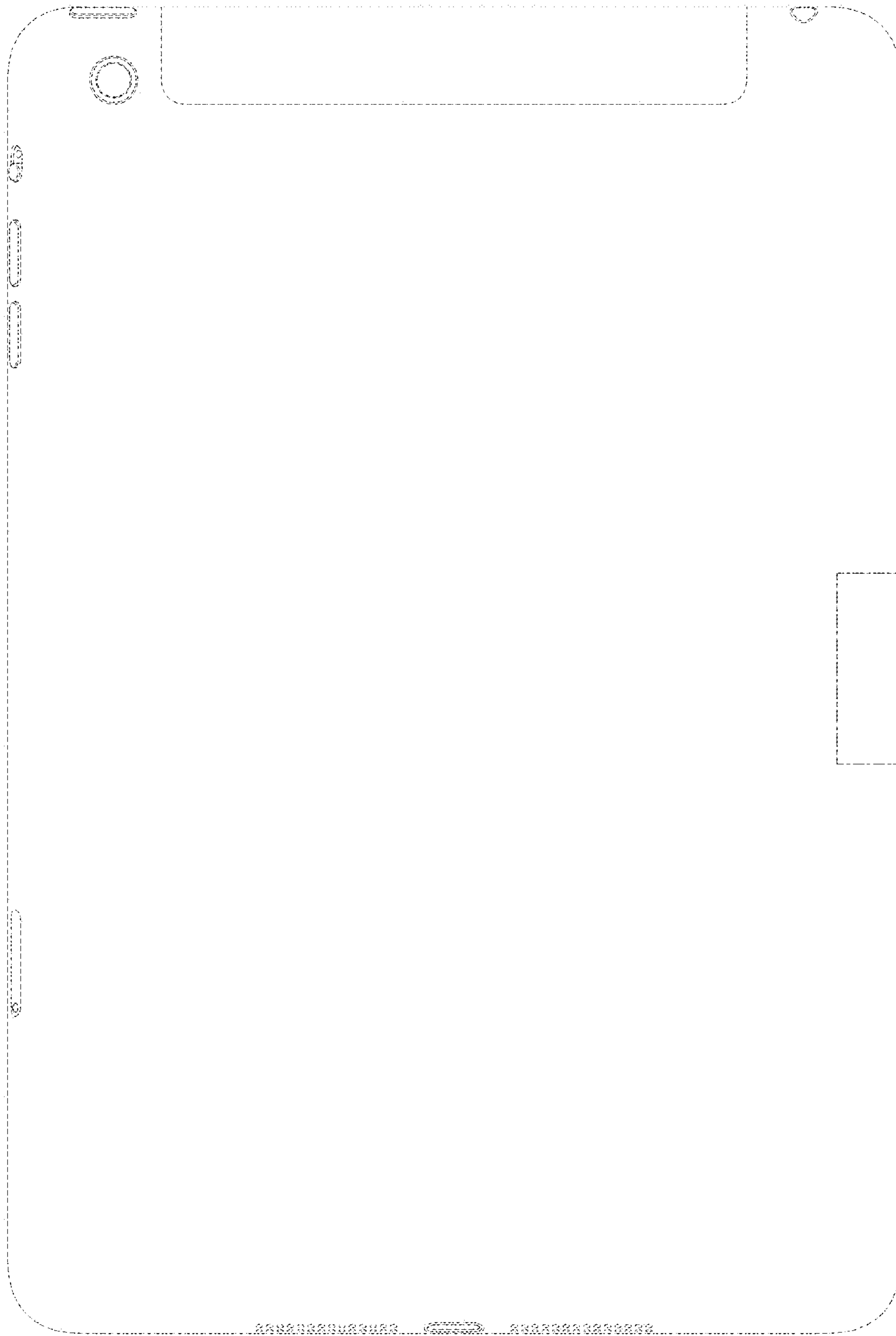


FIG. 5

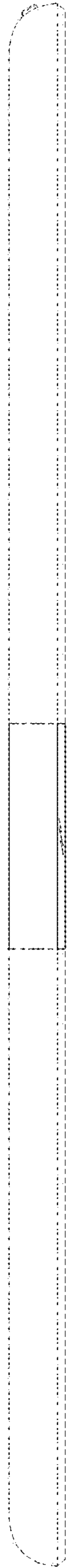


FIG. 6

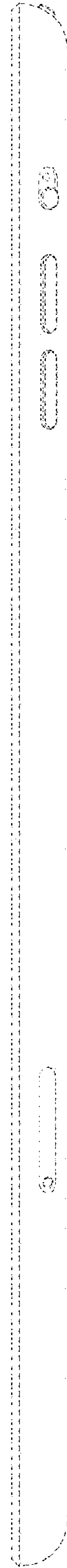


FIG. 7

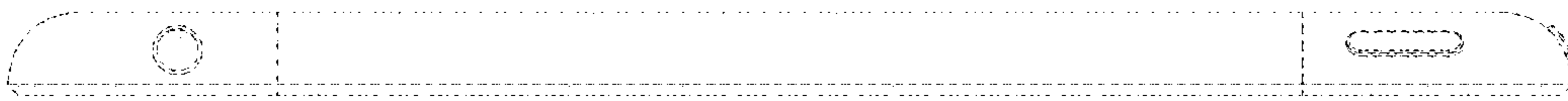


FIG. 8

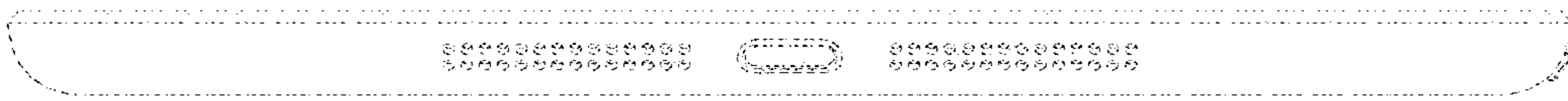


FIG. 9

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : D756,353 S
APPLICATION NO. : 29/489535
DATED : May 17, 2016
INVENTOR(S) : Jody Akana

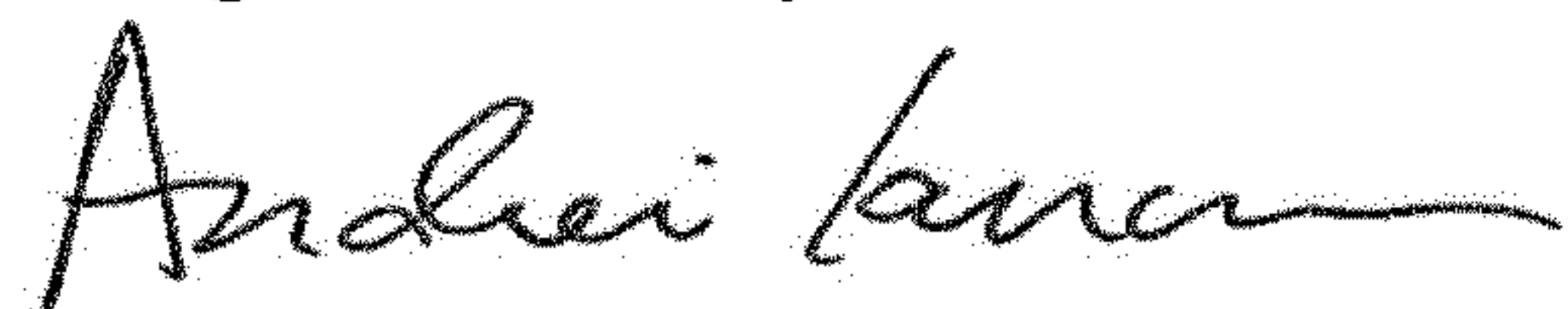
Page 1 of 3

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Delete the title page and replace with the attached title page showing the corrected illustrative figure.

Delete Drawing Sheet 6 of 7 and replace with the attached Drawing Sheet 6 of 7 with replacement Figures 6 and 7.

Signed and Sealed this
Eighteenth Day of June, 2019



Andrei Iancu
Director of the United States Patent and Trademark Office

(12) **United States Design Patent** (10) **Patent No.:** **US D756,353 S**
Akana et al. (45) **Date of Patent:** **** May 17, 2016**

(54) **ELECTRONIC DEVICE**
 (71) Applicant: **Apple Inc.**, Cupertino, CA (US)
 (72) Inventors: **Jody Akana**, San Francisco, CA (US); **Bartley K. Andre**, Palo Alto, CA (US); **Jeremy Bataillou**, San Francisco, CA (US); **Daniel J. Coster**, San Francisco, CA (US); **Daniele De Iullis**, San Francisco, CA (US); **M. Evans Hankey**, San Francisco, CA (US); **Julian Hoenig**, San Francisco, CA (US); **Richard P. Howarth**, San Francisco, CA (US); **Jonathan P. Ive**, San Francisco, CA (US); **Duncan Robert Kerr**, San Francisco, CA (US); **Shin Nishibori**, Kailua, HI (US); **Matthew Dean Rohrbach**, San Francisco, CA (US); **Peter Russell-Clarke**, San Francisco, CA (US); **Mikael Silvano**, San Francisco, CA (US); **Christopher J. Stringer**, Woodside, CA (US); **Eugene Antony Whang**, San Francisco, CA (US); **Rico Zörkendörfer**, San Francisco, CA (US)

(73) Assignee: **Apple Inc.**, Cupertino, CA (US)
 (**) Term: **14 Years**

(21) Appl. No.: **29/489,535**
 (22) Filed: **Apr. 30, 2014**

Related U.S. Application Data

(63) Continuation of application No. 29/454,126, filed on May 7, 2013, now Pat. No. Des. 705,779, which is a continuation of application No. 29/429,478, filed on Aug. 11, 2012, now Pat. No. Des. 681,632.

(51) **LOC (10) Cl.** **14-02**
 (52) **U.S. Cl.**
 USPC **D14/341**

(58) **Field of Classification Search**
 USPC D14/315-318, 341-347, 420, 426, 129, D14/130, 496, 137, 138 R, 138 AA, 138 C, D14/138 G, 147, 203.1, 203.3, 204.4, 203.7, D14/218, 248, 388, 389; D10/50, 65, 104.1; D21/329, 330; D6/308, 310

CPC ... G06F 3/0485; G06F 3/0488; G06F 1/1613; G06F 1/1624; G06F 1/1626; G06F 1/165; H04M 1/0202; H04M 1/0264; H04M 1/0214; H04M 1/0279; H04M 1/0281; H04M 1/0283; H04M 1/72544
 See application file for complete search history.

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Primary Examiner — Barbara Fox

Assistant Examiner — Dana K Weiland

(74) *Attorney, Agent, or Firm* — Sterne, Kessler, Goldstein & Fox P.L.L.C.

(57) **CLAIM**

The ornamental design for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a bottom front perspective view of an electronic device showing our new design; FIG. 2 is a bottom rear perspective view thereof; FIG. 3 is a top rear perspective view thereof; FIG. 4 is a front view thereof; FIG. 5 is a rear view thereof; FIG. 6 is a left side view thereof; FIG. 7 is a right side view thereof; FIG. 8 is a top view thereof; and, FIG. 9 is a bottom view thereof. The broken lines in the Figures show portions of the electronic device which form no part of the claimed design. The dot-dash lines in the Figures show a boundary and form no part of the claimed design.

1 Claim, 7 Drawing Sheets

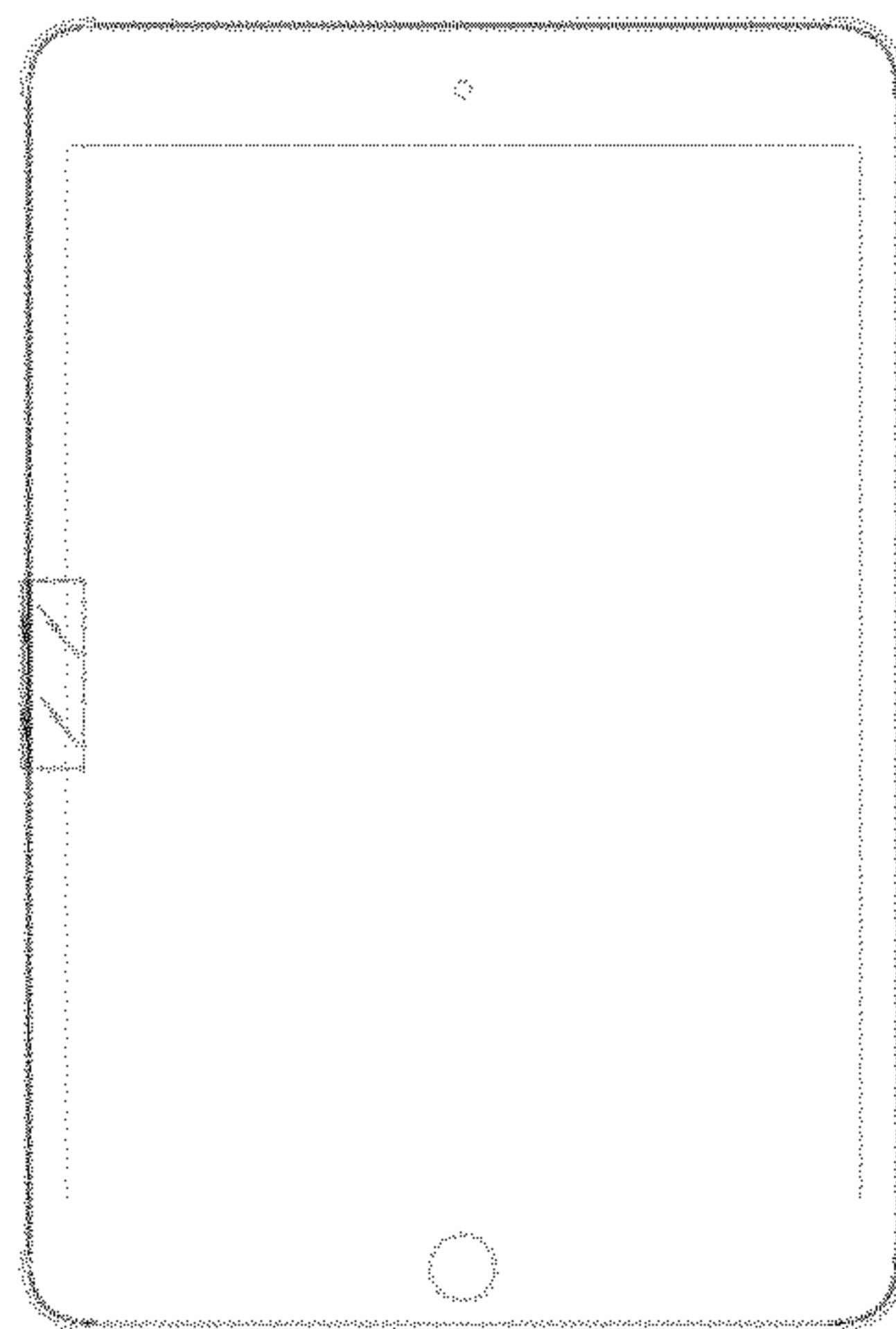




FIG. 6



FIG. 7