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**Manabe et al.**

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(54) **ELECTRONIC DEVICE STAND**

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(30) **Foreign Application Priority Data**

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(52) **U.S. Cl.**  
USPC ..... **D14/447**

(58) **Field of Classification Search**  
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D14/457, 250, 251, 253; D3/218; D6/406.3,  
D6/406.4, 406.5, 406.6  
CPC ..... A45C 2011/003; A45C 2011/002;  
A45C 13/002; A47B 2023/049; A47B  
2097/005; A47B 2097/006; A47B 21/04;  
A47B 23/00; A47B 23/043; A47B 2021/0307;  
A47B 2023/005; F16M 2200/00; H04B  
1/3888; H04M 1/04; H04M 1/185

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for an electronic device stand, as shown and described.

**DESCRIPTION**

FIG. 1 is a front perspective view of an electronic device stand;  
FIG. 2 is a front elevational view thereof;  
FIG. 3 is a rear elevational view thereof;  
FIG. 4 is a left side view thereof;  
FIG. 5 is a right side view thereof;  
FIG. 6 is a top plan view thereof; and,  
FIG. 7 is a bottom plan view thereof.

**1 Claim, 7 Drawing Sheets**

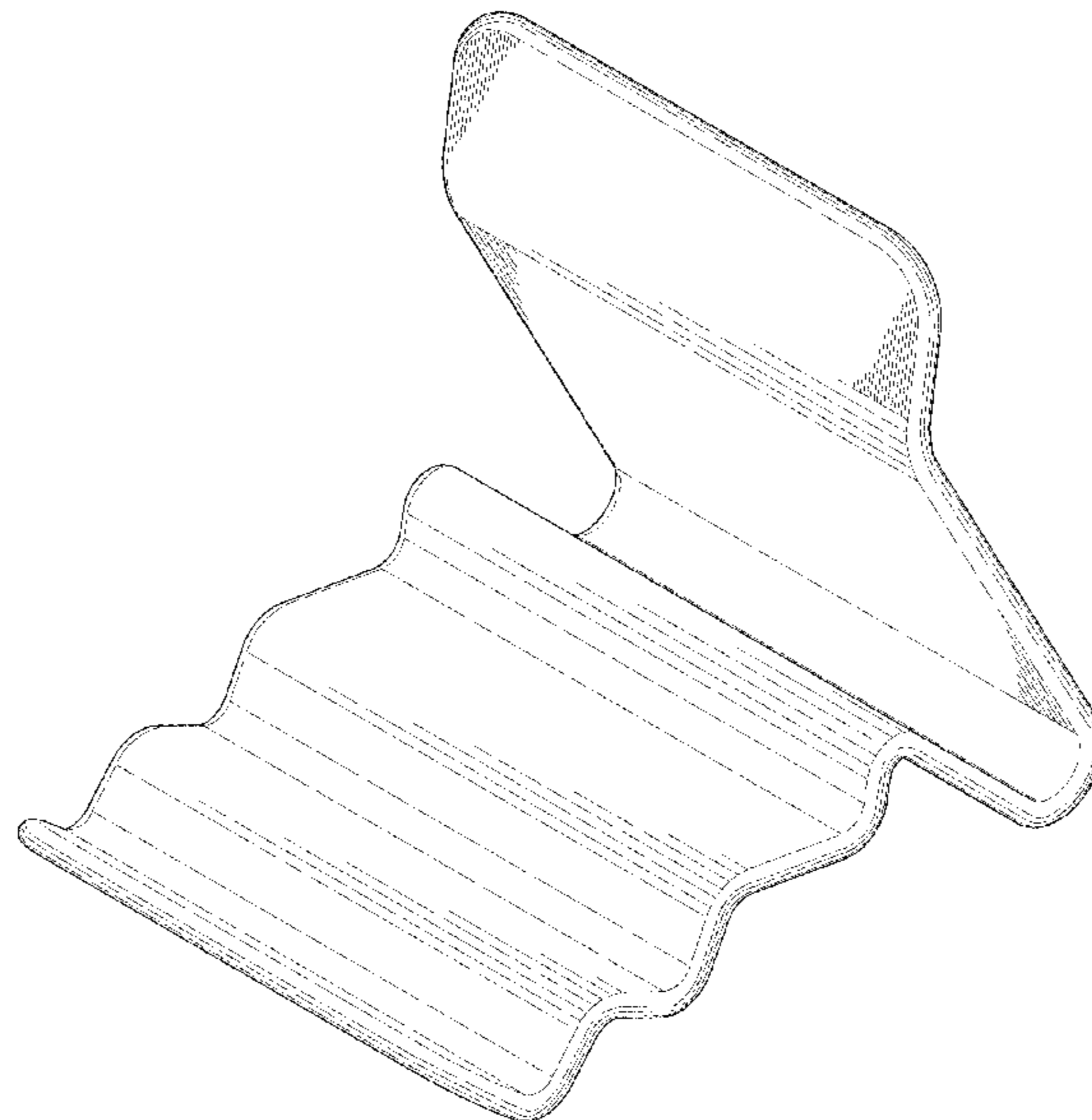


Fig. 1

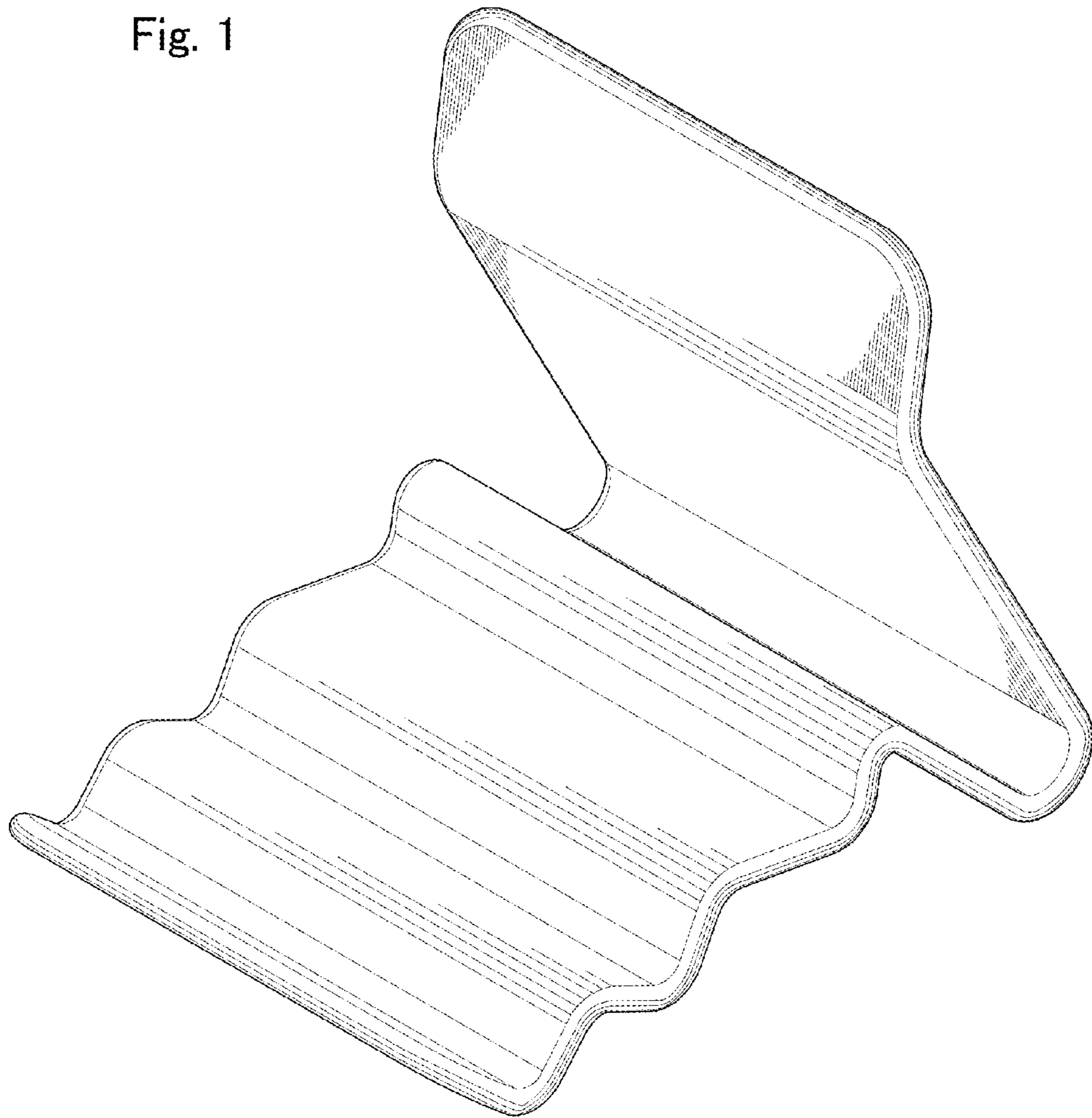


Fig. 2

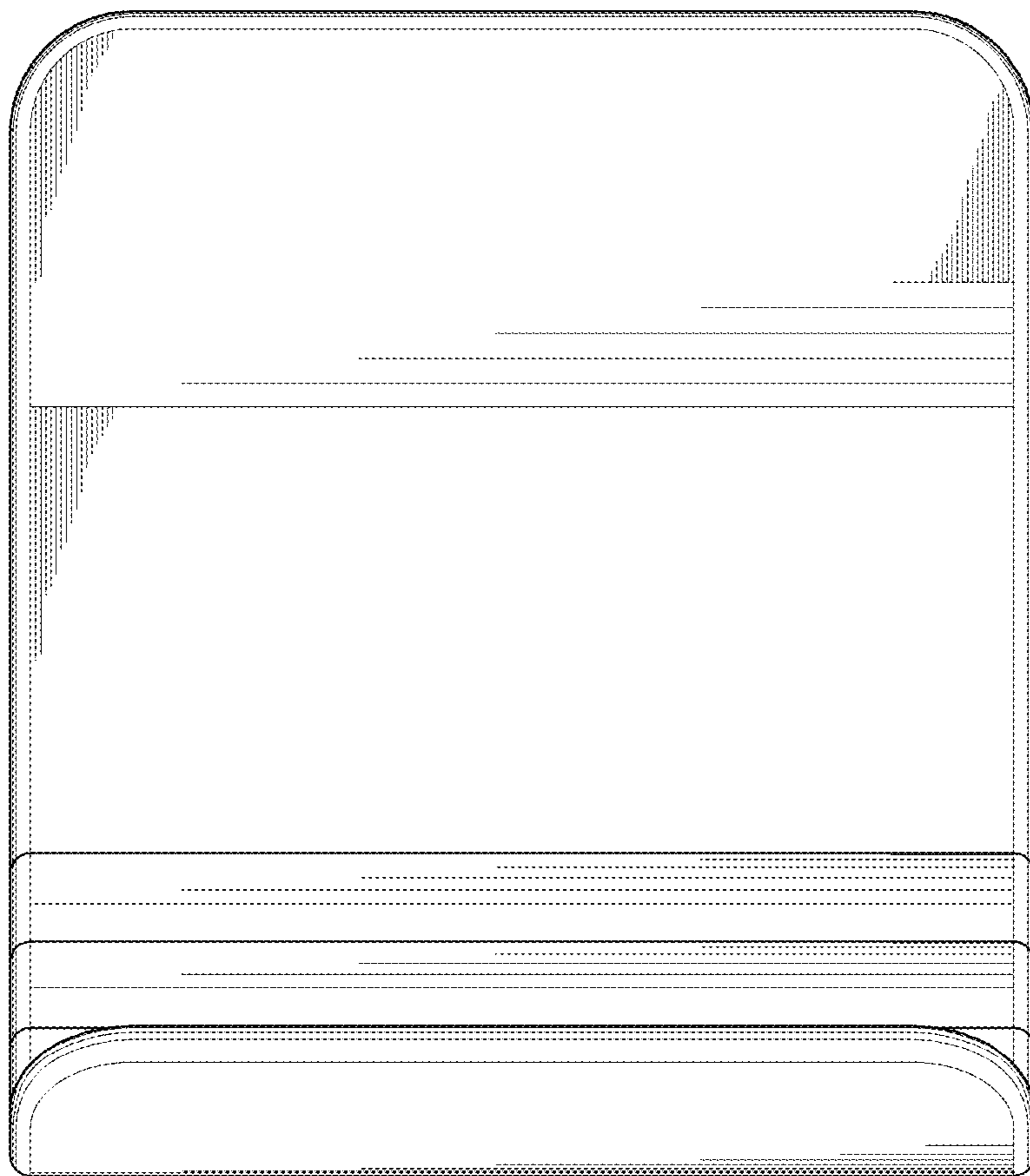


Fig. 3

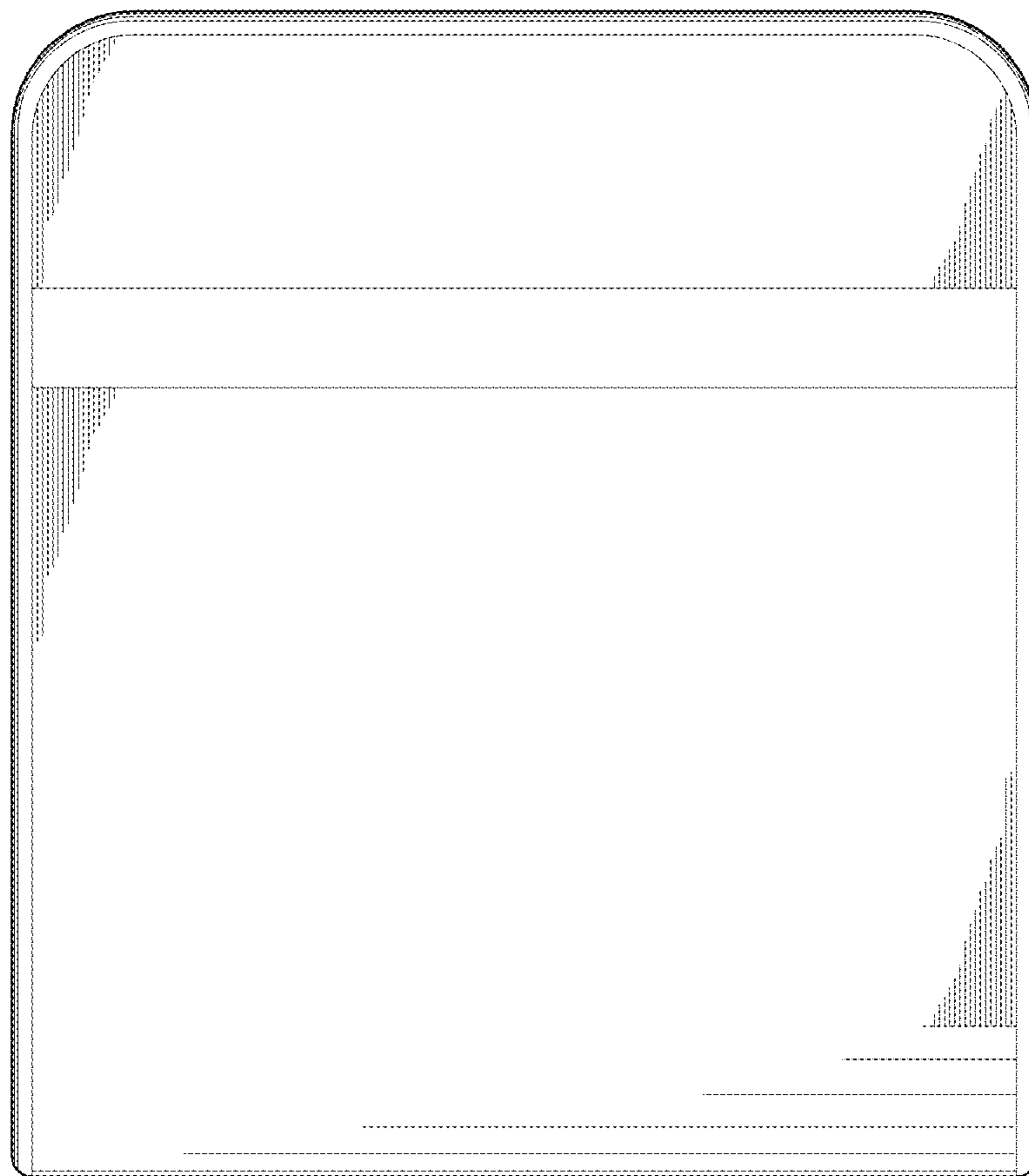


Fig. 4

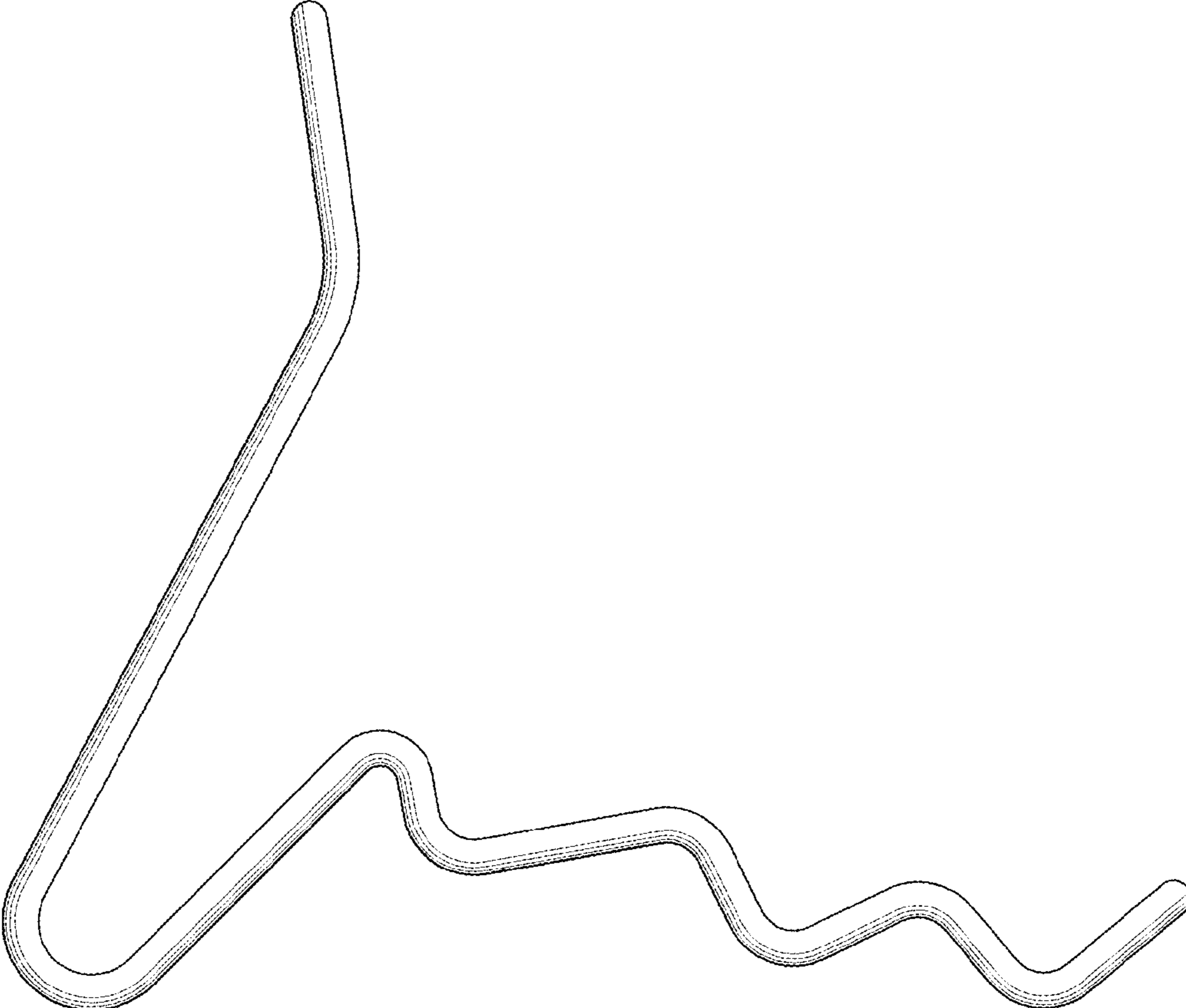


Fig. 5

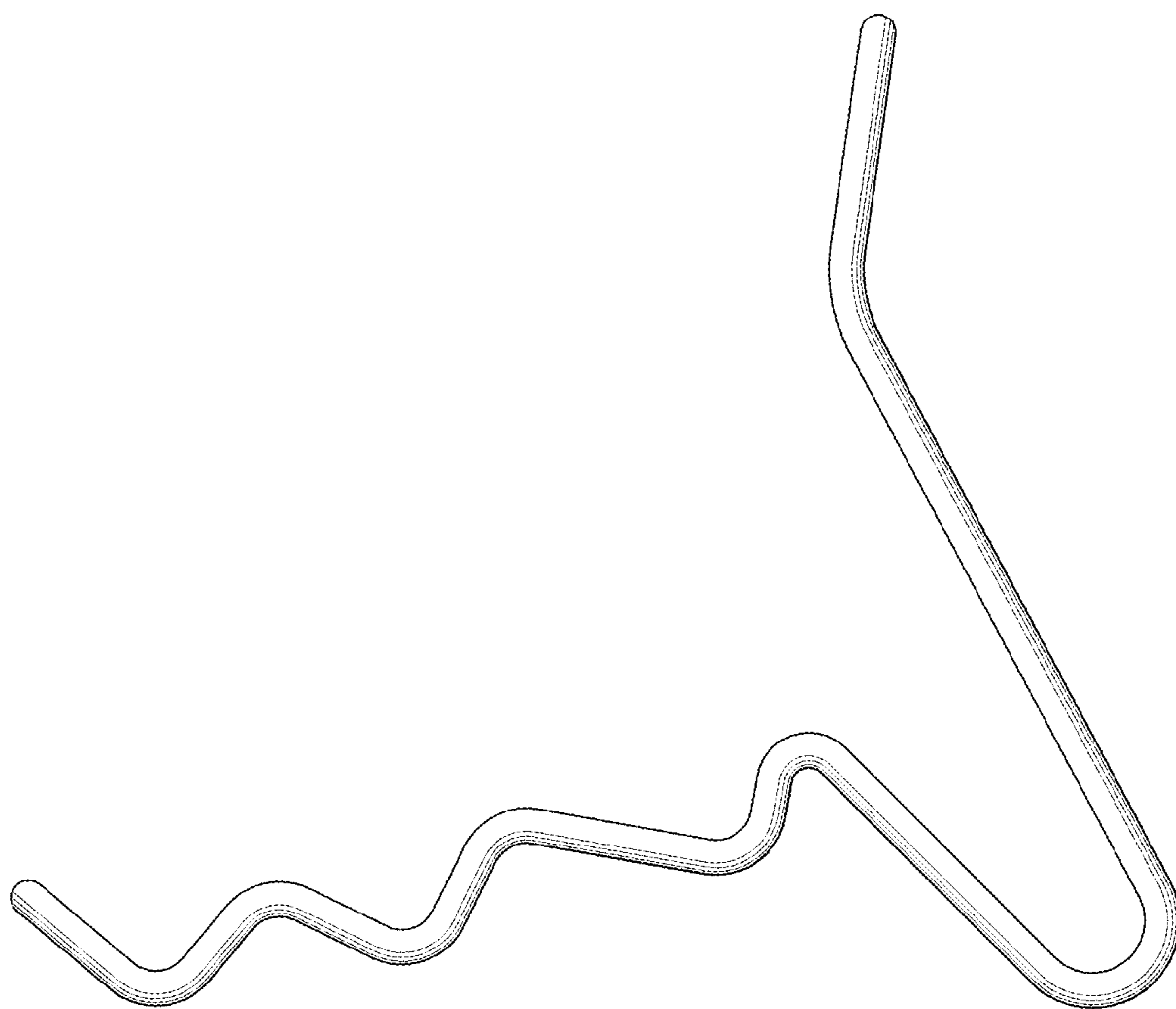




Fig. 6

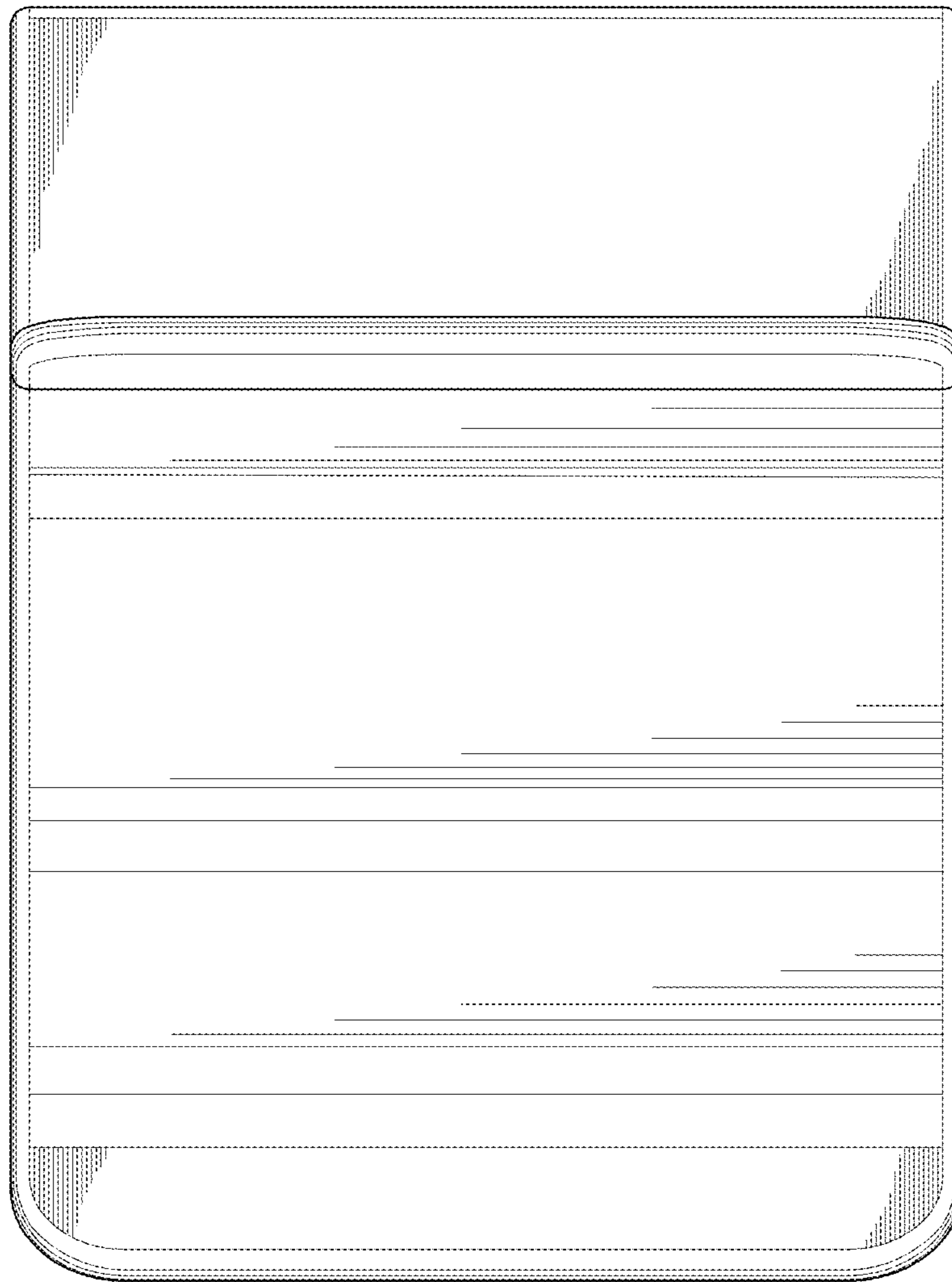


Fig. 7

