

US00D754253S

(12) **United States Design Patent**
Dashe

(10) **Patent No.:** **US D754,253 S**
(45) **Date of Patent:** **** Apr. 19, 2016**

- (54) **MULTI-PANEL DISPLAY BLANK**
- (71) Applicant: **Artskills**, Easton, PA (US)
- (72) Inventor: **Stephen P. Dashe**, Bethlehem, PA (US)
- (73) Assignee: **ArtSkills**
- (**) Term: **14 Years**
- (21) Appl. No.: **29/493,393**
- (22) Filed: **Jun. 9, 2014**

4,711,046 A * 12/1987 Herrgard G09F 15/0068
160/135
5,960,848 A 10/1999 Schirer
6,382,433 B1 5/2002 Podergois
6,945,398 B2 * 9/2005 Shumake B65D 75/20
206/224
7,434,340 B2 * 10/2008 Virvo G09F 1/06
40/124.09

(Continued)

Primary Examiner — Mary Ann Calabrese
(74) *Attorney, Agent, or Firm* — Belles Katz LLC

(57) **CLAIM**
I claim the ornamental design for a multi-panel display blank,
as shown and described.

Related U.S. Application Data

- (63) Continuation of application No. 29/451,100, filed on Mar. 27, 2013, now Pat. No. Des. 739,467, which is a continuation-in-part of application No. 13/769,706, filed on Feb. 18, 2013, now Pat. No. 8,955,243.
- (51) **LOC (10) Cl.** **20-02**
- (52) **U.S. Cl.**
USPC **D20/40**
- (58) **Field of Classification Search**
USPC D20/10, 19, 22–28, 40–42, 99;
D10/109.1, 109.2, 113.4; D9/432, 434;
D19/1
CPC G09F 1/00; G09F 1/02; G09F 1/04;
G09F 1/08; G09F 1/10; G09F 1/14; G09F
7/00; G09F 7/02; G09F 7/06; G09F 7/08;
G09F 7/18; G09F 9/00; G09F 13/00; G09F
13/02; G09F 13/04; G09F 13/06; G09F 13/16;
G09F 15/00; G09F 15/02; G09F 15/0062;
G09F 15/0068; G09F 15/0006
See application file for complete search history.

DESCRIPTION

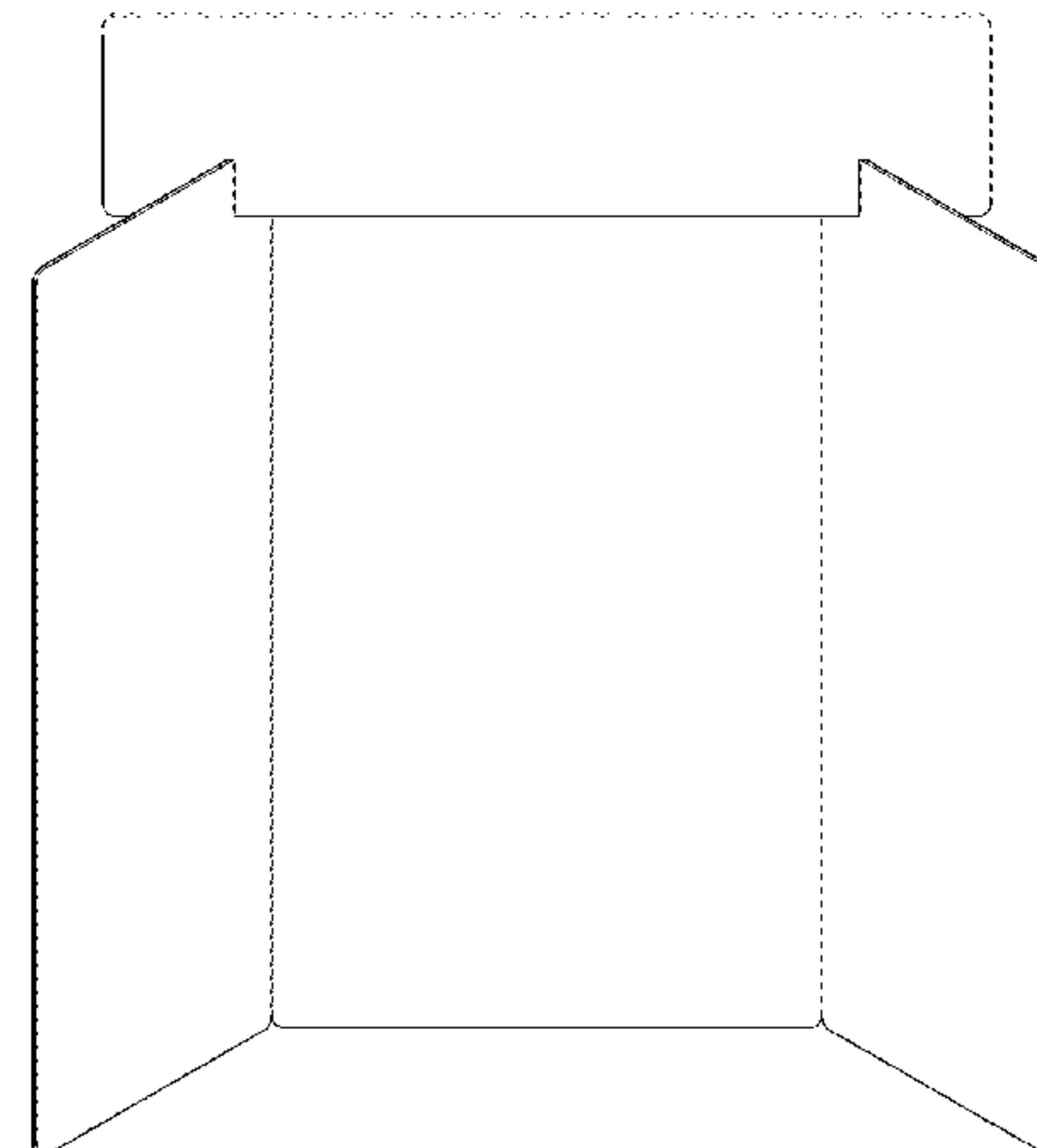
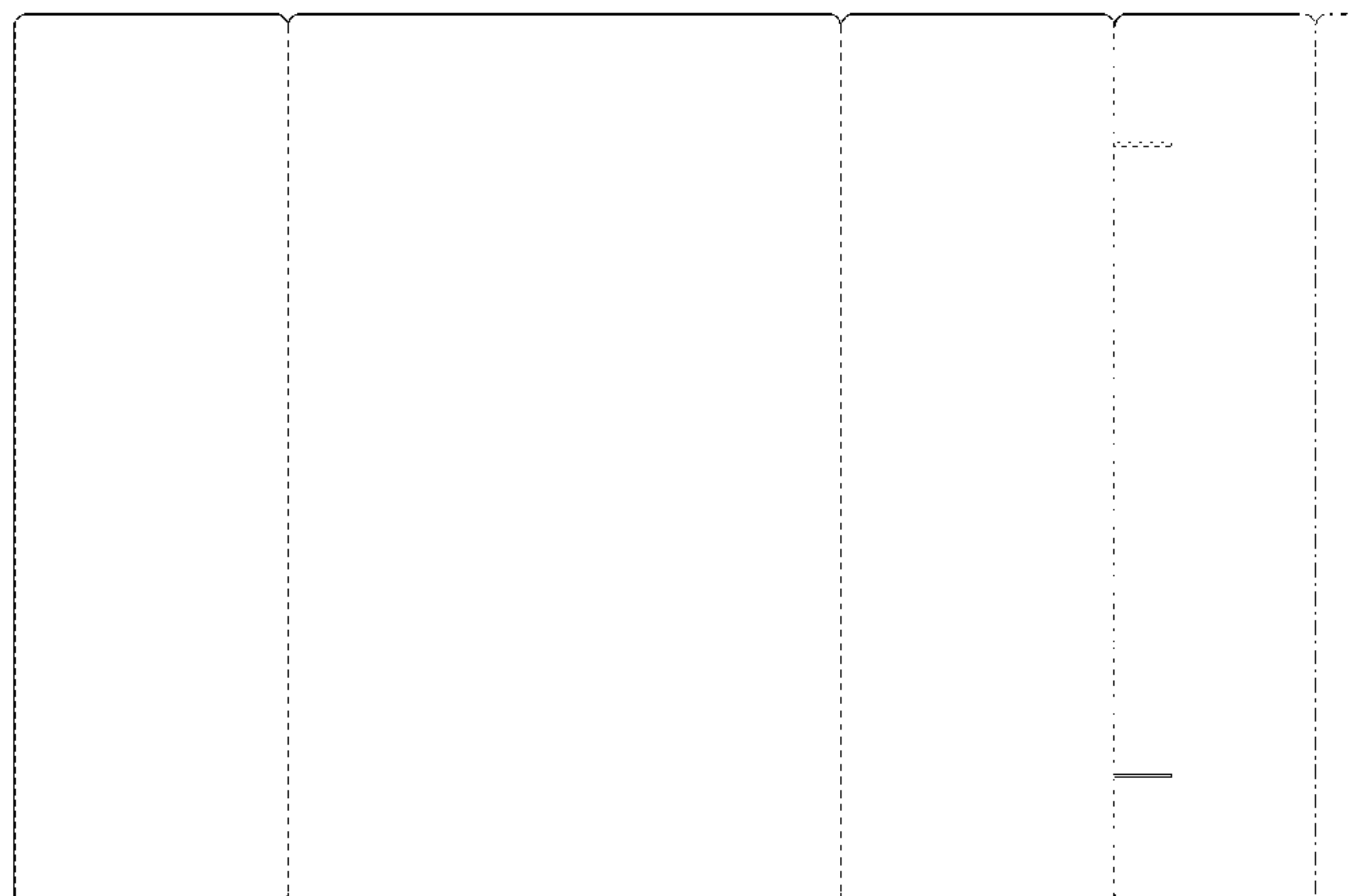
FIG. 1 is a front view of a multi-panel display blank in accordance with the present invention.
FIG. 2 is a rear view of the multi-panel display blank of FIG. 1.
FIG. 3 is a front perspective view of a multi-panel display blank of FIG. 1 in a partially assembled state.
FIG. 4 is a front perspective view of the multi-panel display blank of FIG. 3 in a fully assembled state.
FIG. 5 is a front view of the multi-panel display blank of FIG. 4.
FIG. 6 is a rear view of the multi-panel display blank of FIG. 4.
FIG. 7 is a top view of the multi-panel display blank of FIG. 4.
FIG. 8 is a bottom view of the multi-panel display blank of FIG. 4.
FIG. 9 is a right-side view of the multi-panel display blank of FIG. 4; and,
FIG. 10 is a left-side view of the multi-panel display blank of FIG. 4.
The broken lines formed by linear segments of equal length in FIGS. 1 and 2 represent pre-weakened areas in the blank. The broken lines formed by a dot-dash-dot sequence in FIGS. 1-10 represent structure that form no part of the claimed design.

(56) **References Cited**

U.S. PATENT DOCUMENTS

- 1,485,695 A 3/1924 Singer et al.
- 3,469,335 A * 9/1969 Leigh G09F 1/06
40/539
- D270,358 S * 8/1983 Errigo D20/10

1 Claim, 8 Drawing Sheets



(56)

References Cited

U.S. PATENT DOCUMENTS

7,451,800	B2	11/2008	Johnson et al.	2004/0148833	A1	8/2004	Virvo	
7,647,718	B2	1/2010	Burtch	2005/0086842	A1	4/2005	Ternovits et al.	
7,823,309	B2	11/2010	Albenda	2005/0155259	A1*	7/2005	Virvo	G09F 1/06 40/124.09
7,886,465	B2	2/2011	Virvo	2006/0048421	A1	3/2006	Oleksak	
D653,880	S *	2/2012	Polidoros	2006/0060643	A1	3/2006	Sheffer	
D706,872	S *	6/2014	Dashe	2007/0151132	A1*	7/2007	Repecki	G09F 1/14 40/124.12
D739,467	S *	9/2015	Dashe	2014/0061288	A1*	3/2014	Dashe	G09F 1/06 229/100
2003/0205489	A1*	11/2003	Shumake					
			B65D 75/20 206/224					

* cited by examiner



FIG. 1



FIG. 2

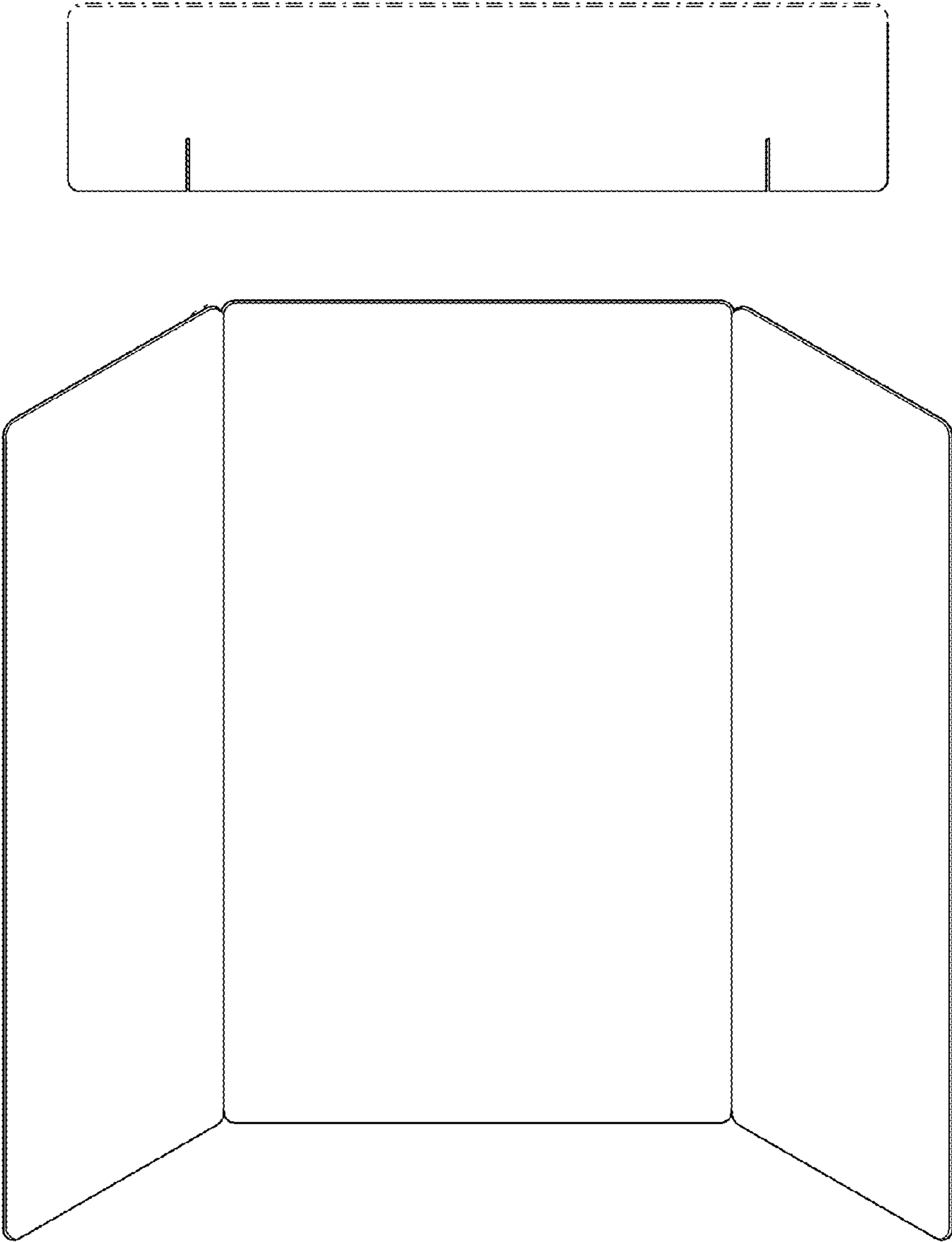


FIG. 3

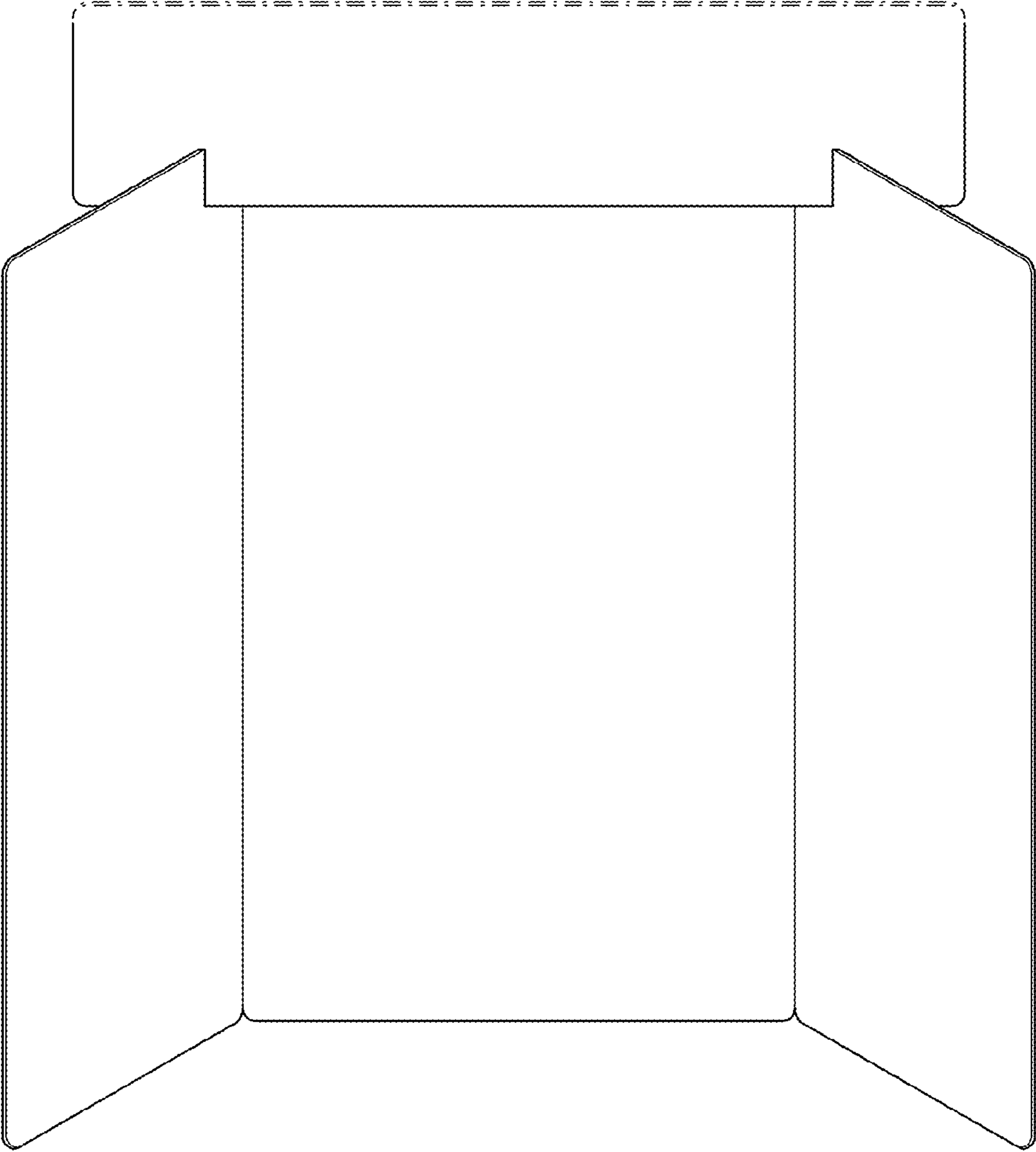


FIG. 4

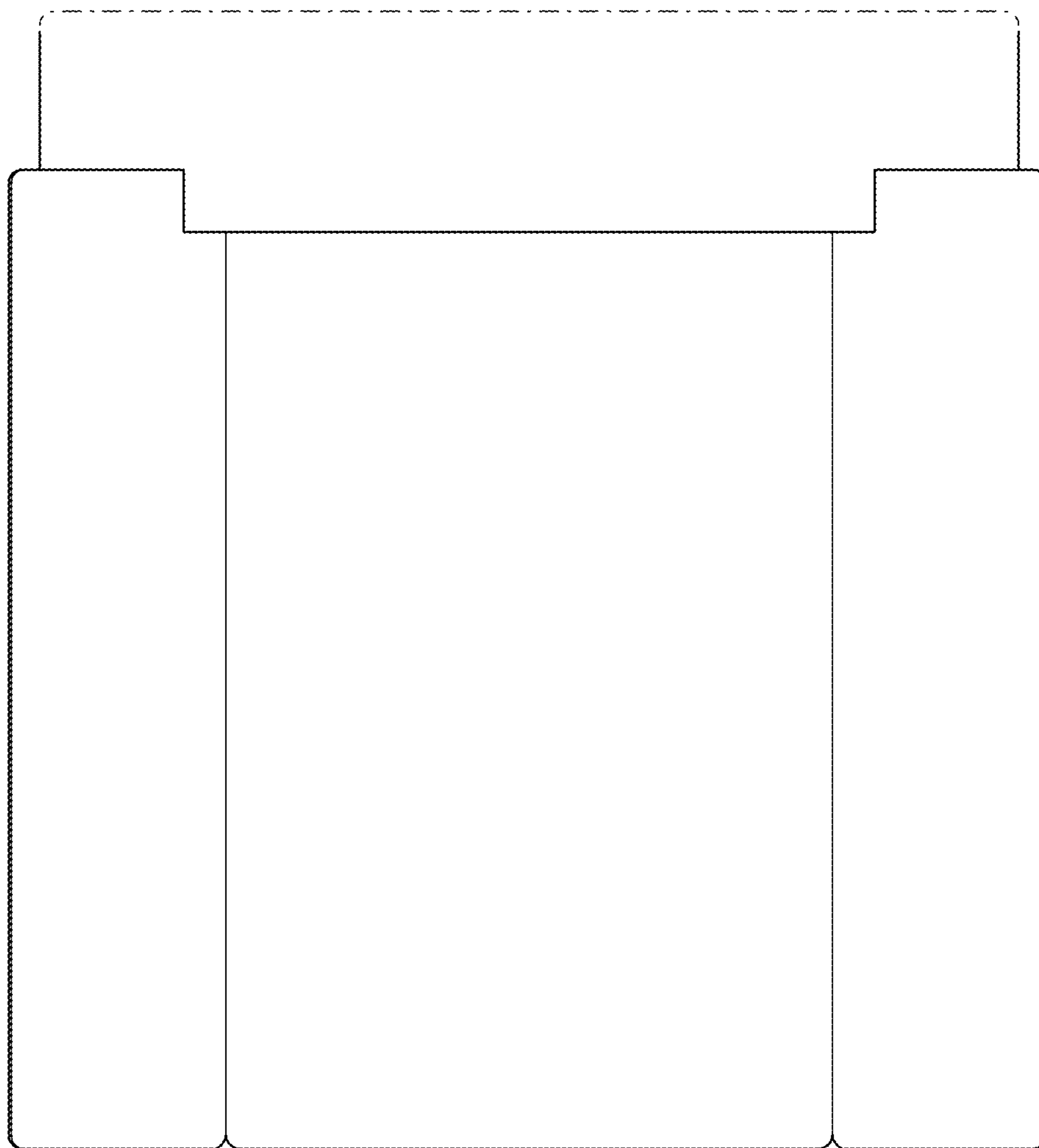


FIG. 5

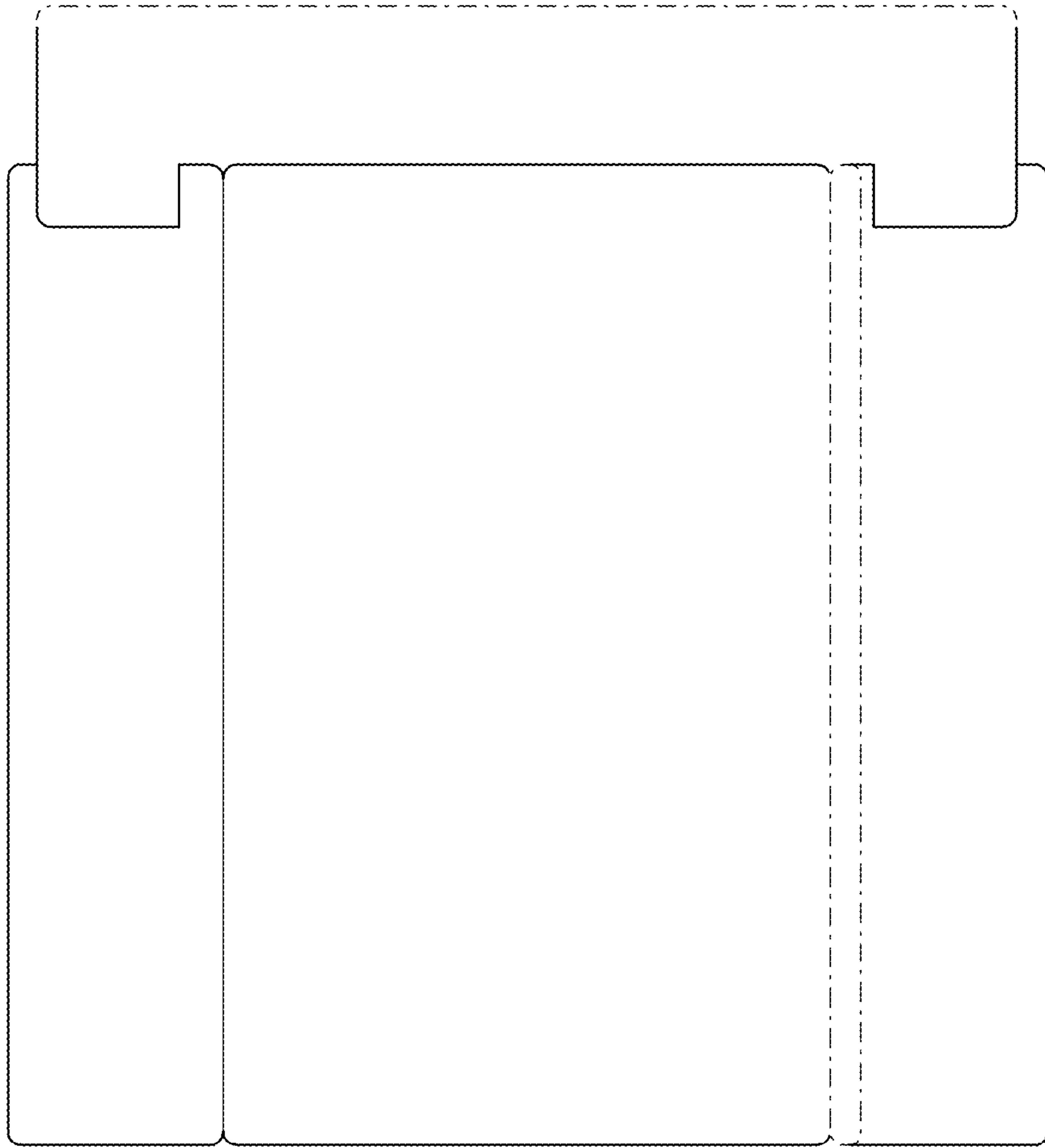


FIG. 6

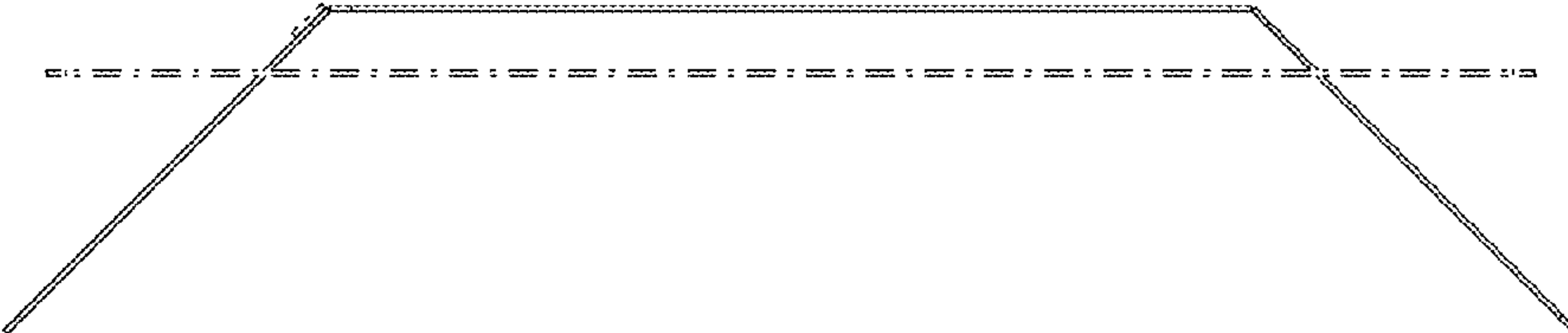


FIG. 7

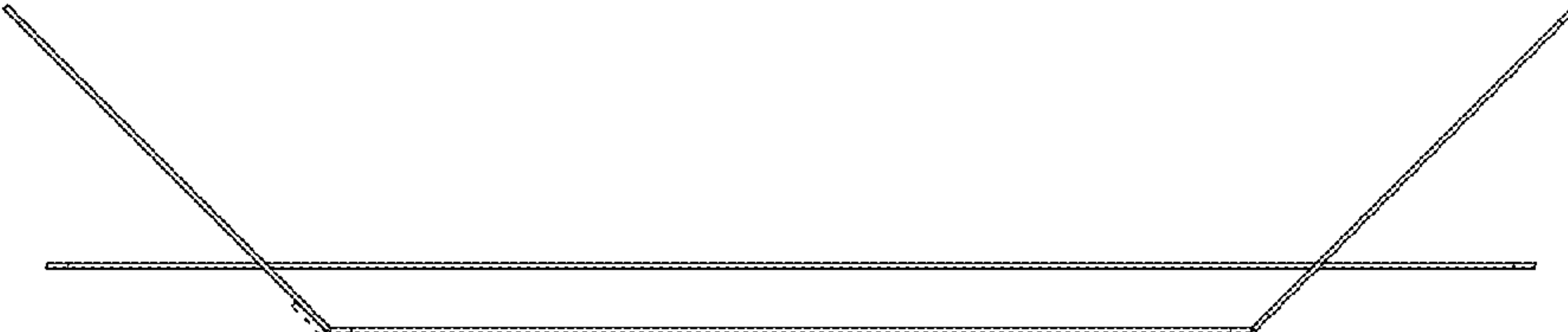


FIG. 8

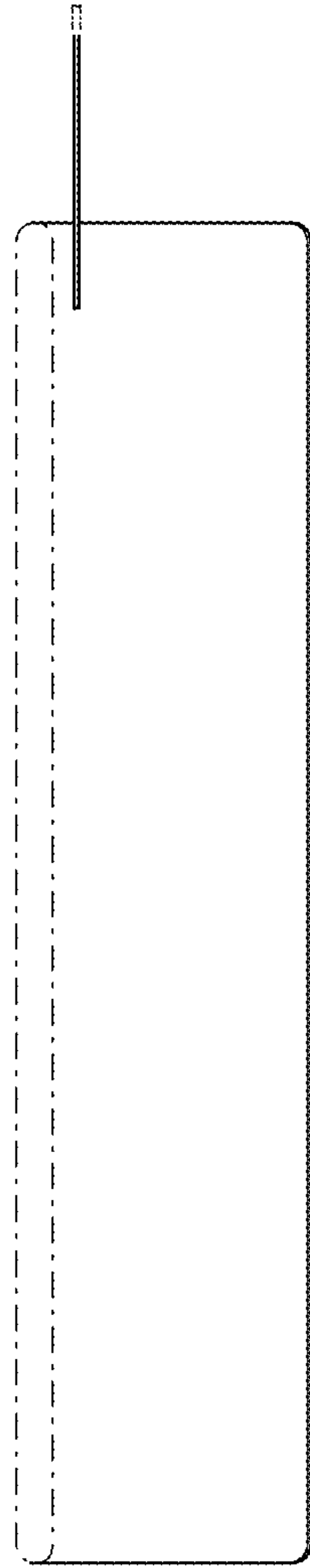


FIG. 9

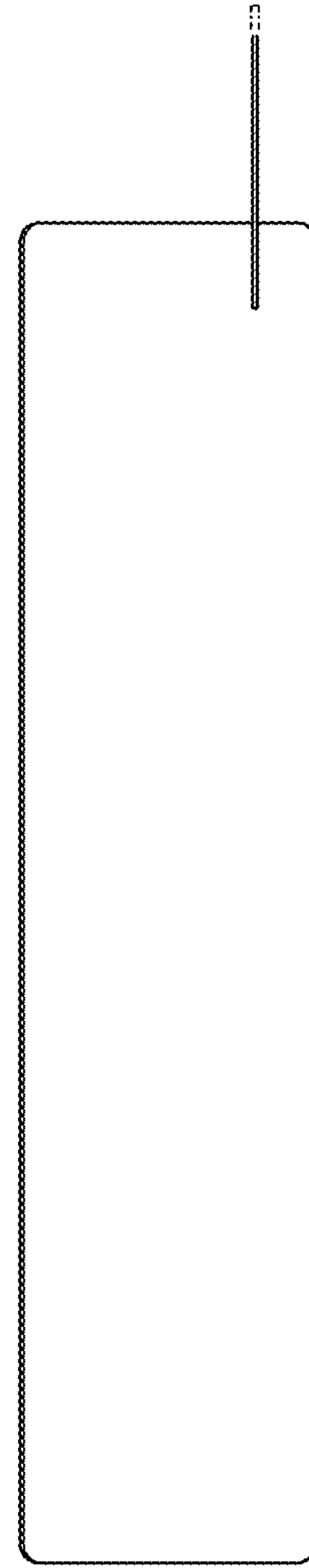


FIG. 10