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(12) **United States Design Patent**
Sponring

(10) **Patent No.:** **US D753,073 S**
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(54) **PRINTED CIRCUIT BOARD**

(71) Applicant: **Altia Systems, Inc.**, Cupertino, CA (US)

(72) Inventor: **Otto Sponring**, Los Altos, CA (US)

(73) Assignee: **Altia Systems, Inc.**, Cupertino, CA (US)

(**) Term: **14 Years**

(21) Appl. No.: **29/513,427**

(22) Filed: **Dec. 30, 2014**

(51) **LOC (10) Cl.** **13-03**

(52) **U.S. Cl.**

USPC **D13/182**

(58) **Field of Classification Search**

USPC D13/182; 174/68.1, 250, 256, 257;
318/567, 568.1; 700/22; 361/720, 722,
361/748, 749, 750, 751, 757, 752, 761, 762,
361/763, 764

CPC H05K 1/00; H05K 1/0286; H05K 1/029;
H05K 1/0206; H05K 1/0061; H05K 1/141;
H05K 1/142; H05K 3/34; H05K 3/341;
H05K 3/3415; H05K 3/3405; H05K 3/3447;
H05K 3/0058; H01L 25/00

See application file for complete search history.

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Primary Examiner — Elizabeth J Oswecki

(74) *Attorney, Agent, or Firm* — HM Law Group LLP; Vani
Moodley, Esq.

(57) **CLAIM**

The ornamental design for a printed circuit board, as shown
and described.

(56) **References Cited**

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DESCRIPTION

FIG. 1 is a perspective view of a printed circuit board showing
my new design;

FIG. 2 is another perspective view thereof;

FIG. 3 is a front view thereof;

FIG. 4 is a rear view thereof;

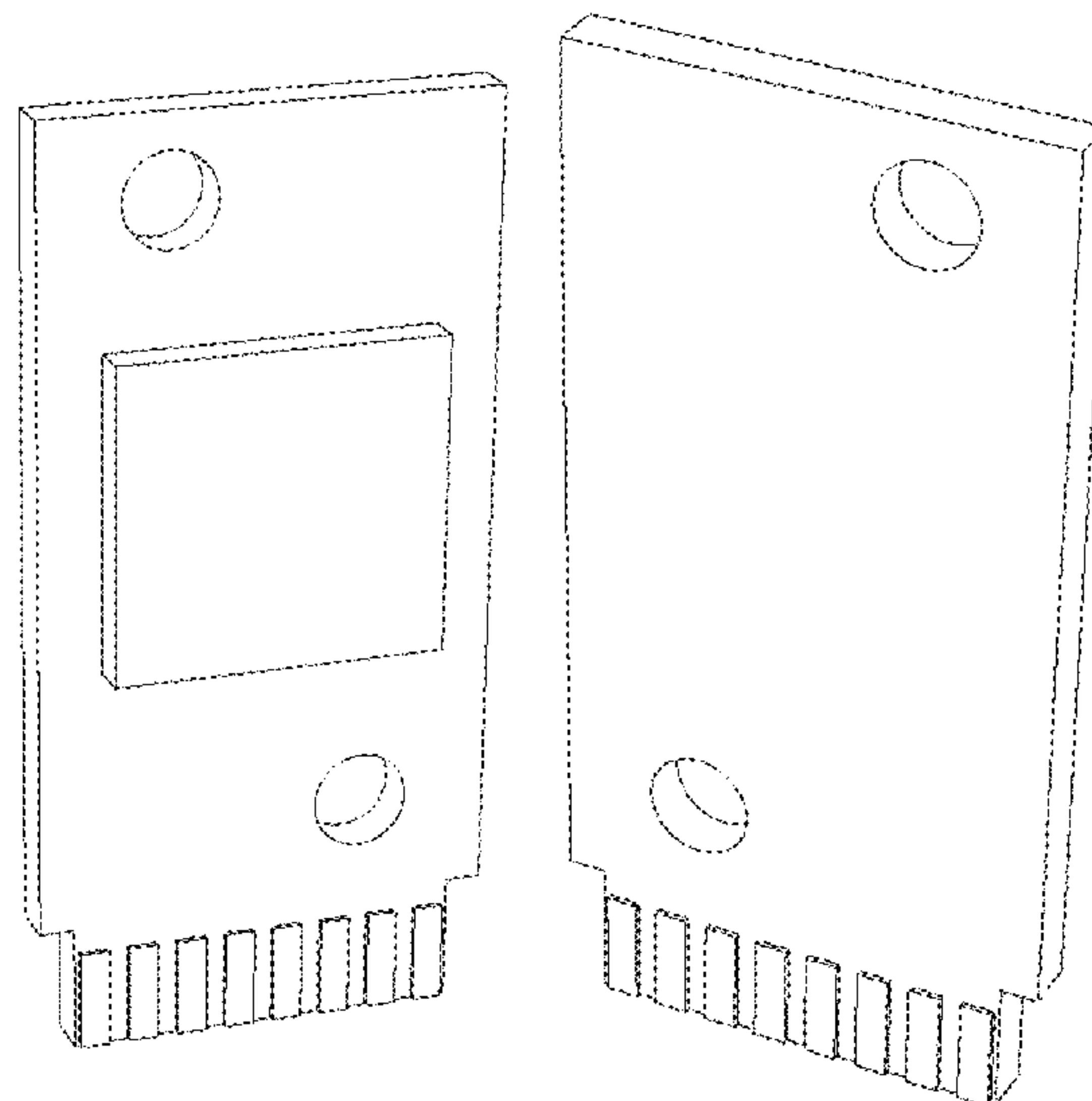
FIG. 5 is a top plan view thereof;

FIG. 6 is a bottom plan view thereof;

FIG. 7 is a side view thereof; and,

FIG. 8 is an opposite side view thereof.

1 Claim, 8 Drawing Sheets



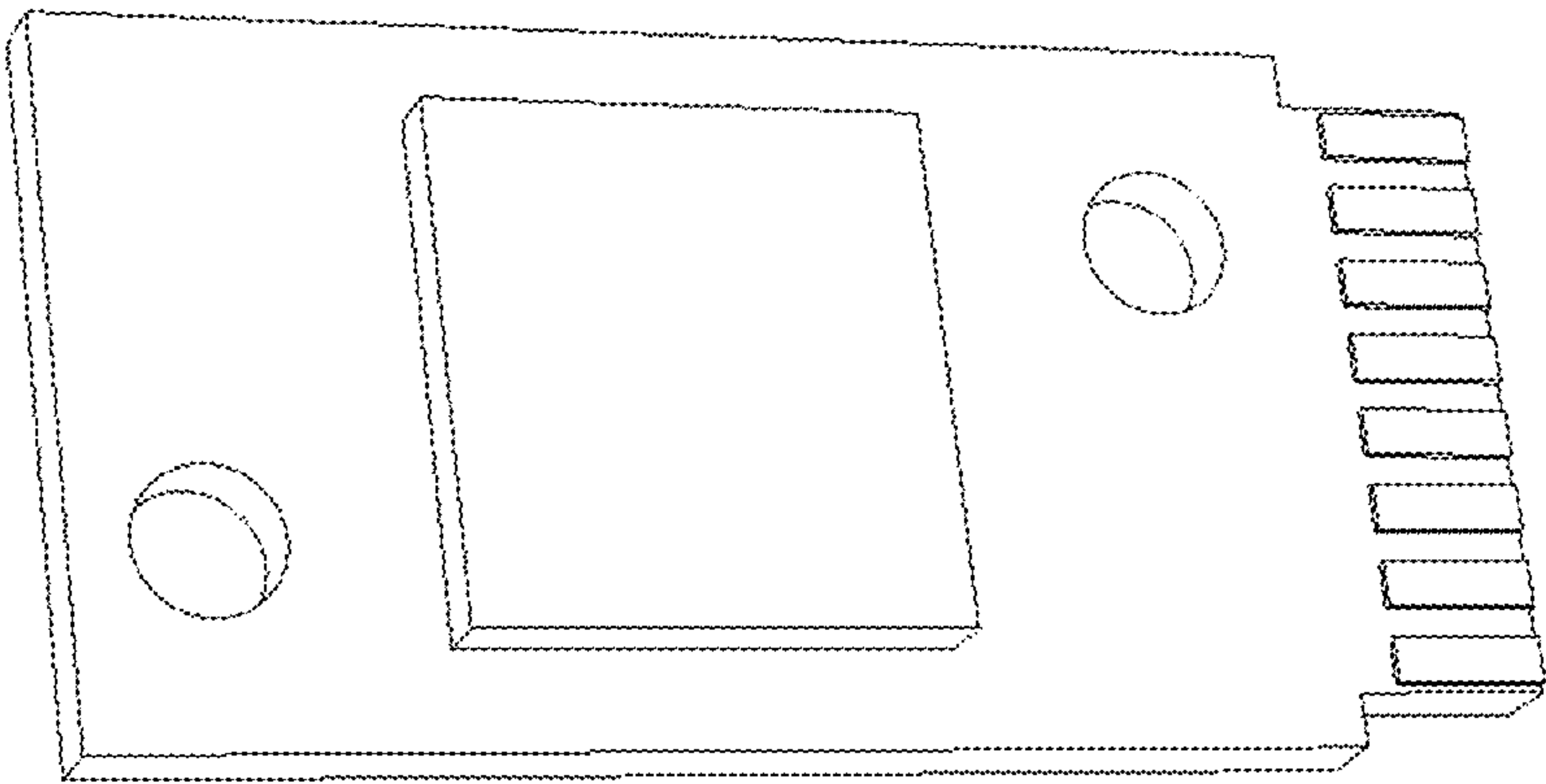


FIG. 1

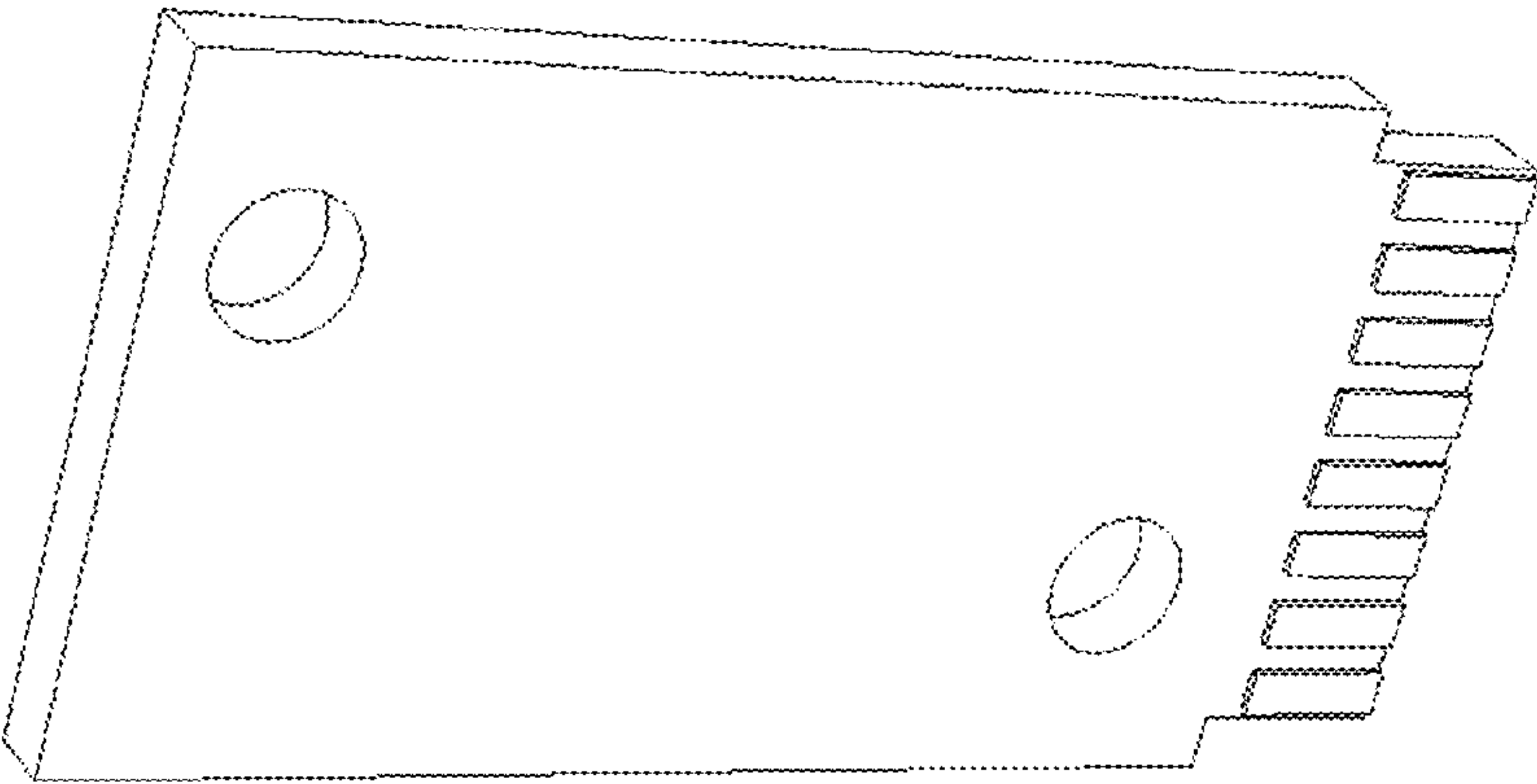


FIG. 2

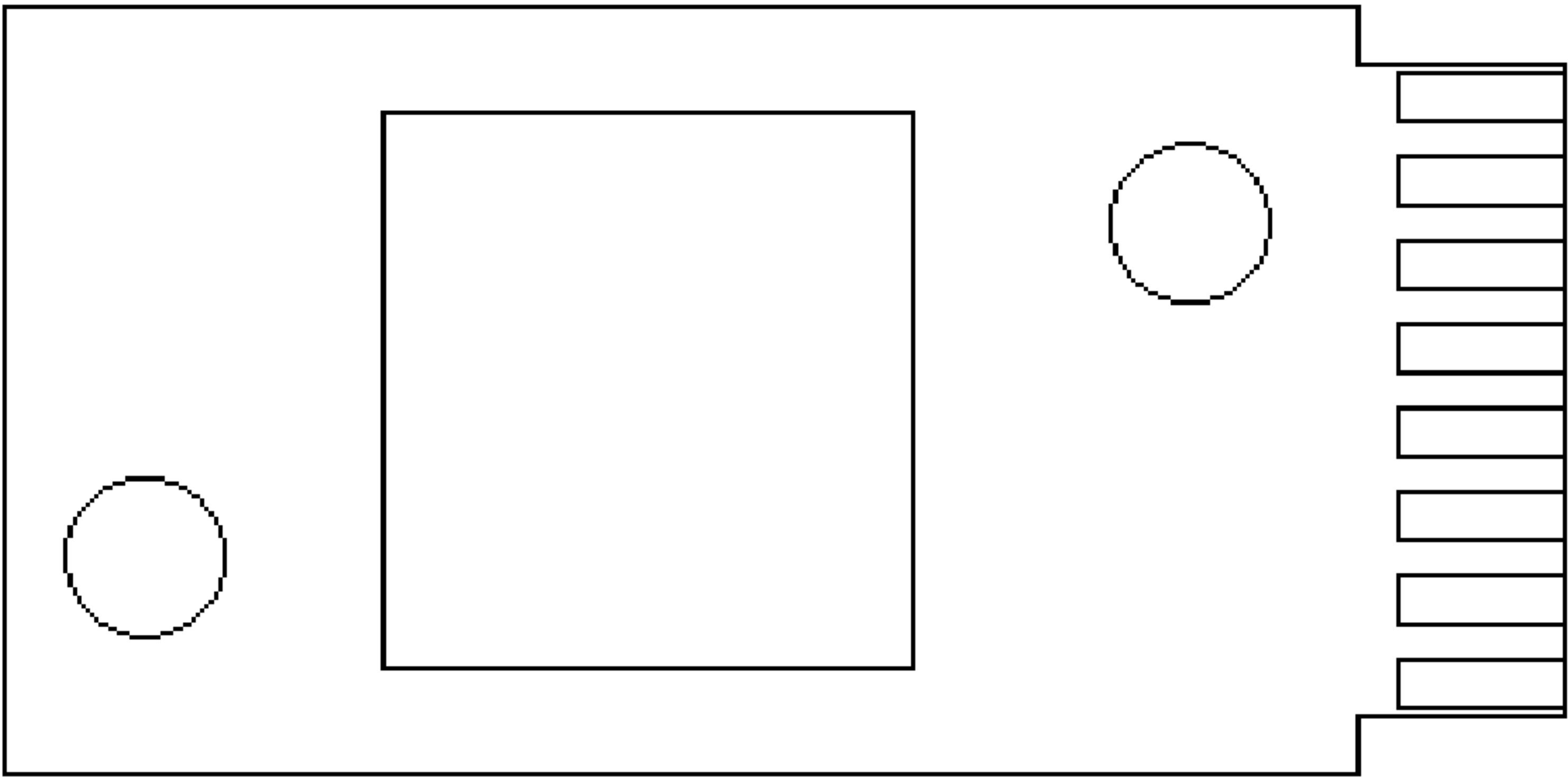


FIG. 3

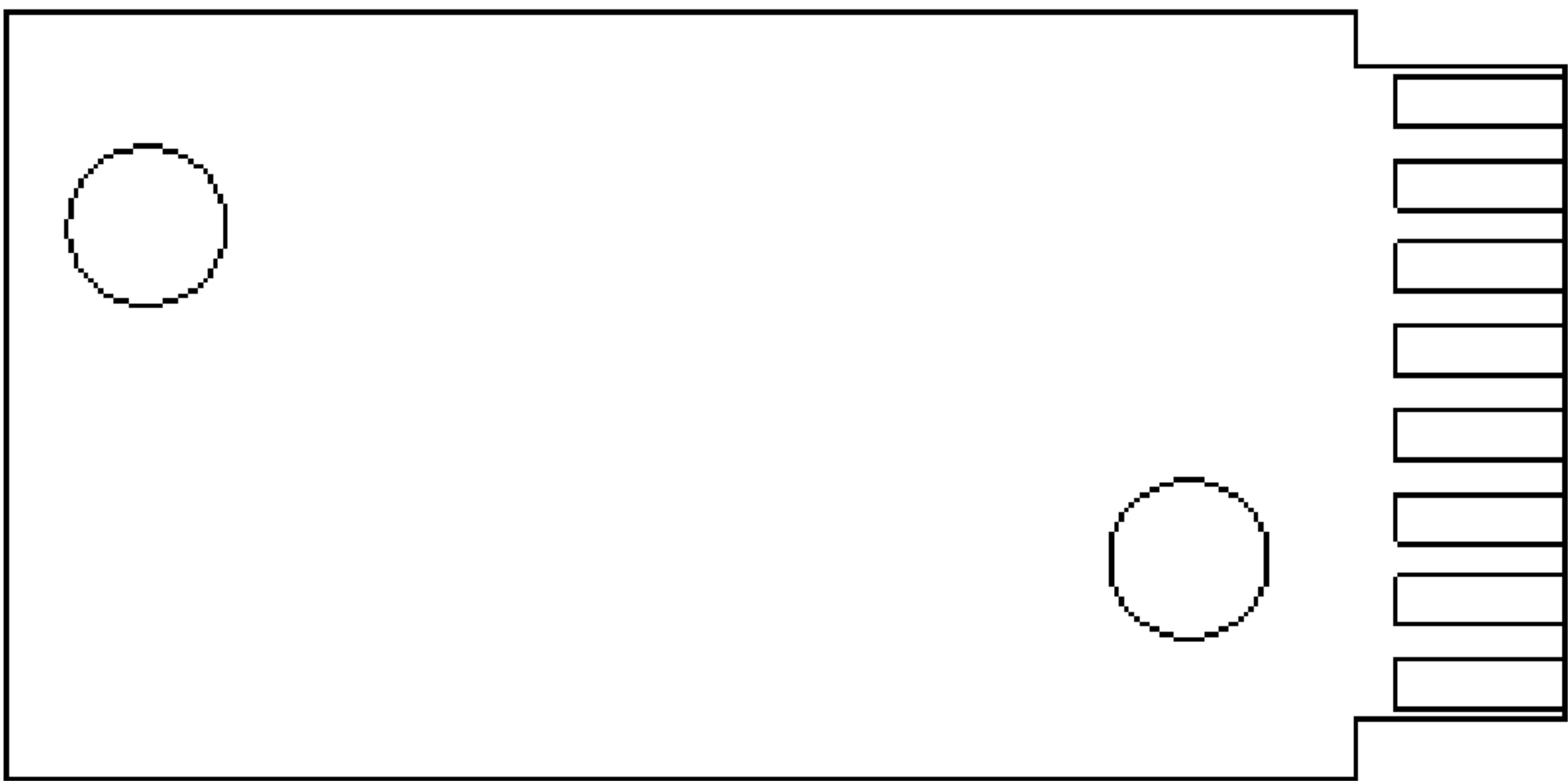


FIG. 4

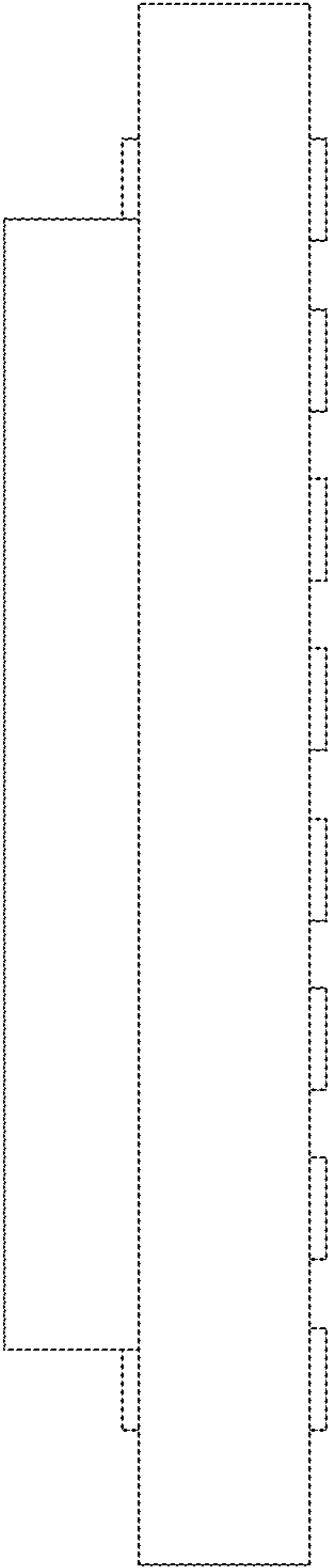


FIG. 5

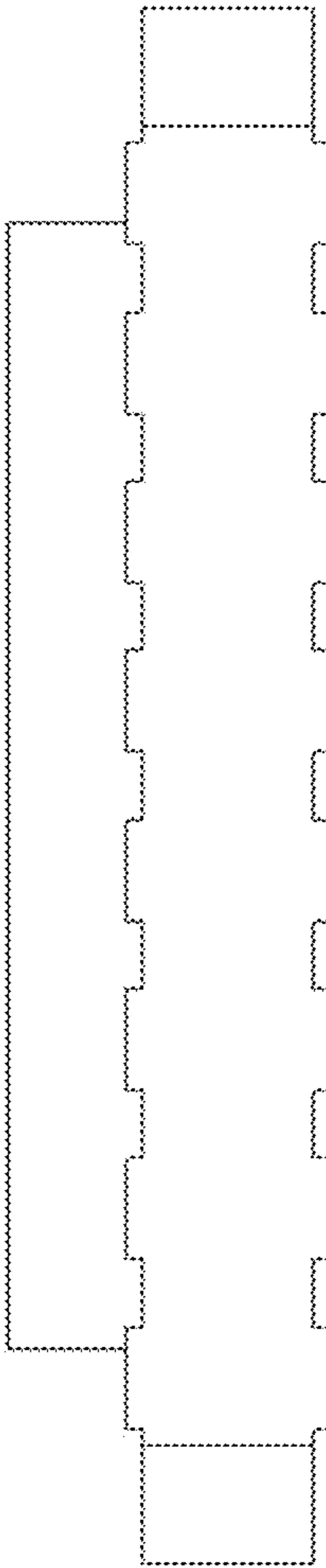


FIG. 6

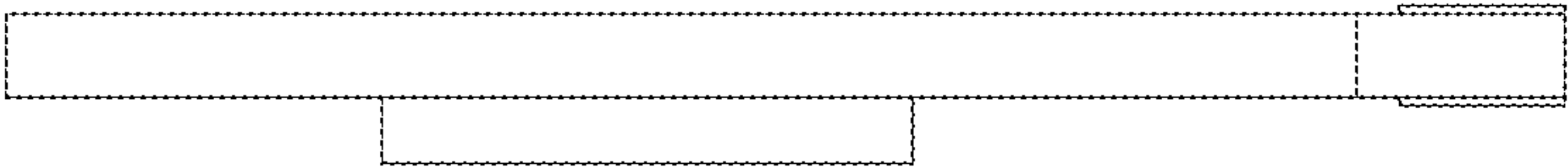


FIG. 7

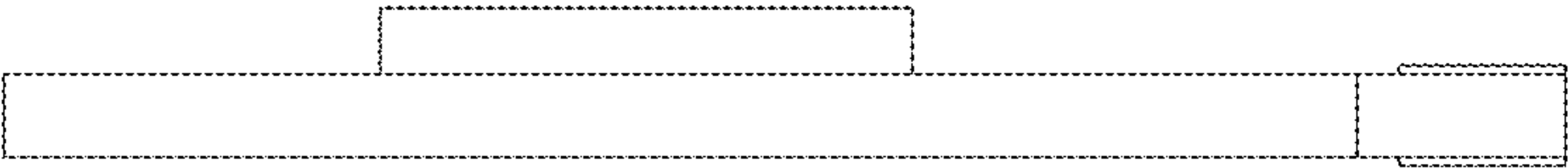


FIG. 8