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(12) **United States Design Patent**
Oka et al.

(10) **Patent No.:** **US D747,817 S**
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(54) **LIGHT EMITTING DIODE**

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(73) Assignee: **NICHIA CORPORATION**, Tokushima (JP)

(**) Term: **14 Years**

(21) Appl. No.: **29/502,605**

(22) Filed: **Sep. 17, 2014**

(30) **Foreign Application Priority Data**

Mar. 27, 2014 (JP) 2014-006606
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(51) **LOC (10) Cl.** **26-04**

(52) **U.S. Cl.**
USPC **D26/1**

(58) **Field of Classification Search**
USPC D26/1-4; 313/313, 315, 316, 317, 318,
313/493; 315/52, 53, 56, 57, 58
CPC H01J 5/48; H01J 5/50; H01J 19/54;
F21V 5/00
See application file for complete search history.

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Primary Examiner — Marcus Jackson

(74) *Attorney, Agent, or Firm* — Foley & Lardner LLP

(57) **CLAIM**

The ornamental design for a light emitting diode, as shown and described.

DESCRIPTION

The patent or application file contains at least one drawing executed in color. Copies of this patent or patent application publication with color drawing(s) will be provided by the Office upon request and payment of the necessary fee.

FIG. 1 is a front, top, right side perspective view of the first embodiment of a light emitting diode showing our new design;

FIG. 2 is a rear, bottom, left side perspective view thereof;

FIG. 3 is a front elevational view thereof;

FIG. 4 is a rear elevational view thereof;

FIG. 5 is a top plan view thereof;

FIG. 6 is a bottom plan view thereof;

FIG. 7 is a right side elevational view thereof;

FIG. 8 is a left side elevational view thereof;

FIG. 9 is a front, top, right side perspective view of the second embodiment of a light emitting diode showing our new design;

FIG. 10 is a rear, bottom, left side perspective view thereof;

FIG. 11 is a front elevational view thereof;

FIG. 12 is a rear elevational view thereof;

FIG. 13 is a top plan view thereof;

FIG. 14 is a bottom plan view thereof;

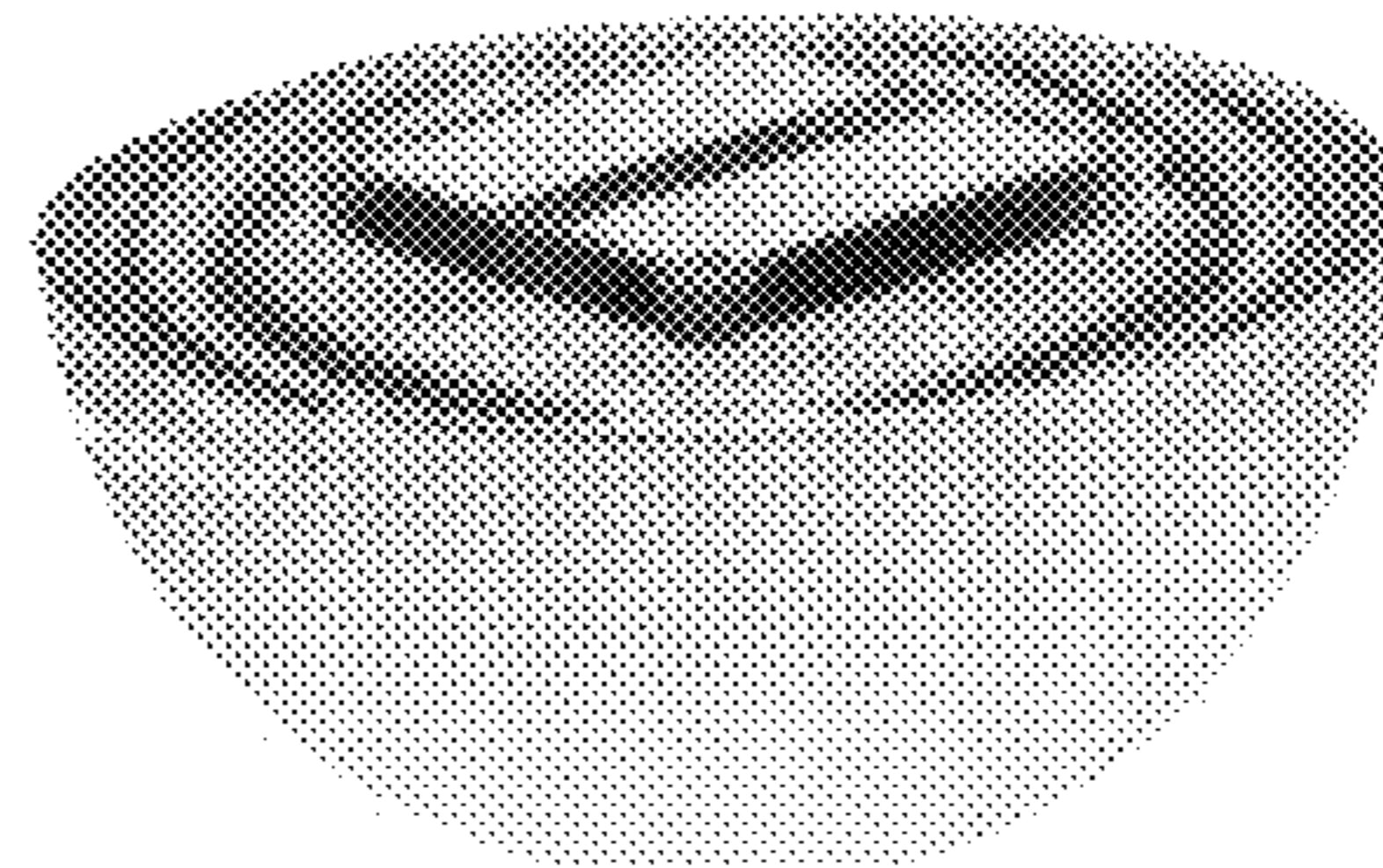
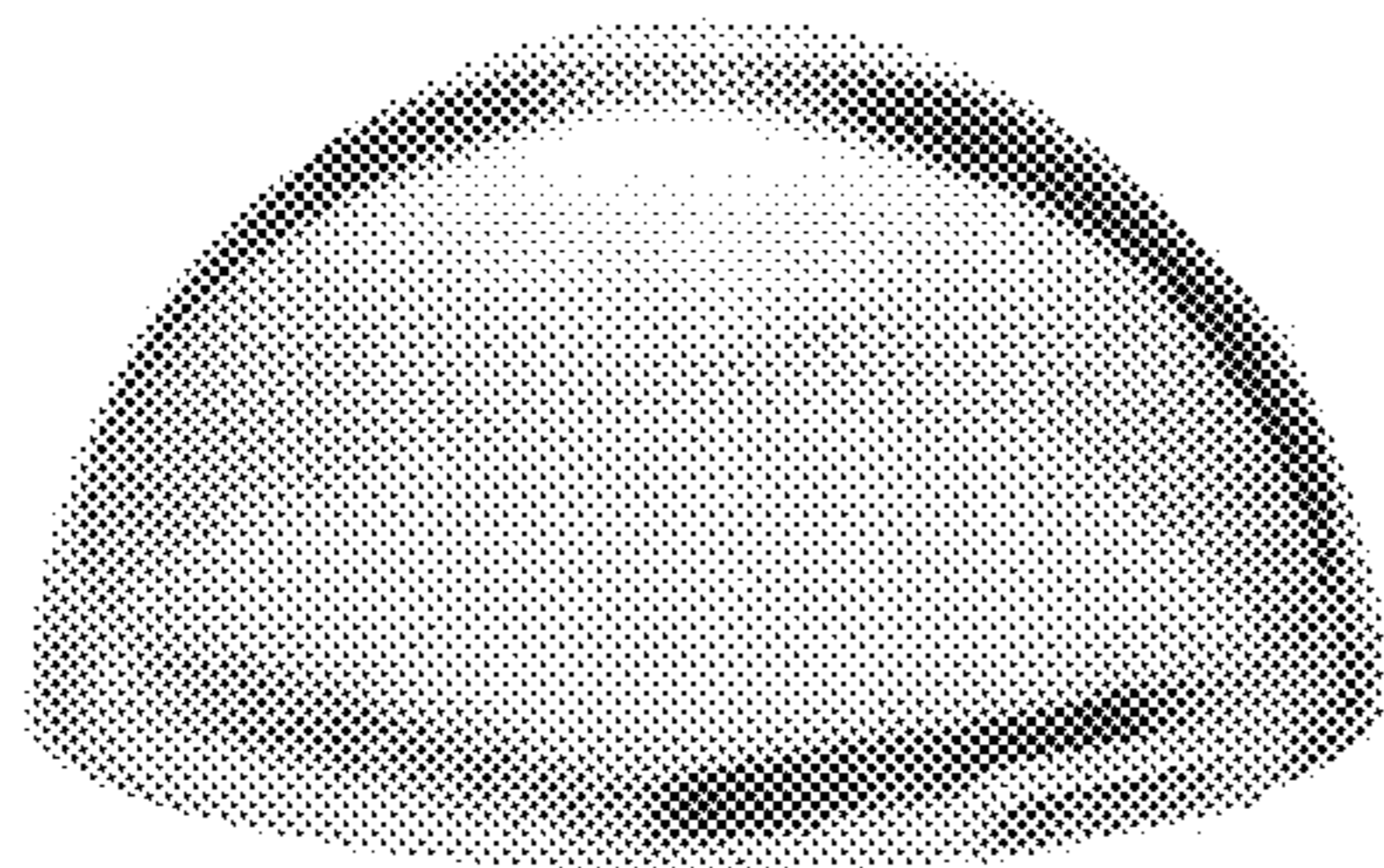
FIG. 15 is a right side elevational view thereof; and,

FIG. 16 is a left side elevational view thereof.

The light emitting diode has a substrate with electrodes. The substrate is located on the bottom of a lens, which is made from transparent resin. A frame part, which covers sides of the substrate, is also made from transparent resin.

This article is transparent, except for a bottom portion of the substrate which is located on the bottom of the lens. The roughly square shaped light emitting diode chip, which is located on a top portion of the substrate, is optically distorted in FIGS. 1, 3-5, 7-9, 11-13, 15, and 16 due to the effect of the lens.

1 Claim, 8 Drawing Sheets
(8 of 8 Drawing Sheet(s) Filed in Color)



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Fig. 1

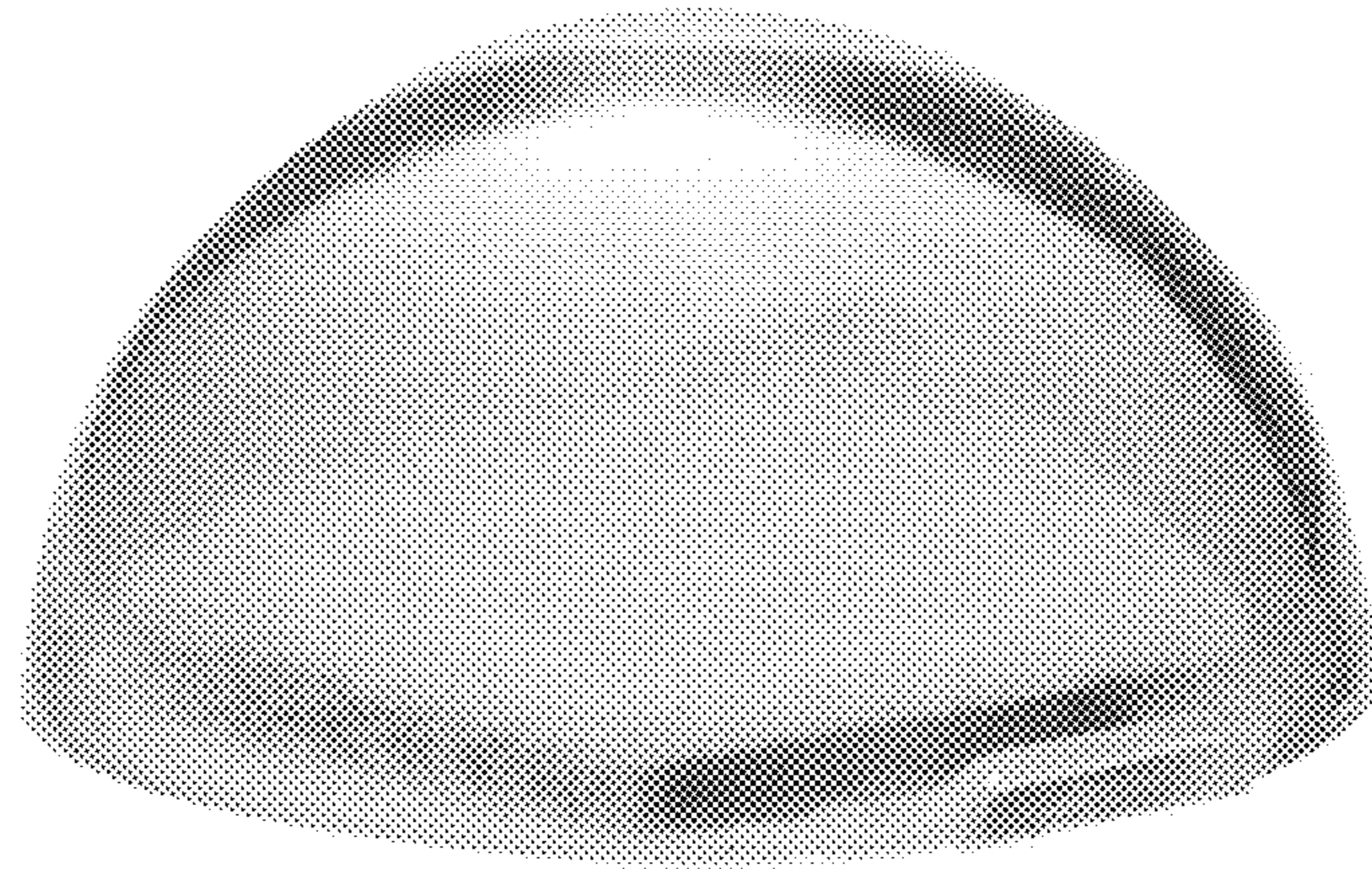


Fig. 2

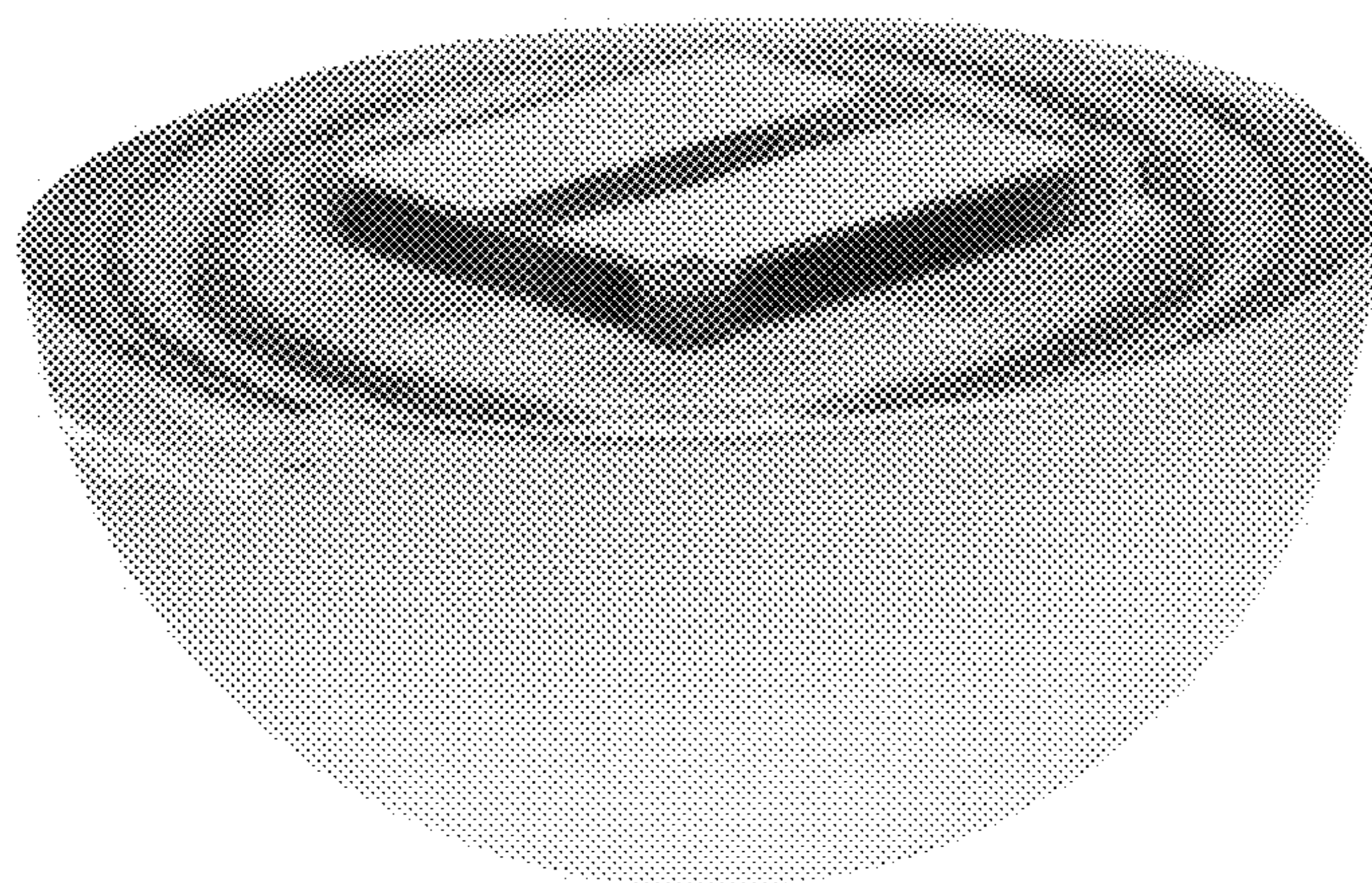


Fig.3

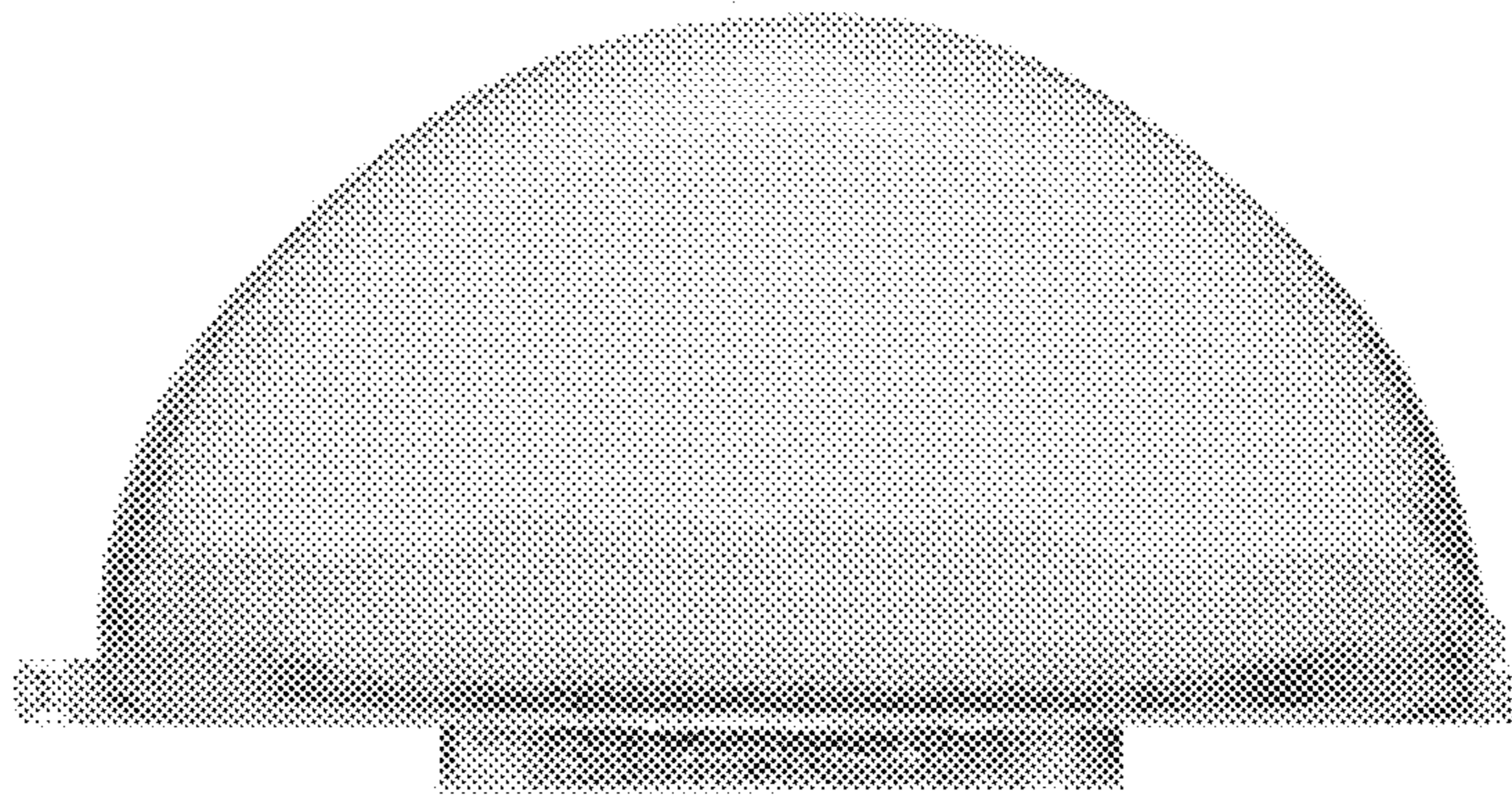


Fig.4

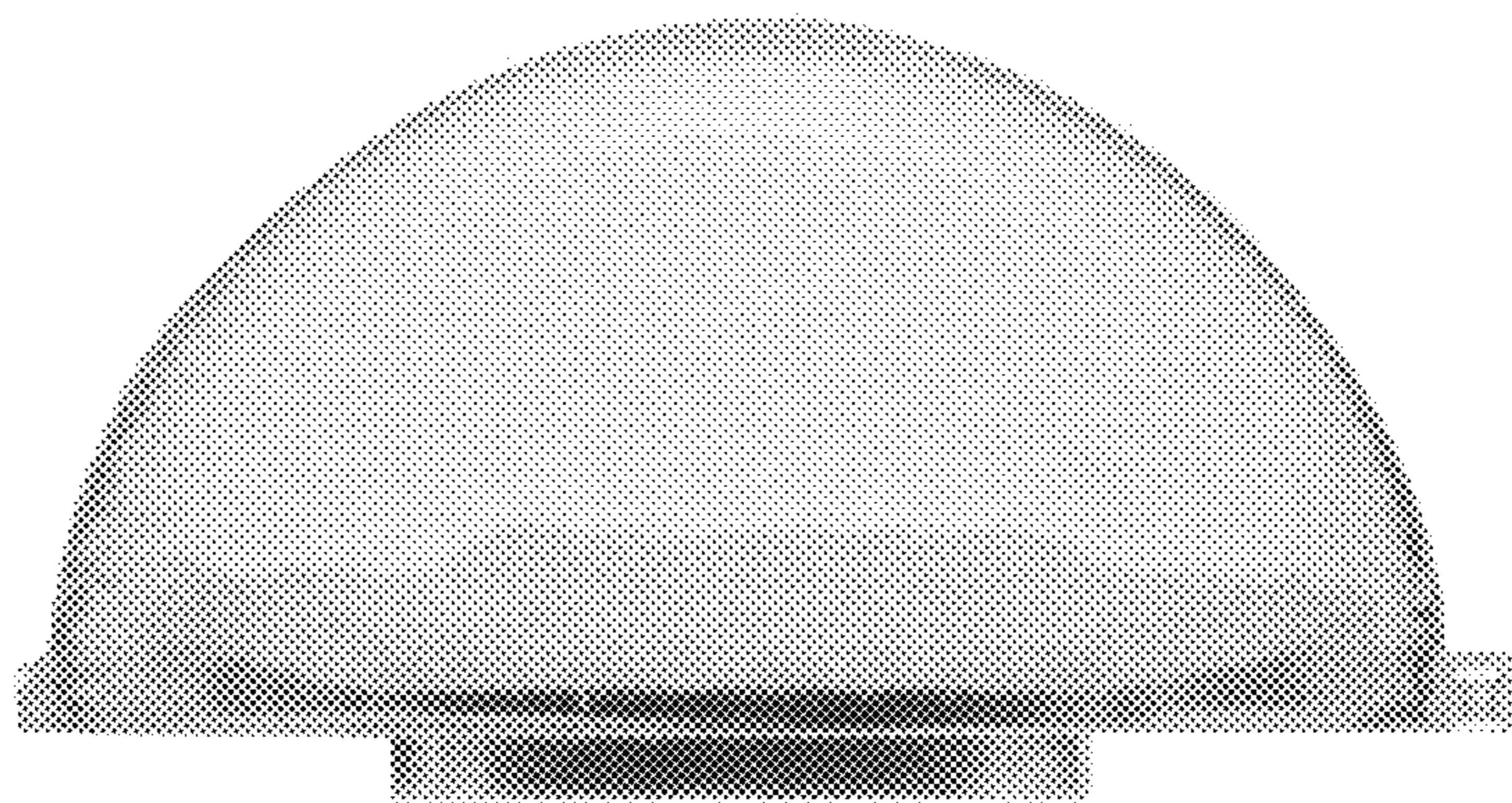


Fig.5

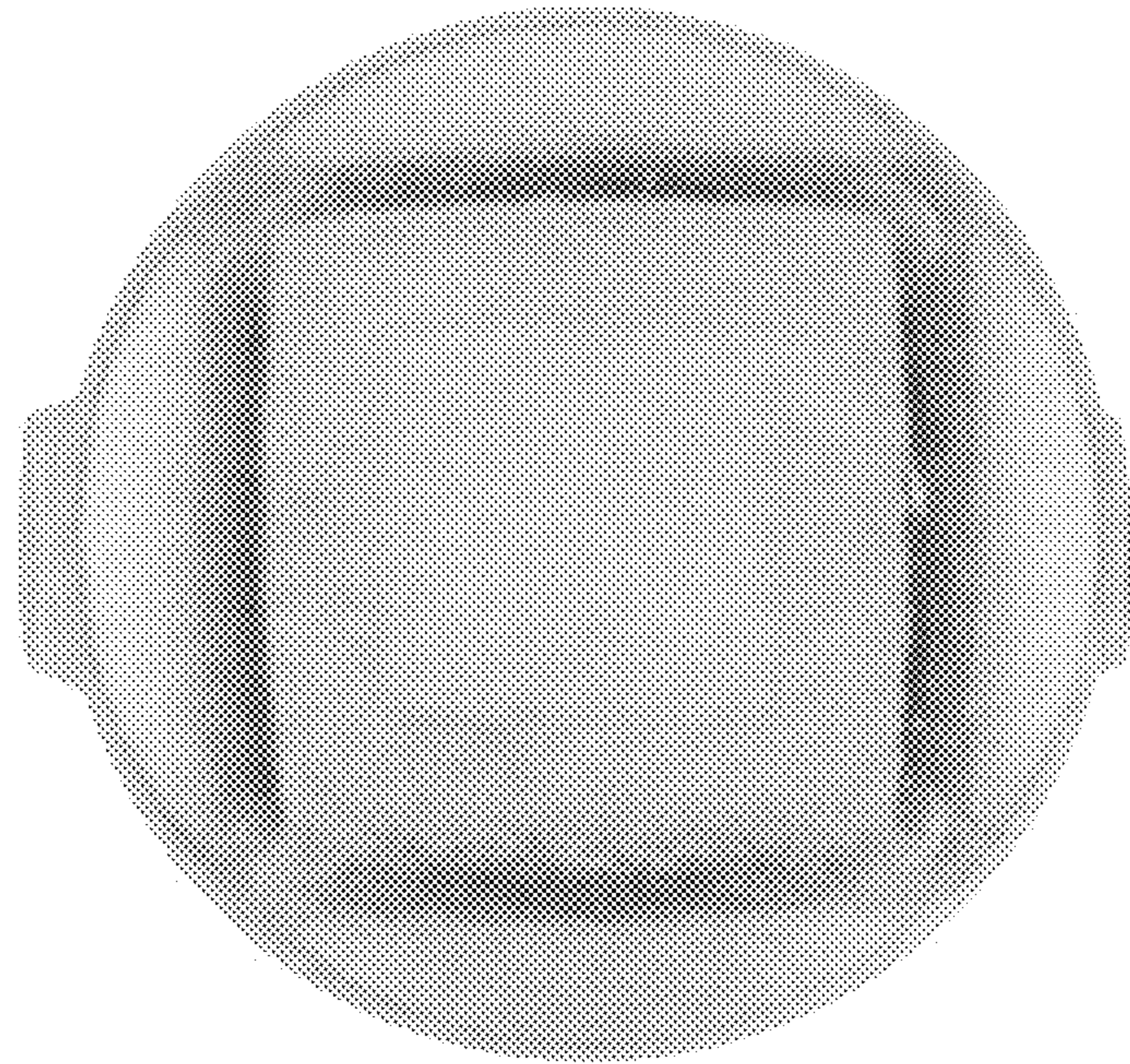


Fig.6

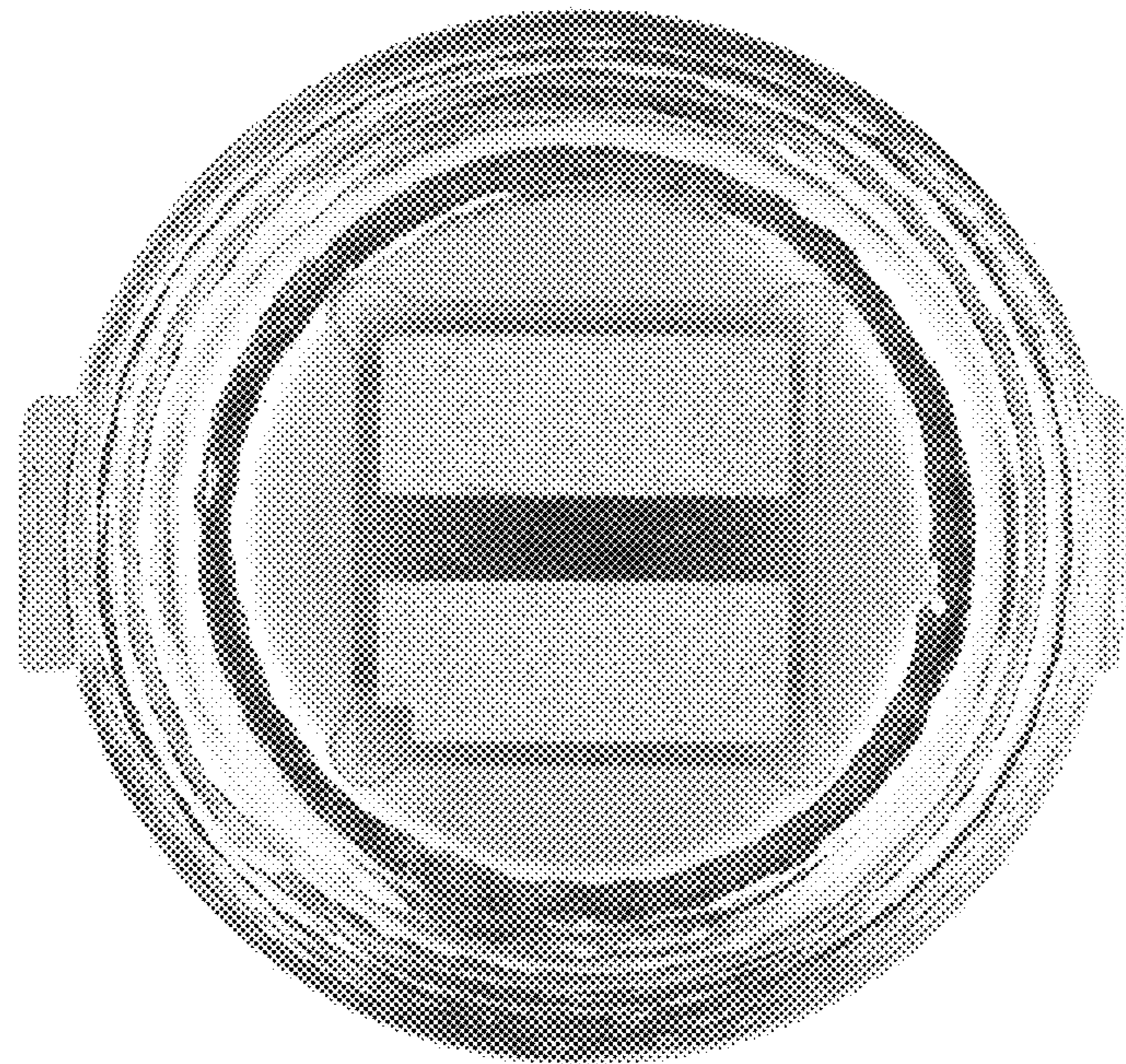


Fig.7

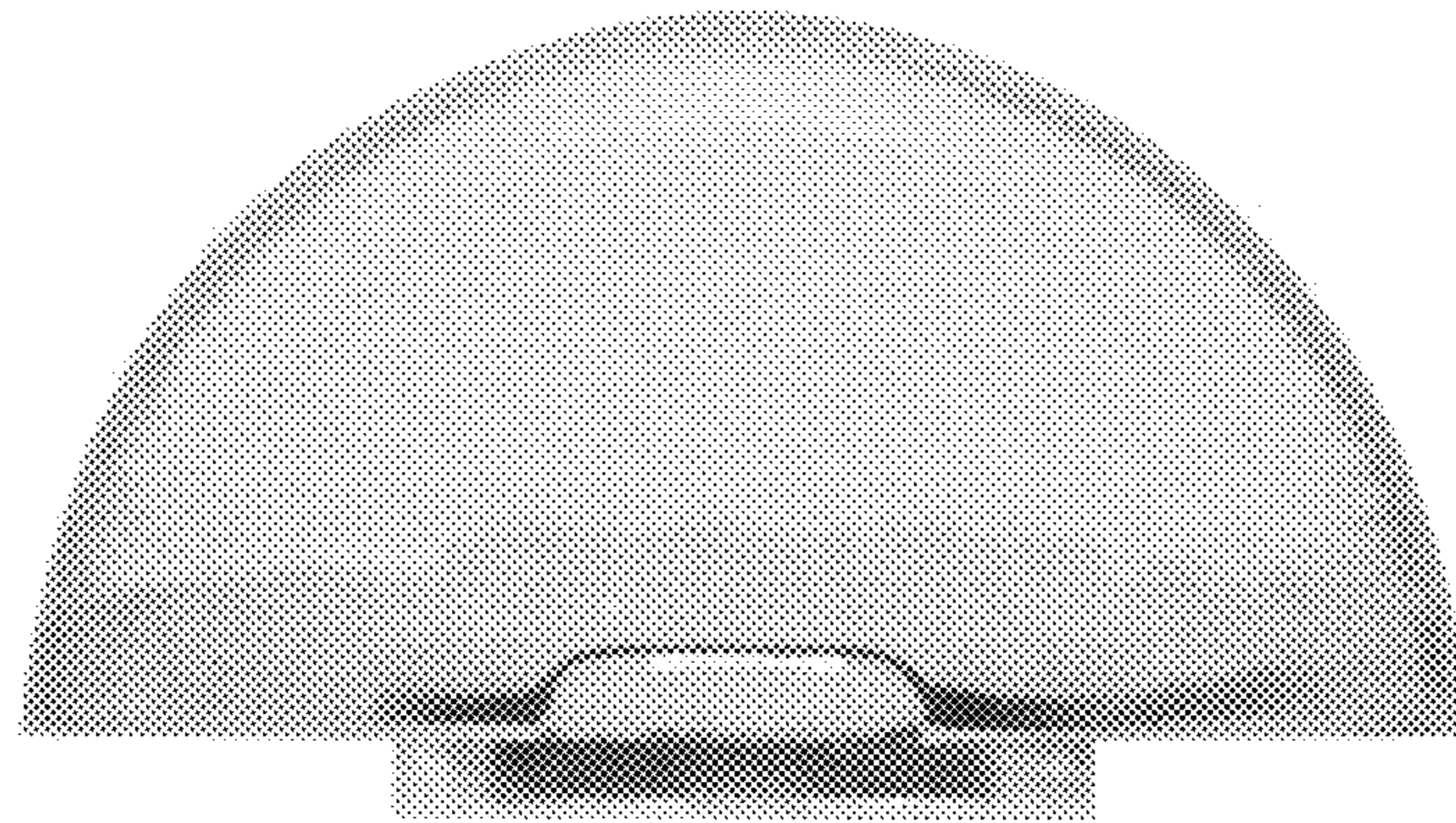


Fig.8

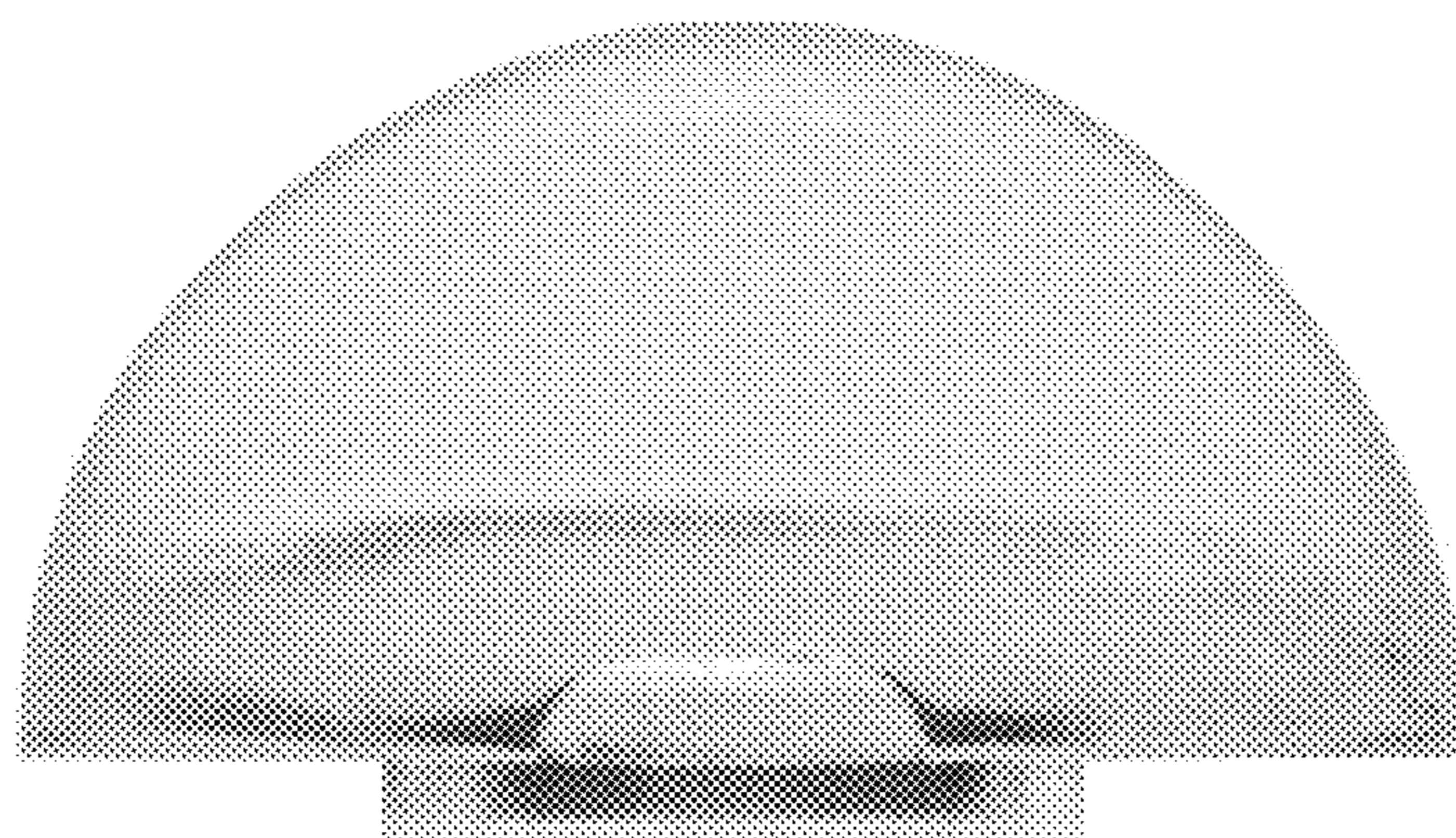


Fig.9



Fig.10

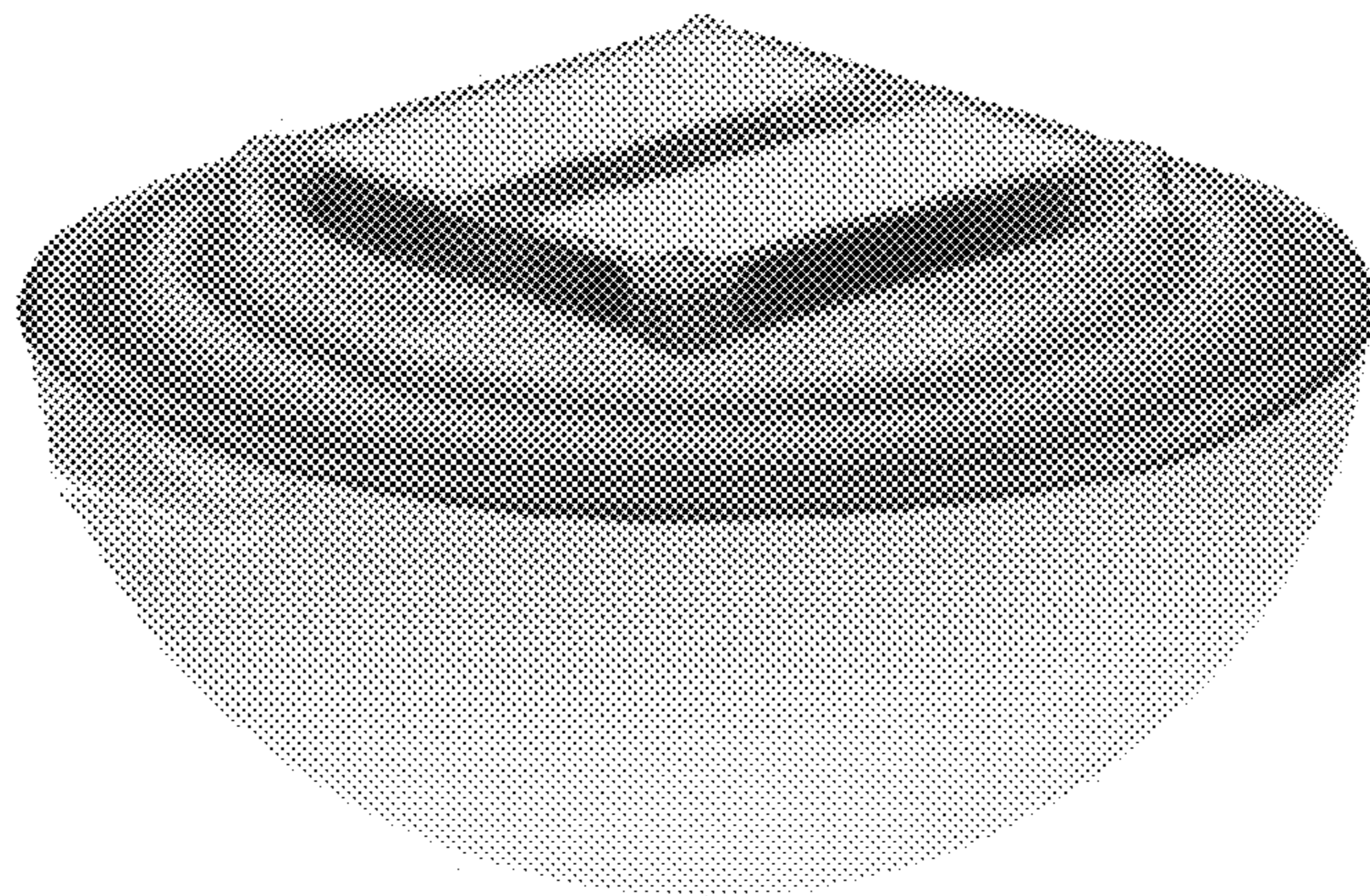


Fig. 11

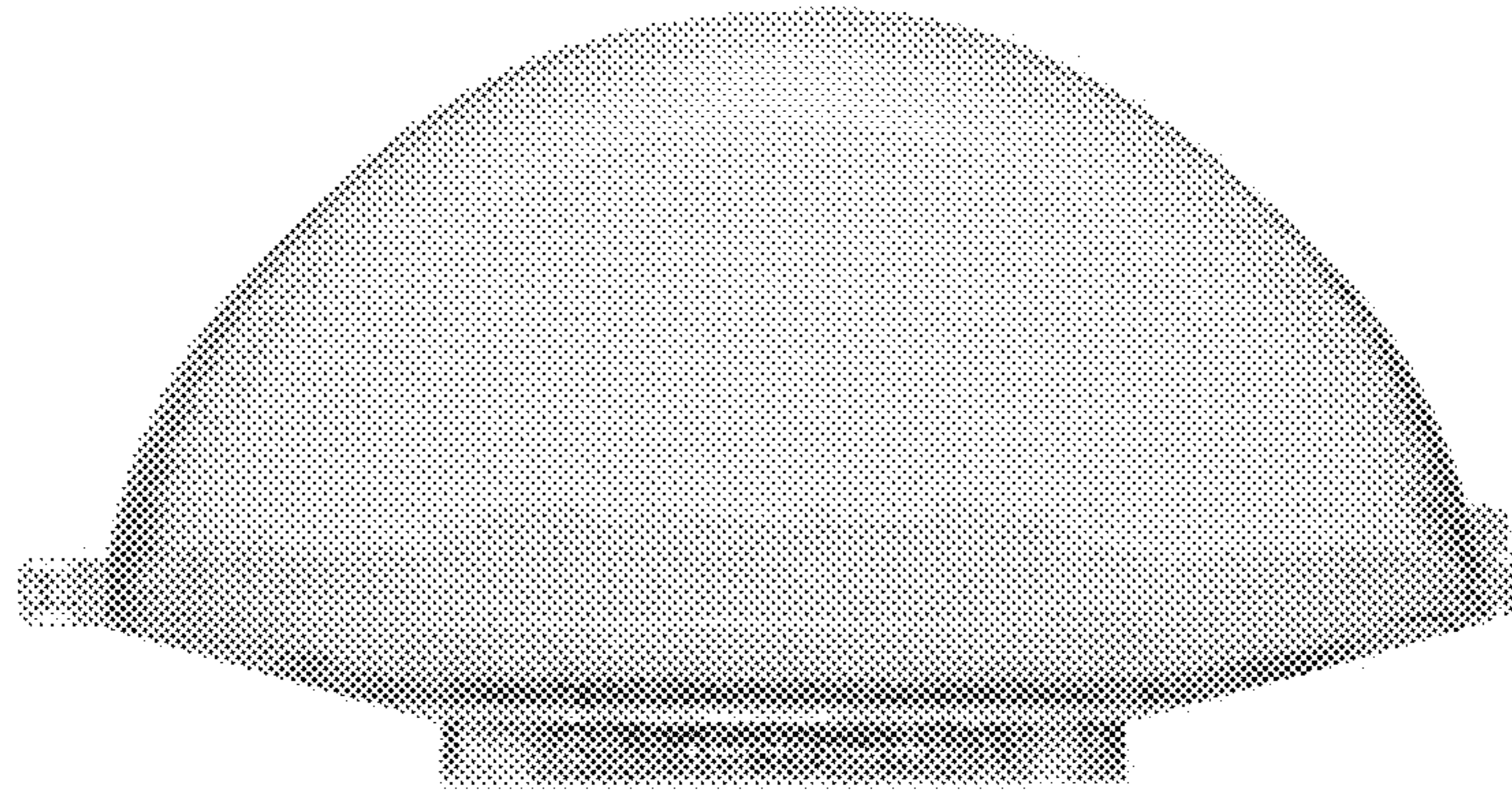


Fig. 12

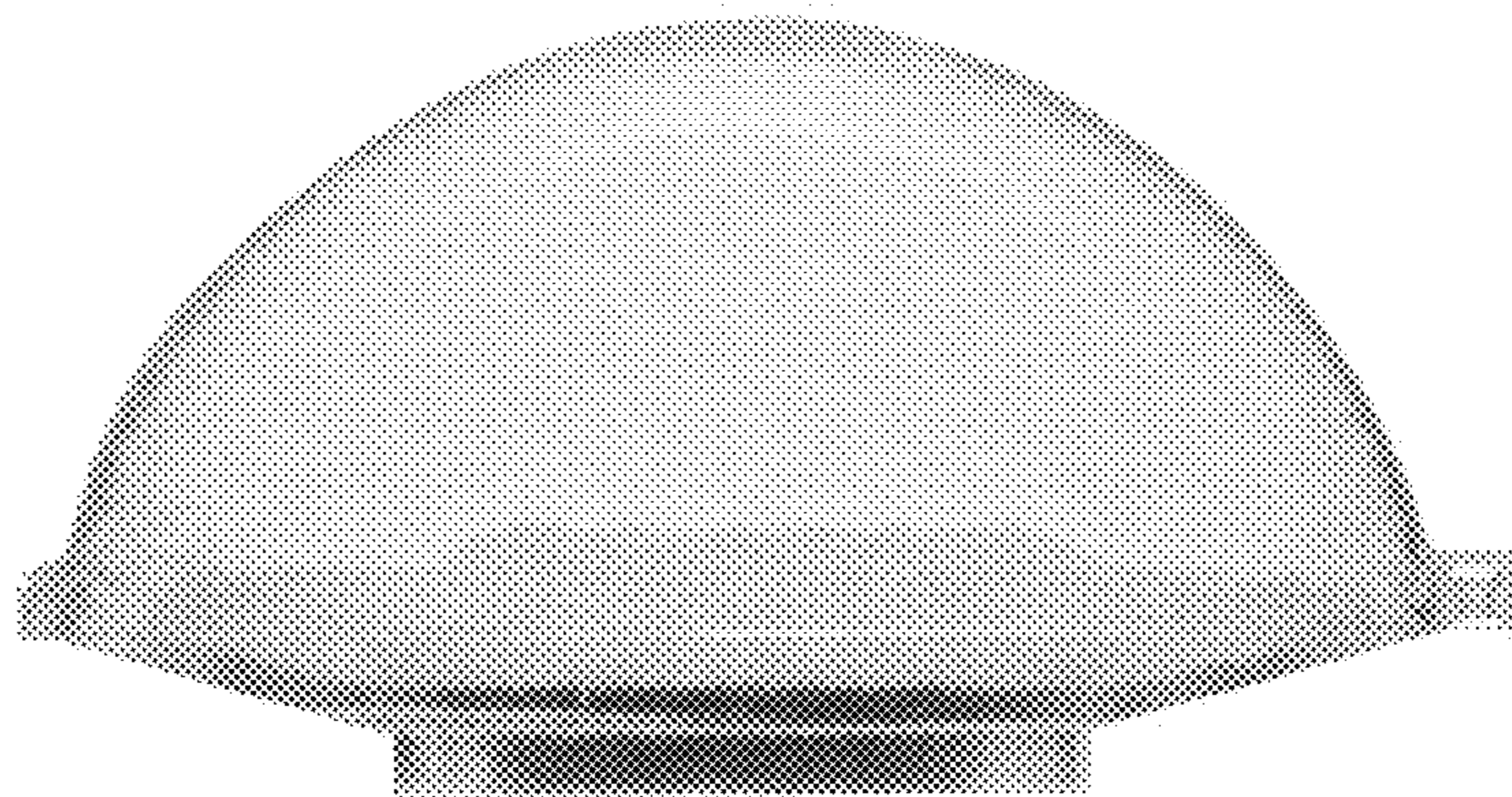


Fig. 13

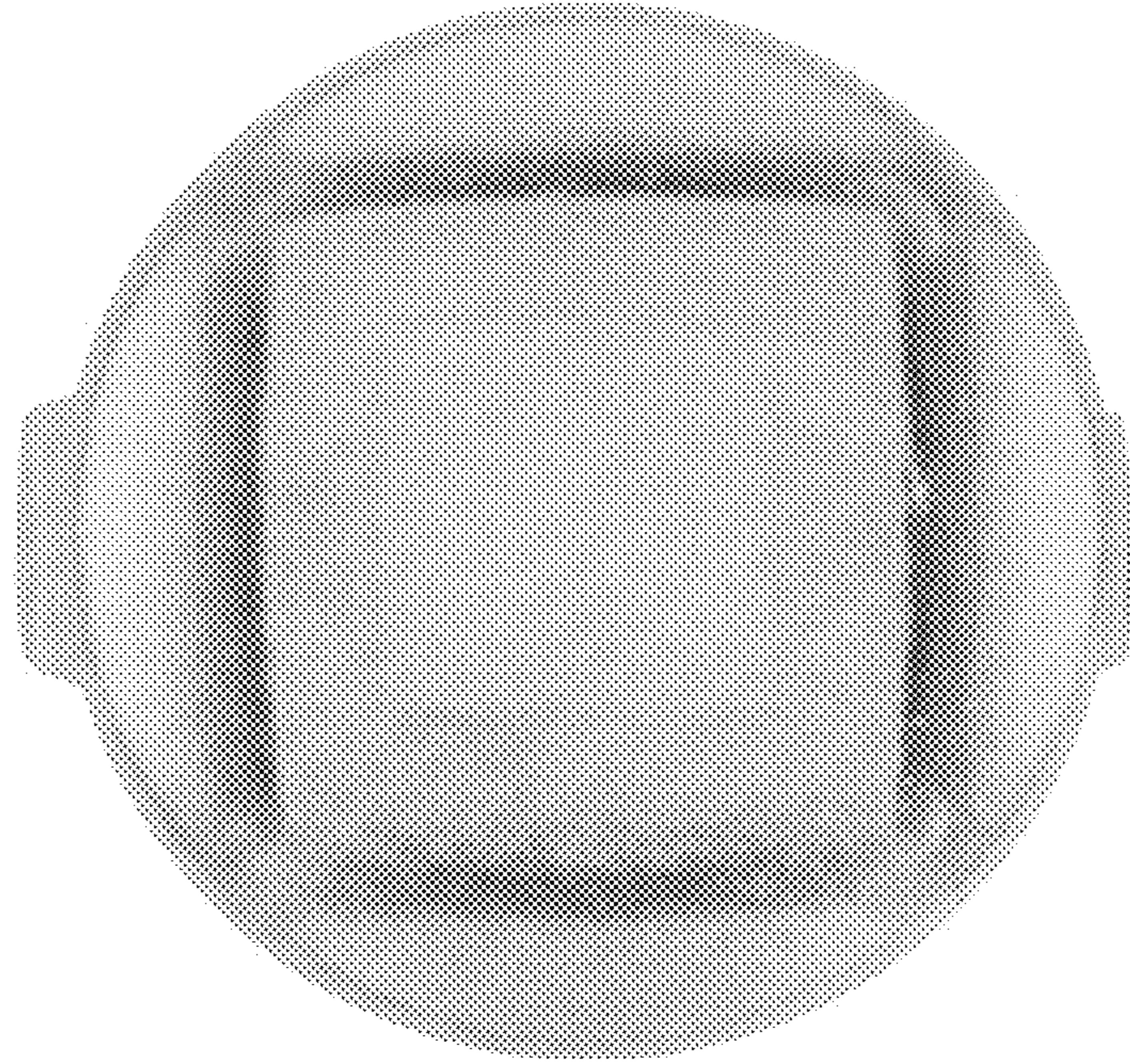


Fig. 14

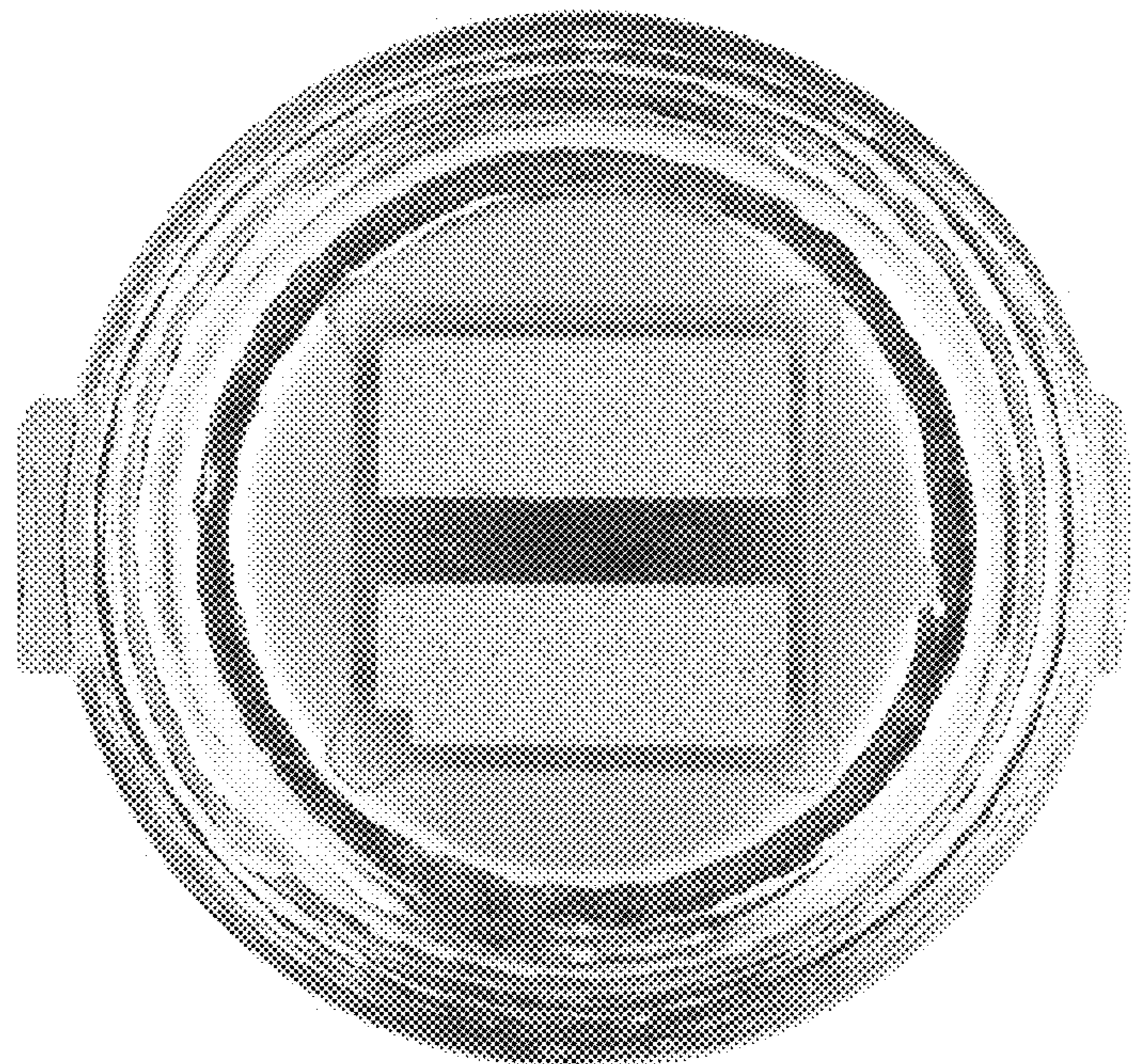


Fig. 15

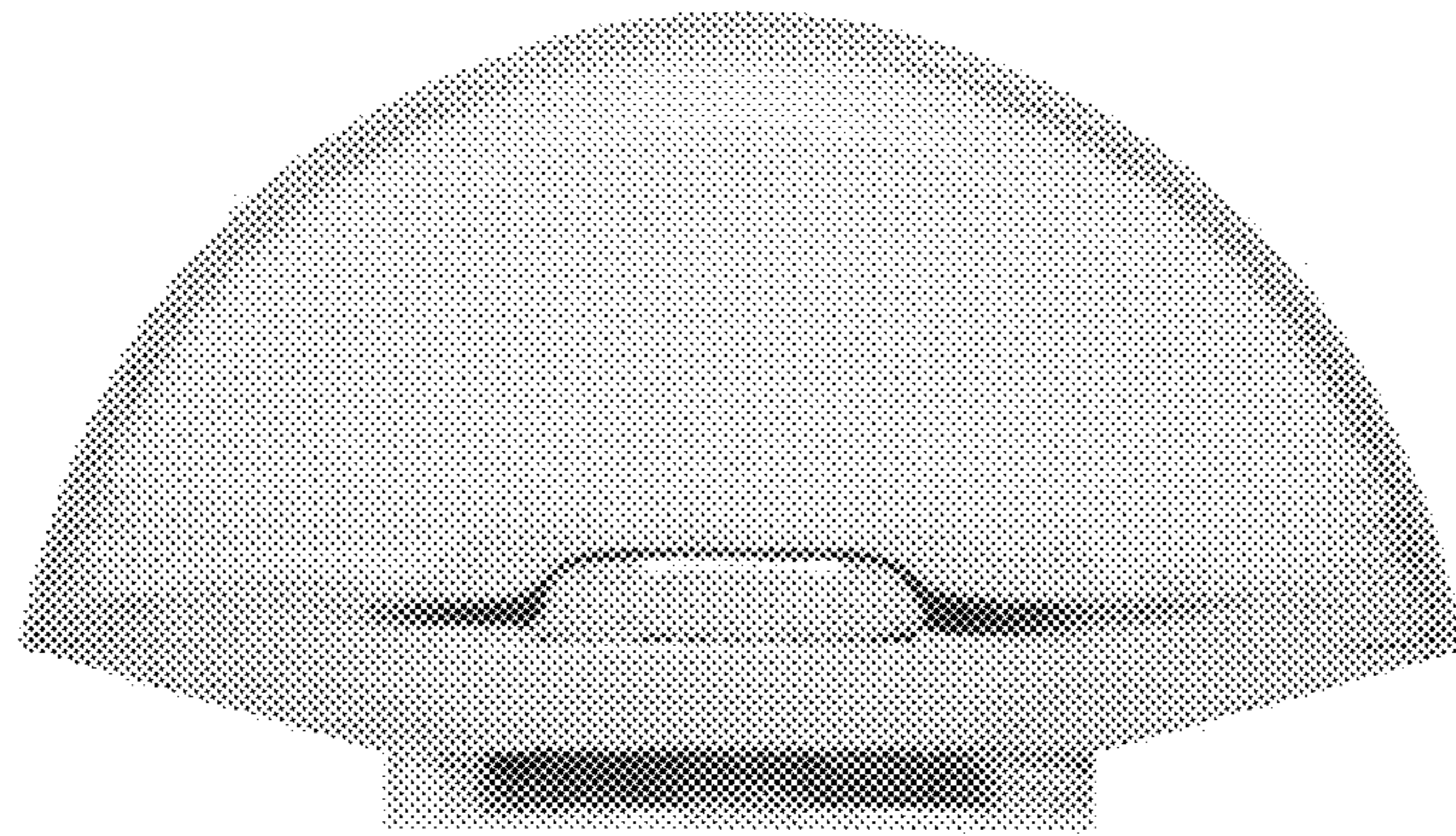


Fig. 16

