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(12) **United States Design Patent**  
**Ringer**

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(54) **PERIPHERY DATA PROCESSING DEVICE**

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(DE)

(\*\*) Term: **14 Years**

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**Related U.S. Application Data**

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(30) **Foreign Application Priority Data**

Dec. 16, 2011 (EM) ..... 001306500

(51) **LOC (10) Cl.** ..... **13-03**

(52) **U.S. Cl.**  
USPC ..... **D13/162; D13/162.1**

(58) **Field of Classification Search**  
USPC ..... D13/162, 162.1; D14/301, 439  
CPC ..... G05B 19/05; G06F 3/147; G06F 11/3636;  
H05K 7/1432; H05K 7/1467; H05K 7/1468;  
H05K 7/1471; H05K 7/1474; H05K 7/1478;  
H05K 7/1481  
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a periphery data processing device, as shown and described.

**DESCRIPTION**

FIG. 1 is a front elevational view of a periphery data processing device for a programmable logic controller (PLC) showing my new design;

FIG. 2 is a rear elevational view of the periphery data processing device;

FIG. 3 is a top plan view of the periphery data processing device;

FIG. 4 is a bottom plan view of the periphery data processing device;

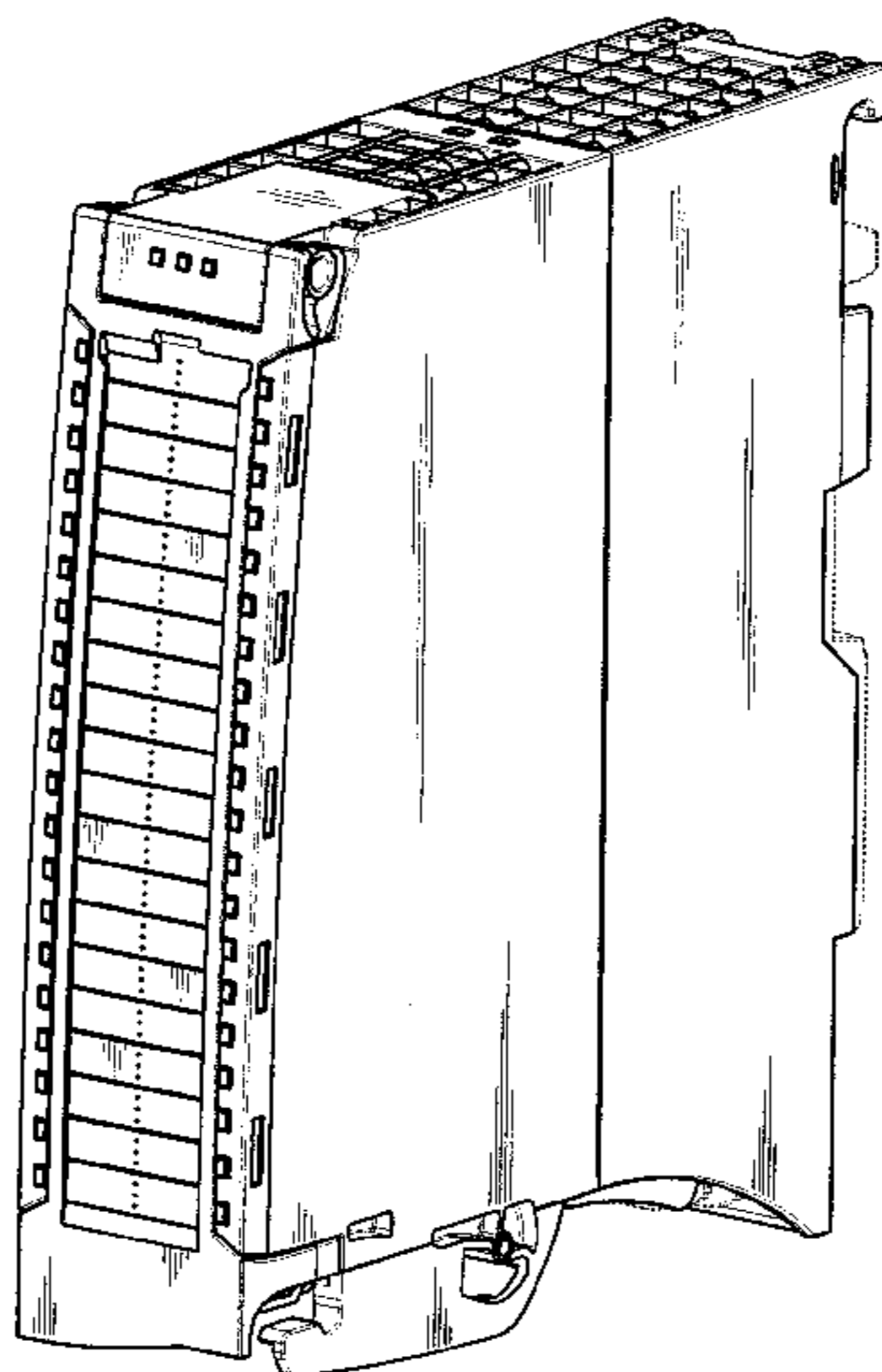
FIG. 5 is a right-side elevation view of the periphery data processing device;

FIG. 6 is left-side elevation view of the periphery data processing device; and,

FIG. 7 is a perspective view of the periphery data processing device.

The broken lines shown in the drawings views are for illustrative purposes only and forms no part of the claimed design.

**1 Claim, 7 Drawing Sheets**



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FIG 1

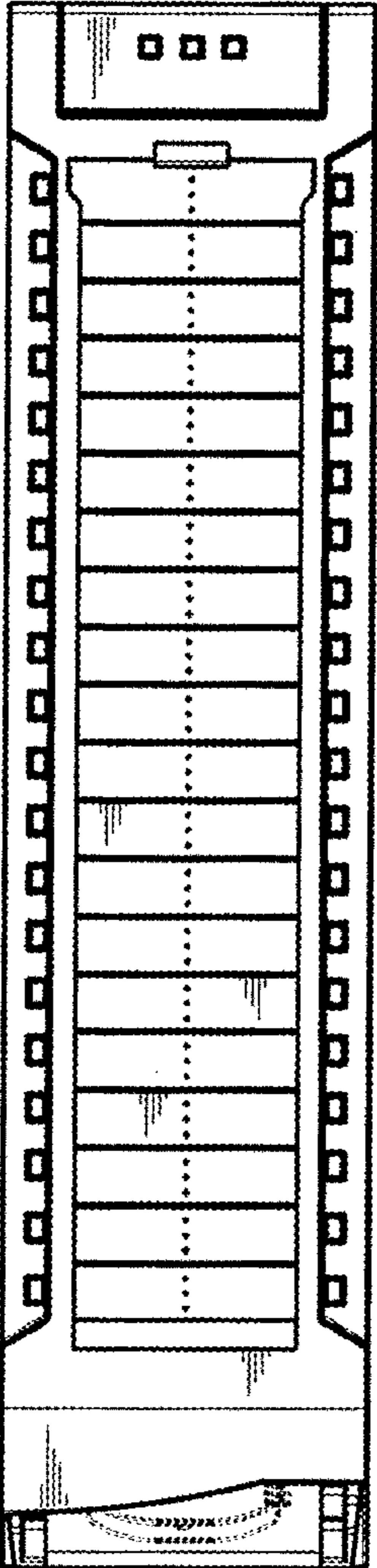


FIG 2

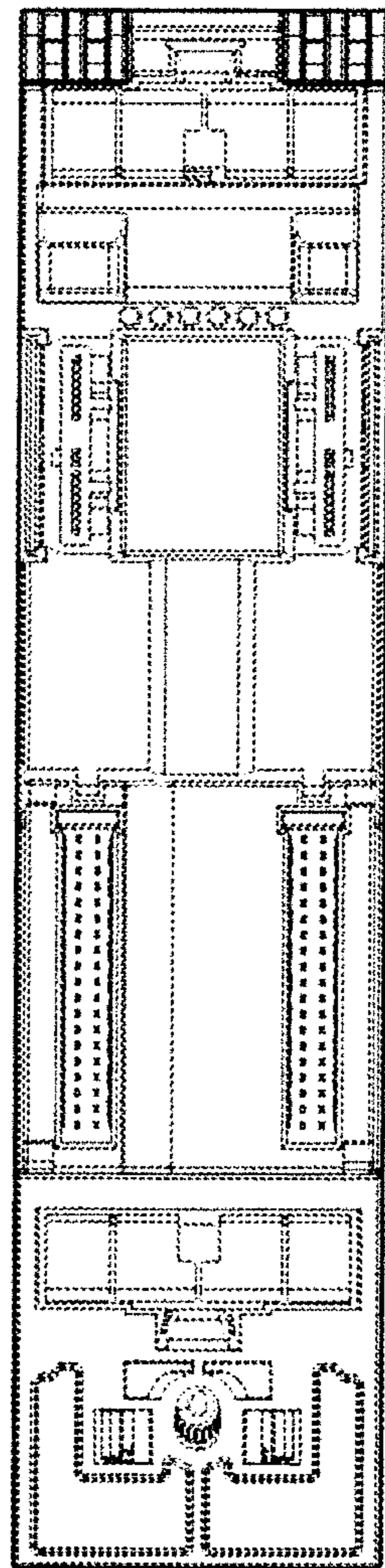


FIG 3

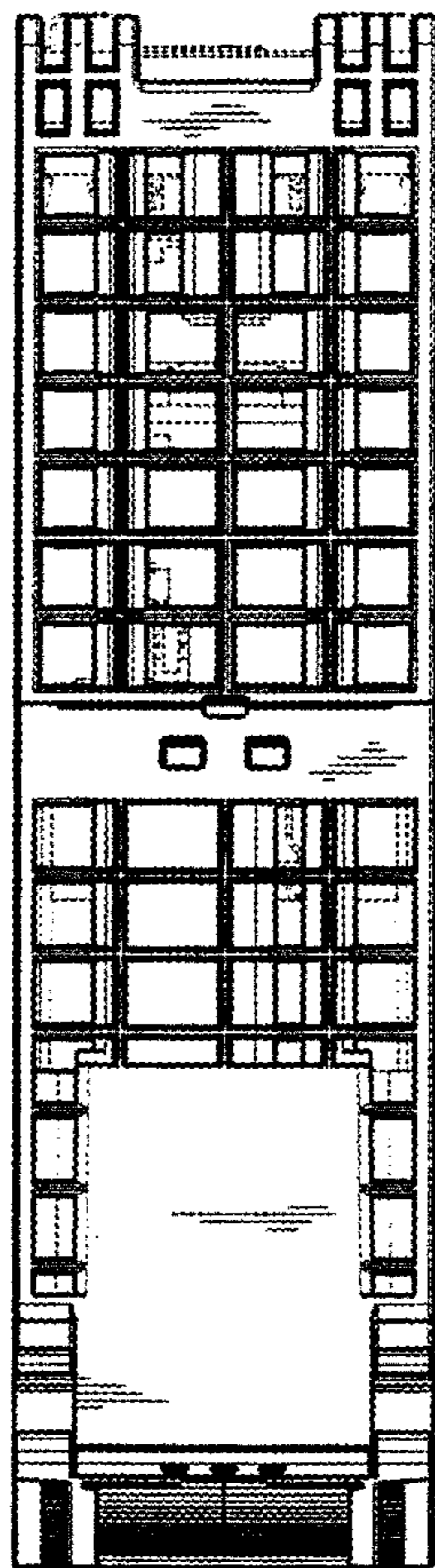


FIG 4

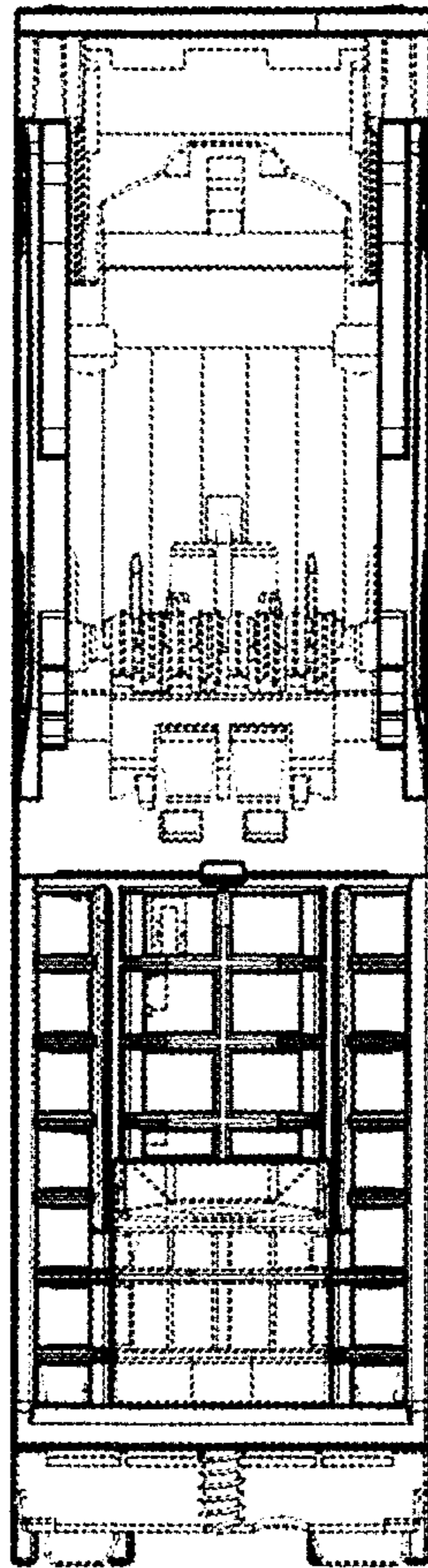


FIG 5

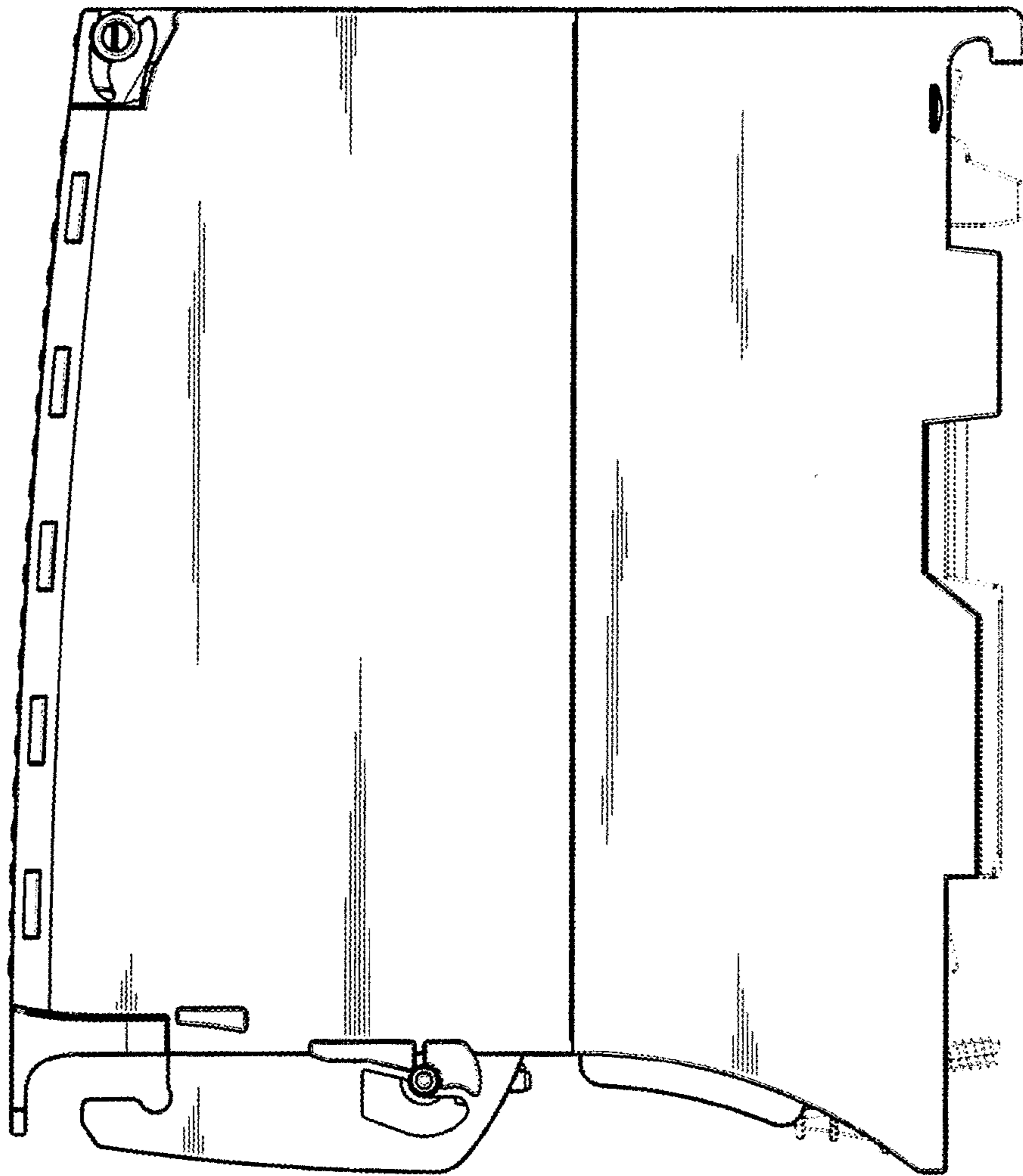


FIG 6

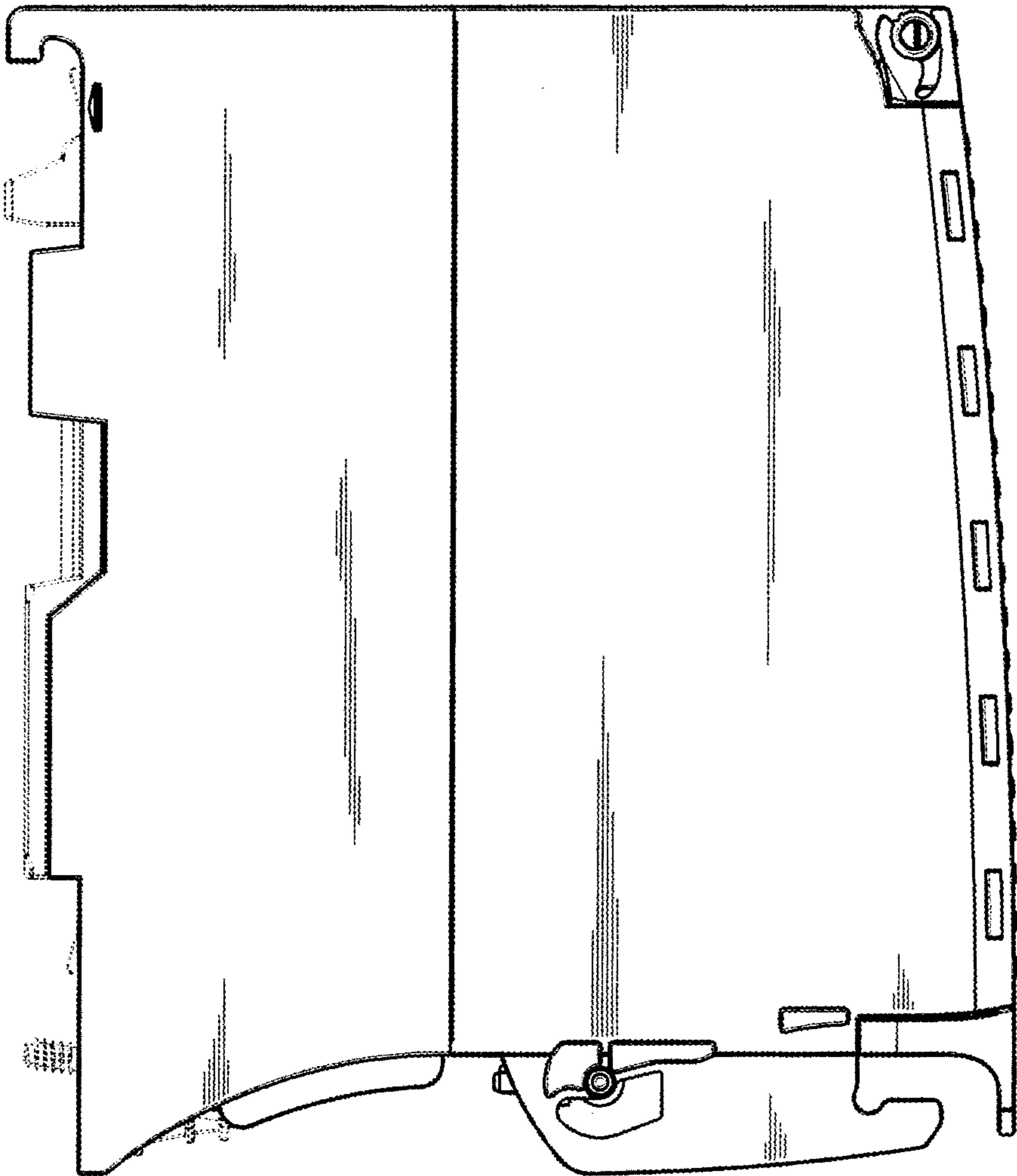




FIG 7

