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(12) **United States Design Patent**  
**Lay et al.**

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(54) **MOUNT FOR ELECTRONIC DEVICE**

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(\*\*) Term: **14 Years**

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(51) **LOC (10) Cl.** ..... **14-02**

(52) **U.S. Cl.**  
USPC ..... **D14/447; D10/80**

(58) **Field of Classification Search**  
USPC ..... D10/80, 103; D14/250, 251, 253, 240,  
D14/217, 496, 440, 203.3, 203.4, 203.5,  
D14/203.8, 341, 318, 447, 451; 248/205.1;  
439/335, 38; 455/575.8, 351  
CPC .... H01R 13/60; H01R 13/62; H01R 13/6205;  
H01R 13/621; H01R 13/6215; H01R 13/622;  
H01R 13/623; H01R 13/625; H01R 13/627;  
H01R 13/6271; H01R 13/6272; H01R  
13/6273; H01R 13/6275; H01R 13/6276;  
H01R 13/6277; H01R 13/6278  
See application file for complete search history.

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*Primary Examiner* — Antoine D Davis

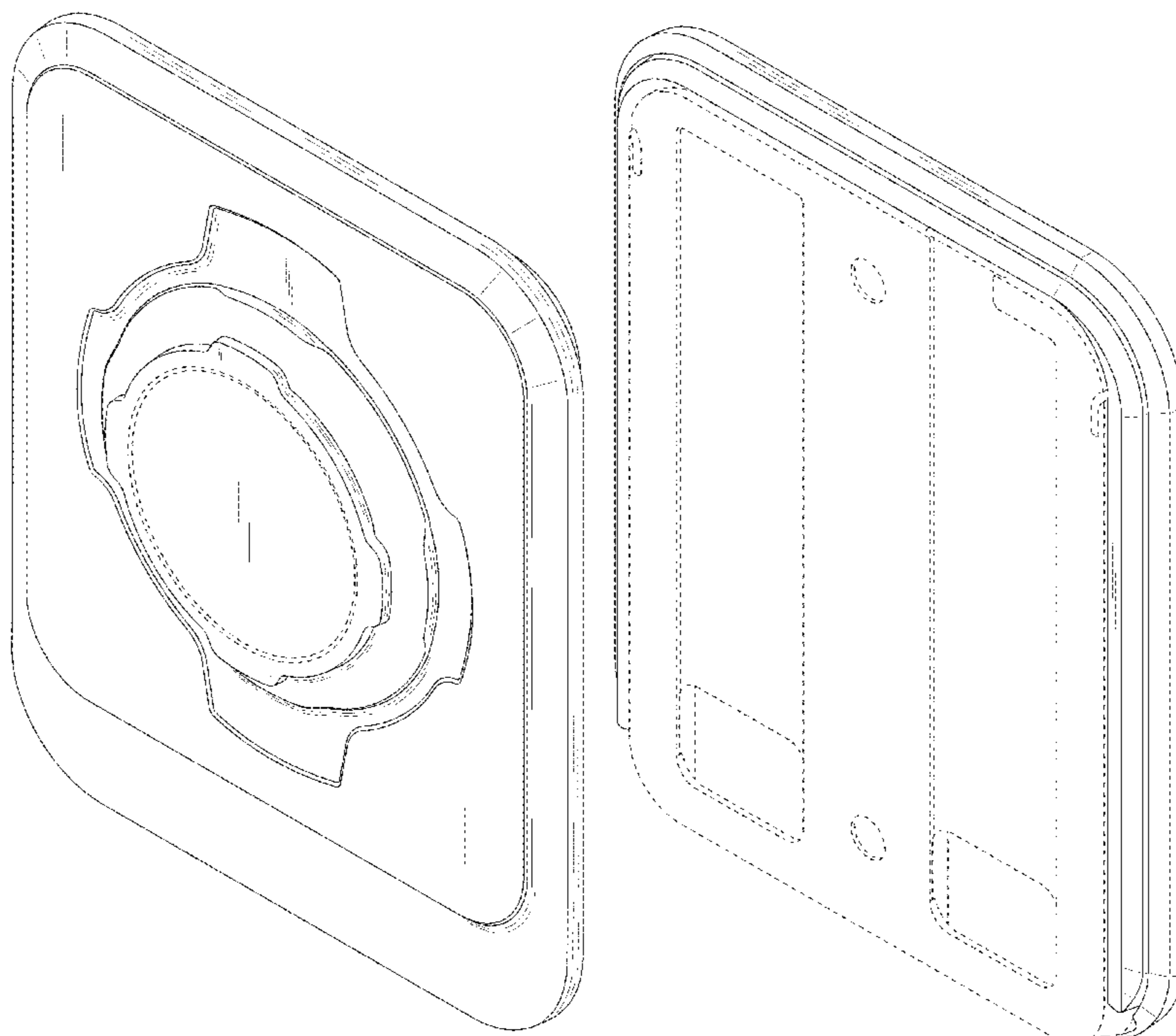
(57) **CLAIM**

The ornamental design for a mount for electronic device, as shown and described.

**DESCRIPTION**

FIG. 1 is a front isometric view of a mount for electronic device showing our new design; FIG. 2 is a rear isometric view thereof; FIG. 3 is a front view thereof; FIG. 4 is a rear view thereof; FIG. 5 is a left view thereof; FIG. 6 is a right view thereof; FIG. 7 is a top view thereof; and, FIG. 8 is a bottom view thereof. The broken lines depict unclaimed environment. The broken lines form no part of the claimed design.

**1 Claim, 6 Drawing Sheets**



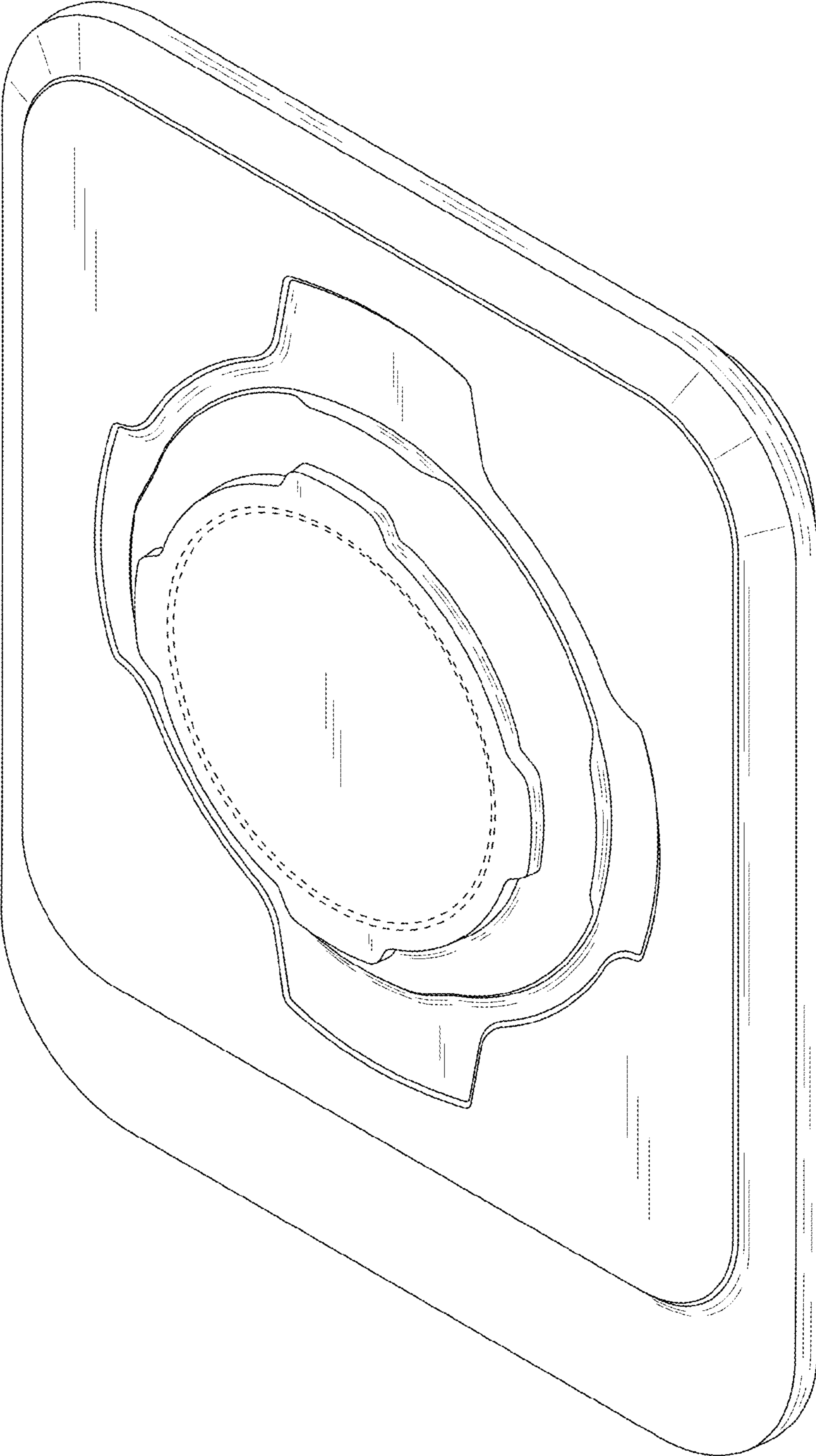


FIG. 1

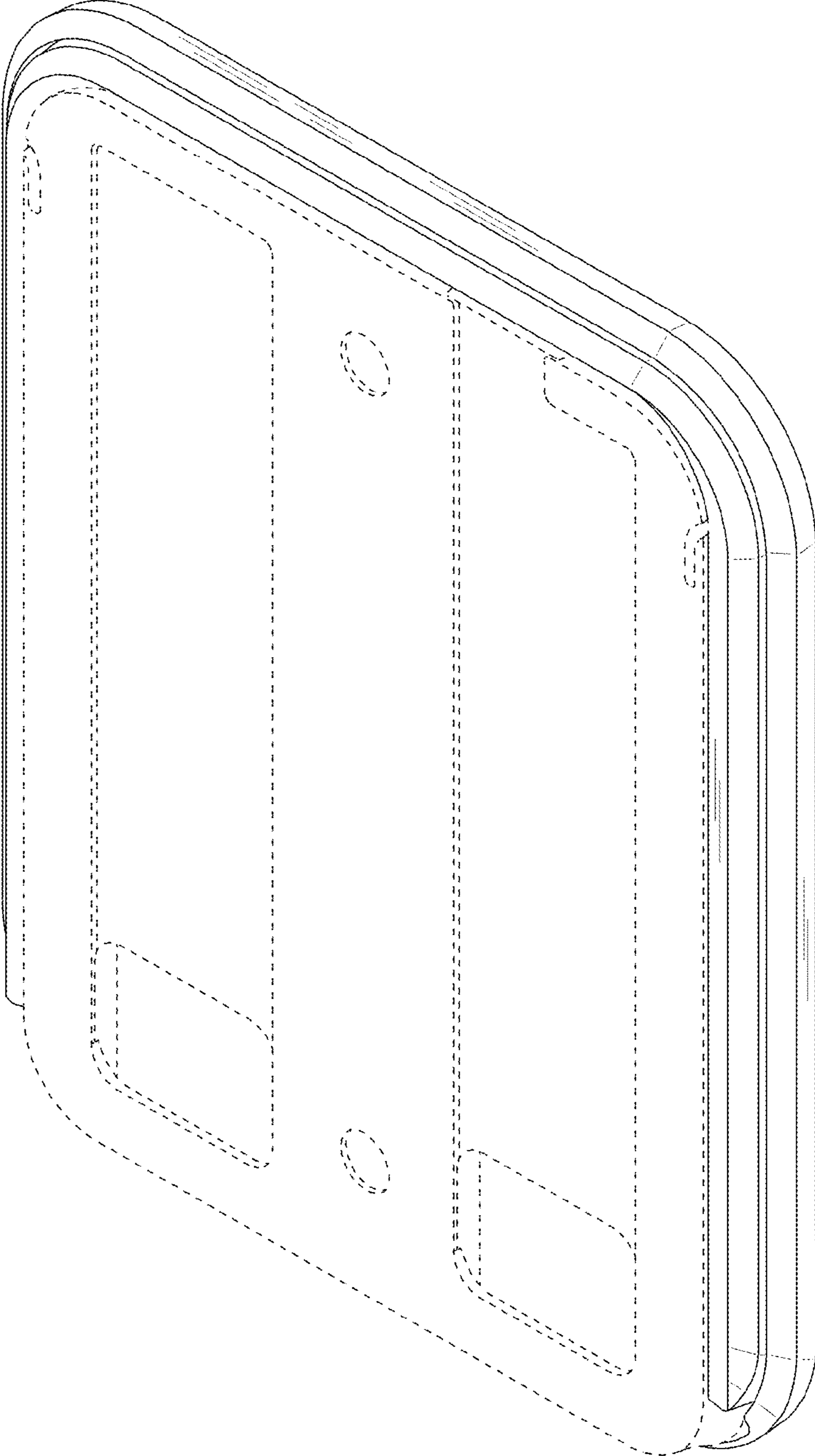


FIG. 2

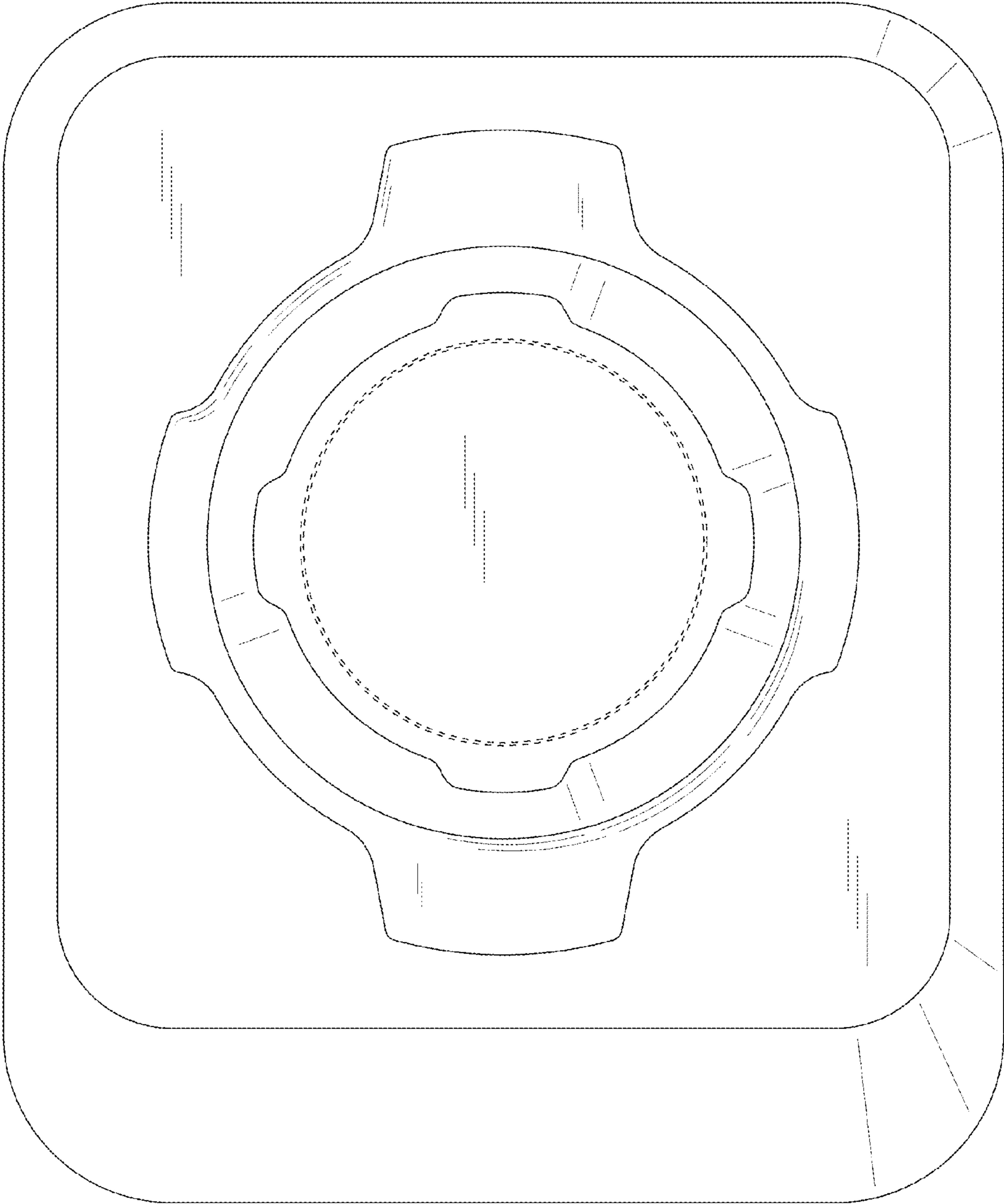


FIG. 3

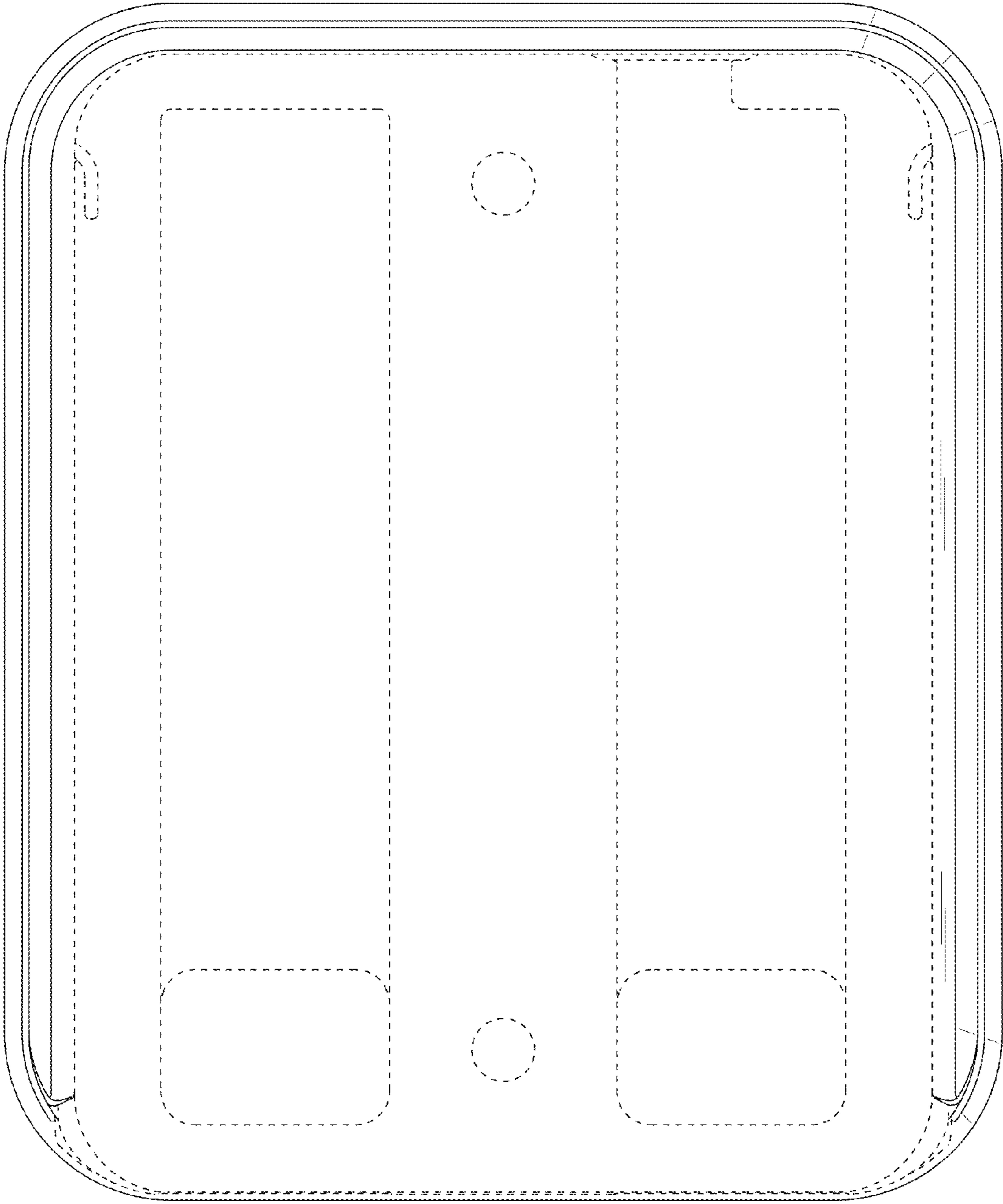


FIG. 4

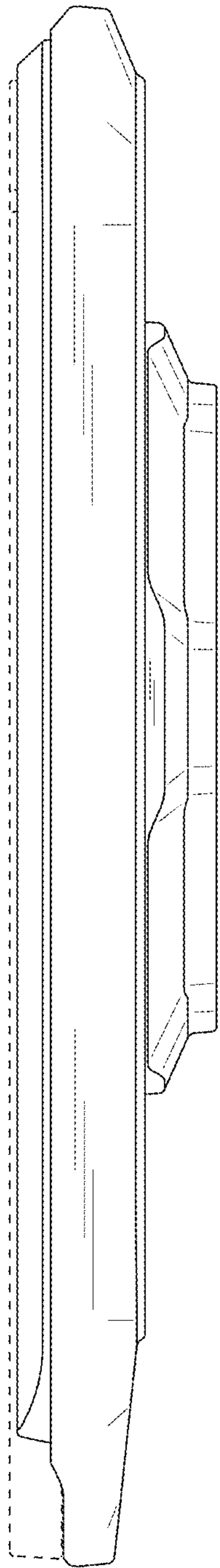


FIG. 5

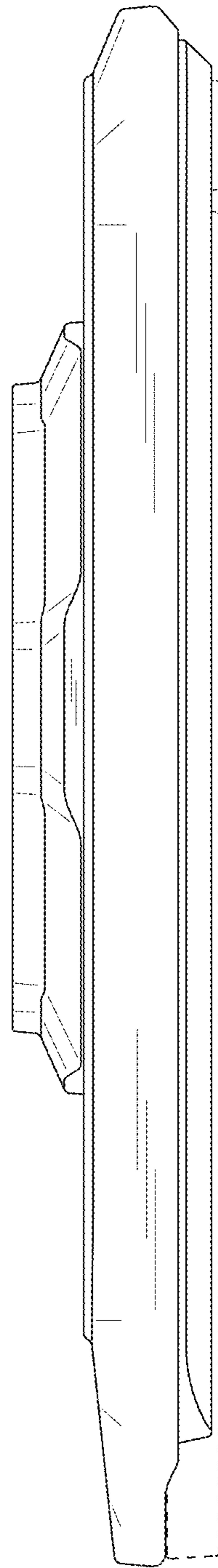


FIG. 6

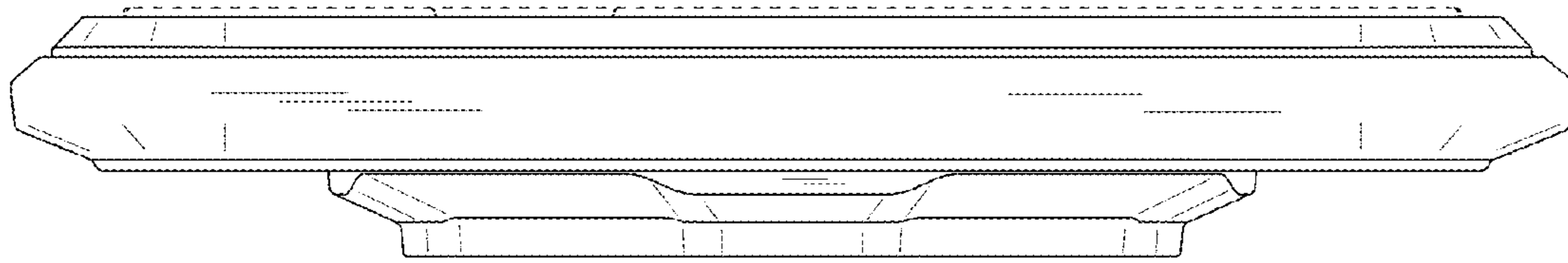


FIG. 7

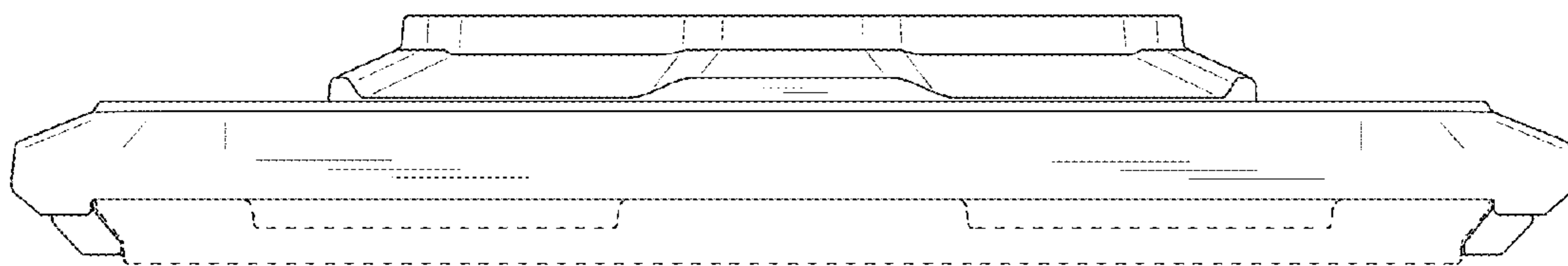


FIG. 8