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(12) **United States Design Patent**  
**Adamson et al.**

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(54) **ELECTRONIC COMPUTER WITH AN AT LEAST PARTIALLY TRANSPARENT INPUT DEVICE**

(71) Applicant: **Intel Corporation**, Santa Clara, CA (US)

(72) Inventors: **Peter S. Adamson**, Portland, OR (US);  
**Gary Paek**, Beaverton, OR (US);  
**Nicholas W. Oakley**, Portland, OR (US)

(73) Assignee: **Intel Corporation**, Santa Clara, CA (US)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/478,168**

(22) Filed: **Dec. 31, 2013**

**Related U.S. Application Data**

(62) Division of application No. 29/423,471, filed on May 31, 2012, now Pat. No. Des. 698,350.

(51) **LOC (10) Cl.** ..... **14-02**

(52) **U.S. Cl.**  
USPC ..... **D14/318**

(58) **Field of Classification Search**  
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235/145 A, 145 R; 341/22, 23; 345/104,  
345/156, 168, 169, 173; 361/679.08,  
361/679.09, 679.11, 679.26, 679.27  
CPC ..... G06F 1/16; G06F 1/1616; H01R 35/02;  
H03M 11/00; G09B 13/04; H03K 17/94  
See application file for complete search history.

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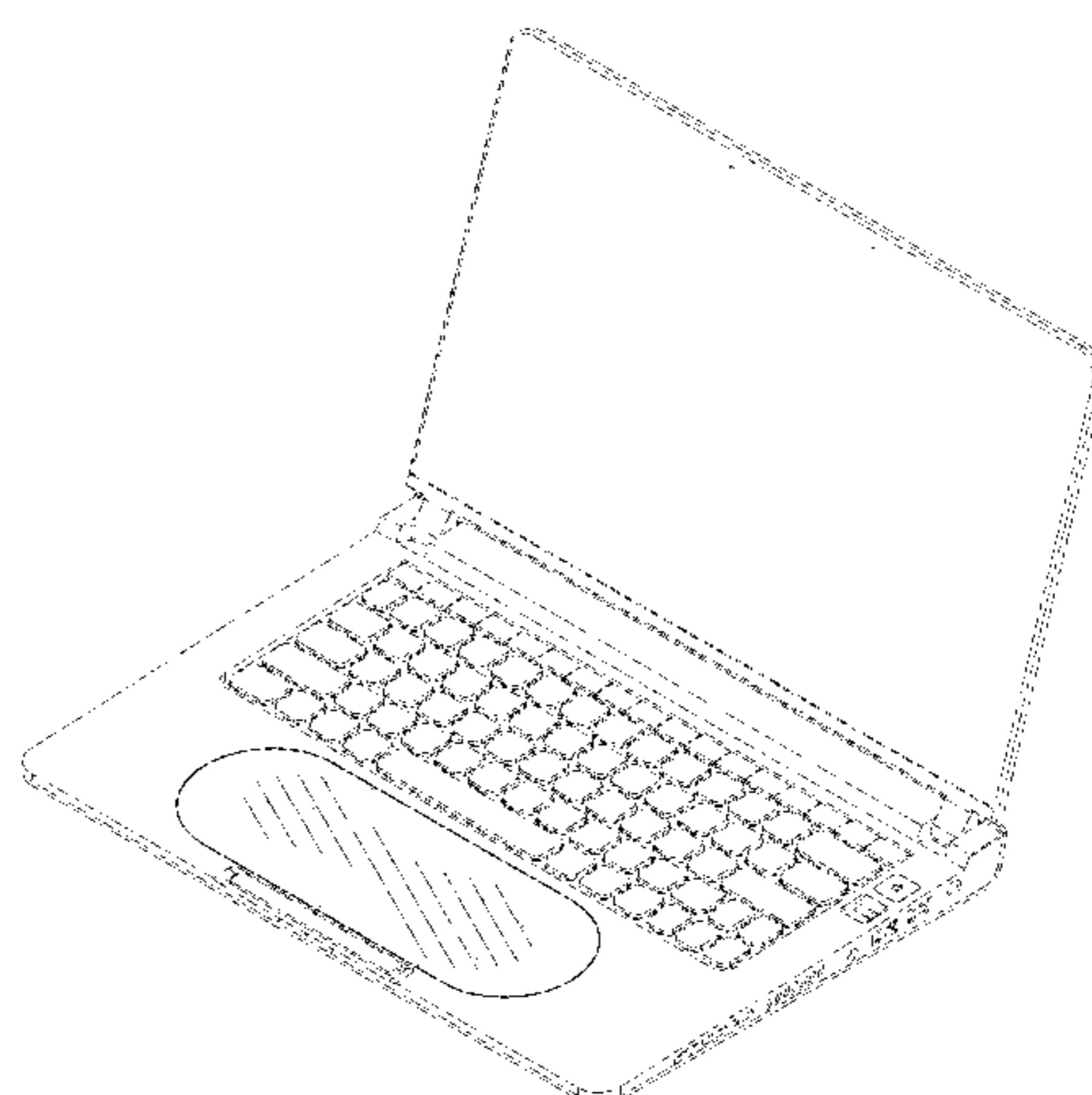
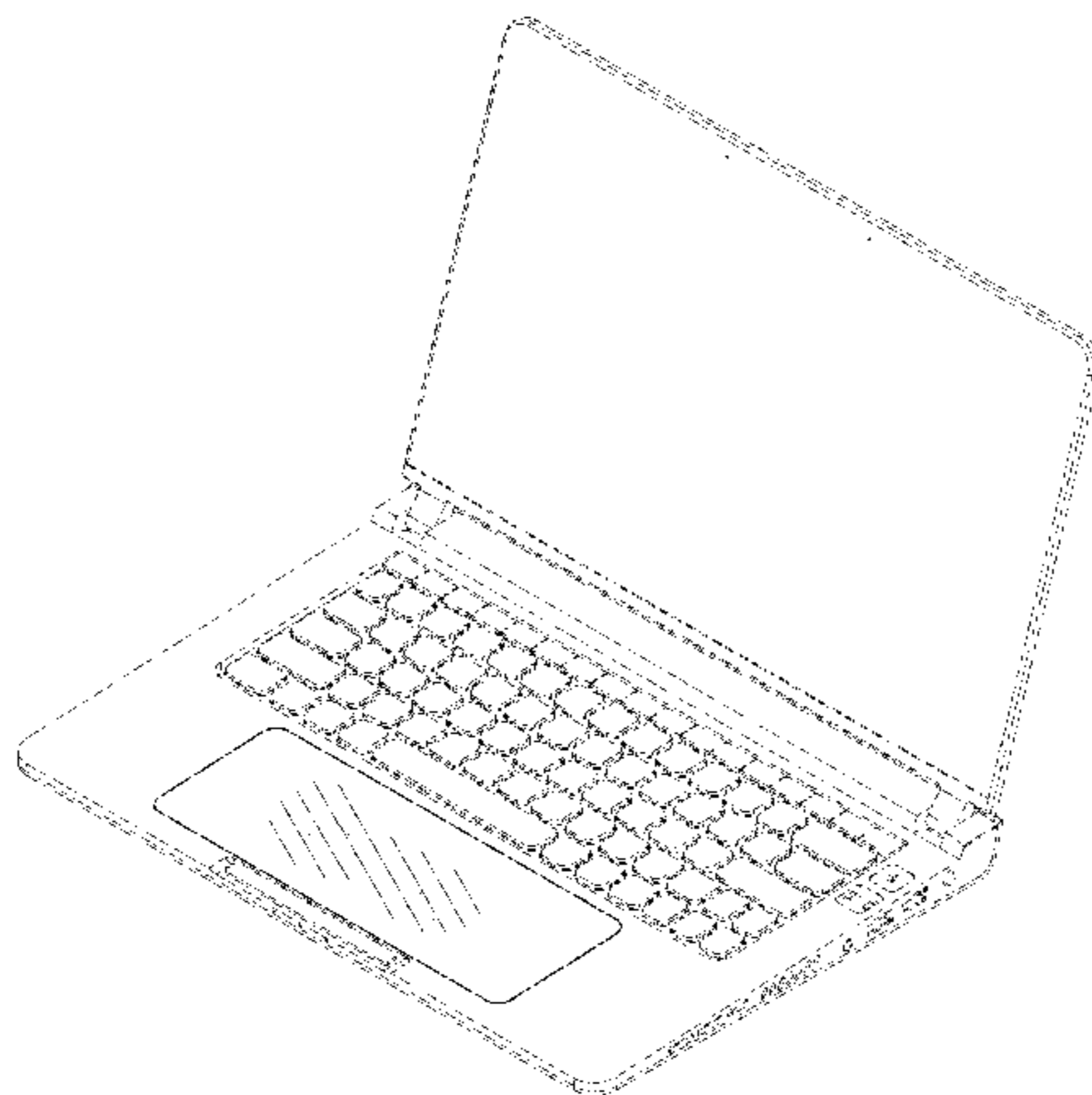
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Primary Examiner — Freda S Nunn

(74) Attorney, Agent, or Firm — Patent Capital Group

(57)

**CLAIM**

The ornamental design for electronic computer with an at least partially transparent input device, as shown and described.

**DESCRIPTION**

FIG. 1 is a top perspective view showing a first embodiment of our new design for electronic computer with an at least partially transparent input device;  
 FIG. 2 is a front view thereof;  
 FIG. 3 is a back view thereof;  
 FIG. 4 is a side view thereof;  
 FIG. 5 is another side view thereof;  
 FIG. 6 is a top view thereof;  
 FIG. 7 is a bottom view thereof;  
 FIG. 8 is a top perspective view showing a second embodiment of a design for electronic computer with an at least partially transparent input device;  
 FIG. 9 is a front view thereof;  
 FIG. 10 is a back view thereof;  
 FIG. 11 is a side view thereof;  
 FIG. 12 is another side view thereof;  
 FIG. 13 is a top view thereof;  
 FIG. 14 is a bottom view thereof;  
 FIG. 15 is a top perspective view showing a third embodiment of a design for electronic computer with an at least partially transparent input device;  
 FIG. 16 is a front view thereof;  
 FIG. 17 is a back view thereof;  
 FIG. 18 is a side view thereof;  
 FIG. 19 is another side view thereof;  
 FIG. 20 is a top view thereof;  
 FIG. 21 is a bottom view thereof;  
 FIG. 22 is a top perspective view showing a fourth embodiment of a design for electronic computer with an at least partially transparent input device;  
 FIG. 23 is a front view thereof;  
 FIG. 24 is a back view thereof;  
 FIG. 25 is a side view thereof;  
 FIG. 26 is another side view thereof;  
 FIG. 27 is a top view thereof;  
 FIG. 28 is a bottom view thereof;  
 FIG. 29 is a top perspective view showing a fifth embodiment of a design for electronic computer with an at least partially transparent input device;  
 FIG. 30 is a front view thereof;  
 FIG. 31 is a back view thereof;  
 FIG. 32 is a side view thereof;  
 FIG. 33 is another side view thereof;  
 FIG. 34 is a top view thereof;  
 FIG. 35 is a bottom view thereof;  
 FIG. 36 is a top perspective view showing a sixth embodiment of a design for electronic computer with an at least partially transparent input device;  
 FIG. 37 is a front view thereof;  
 FIG. 38 is a back view thereof;  
 FIG. 39 is a side view thereof;  
 FIG. 40 is another side view thereof;  
 FIG. 41 is a top view thereof; and,  
 FIG. 42 is a bottom view thereof.  
 The broken lines in the preceding FIGURES illustrate portions of the computer and form no part of the claimed design.

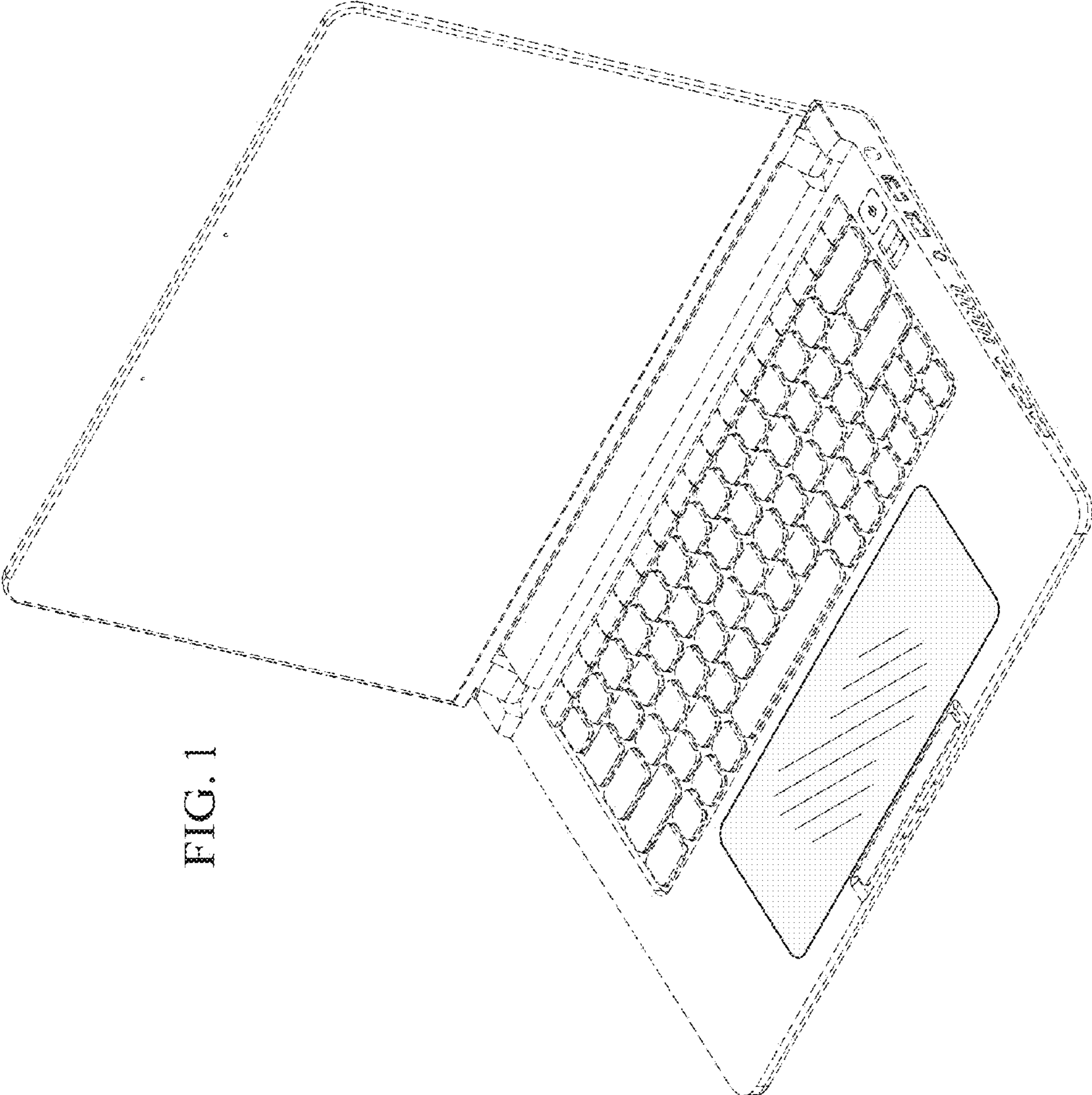


FIG. 1

FIG. 2

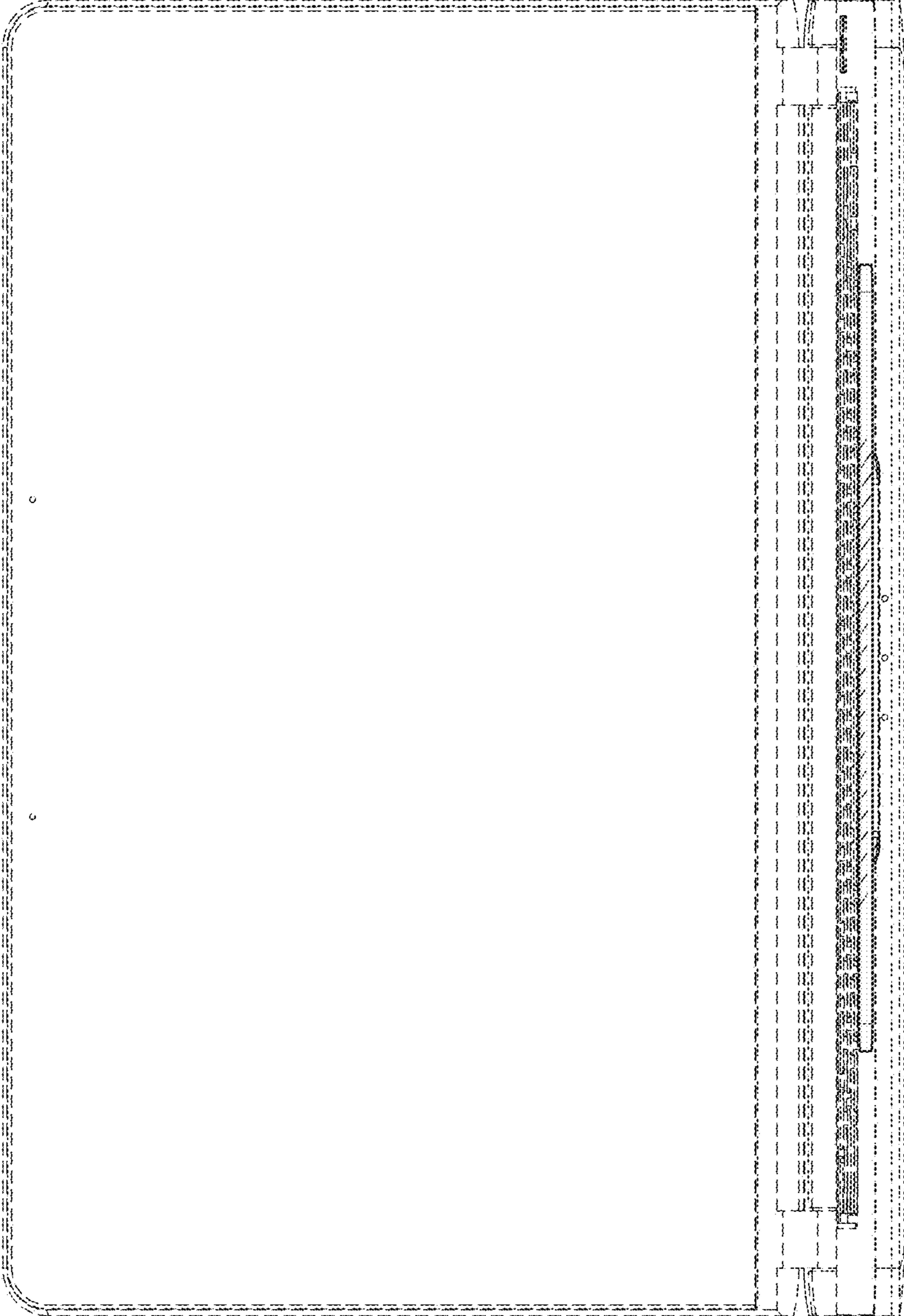


FIG. 3

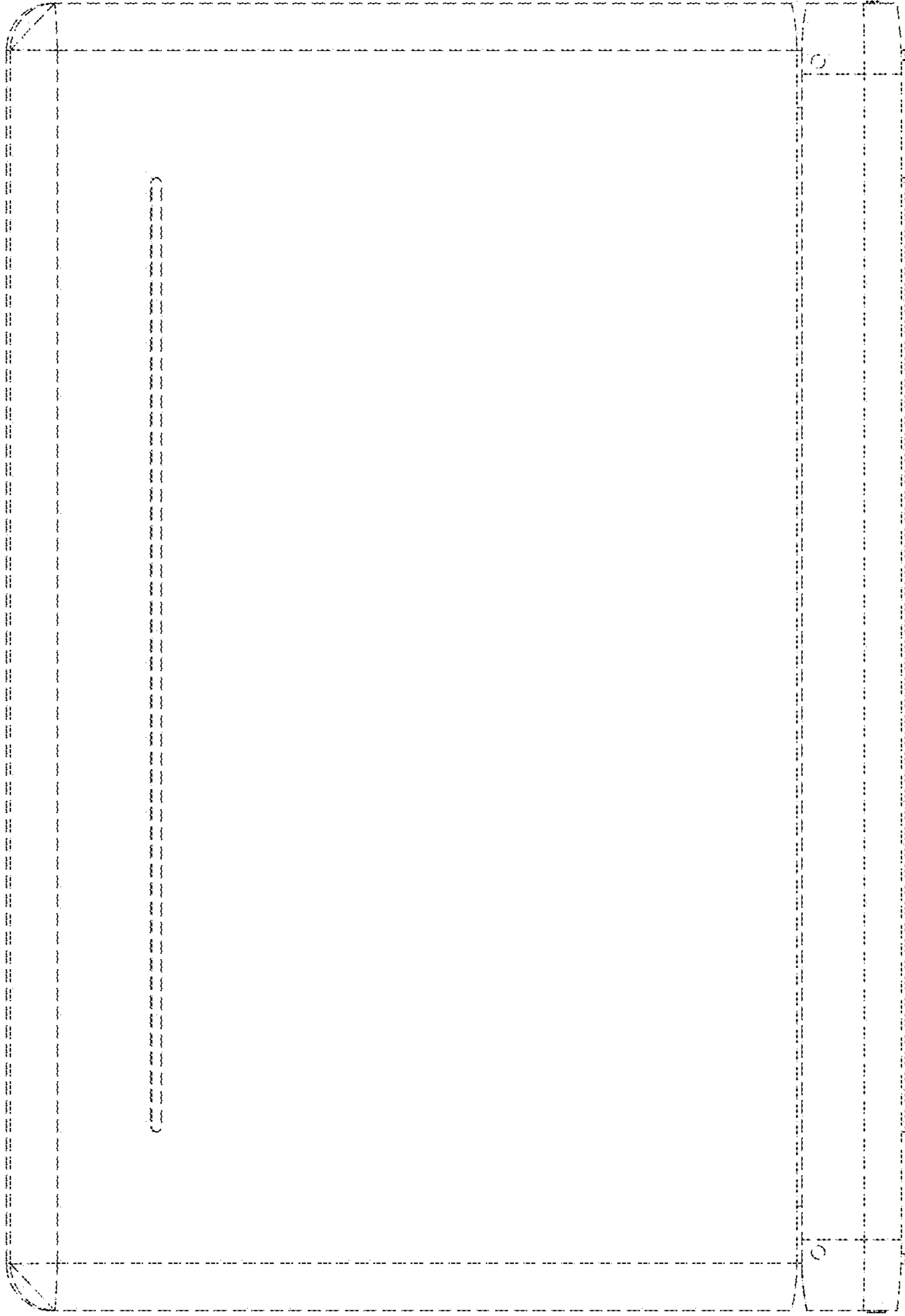


FIG. 4

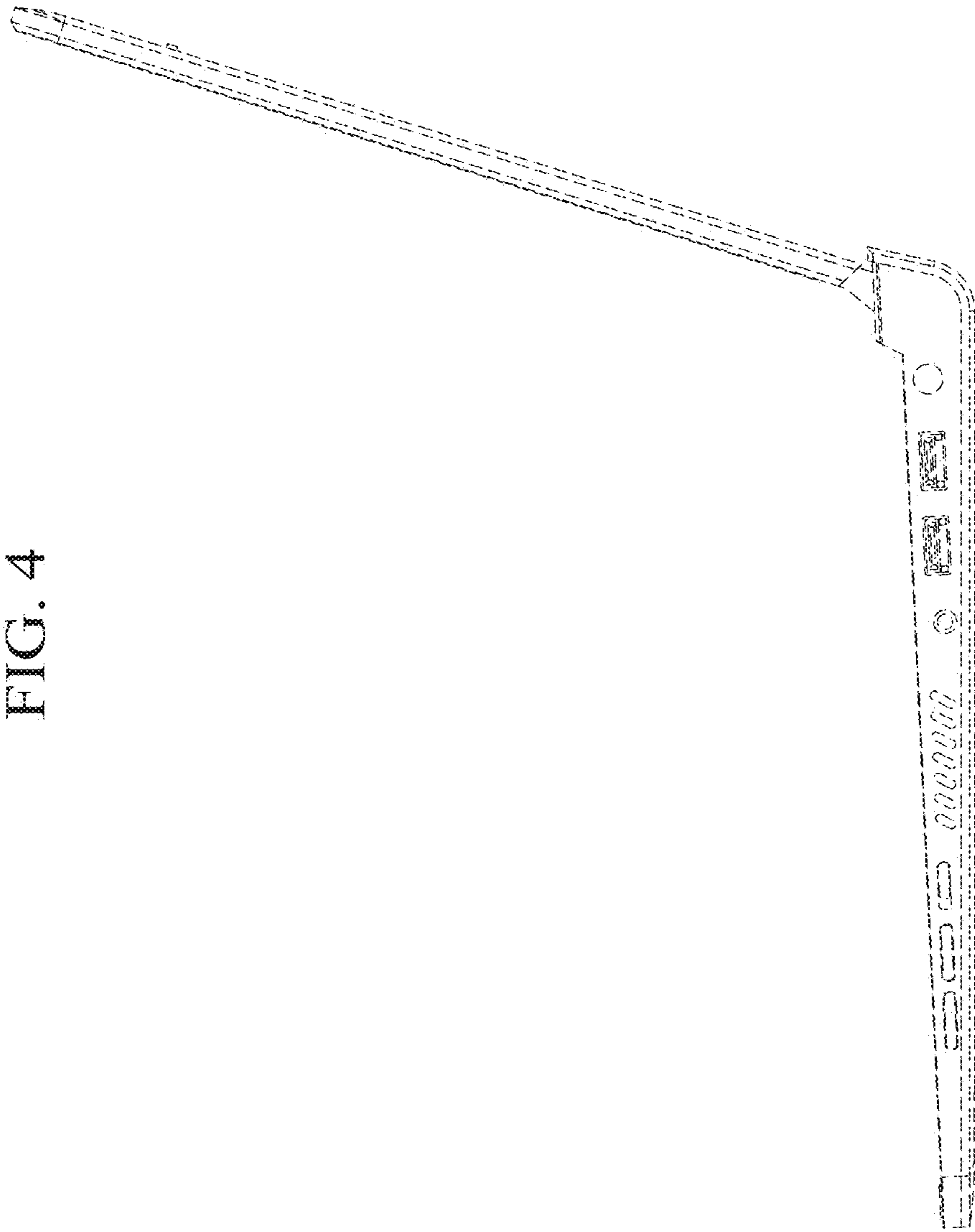


FIG. 5

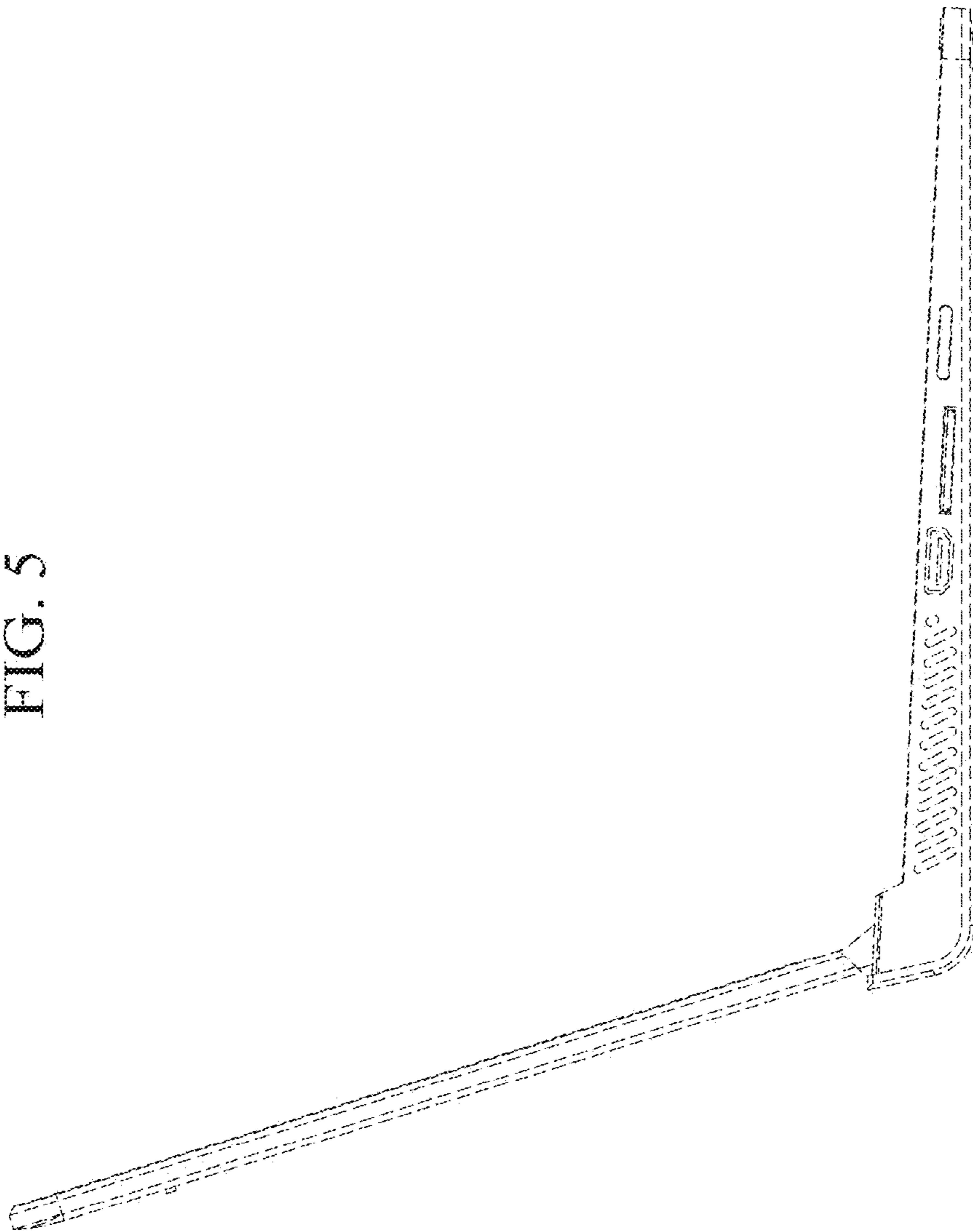


FIG. 6

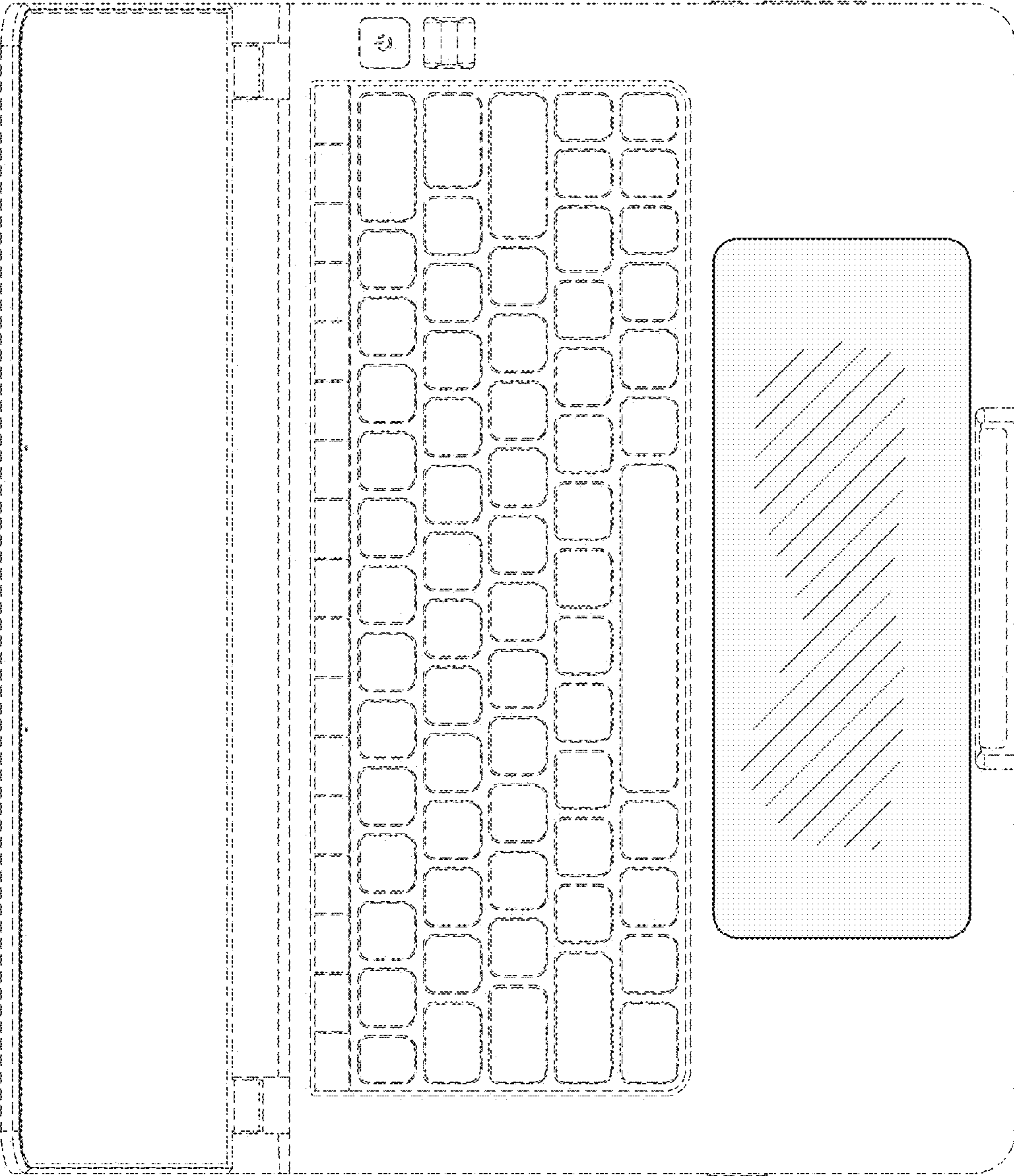
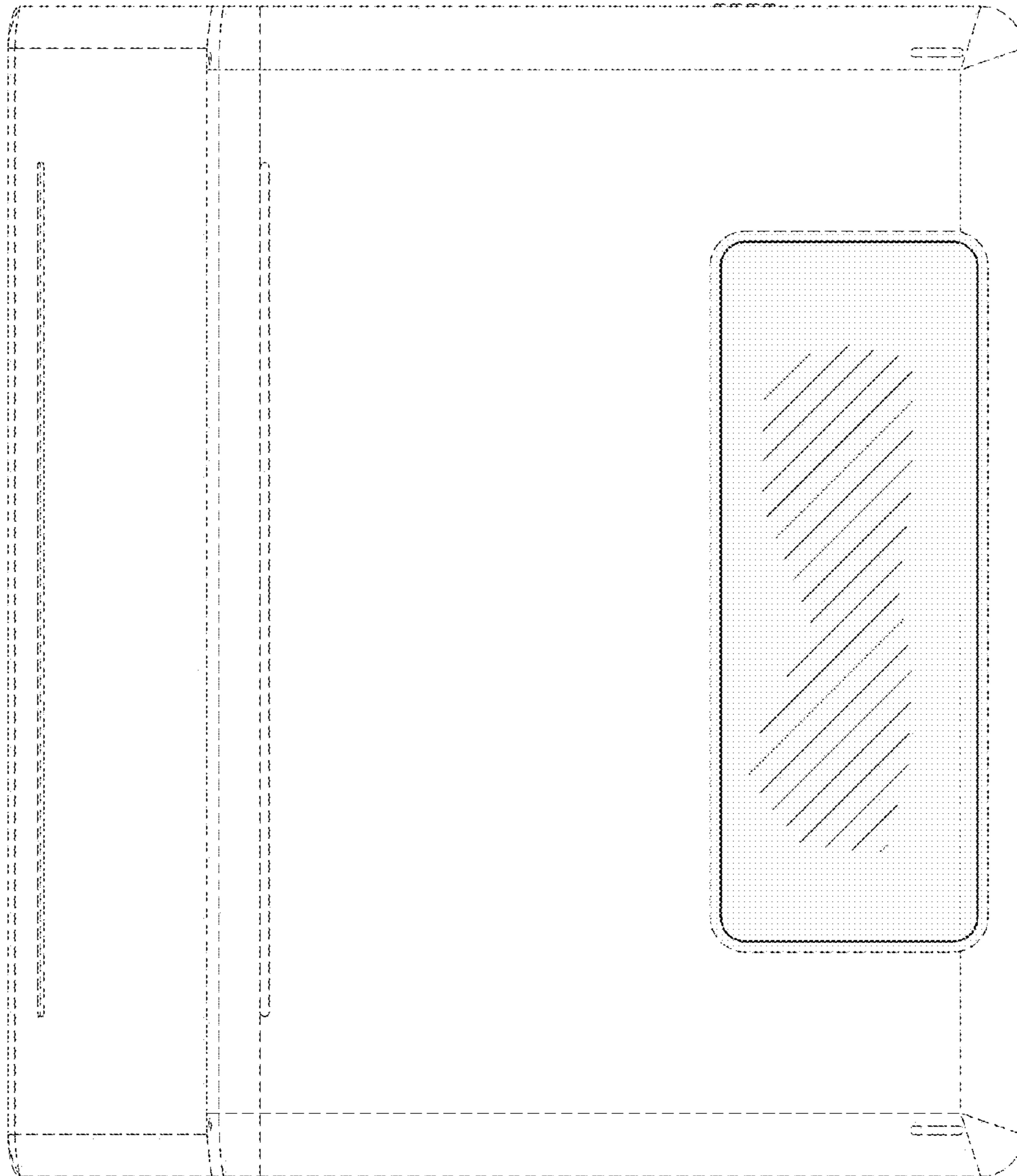




FIG. 7



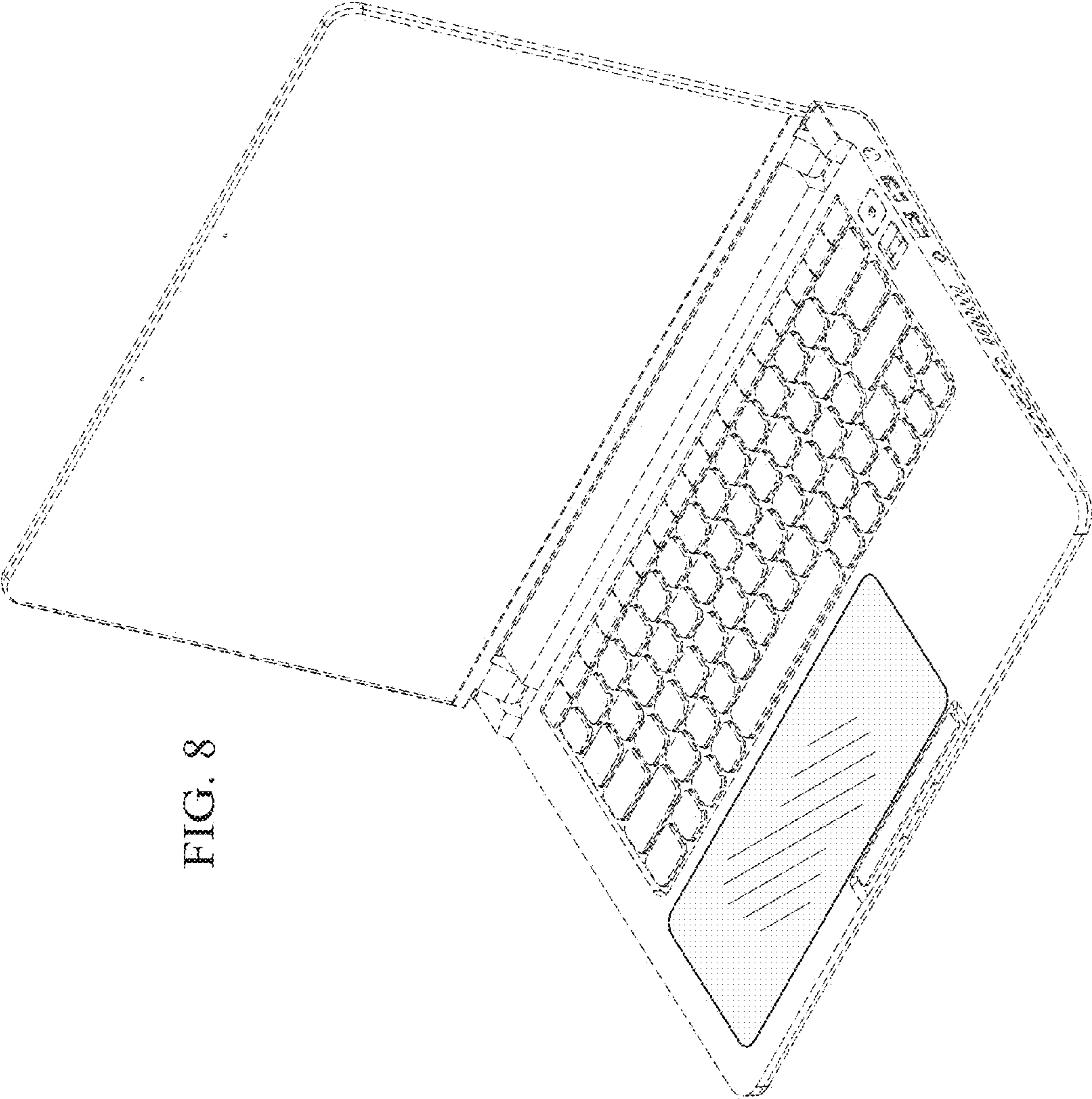


FIG. 8

FIG. 9

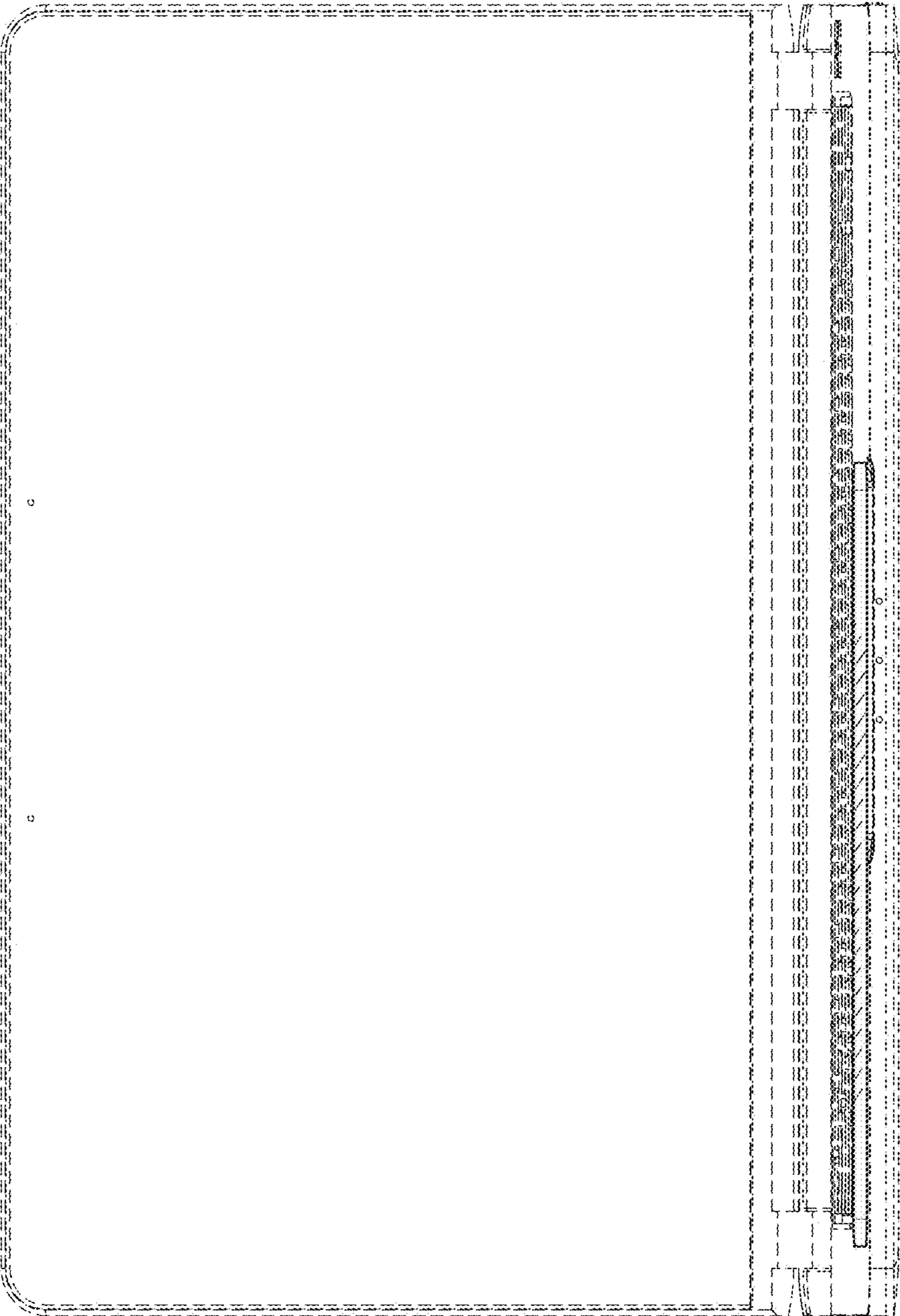


FIG. 10

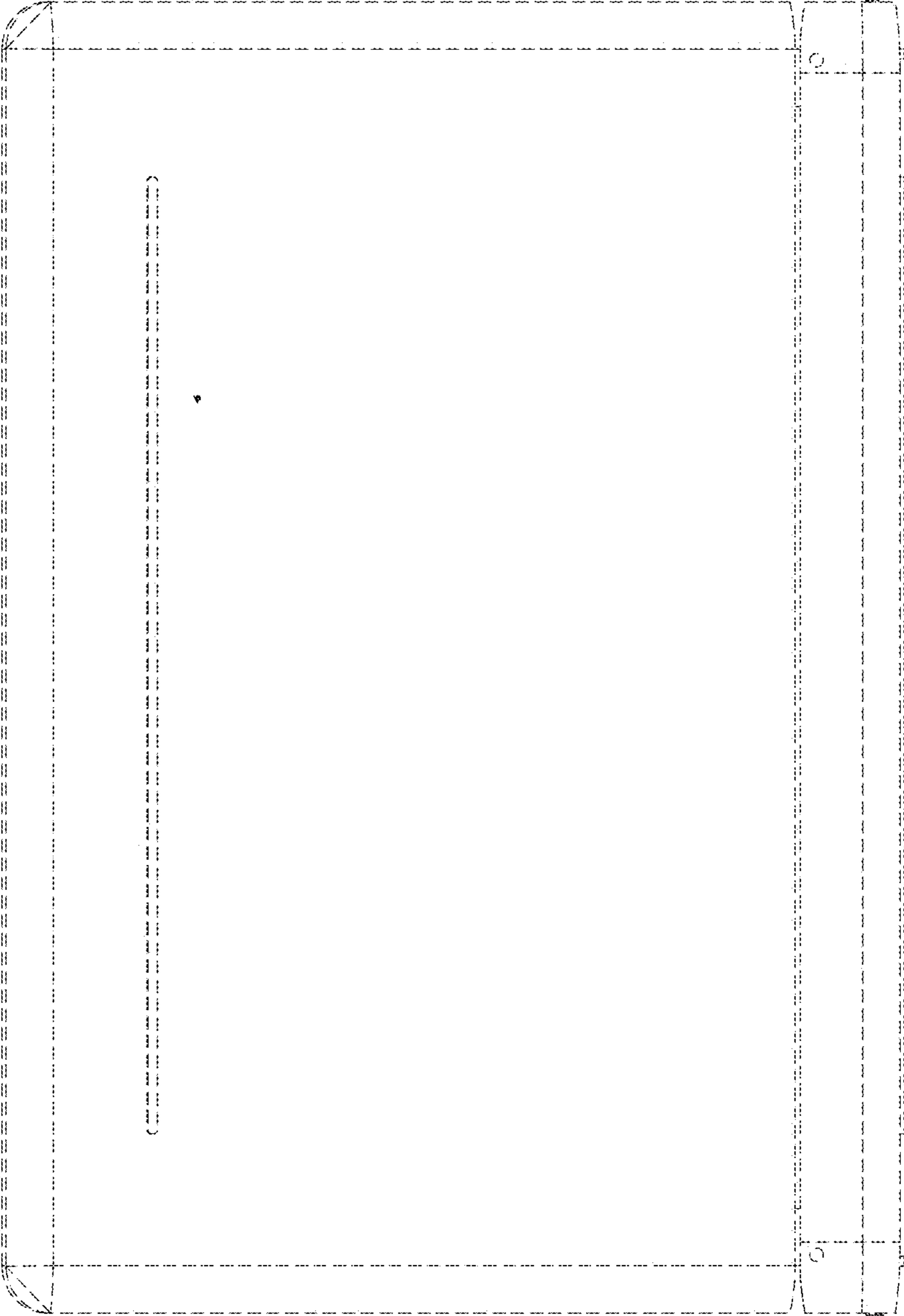


FIG. 11

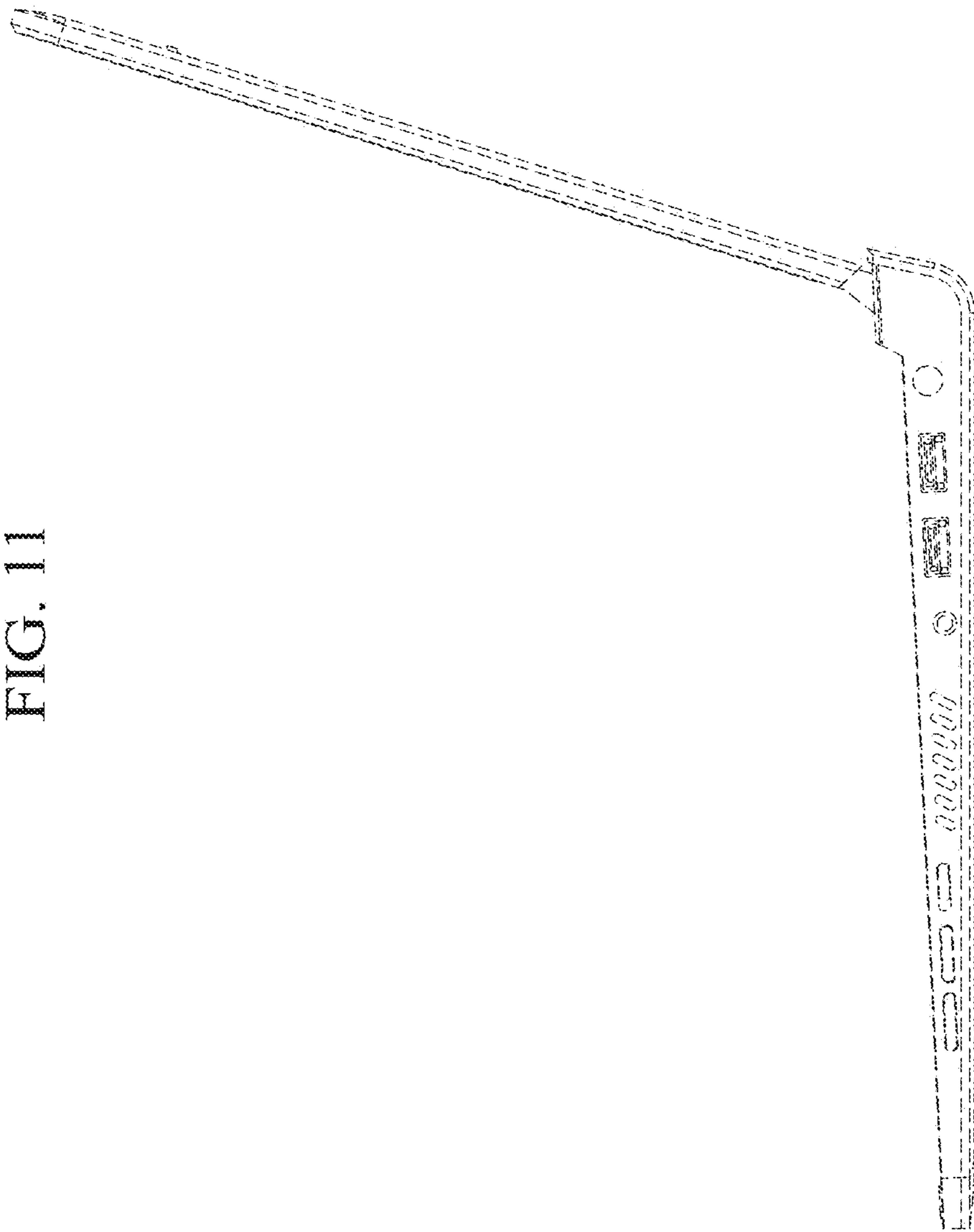


FIG. 12

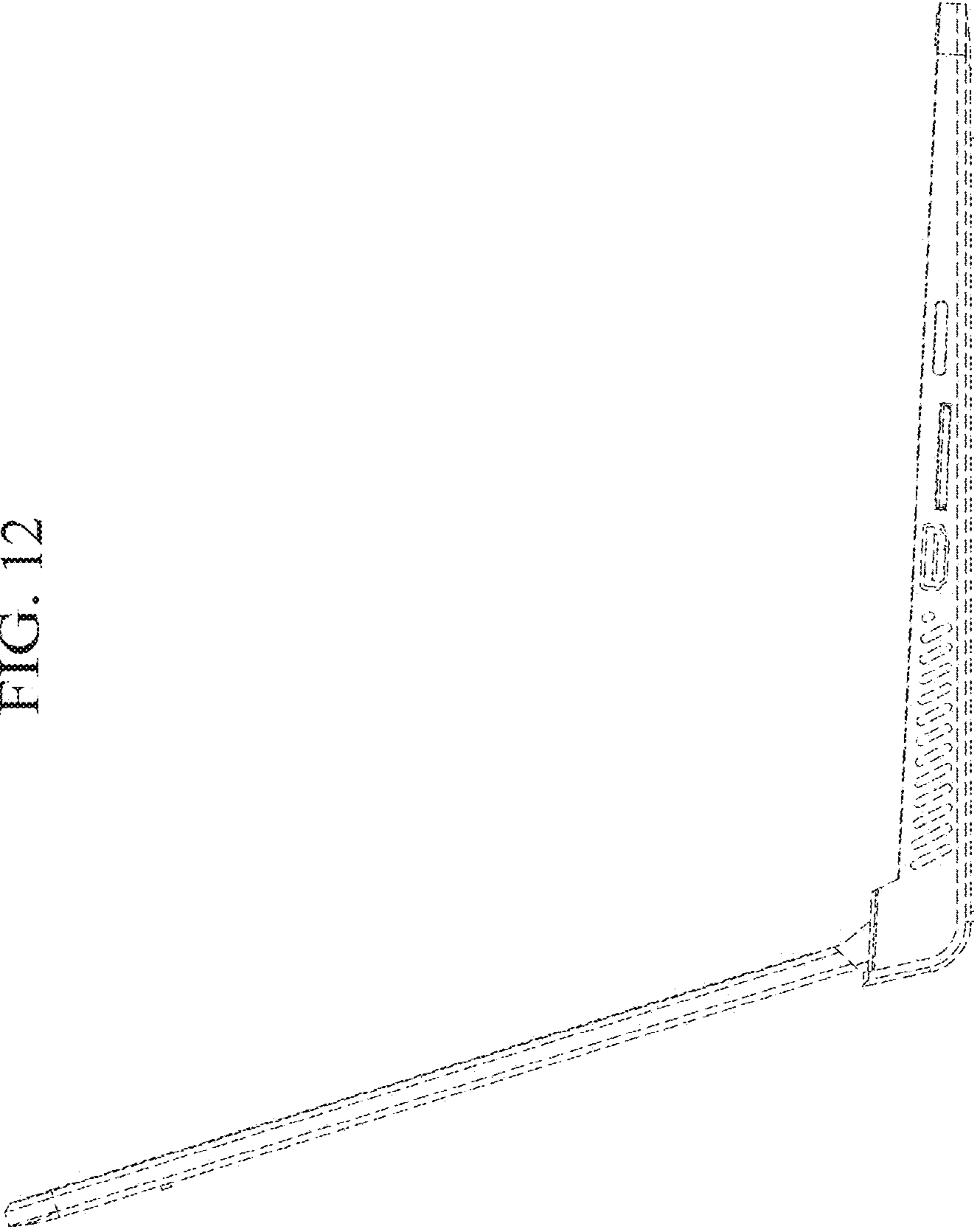


FIG. 13

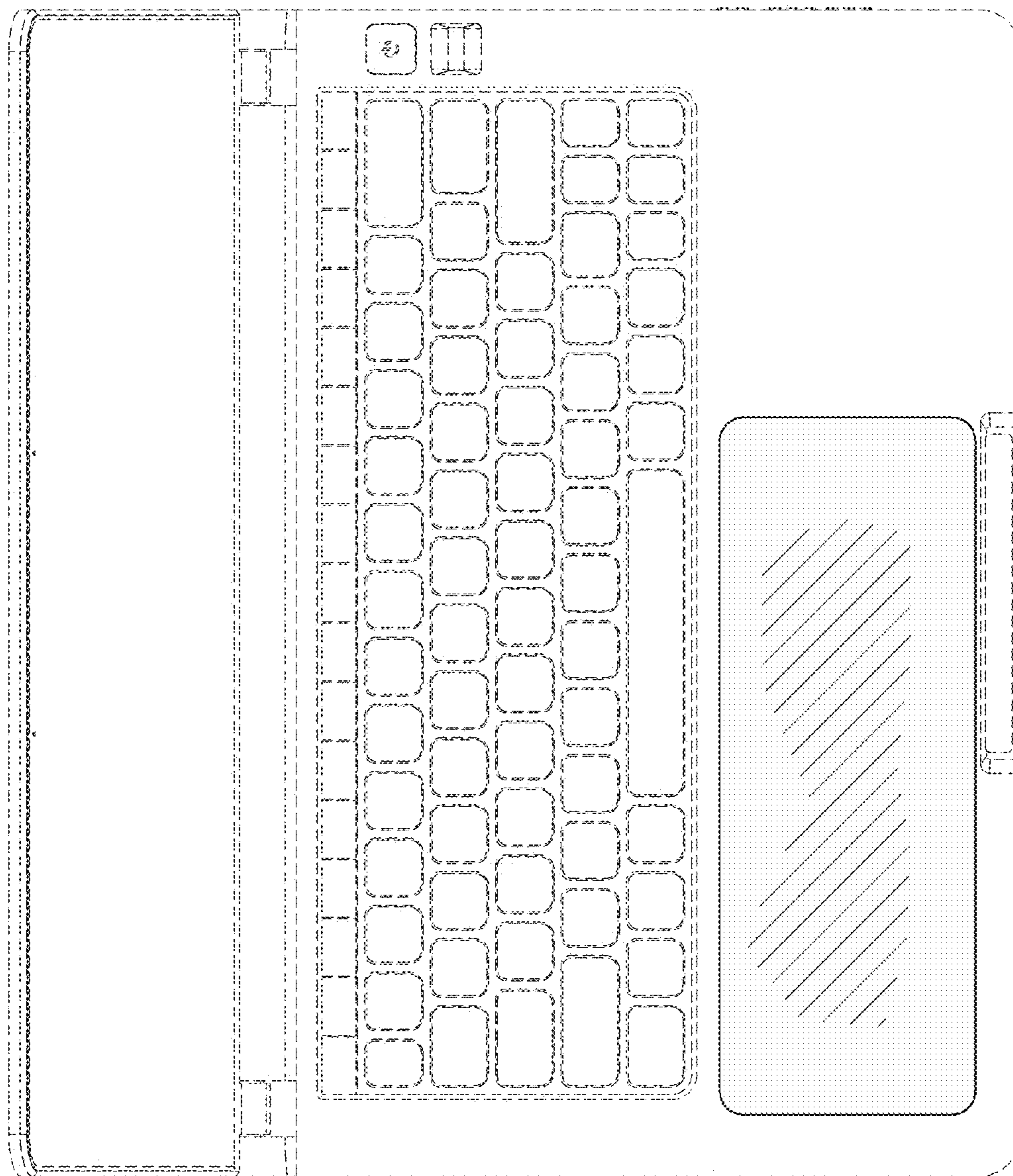
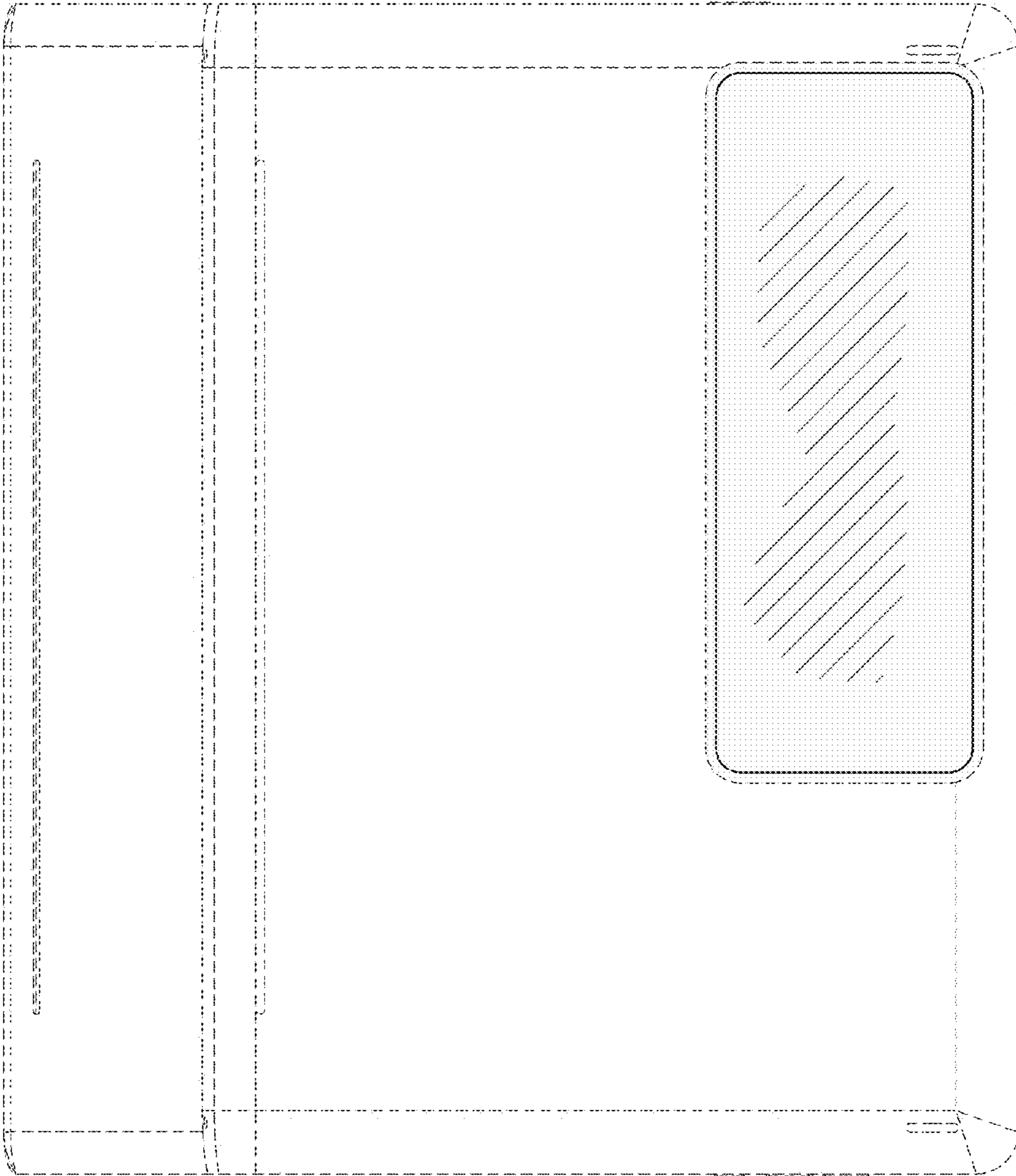


FIG. 14





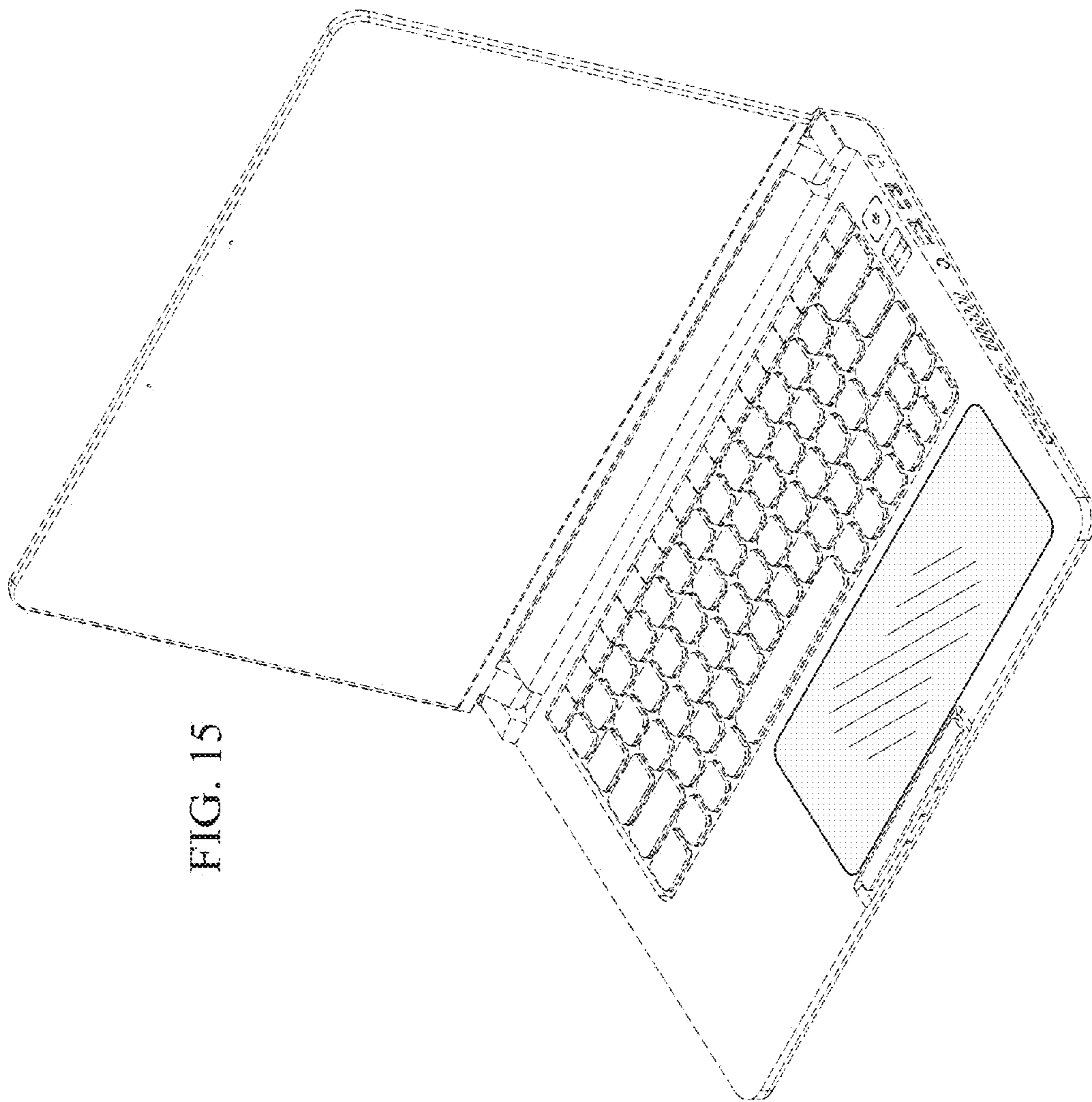


FIG. 15

FIG. 16

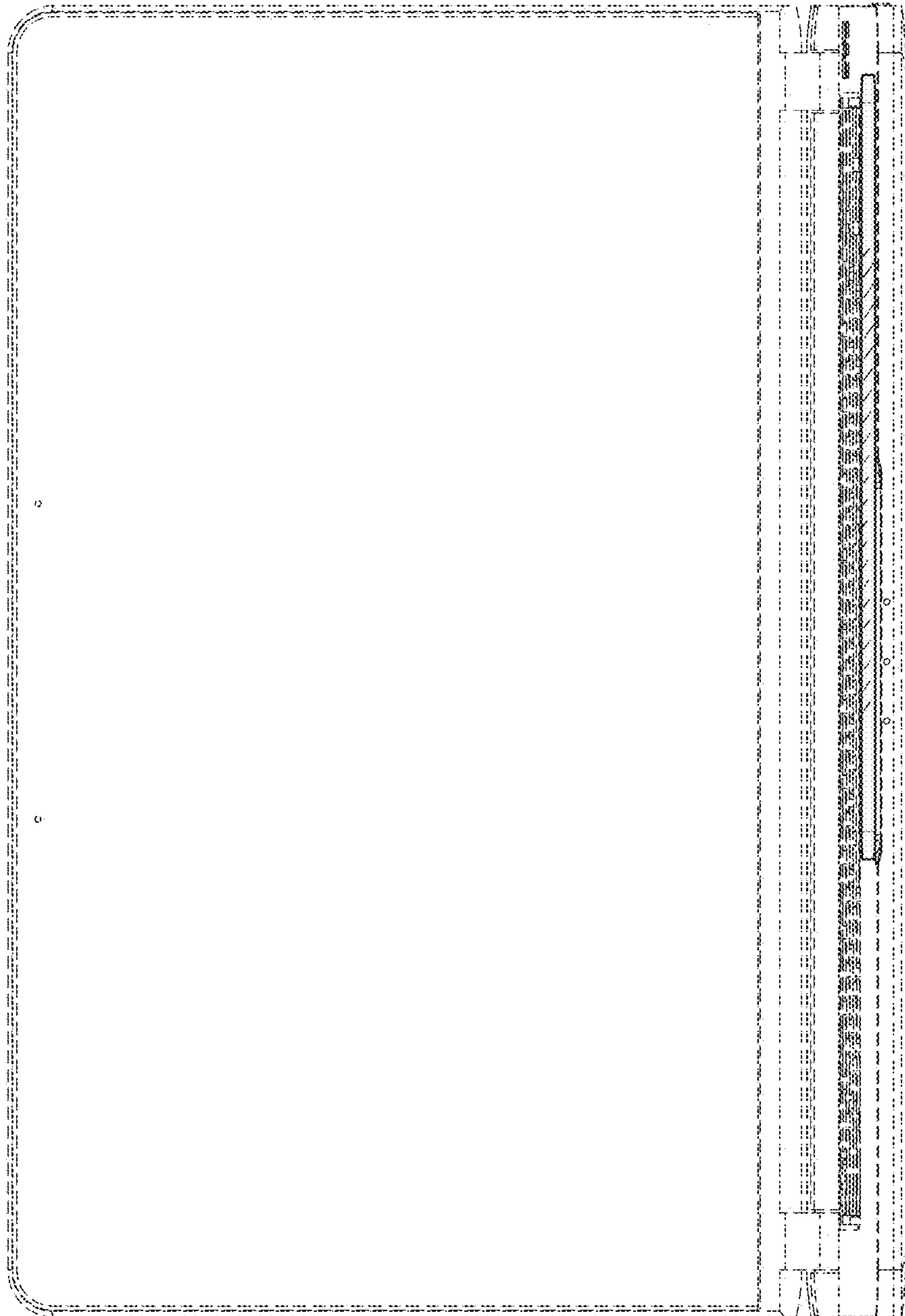
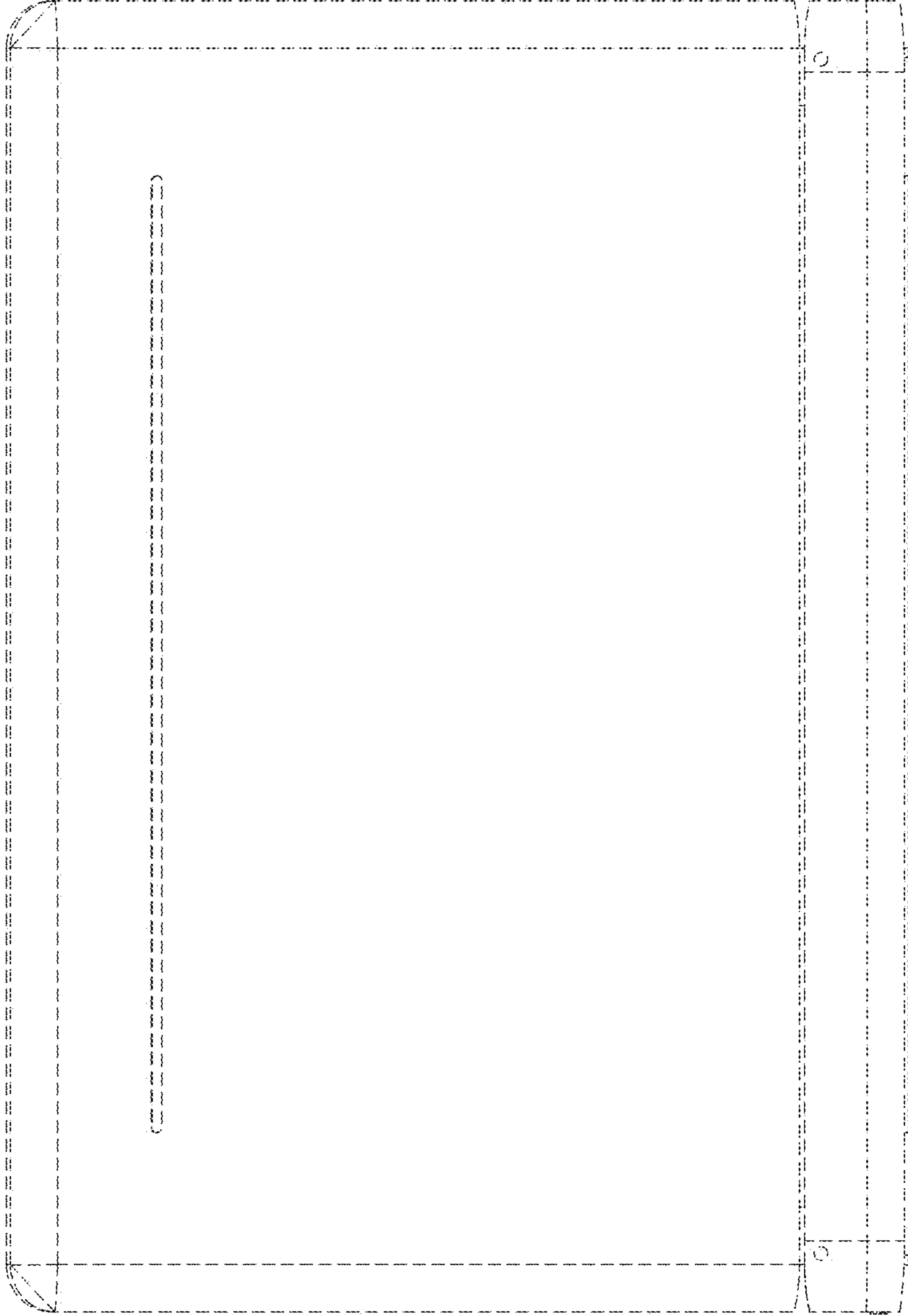


FIG. 17



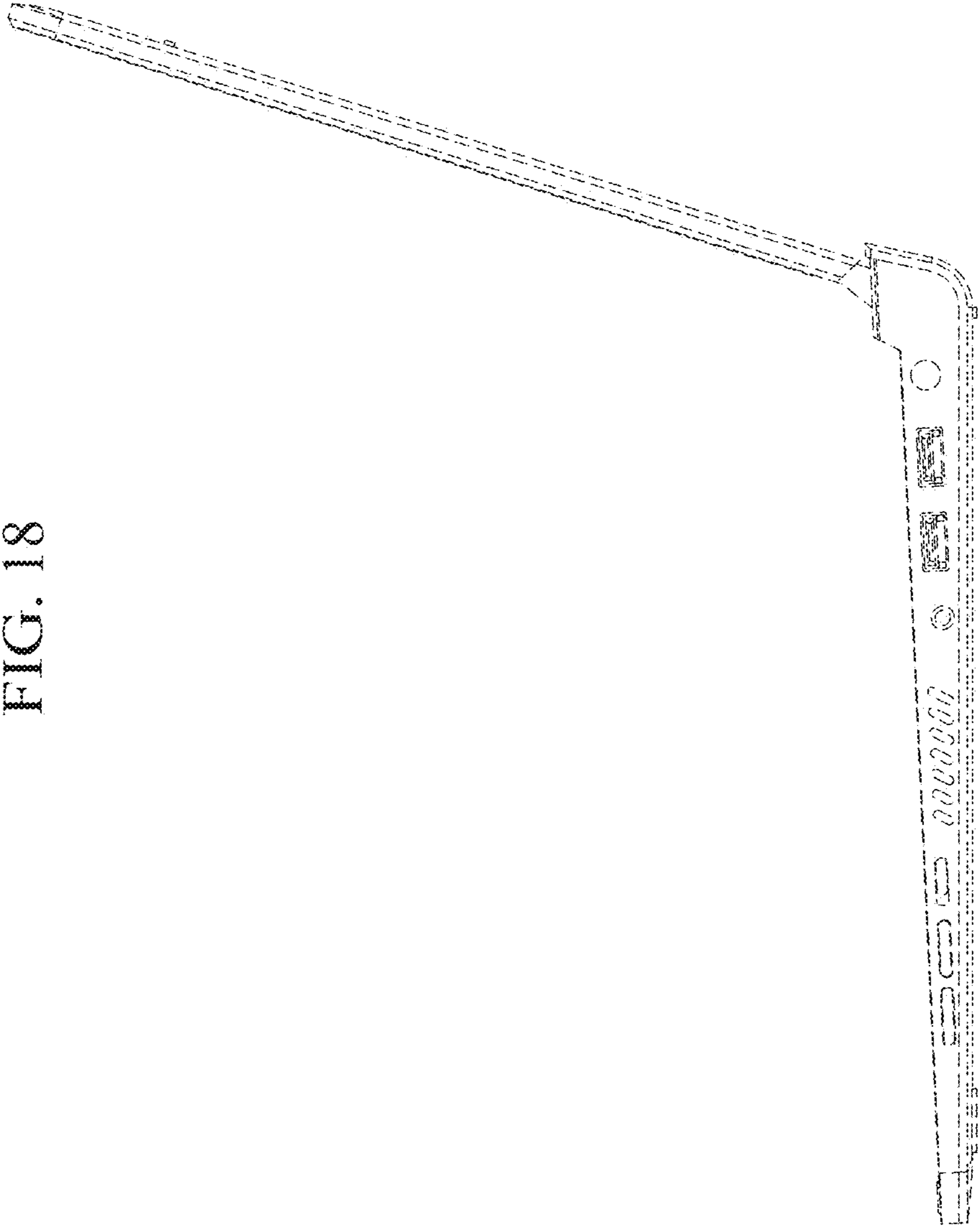


FIG. 18

FIG. 19

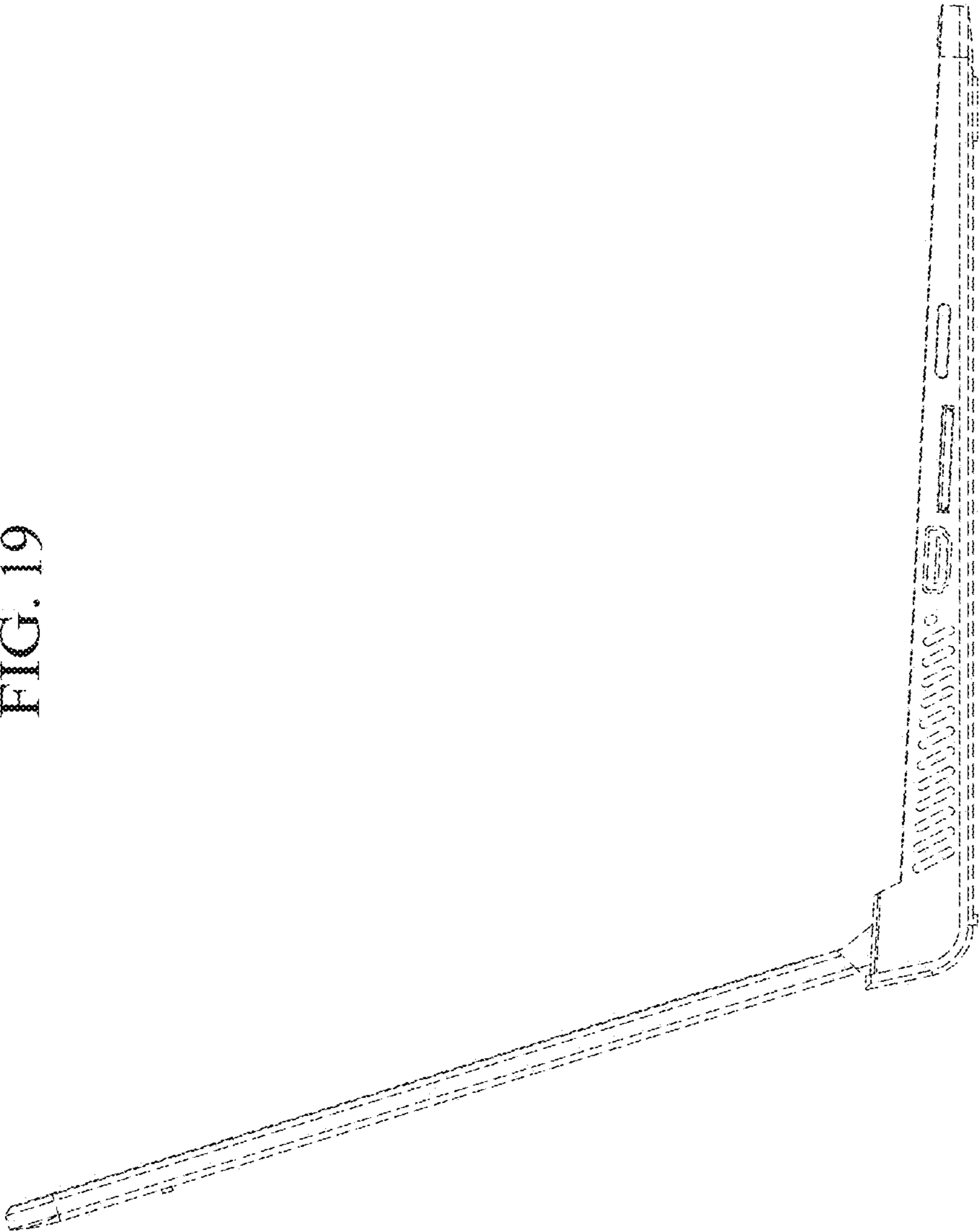


FIG. 20

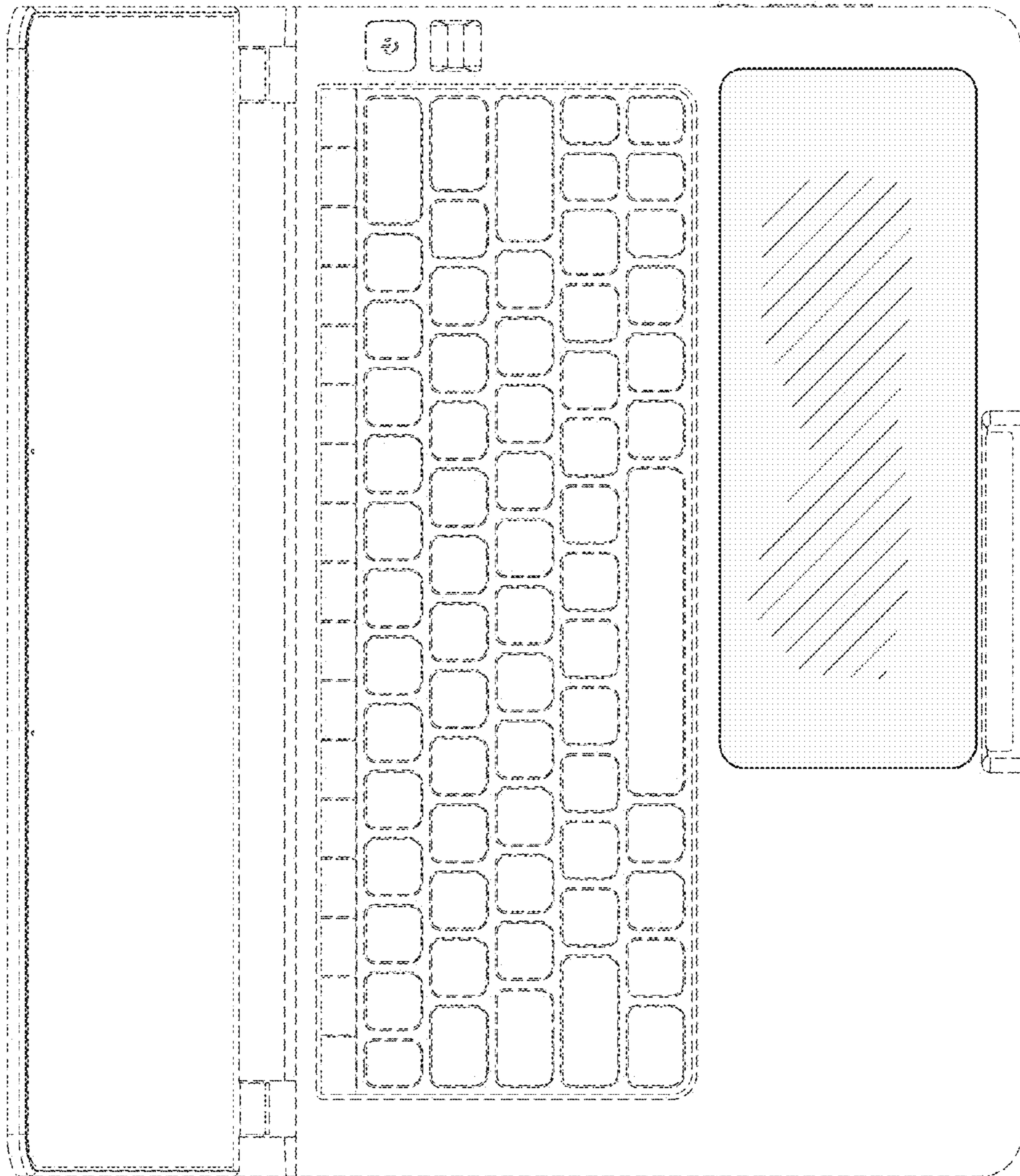
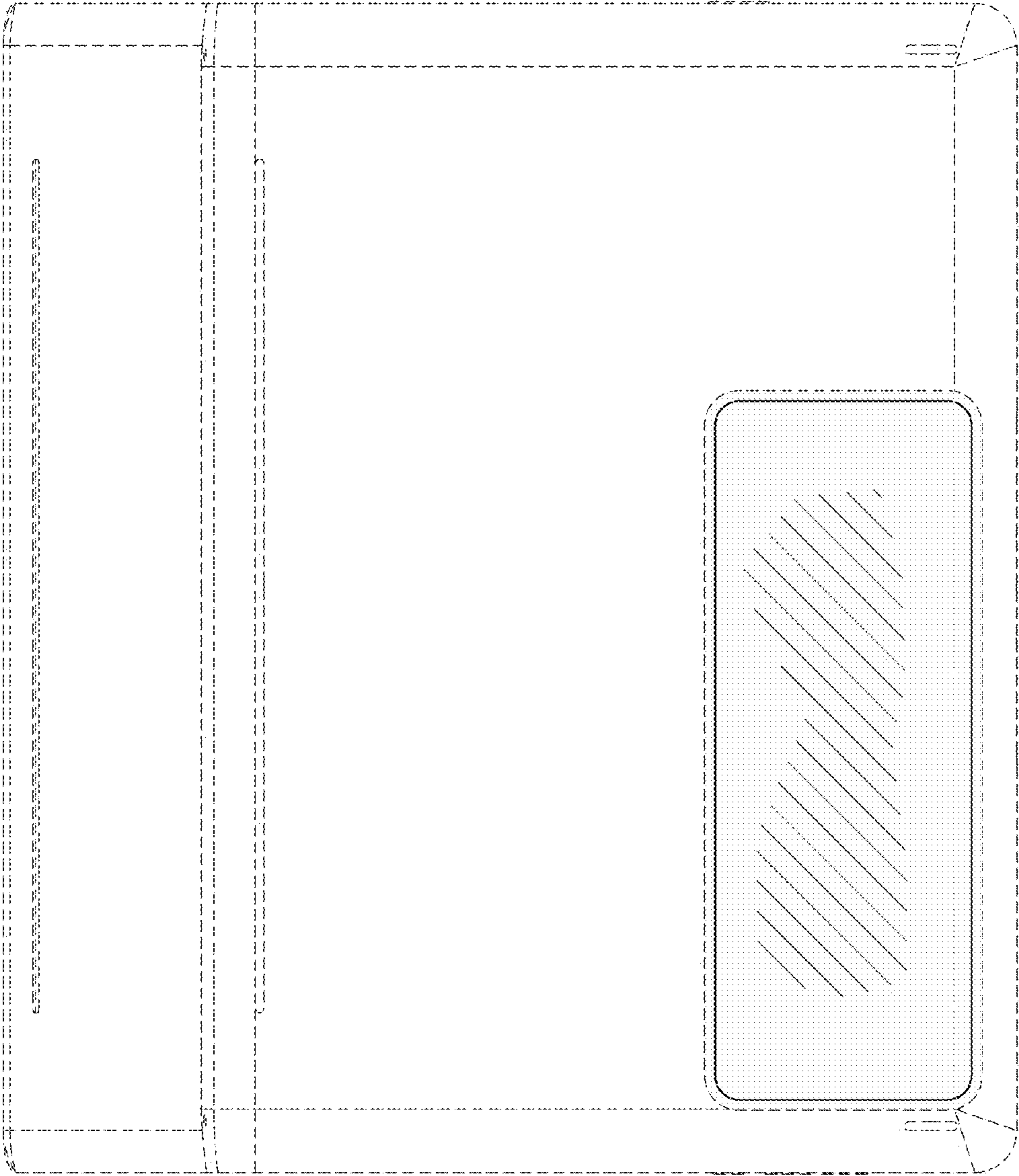


FIG. 21



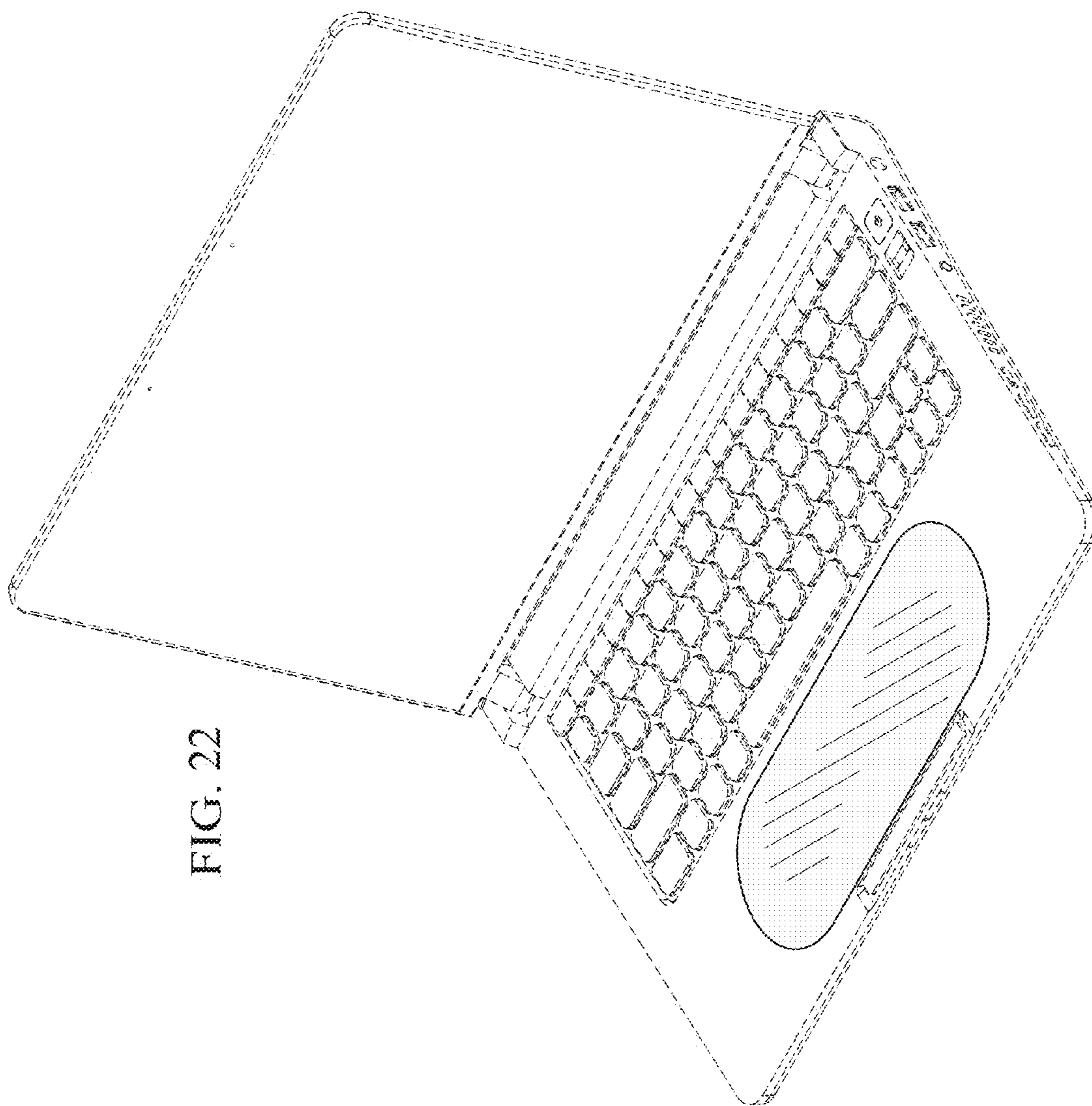


FIG. 22



FIG. 23

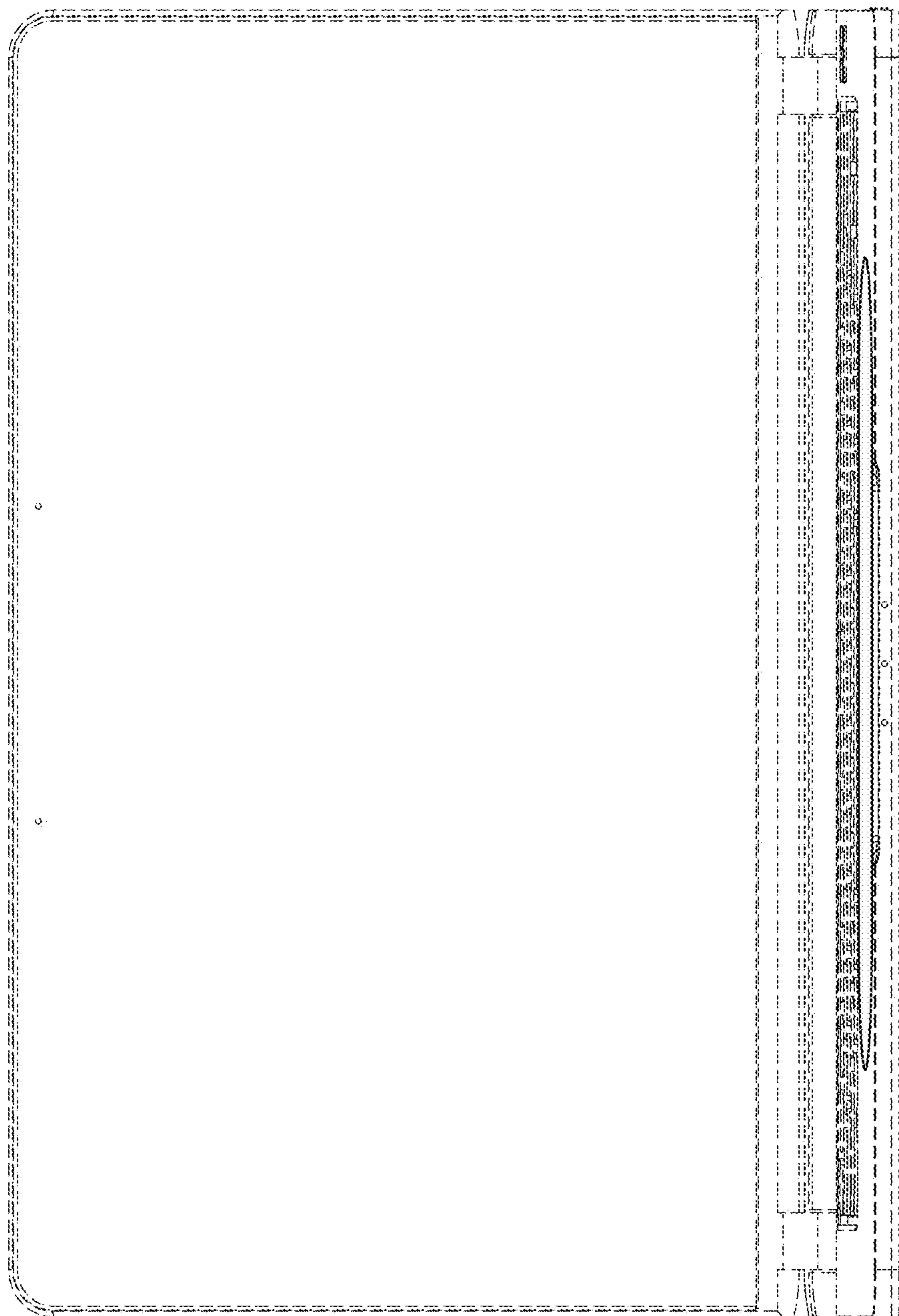


FIG. 24

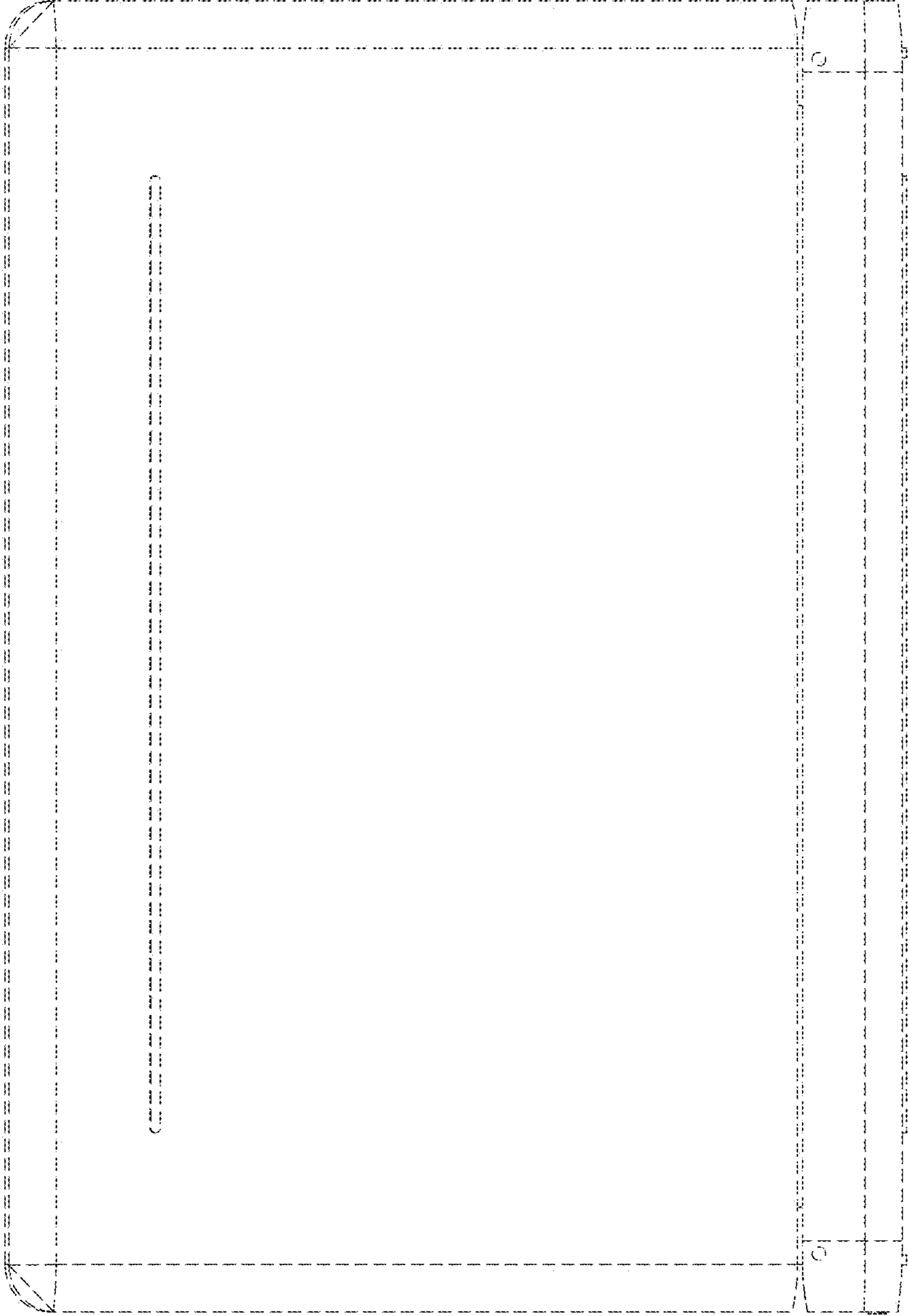


FIG. 25

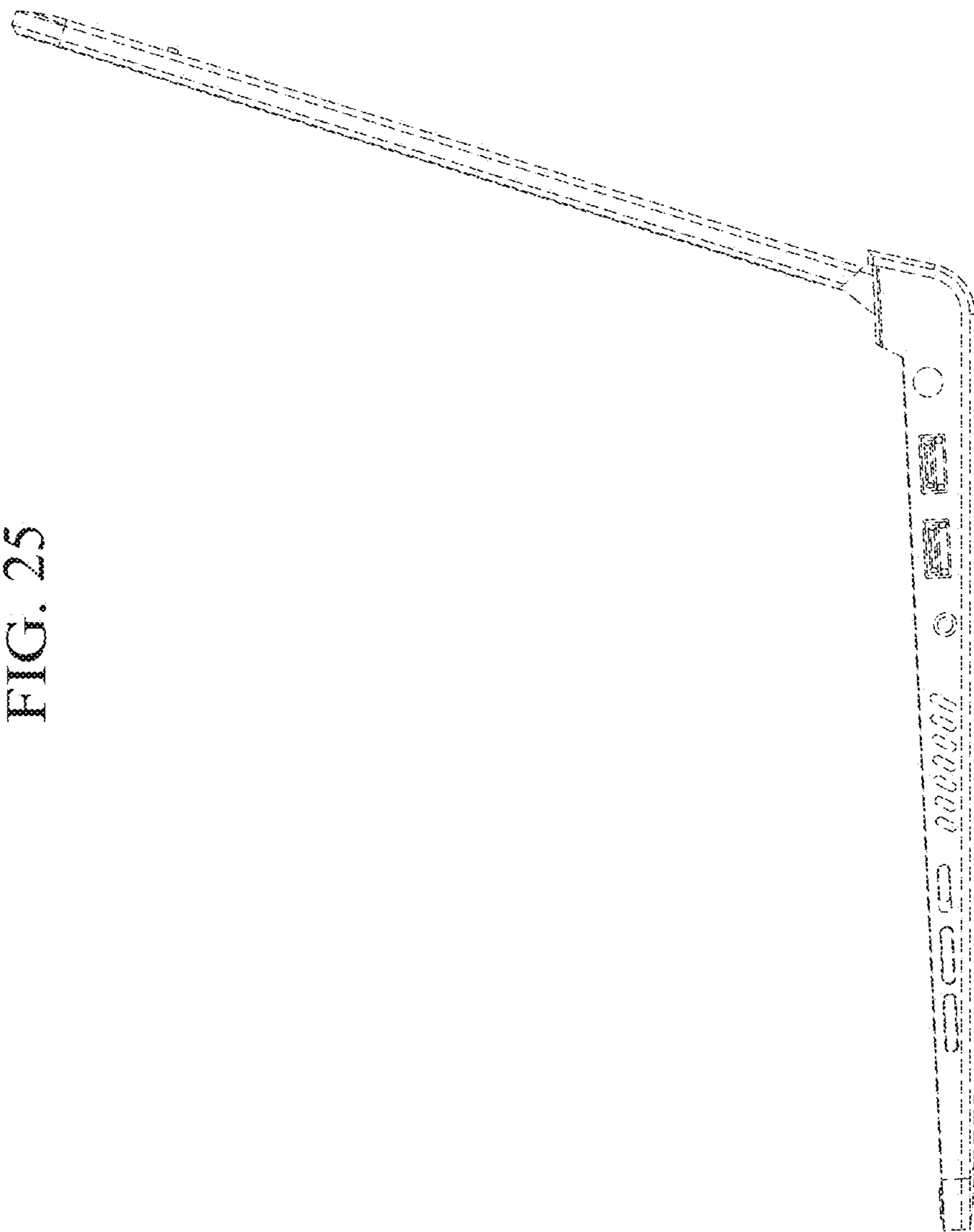


FIG. 26

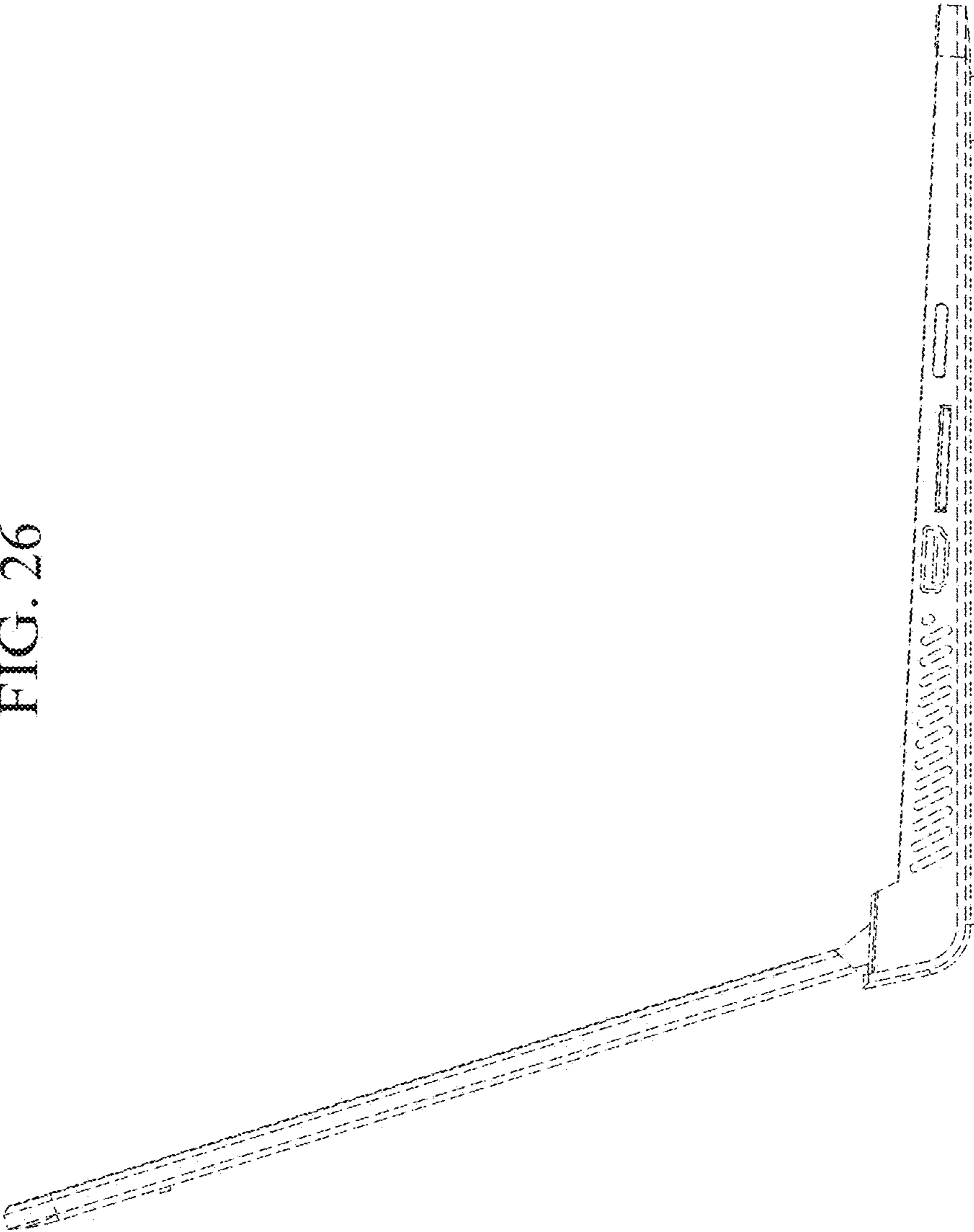


FIG. 27

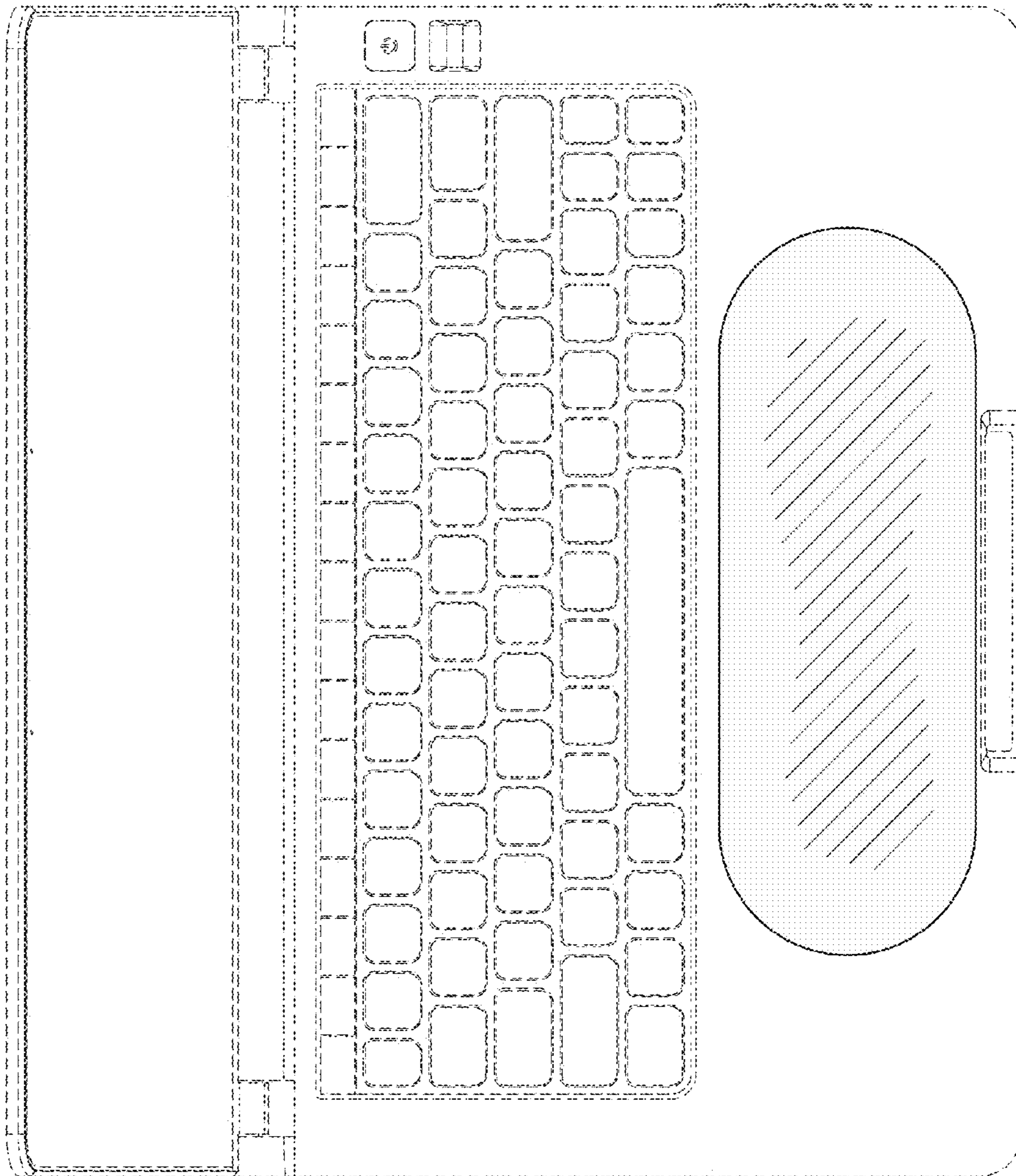
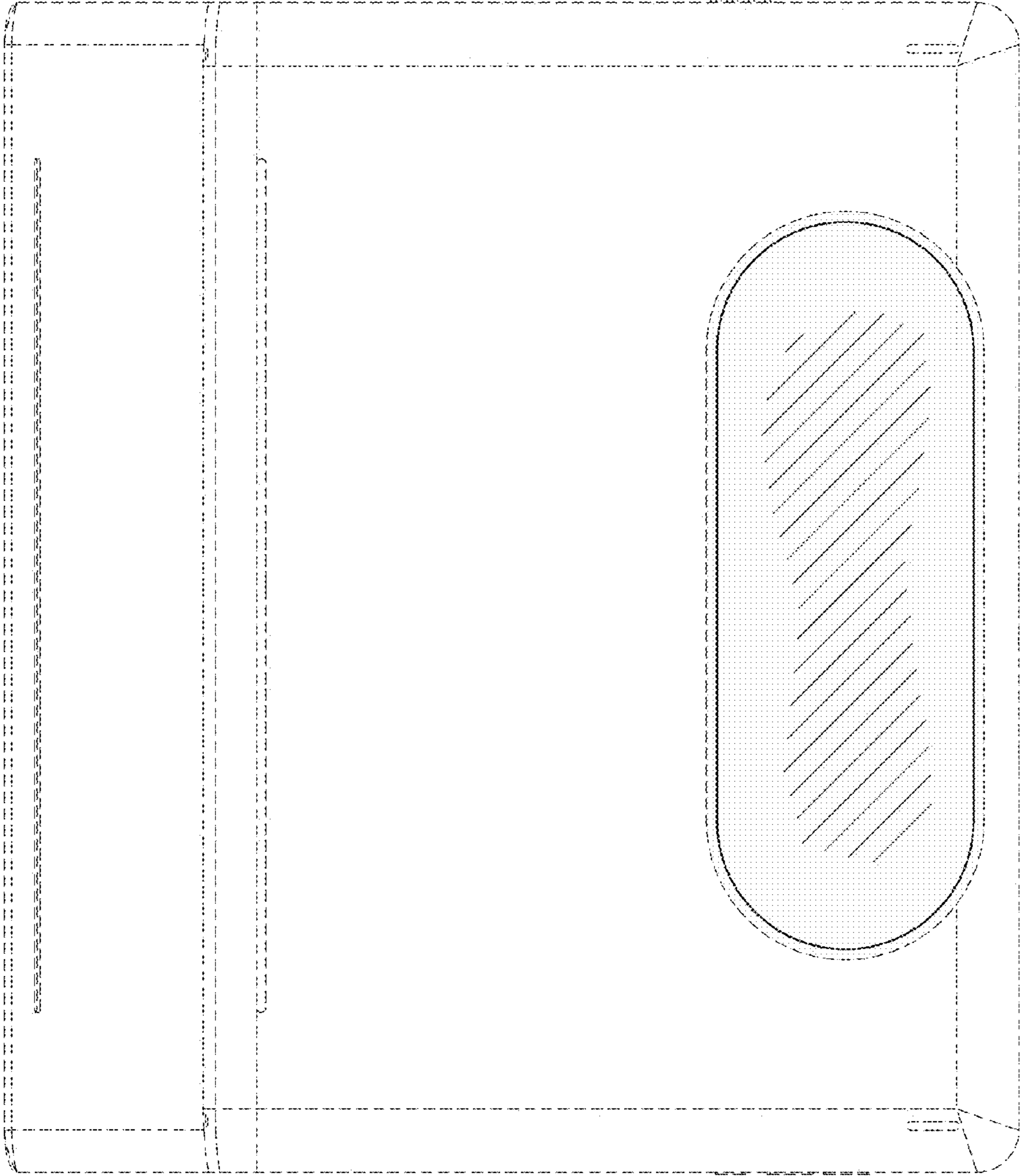


FIG. 28



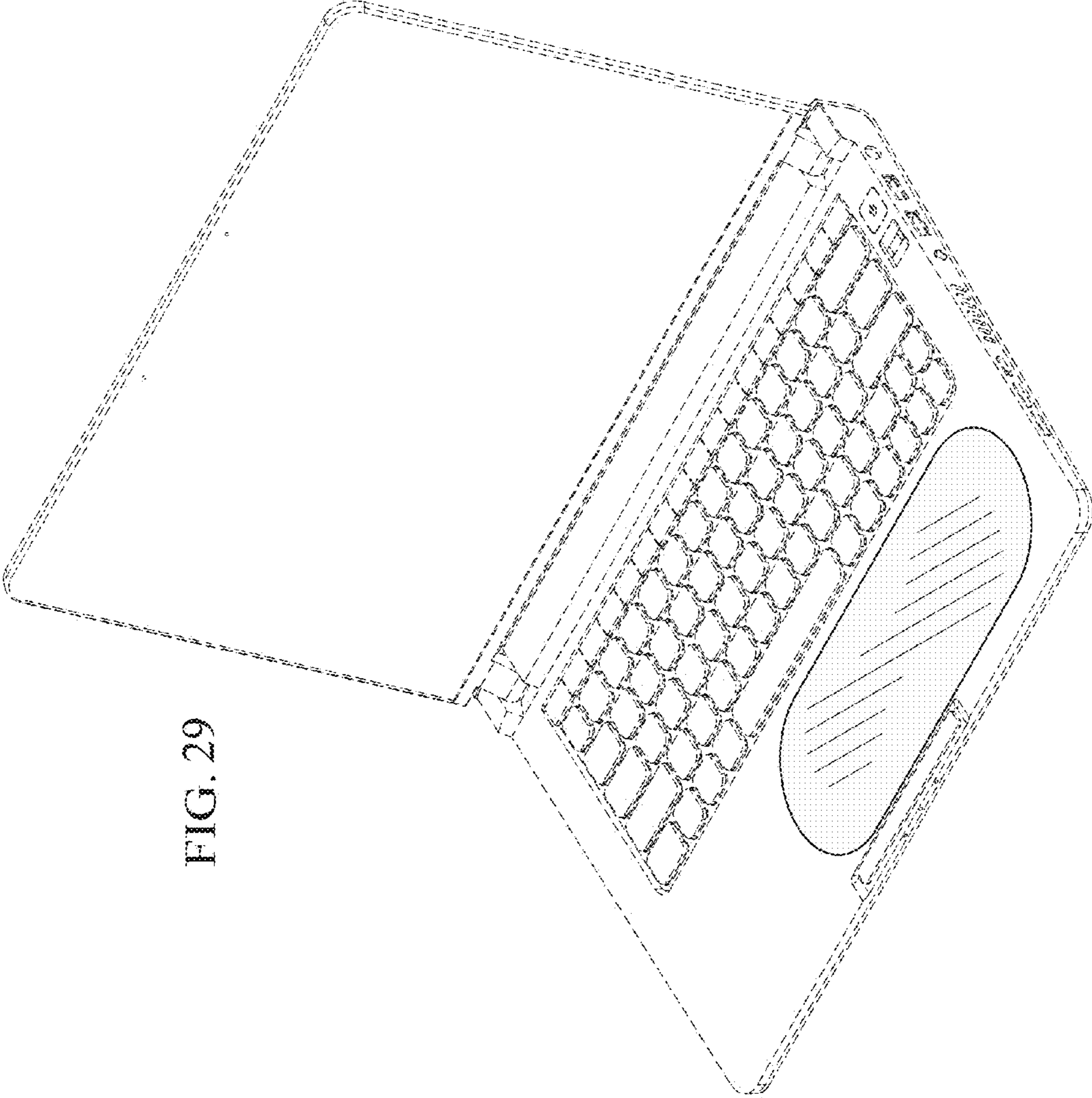


FIG. 29

FIG. 30

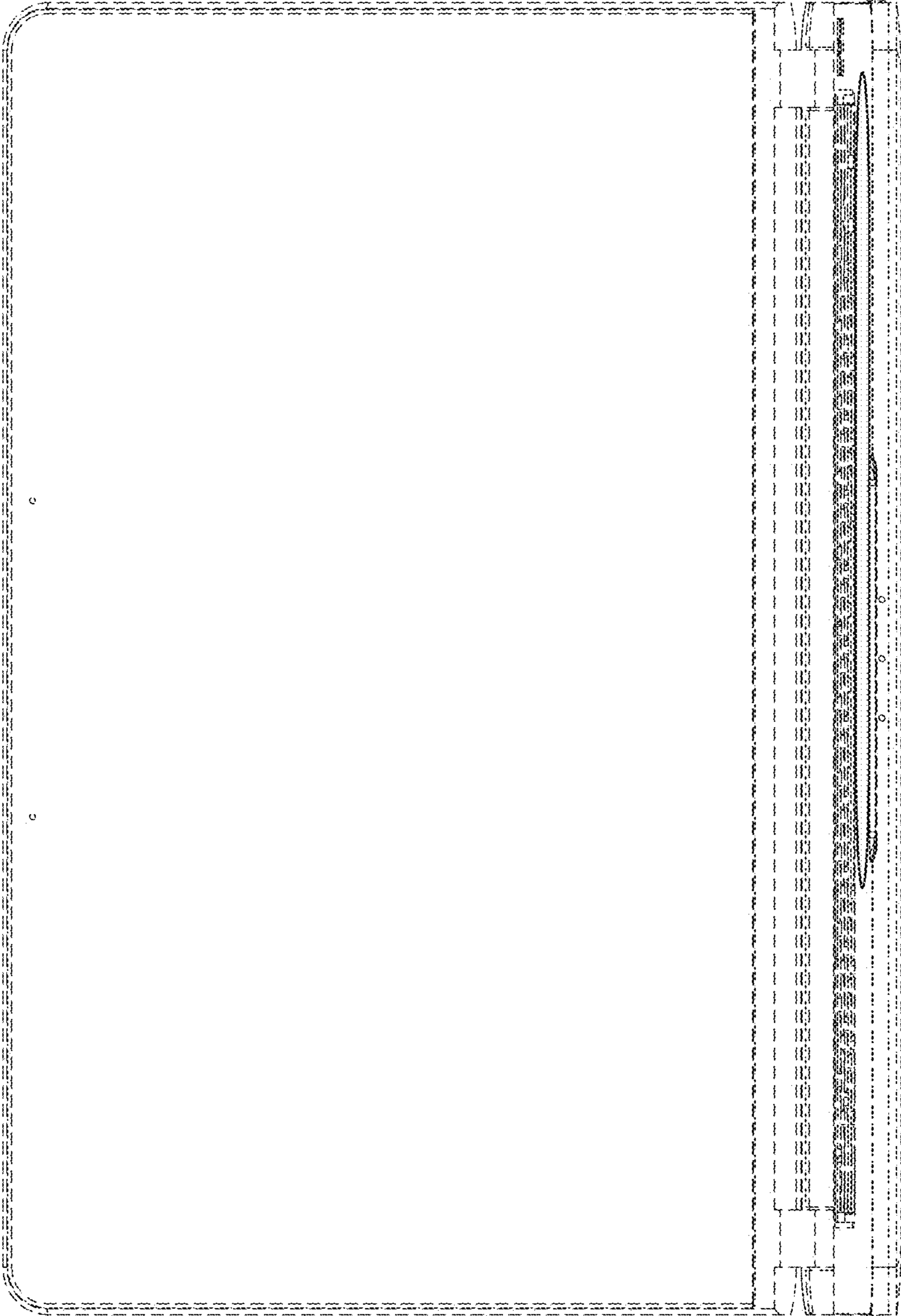




FIG. 31

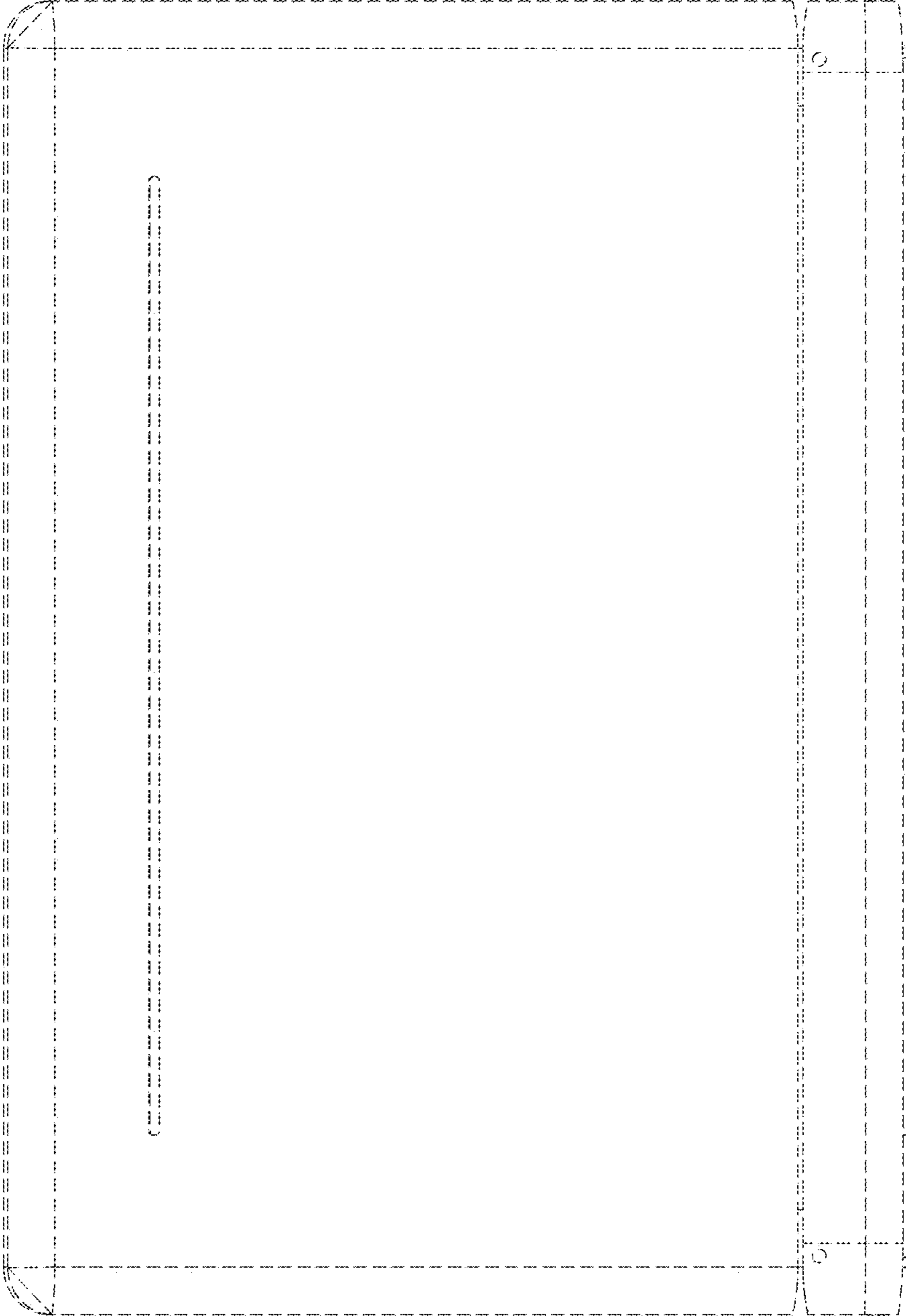


FIG. 32

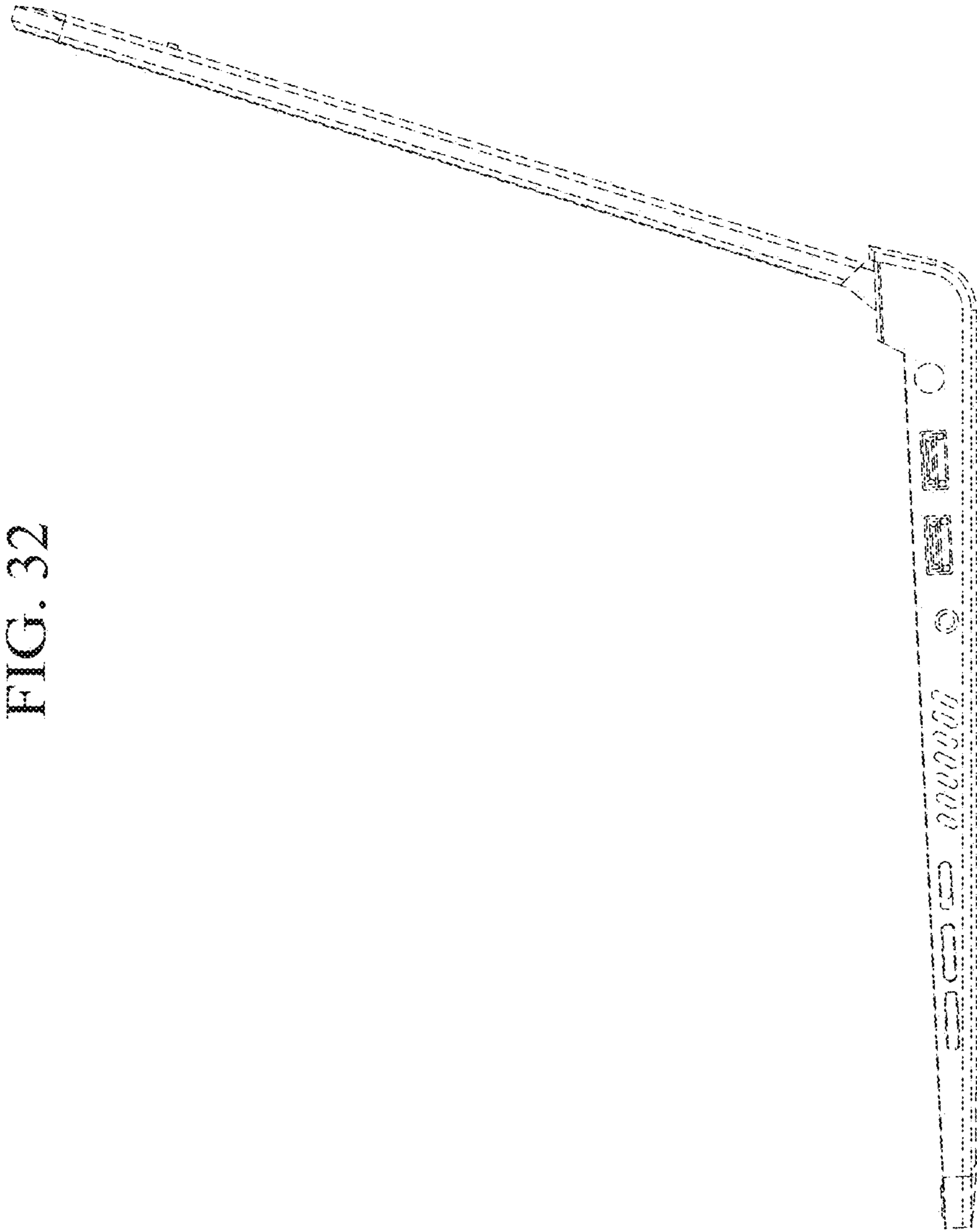


FIG. 33

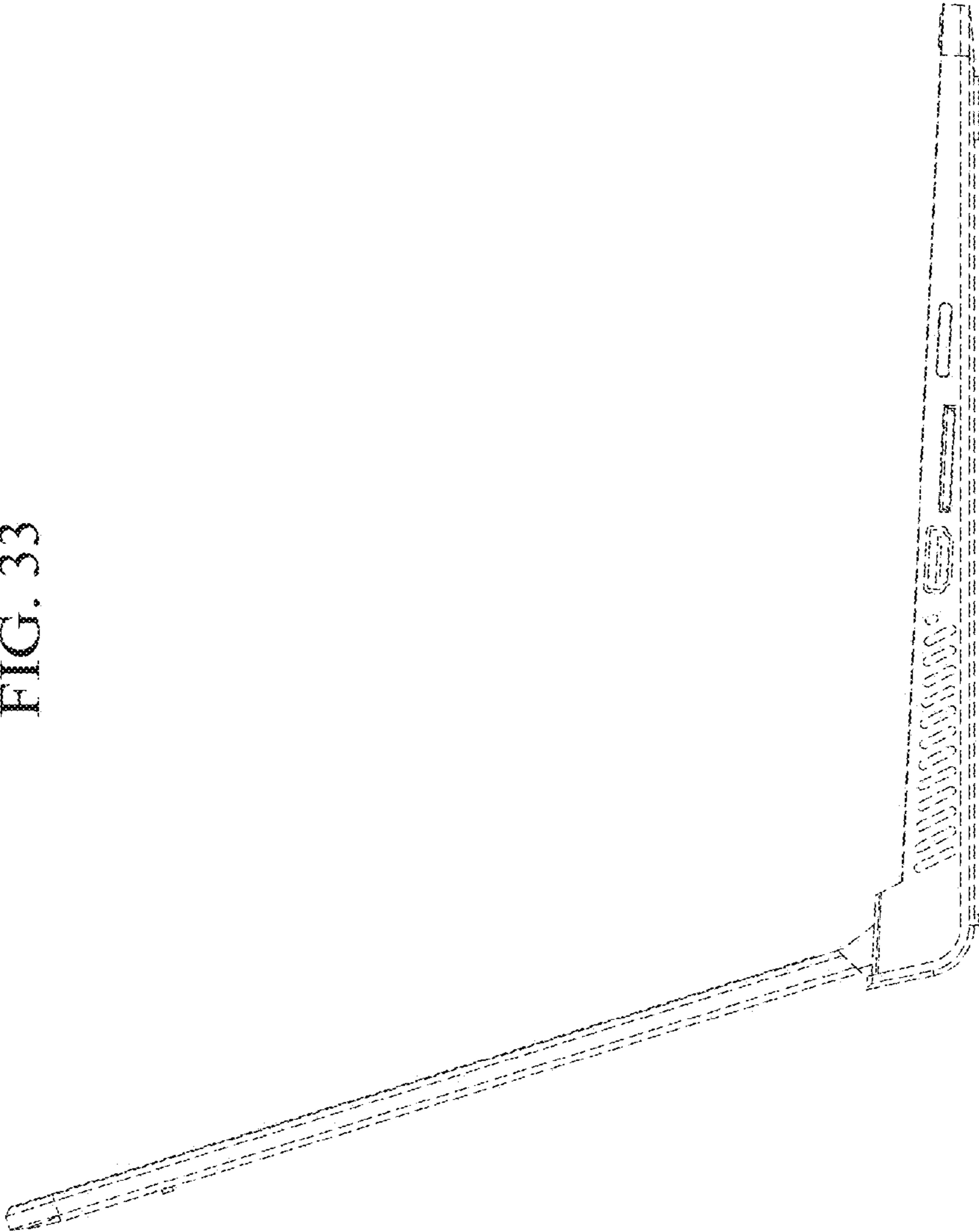


FIG. 34

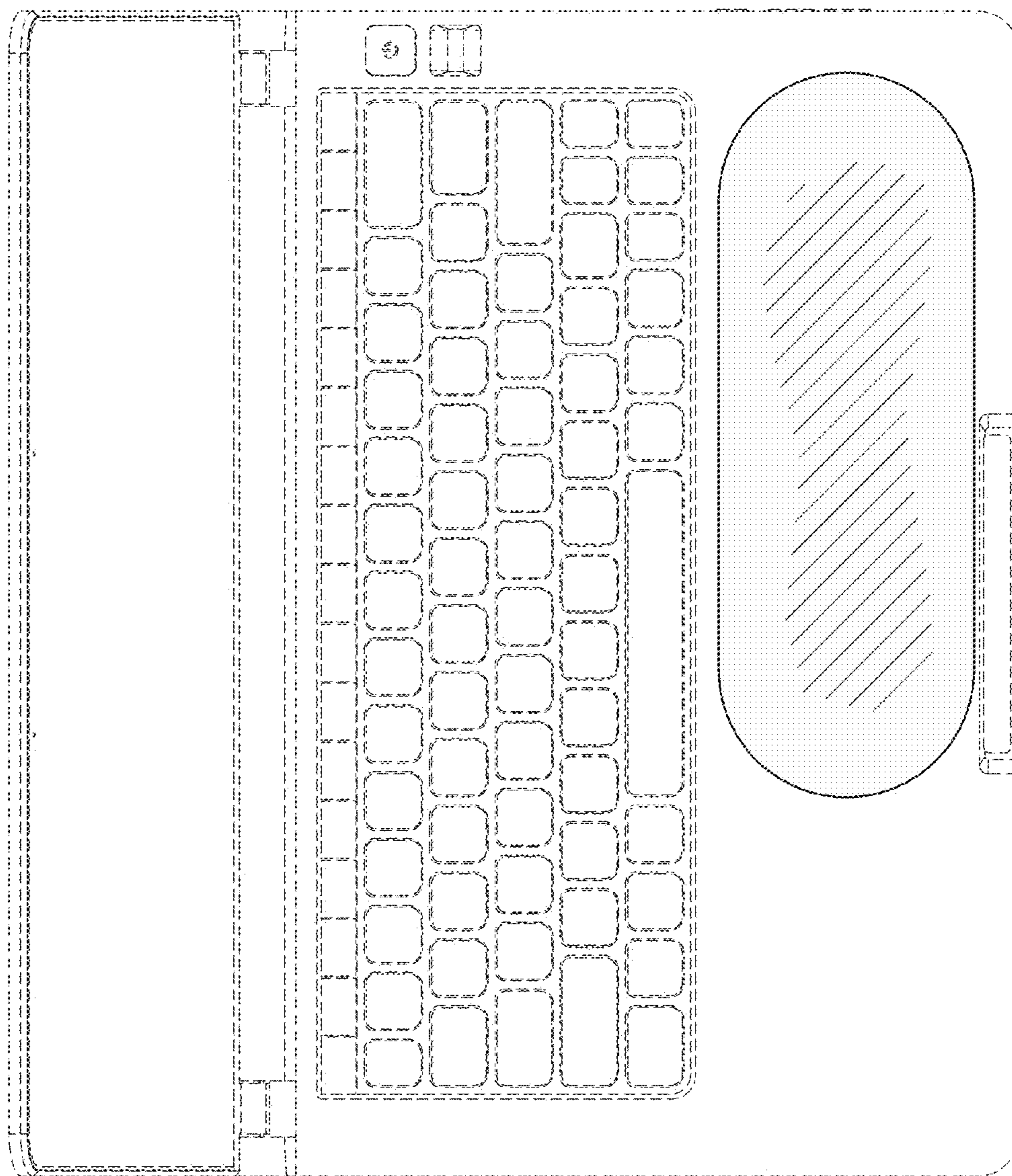
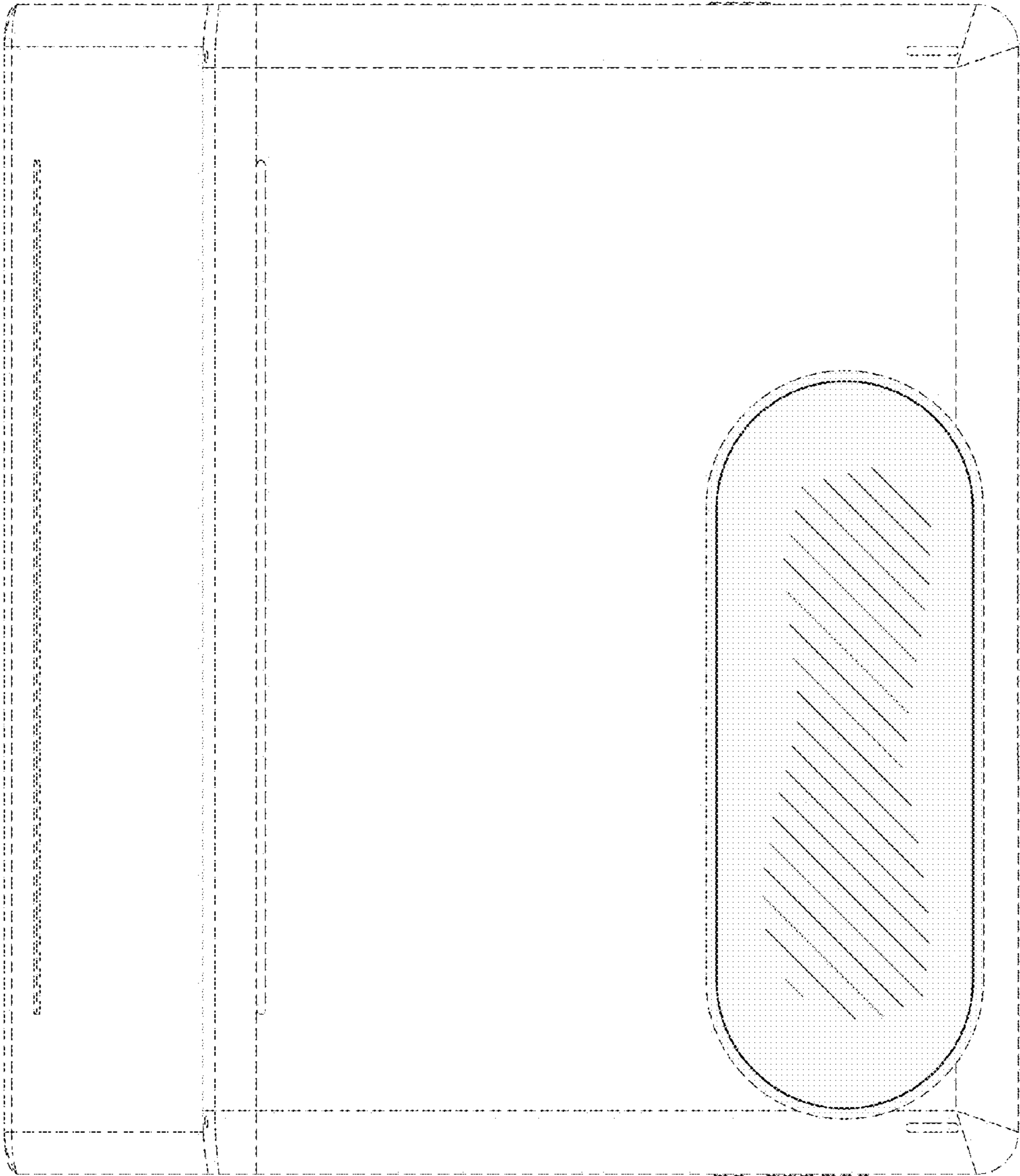


FIG. 35



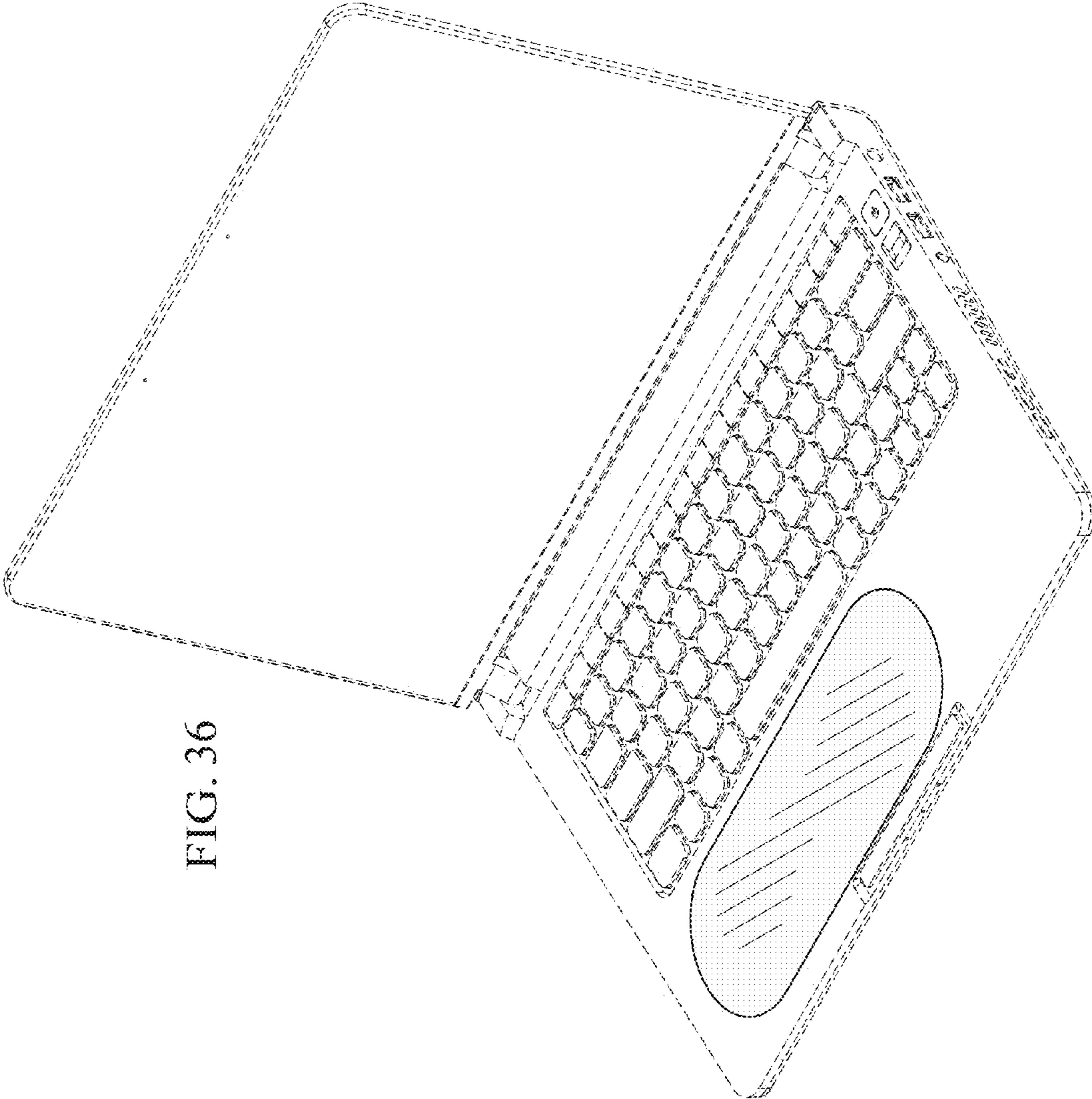


FIG. 36

FIG. 37

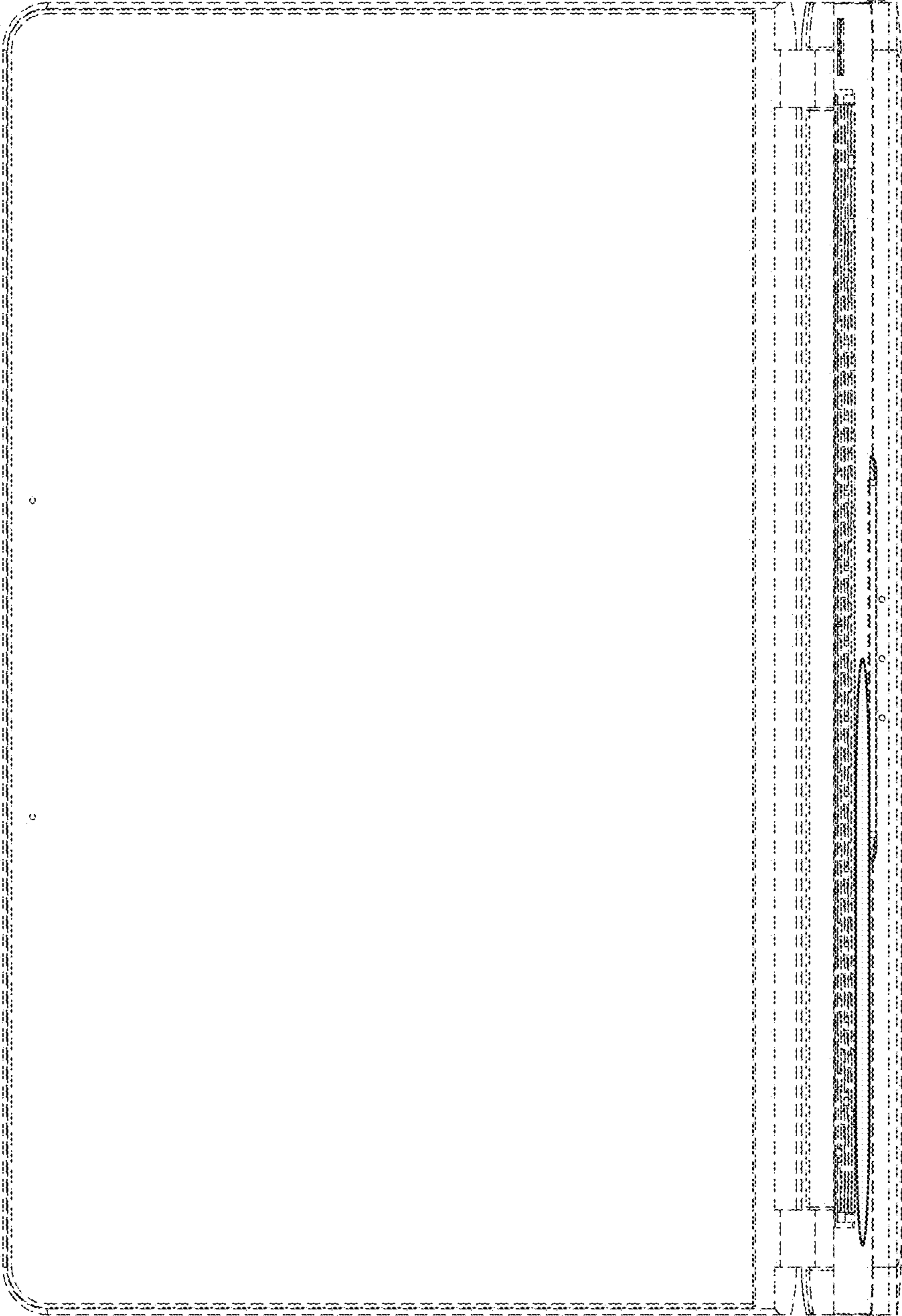
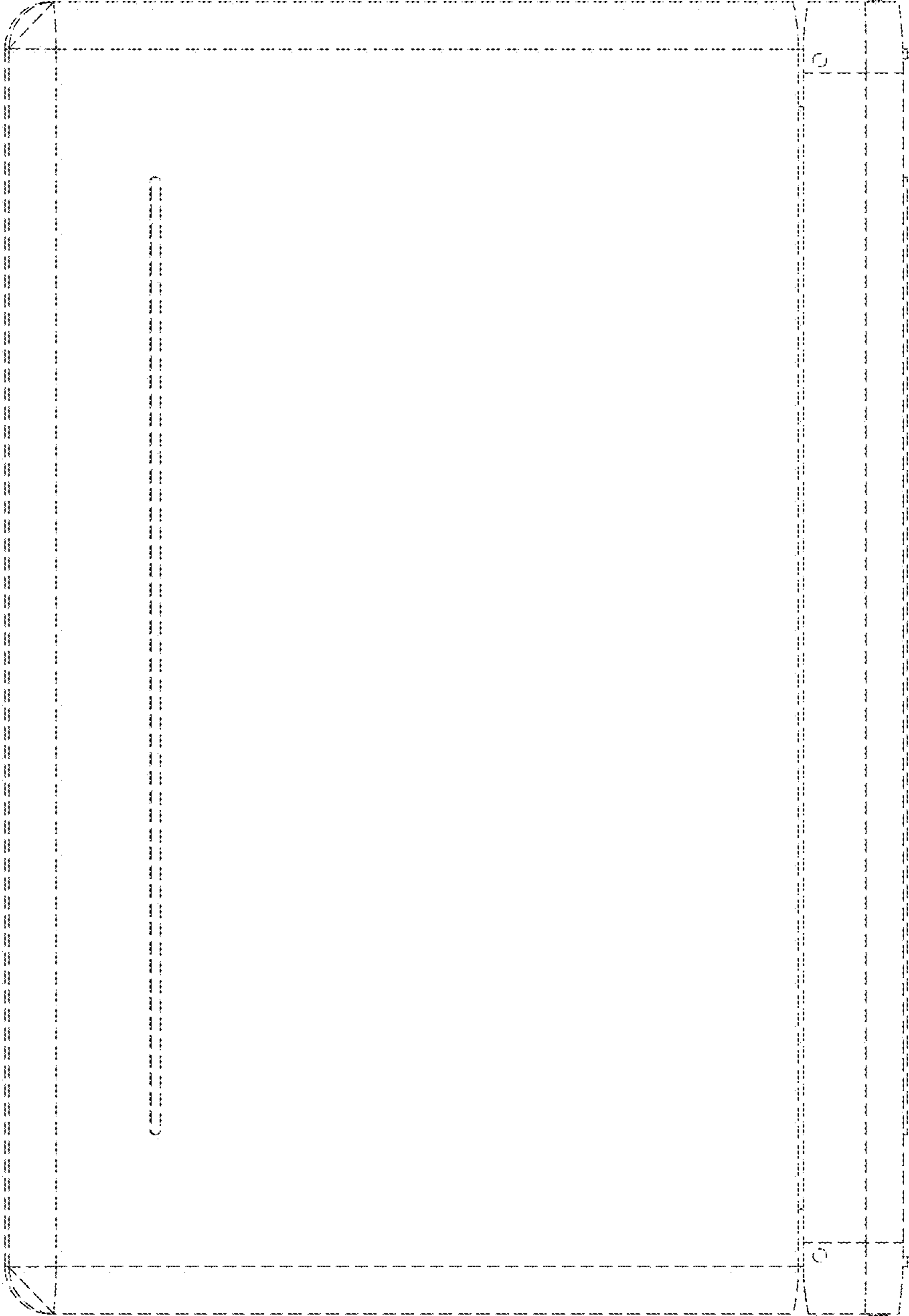


FIG. 38





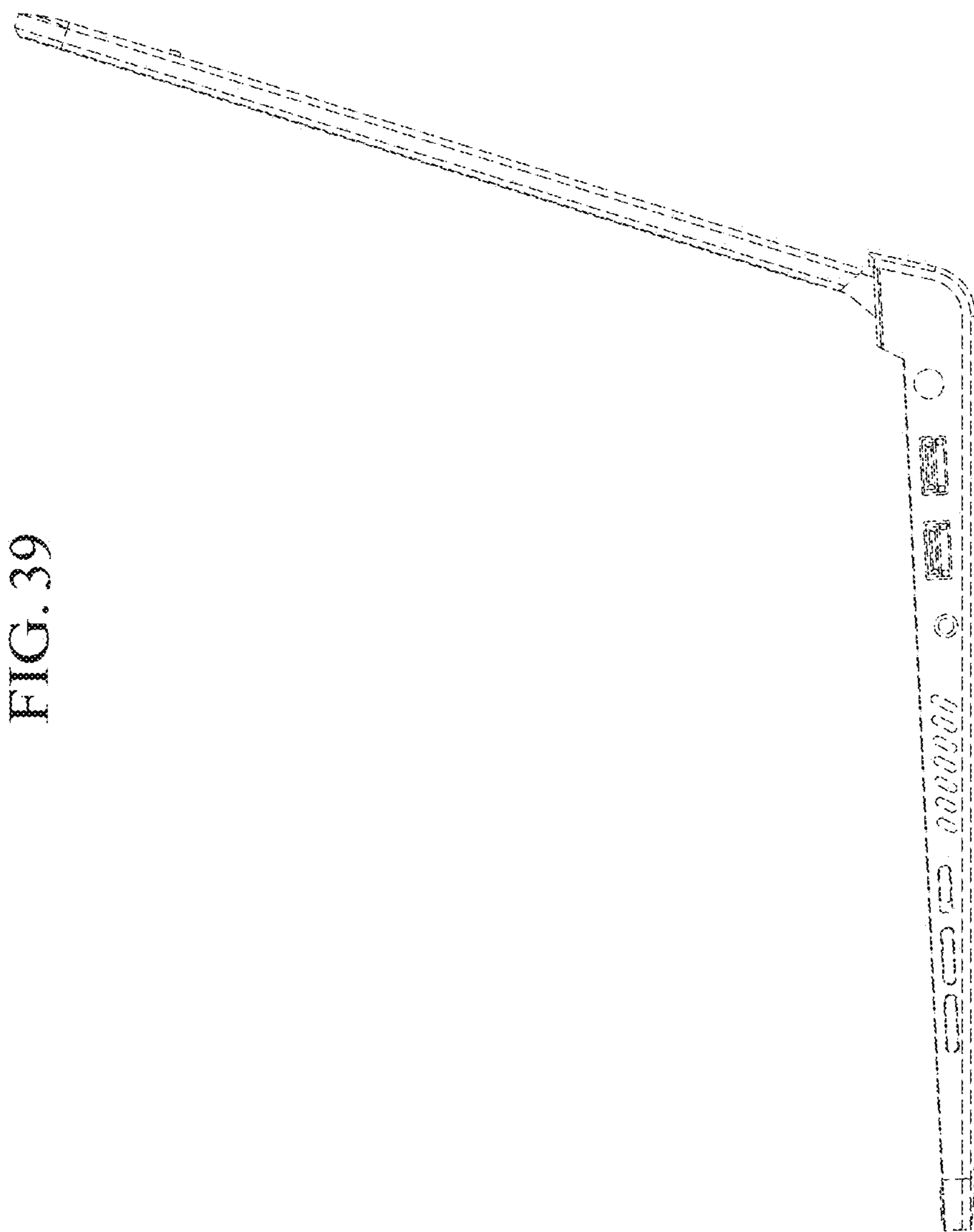


FIG. 39

FIG. 40

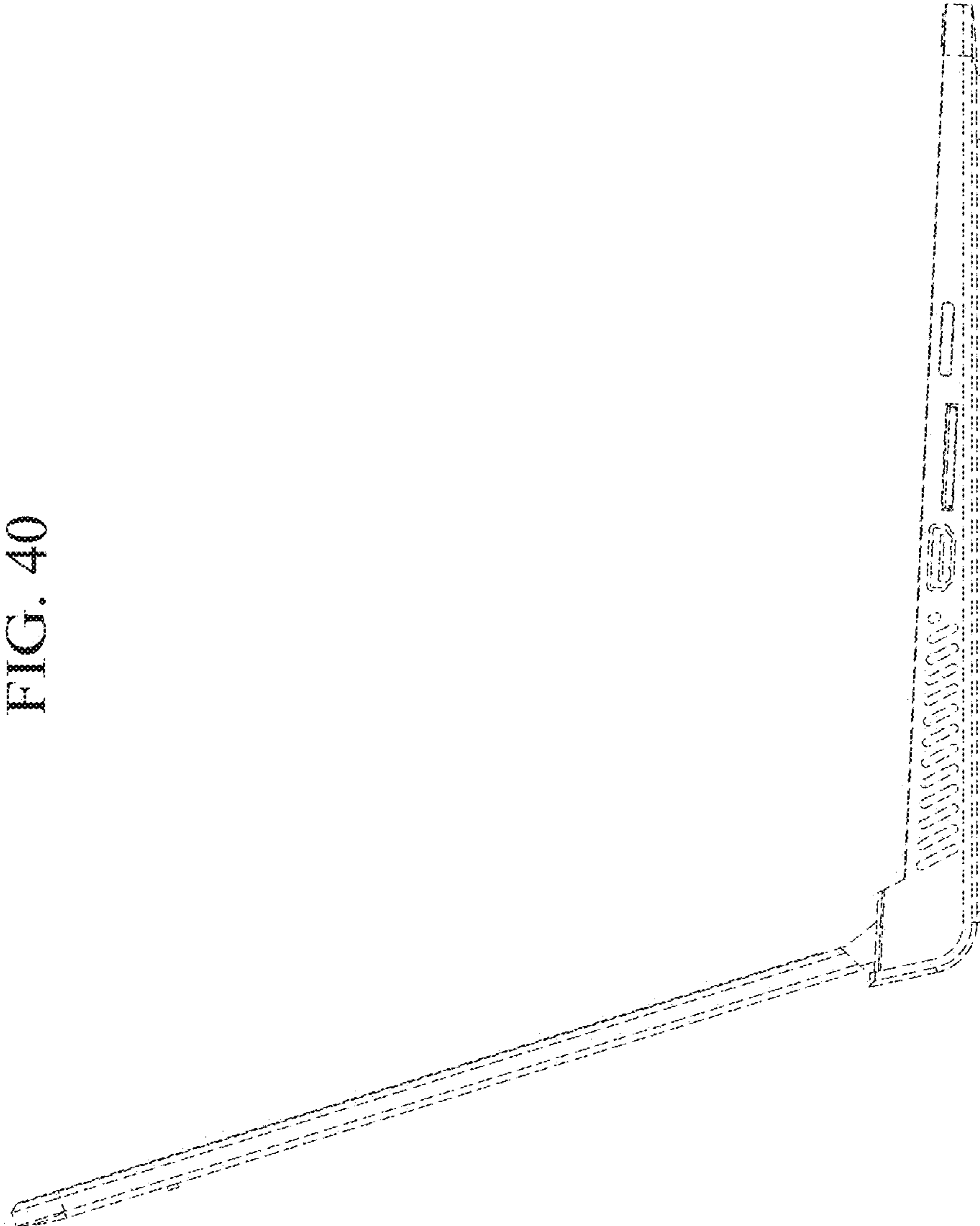


FIG. 41

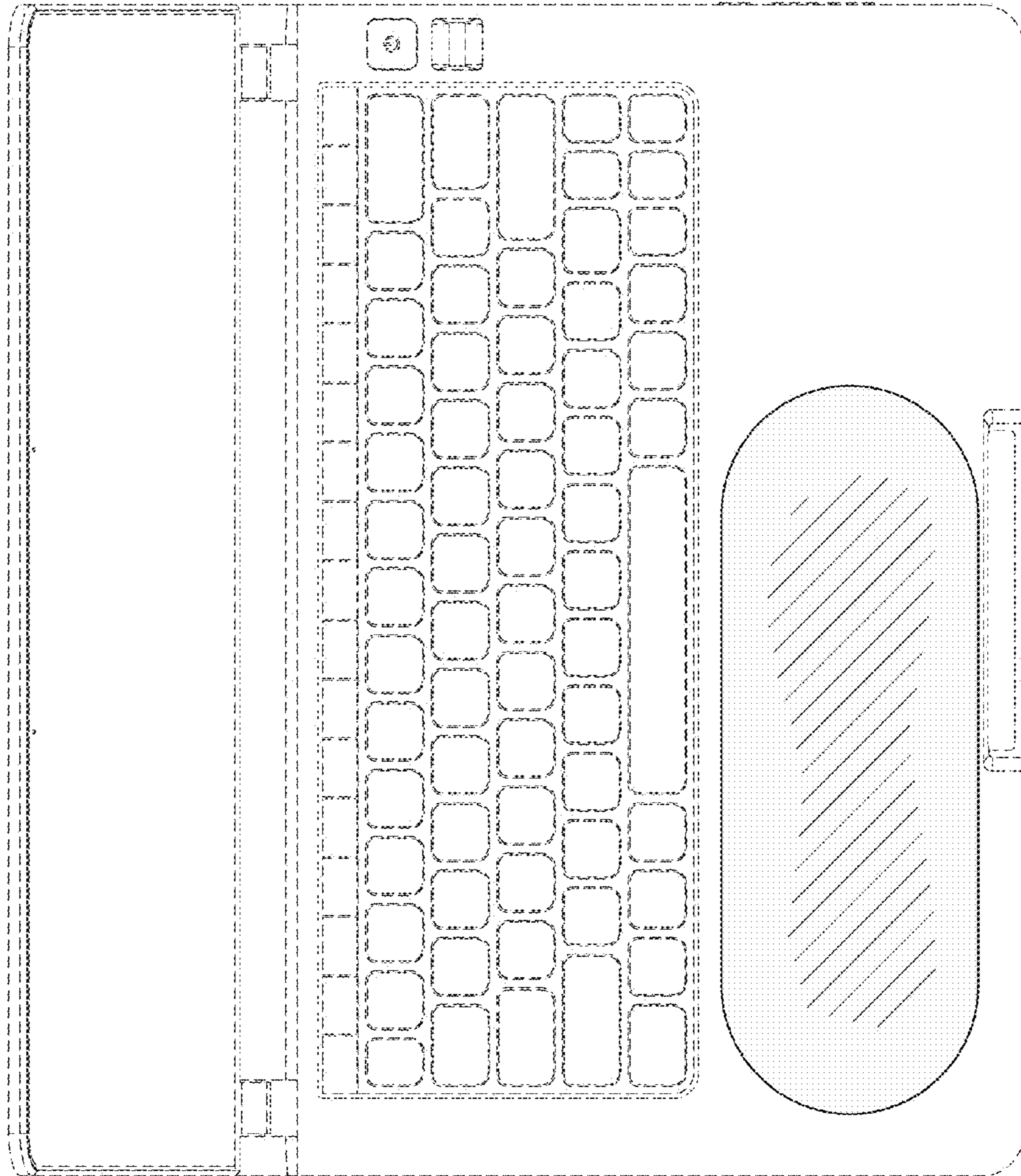


FIG. 42

