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(12) **United States Design Patent**  
**Adamson et al.**

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(54) **ELECTRONIC COMPUTER WITH AN AT LEAST PARTIALLY TRANSPARENT INPUT DEVICE**

(71) Applicant: **Intel Corporation**, Santa Clara, CA (US)

(72) Inventors: **Peter S. Adamson**, Portland, OR (US);  
**Gary Paek**, Beaverton, OR (US);  
**Nicholas W. Oakley**, Portland, OR (US)

(73) Assignee: **Intel Corporation**, Santa Clara, CA (US)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/478,168**

(22) Filed: **Dec. 31, 2013**

**Related U.S. Application Data**

(62) Division of application No. 29/423,471, filed on May 31, 2012, now Pat. No. Des. 698,350.

(51) **LOC (10) Cl.** ..... **14-02**

(52) **U.S. Cl.**  
USPC ..... **D14/318**

(58) **Field of Classification Search**  
USPC ..... D14/315-327; D18/1, 2, 7, 11;  
235/145 A, 145 R; 341/22, 23; 345/104,  
345/156, 168, 169, 173; 361/679.08,  
361/679.09, 679.11, 679.26, 679.27  
CPC ..... G06F 1/16; G06F 1/1616; H01R 35/02;  
H03M 11/00; G09B 13/04; H03K 17/94  
See application file for complete search history.

(56) **References Cited**

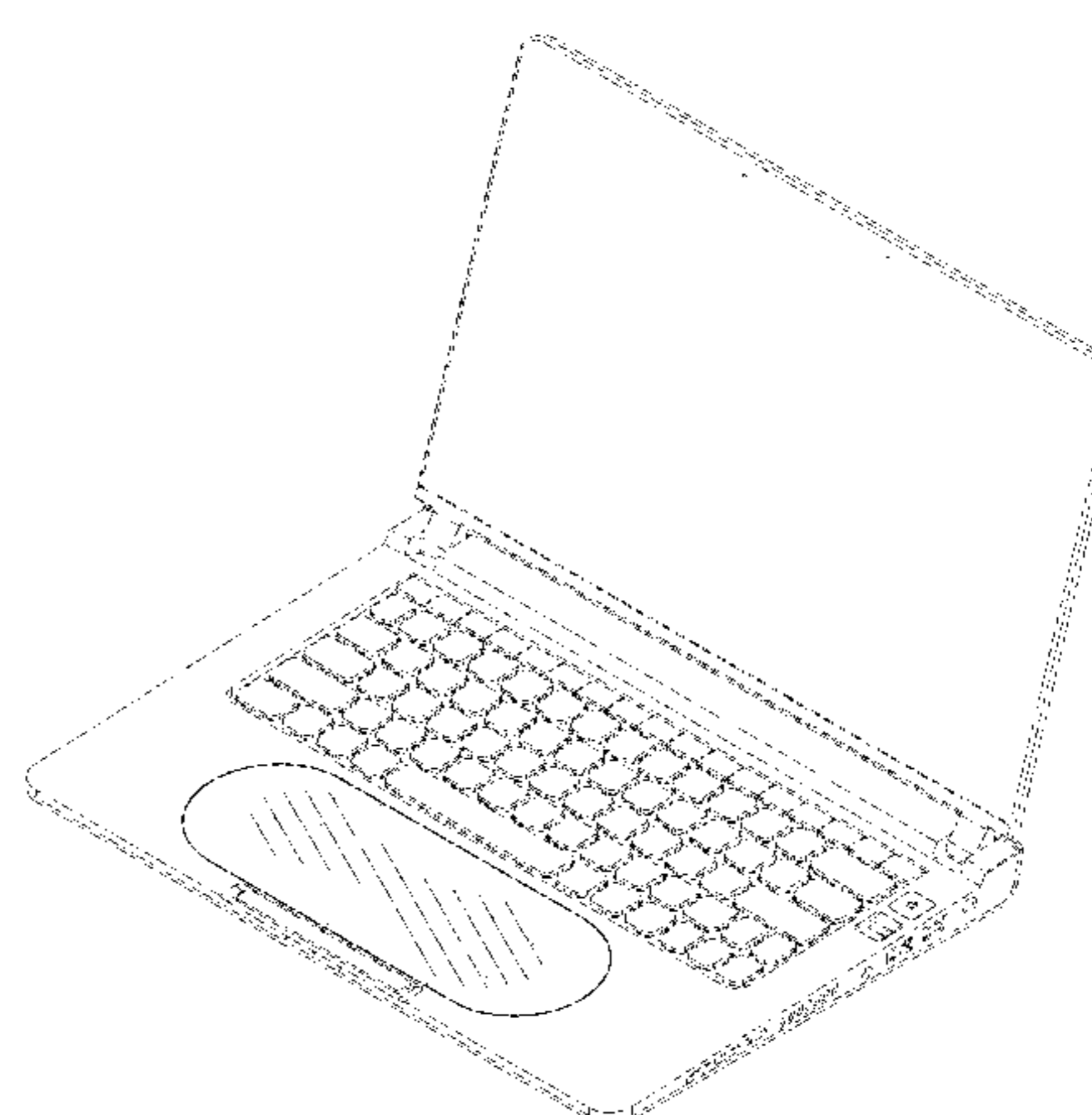
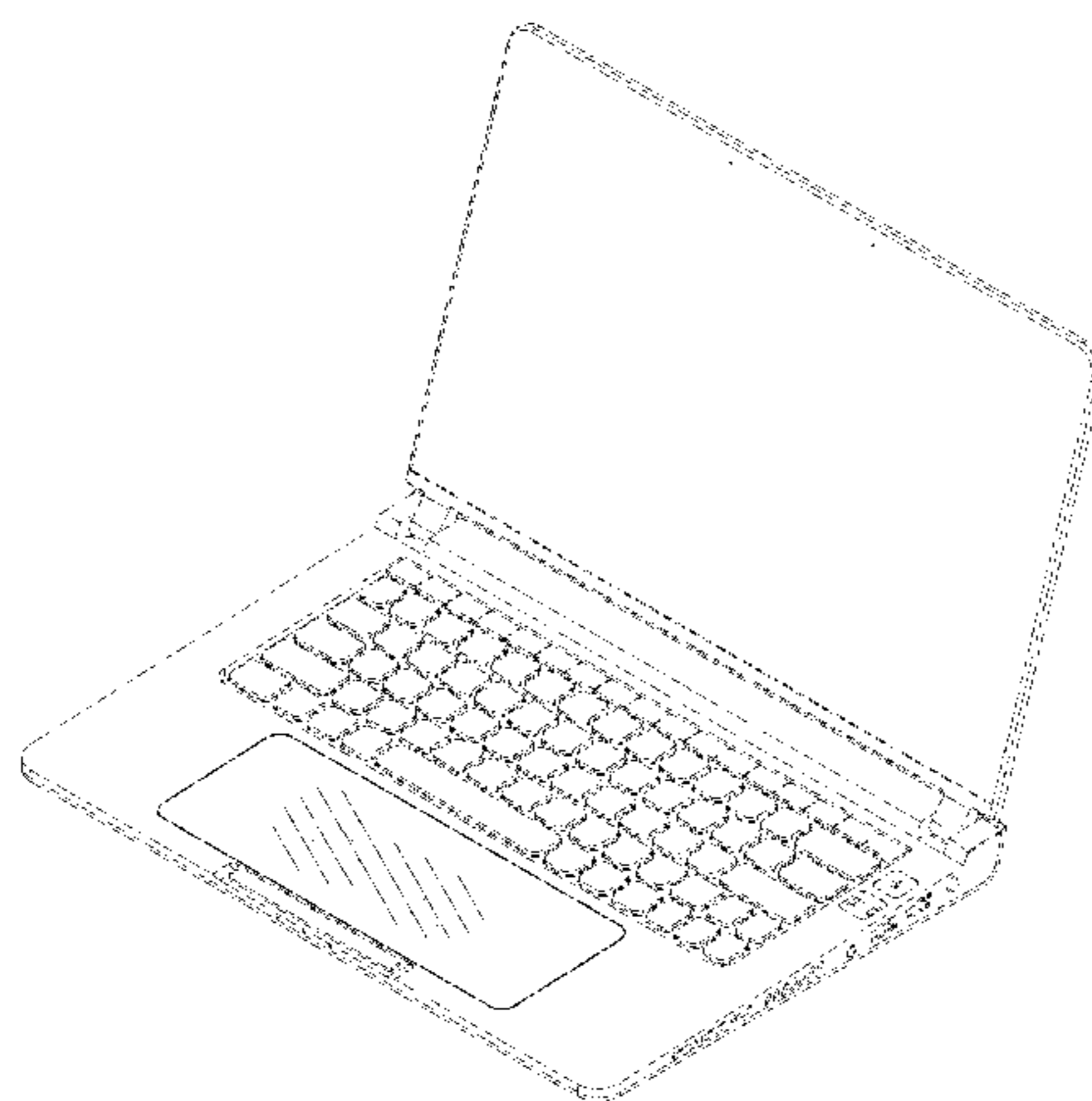
**U.S. PATENT DOCUMENTS**

D358,376 S 5/1995 Poon  
6,459,424 B1 10/2002 Resman  
6,480,373 B1 11/2002 Landry et al.  
D565,045 S 3/2008 Wu et al.  
D616,880 S 6/2010 Andre et al.  
D631,043 S 1/2011 Kell et al.

7,999,792 B2 8/2011 Tsuji et al.  
D649,544 S 11/2011 Chiu et al.  
D658,171 S 4/2012 Masui et al.  
D660,835 S 5/2012 Cheng  
D674,793 S 1/2013 Chen et al.  
D691,995 S 10/2013 Adamson et al.  
D694,232 S 11/2013 Adamson et al.  
D698,350 S \* 1/2014 Adamson et al. .... D14/318  
D698,778 S 2/2014 Adamson et al.  
D701,501 S 3/2014 Adamson et al.  
D729,791 S 5/2015 Adamson et al.  
2005/0135053 A1 6/2005 Carroll  
2005/0243783 A1 11/2005 Lee et al.  
2007/0120762 A1 5/2007 O’Gorman  
2008/0150903 A1 6/2008 Chuang  
2009/0179859 A1 7/2009 Wisebourt et al.  
2009/0322689 A1 12/2009 Kwong et al.  
2010/0052879 A1 3/2010 Nanos  
2010/0277439 A1 11/2010 Charlier et al.  
2011/0012928 A1 1/2011 Cholewin et al.  
2011/0242750 A1 10/2011 Oakley  
2011/0255727 A1 10/2011 Azuchi  
2012/0162270 A1 6/2012 Fleck et al.  
2012/0212424 A1 8/2012 Sharma  
2012/0235949 A1 9/2012 Ligtenberg  
2013/0321287 A1 12/2013 Adamson et al.  
2013/0321288 A1 12/2013 Adamson et al.

**OTHER PUBLICATIONS**

Final Office Action in U.S. Appl. No. 13/485,633 mailed on Aug. 11, 2014.  
Non-Final Office Action in U.S. Appl. No. 13/485,690 mailed on Aug. 1, 2014.  
Non-Final Office Action in U.S. Appl. No. 13/485,633 mailed on Feb. 14, 2014.  
Non-Final Office Action in U.S. Appl. No. 13/485,690 mailed on Feb. 19, 2014.  
Restriction Requirement received for U.S. Appl. No. 29/423,470, mailed on Apr. 5, 2013.  
Notice of Allowance received for U.S. Appl. No. 29/423,470, mailed on May 30, 2013.  
Design U.S. Appl. No. 29/468,750 entitled At Least Partially Transparent Input Device for an Electronic Computer, filed Oct. 2, 2013, inventors Peter Adamson, et al.  
Restriction Requirement received for U.S. Appl. No. 29/423,471, mailed on Apr. 16, 2013.  
Office Action Received for U.S. Appl. No. 29/423,471, mailed on May 30, 2013.  
Notice of allowance received for U.S. Appl. No. 29/423,471 mailed on Sep. 18, 2013.



Supplemental Notice of allowance received for U.S. Appl. No. 29/423,471 mailed on Oct. 15, 2013.  
 Supplemental Notice of Allowance received for U.S. Appl. No. 29/423,471 mailed on Dec. 17, 2013.  
 Restriction Requirement received for U.S. Appl. No. 29/423,472, mailed on Apr. 17, 2013.  
 Non-Final Office Action Received for U.S. Appl. No. 29/423,472, mailed on May 30, 2013.  
 Notice of Allowance received for U.S. Appl. No. 29/423,472, mailed on Sep. 19, 2013.  
 Supplemental Notice of allowance received for U.S. Appl. No. 29/423,472, mailed on Dec. 17, 2013.  
 Design U.S. Appl. No. 29/478,161, entitled At Least Partially Transparent Input Device for an Electronic Computer, filed Dec. 31, 2013, inventors Peter S. Adamson et al.  
 Restriction Requirement received for U.S. Appl. No. 29/423,474, mailed on May 1, 2013.  
 Non-Final Office Action Received for U.S. Appl. No. 29/423,474, mailed on Jun. 6, 2013.  
 Notice of Allowance received for U.S. Appl. No. 29/423,474, mailed on Oct. 25, 2013.  
 Supplemental Notice of allowance received for U.S. Appl. No. 29/423,474, mailed on Dec. 18, 2013.  
 Design U.S. Appl. No. 29/481,121, entitled At Least Partially Transparent Input Device for an Electronic Computer, filed Jan. 31, 2014, inventors Peter S. Adamson et al.  
 Restriction Requirement in U.S. Appl. No. 29/423,475 mailed on May 2, 2013.  
 Notice of Allowance received for U.S. Appl. No. 29/423,475, mailed on Jul. 2, 2013.  
 Design U.S. Appl. No. 29/473,585, entitled At Least Partially Transparent Input Device for an Electronic Computer, filed Nov. 24, 2013, inventors Peter S. Adamson et al.  
 Design U.S. Appl. No. 29/423,476, entitled Electronic Computer With an At Least Partially Transparent Input Device, filed Jun. 1, 2012, inventors Peter S. Adamson et al.  
 Ex Parte Quayle Action received for U.S. Appl. No. 29/423,476 mailed on Apr. 8, 2013.  
 Notice of Allowance received for U.S. Appl. No. 29/423,476 mailed on Jun. 28, 2013.  
 Supplemental Notice of Allowance received for U.S. Appl. No. 29/423,476 mailed on Aug. 6, 2013.  
 Notice of Allowance received for U.S. Appl. No. 29/423,476, mailed on Oct. 25, 2013.  
 Supplemental Notice of allowance received for U.S. Appl. No. 29/423,476 mailed on Dec. 17, 2013.  
 Non-Final Office Action in U.S. Appl. No. 13/485,633 mailed on Dec. 4, 2014.  
 Final Office Action in U.S. Appl. No. 13/485,690 mailed on Jan. 13, 2015.  
 Notice of Allowance in U.S. Appl. No. 29/468,750 mailed on Jan. 14, 2015.  
 Non-Final Office Action in U.S. Appl. No. 29/473,585 mailed on Jan. 9, 2015.  
 Non-Final Office Action Received for U.S. Appl. No. 29/423,474, mailed on Jun. 26, 2013.  
 Final Office Action in U.S. Appl. No. 13/485,633 mailed on May 28, 2015.  
 Non-Final Office Action in U.S. Appl. No. 13/485,690 mailed on Jun. 4, 2015.  
 Non-Final Office Action in U.S. Appl. No. 29/478,161 mailed on May 27, 2015.  
 Notice of Allowance in U.S. Appl. No. 29/481,121 mailed on May 14, 2015.  
 Notice of Allowance in U.S. Appl. No. 29/473,585 mailed on May 13, 2015.

\* cited by examiner

Primary Examiner — Freda S Nunn

(74) Attorney, Agent, or Firm — Patent Capital Group

(57)

**CLAIM**

The ornamental design for electronic computer with an at least partially transparent input device, as shown and described.

**DESCRIPTION**

FIG. 1 is a top perspective view showing a first embodiment of our new design for electronic computer with an at least partially transparent input device;  
 FIG. 2 is a front view thereof;  
 FIG. 3 is a back view thereof;  
 FIG. 4 is a side view thereof;  
 FIG. 5 is another side view thereof;  
 FIG. 6 is a top view thereof;  
 FIG. 7 is a bottom view thereof;  
 FIG. 8 is a top perspective view showing a second embodiment of a design for electronic computer with an at least partially transparent input device;  
 FIG. 9 is a front view thereof;  
 FIG. 10 is a back view thereof;  
 FIG. 11 is a side view thereof;  
 FIG. 12 is another side view thereof;  
 FIG. 13 is a top view thereof;  
 FIG. 14 is a bottom view thereof;  
 FIG. 15 is a top perspective view showing a third embodiment of a design for electronic computer with an at least partially transparent input device;  
 FIG. 16 is a front view thereof;  
 FIG. 17 is a back view thereof;  
 FIG. 18 is a side view thereof;  
 FIG. 19 is another side view thereof;  
 FIG. 20 is a top view thereof;  
 FIG. 21 is a bottom view thereof;  
 FIG. 22 is a top perspective view showing a fourth embodiment of a design for electronic computer with an at least partially transparent input device;  
 FIG. 23 is a front view thereof;  
 FIG. 24 is a back view thereof;  
 FIG. 25 is a side view thereof;  
 FIG. 26 is another side view thereof;  
 FIG. 27 is a top view thereof;  
 FIG. 28 is a bottom view thereof;  
 FIG. 29 is a top perspective view showing a fifth embodiment of a design for electronic computer with an at least partially transparent input device;  
 FIG. 30 is a front view thereof;  
 FIG. 31 is a back view thereof;  
 FIG. 32 is a side view thereof;  
 FIG. 33 is another side view thereof;  
 FIG. 34 is a top view thereof;  
 FIG. 35 is a bottom view thereof;  
 FIG. 36 is a top perspective view showing a sixth embodiment of a design for electronic computer with an at least partially transparent input device;  
 FIG. 37 is a front view thereof;  
 FIG. 38 is a back view thereof;  
 FIG. 39 is a side view thereof;  
 FIG. 40 is another side view thereof;  
 FIG. 41 is a top view thereof; and,  
 FIG. 42 is a bottom view thereof.  
 The broken lines in the preceding FIGURES illustrate portions of the computer and form no part of the claimed design.

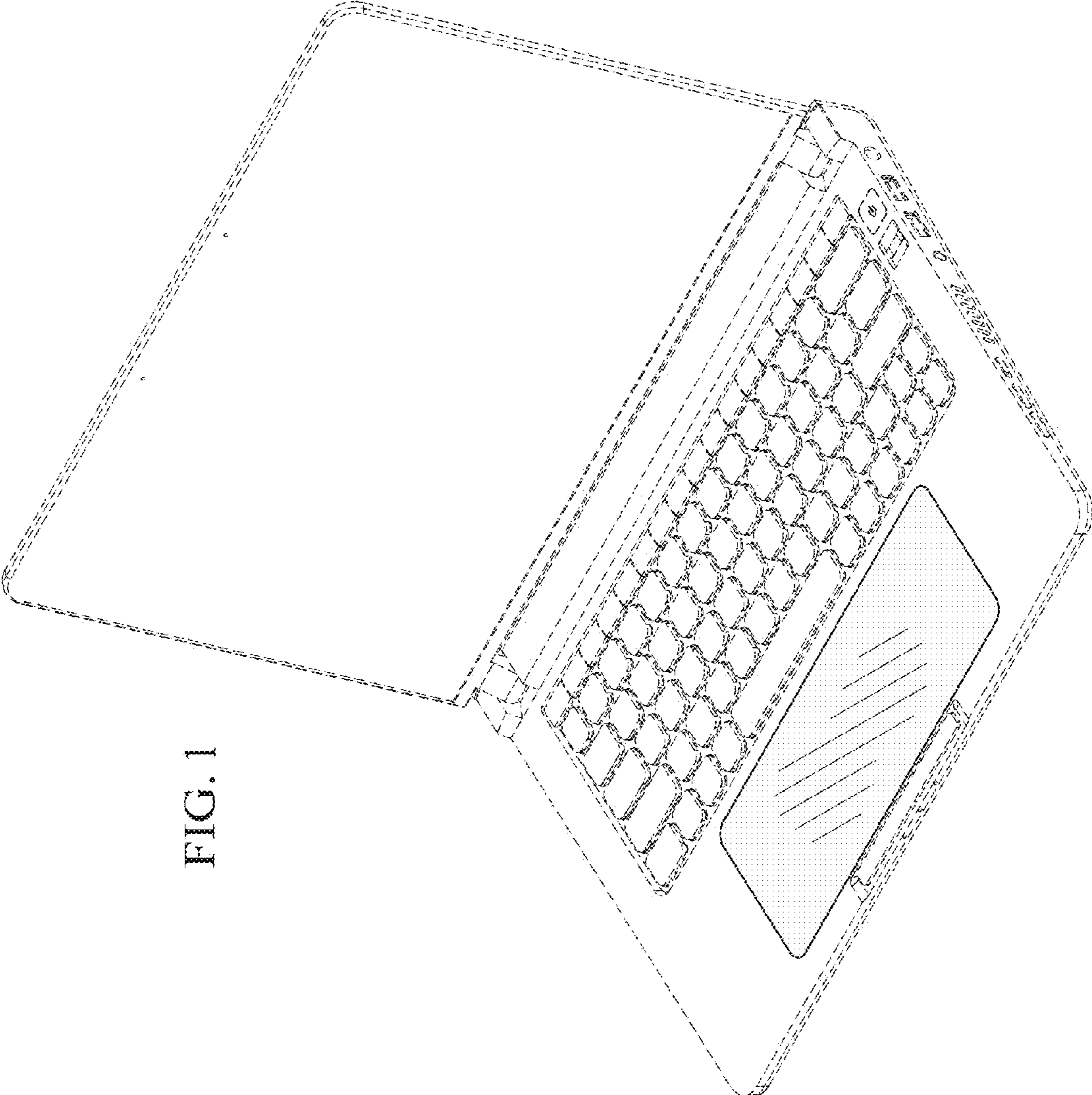


FIG. 1

FIG. 2

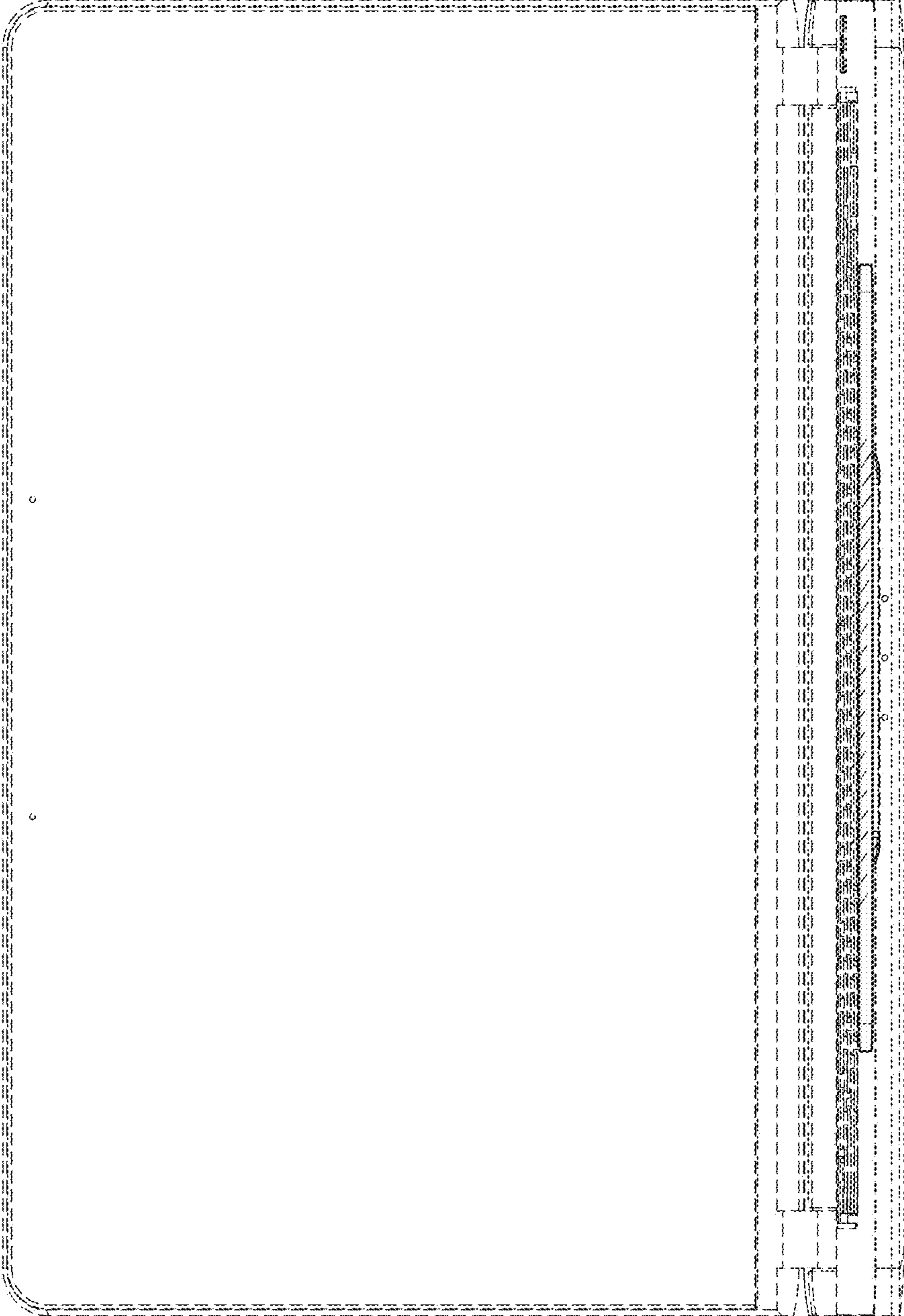


FIG. 3

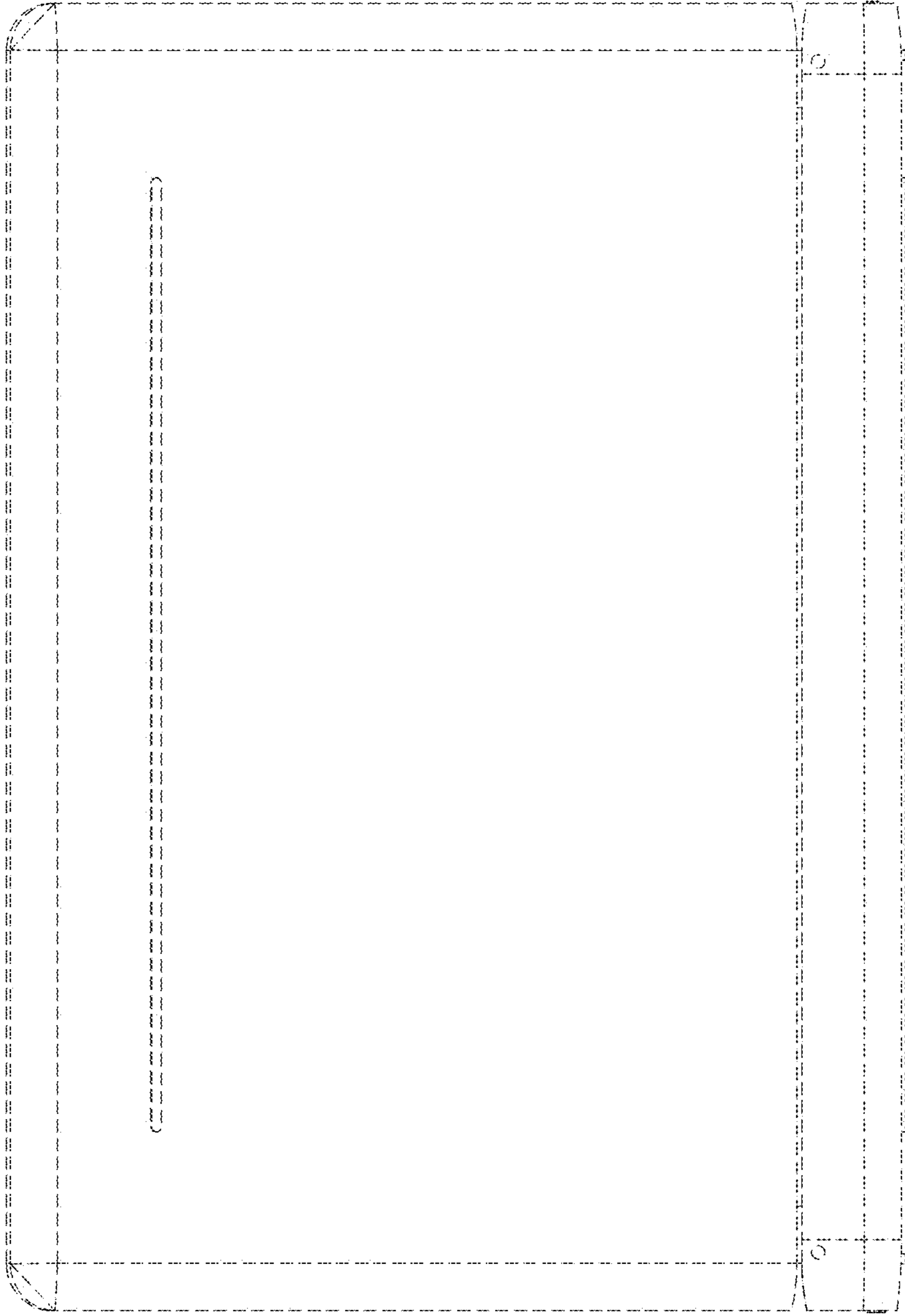


FIG. 4

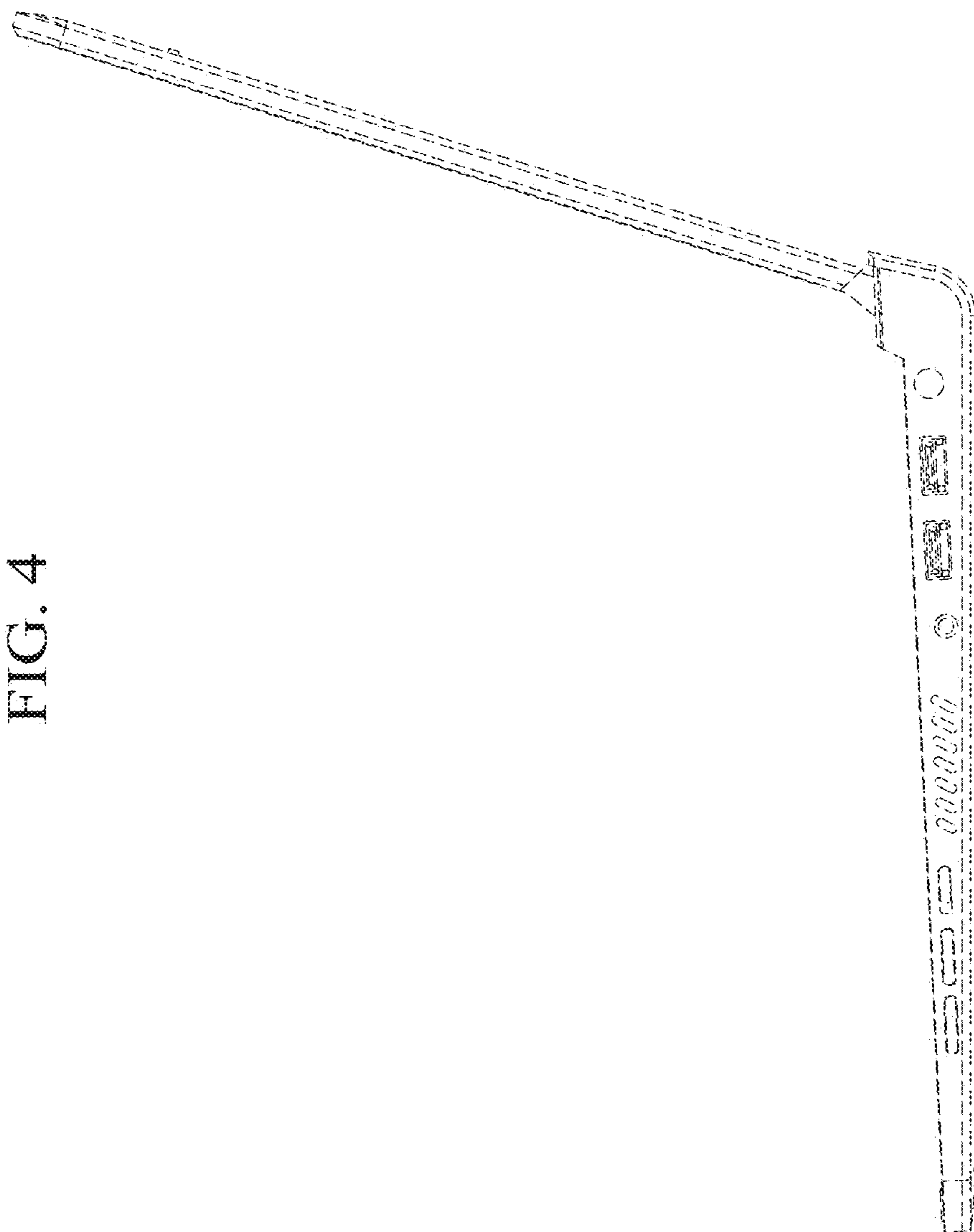


FIG. 5

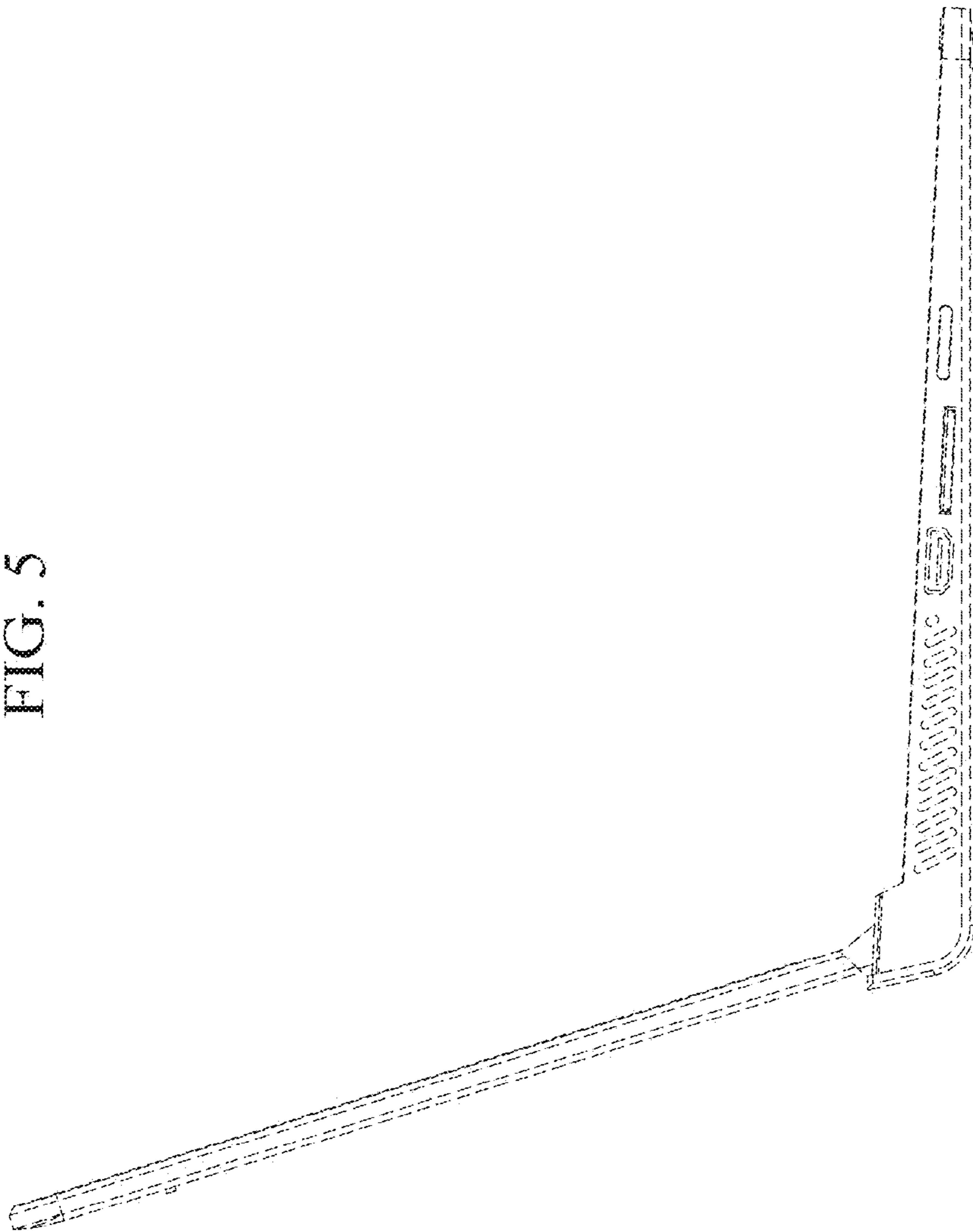


FIG. 6

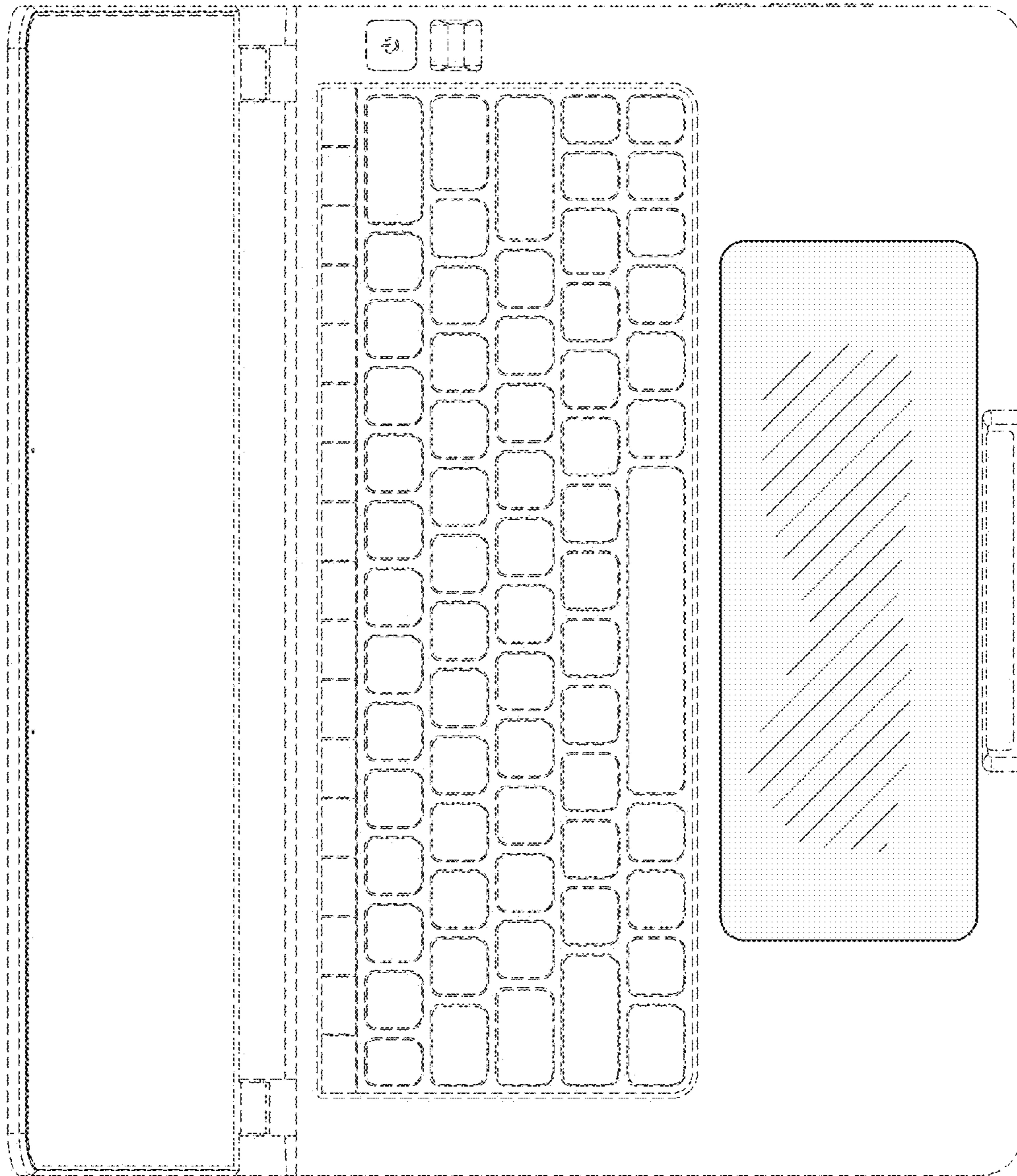
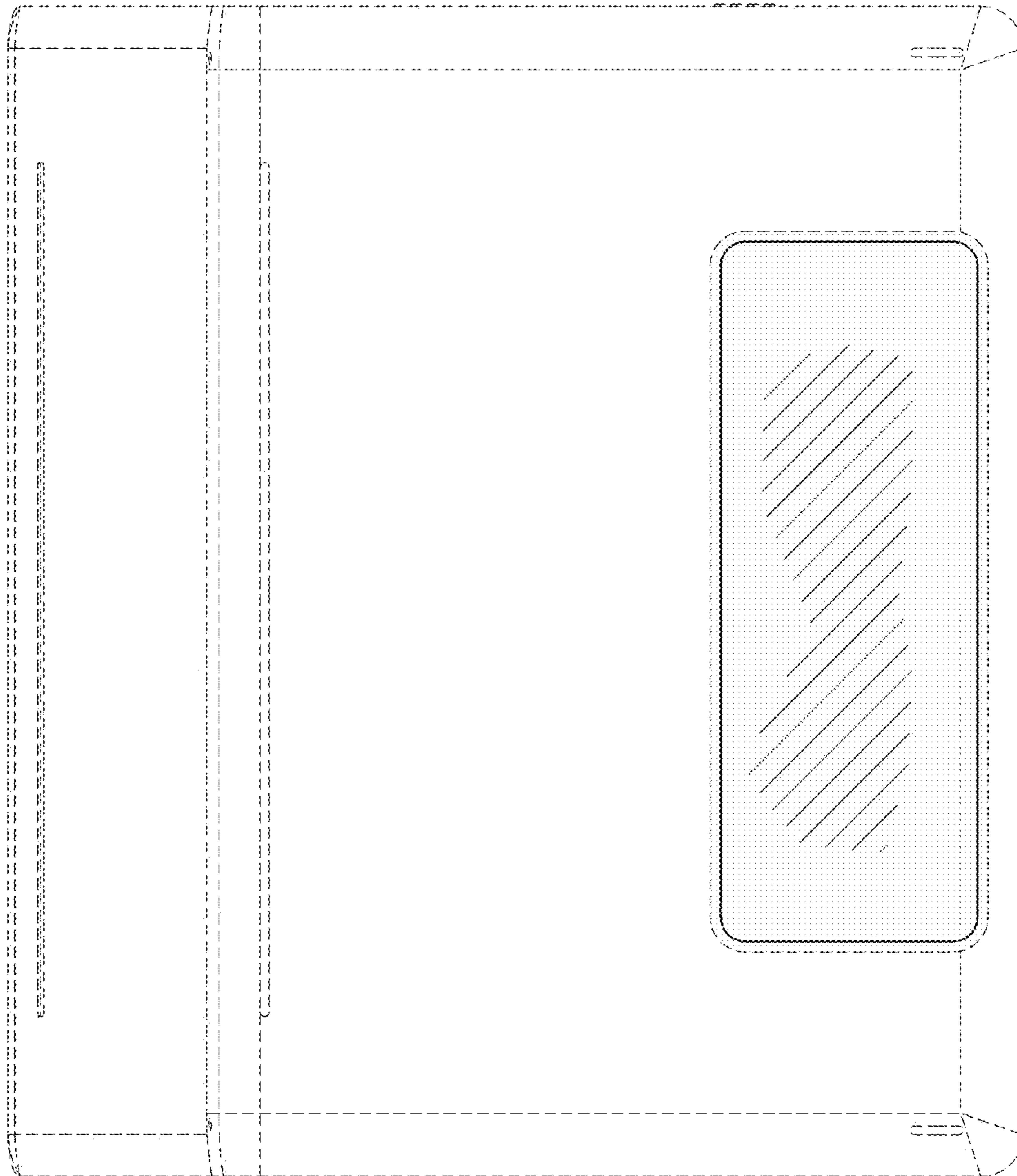




FIG. 7



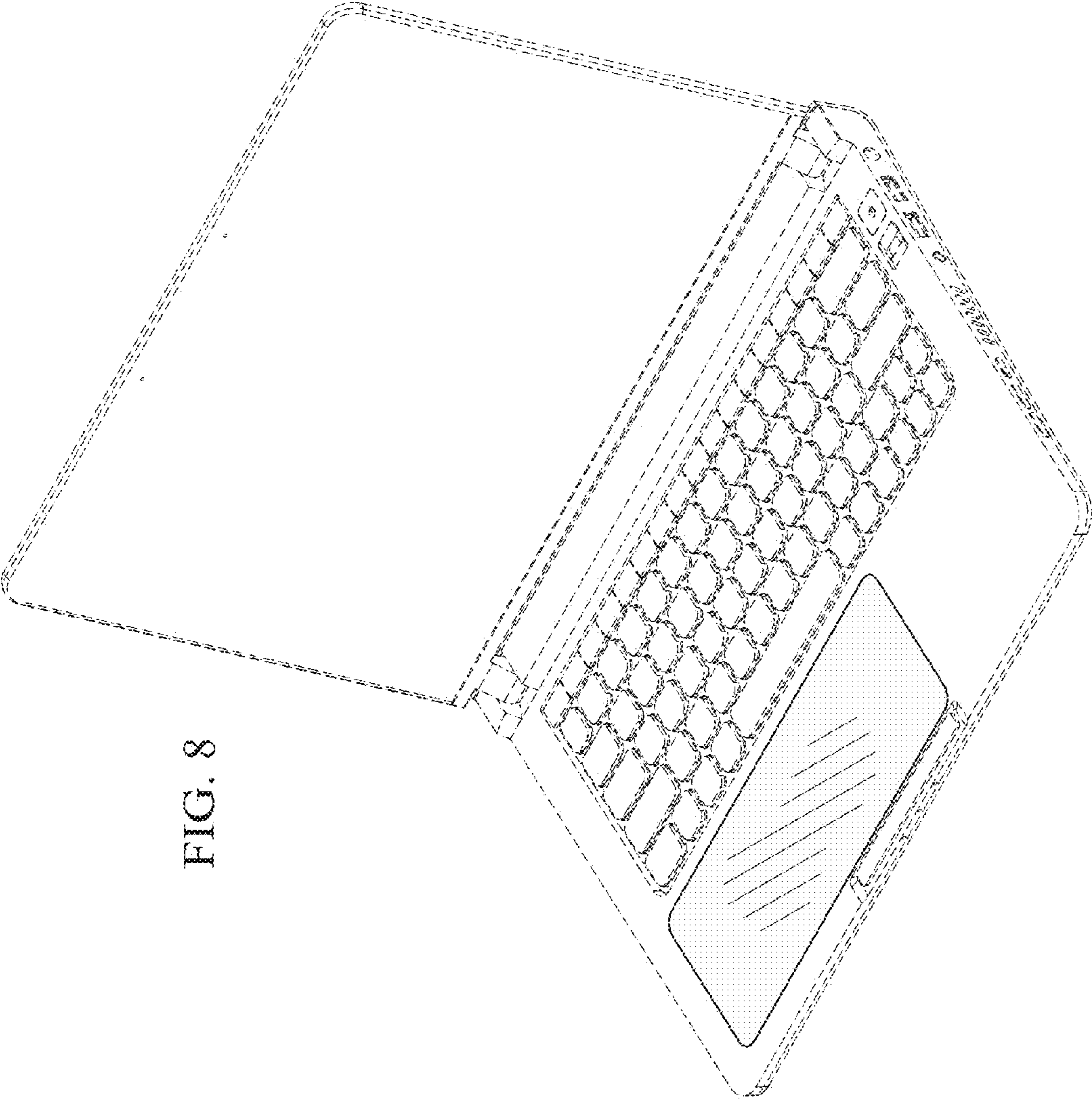


FIG. 8

FIG. 9

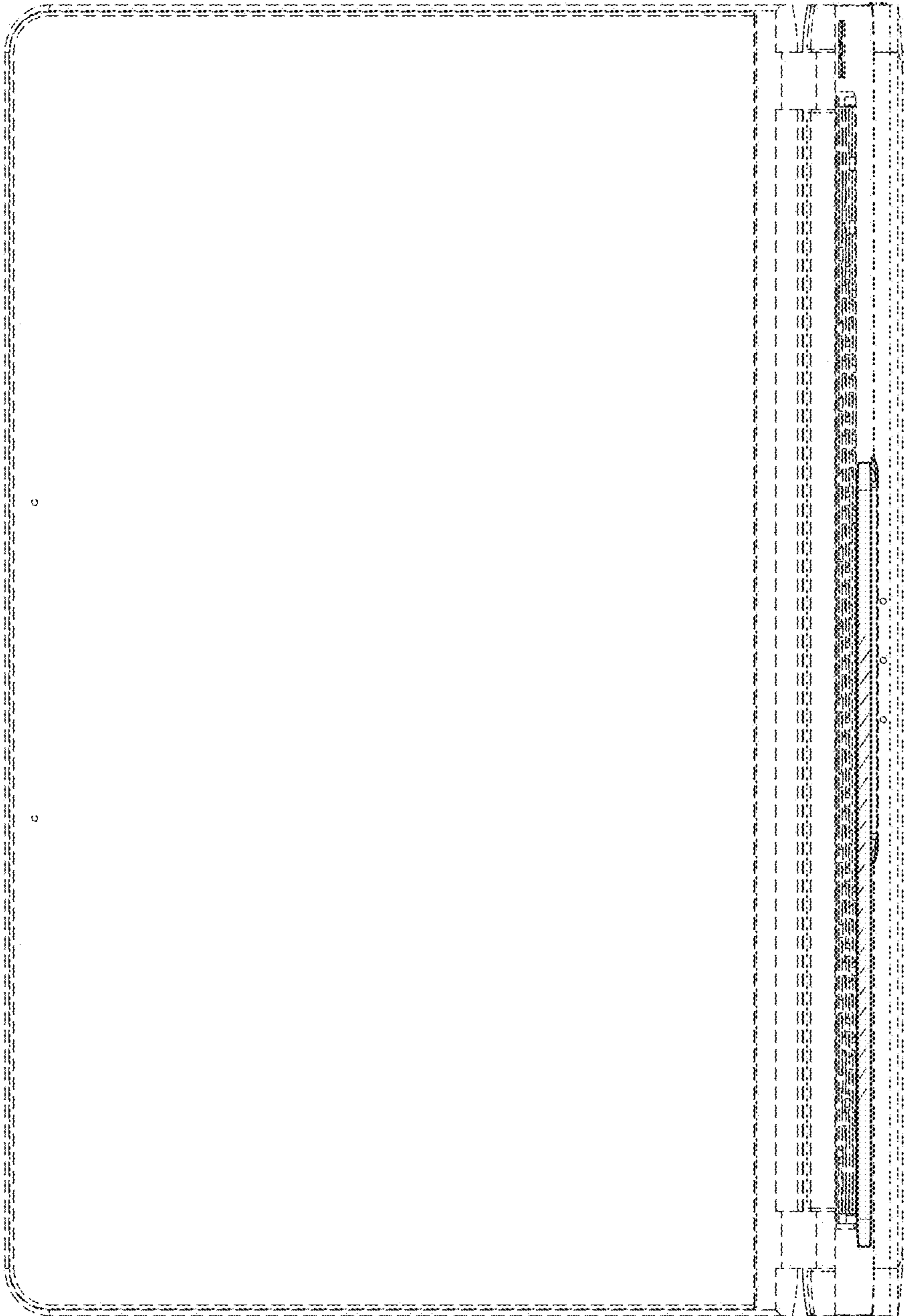


FIG. 10

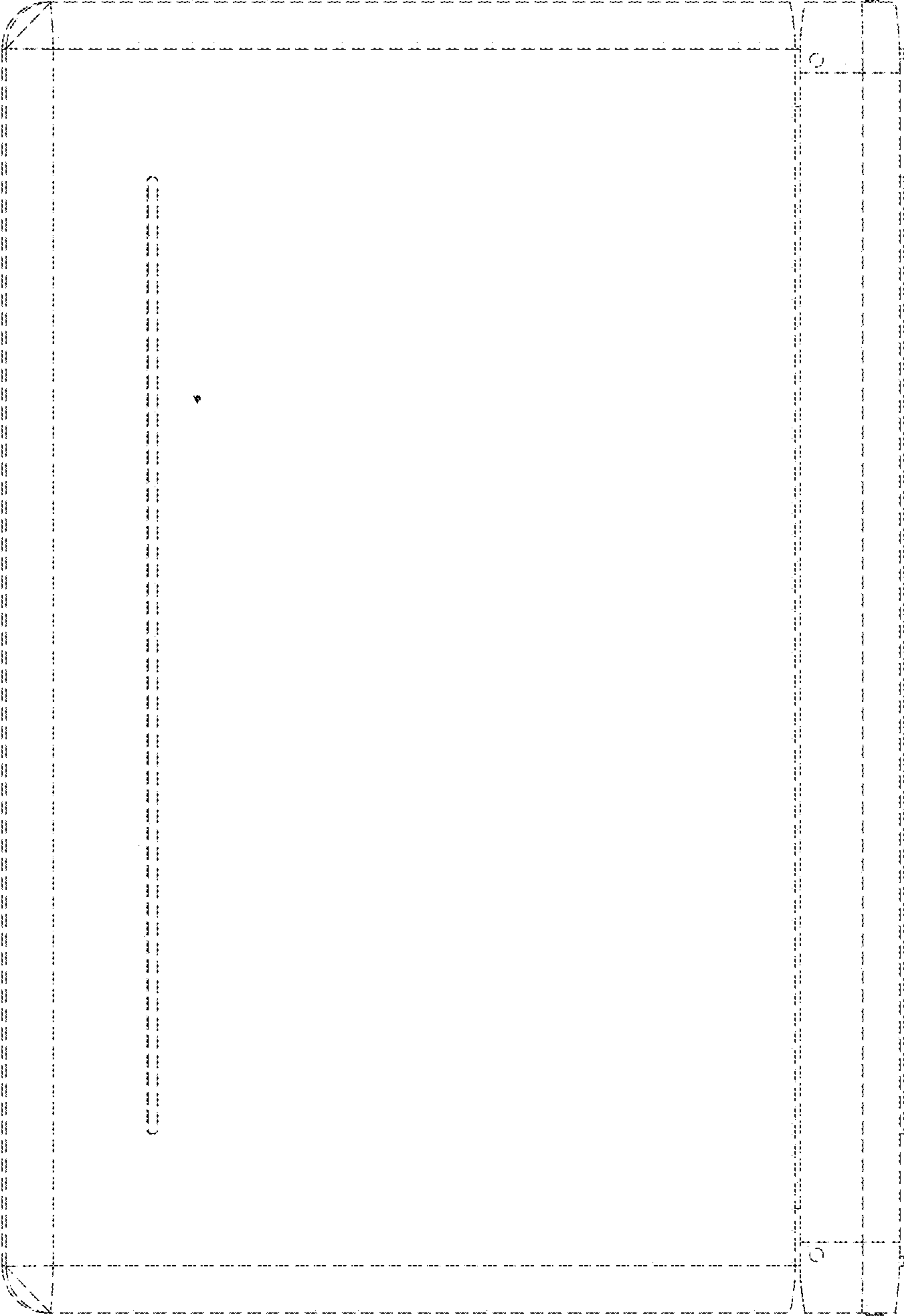


FIG. 11

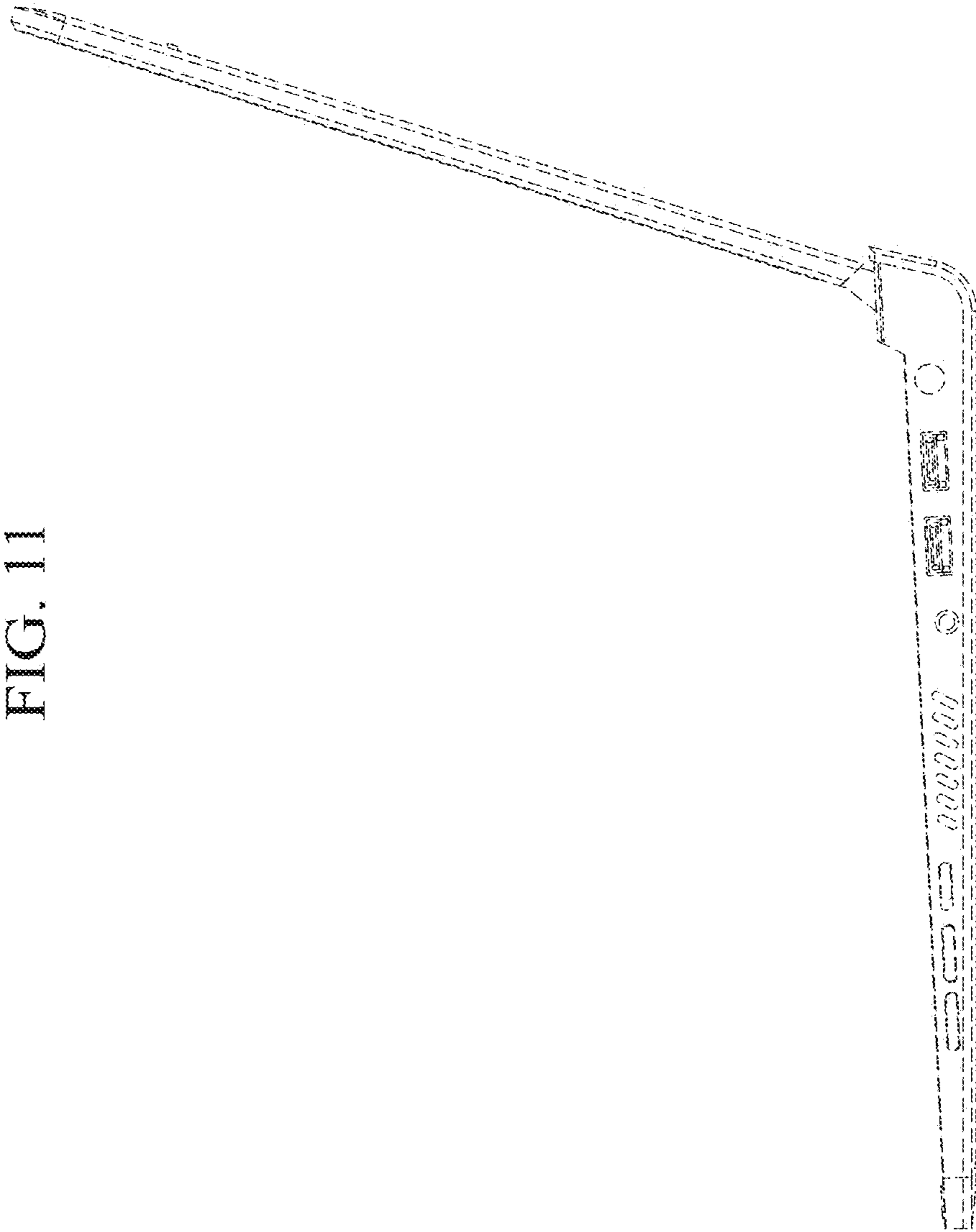


FIG. 12

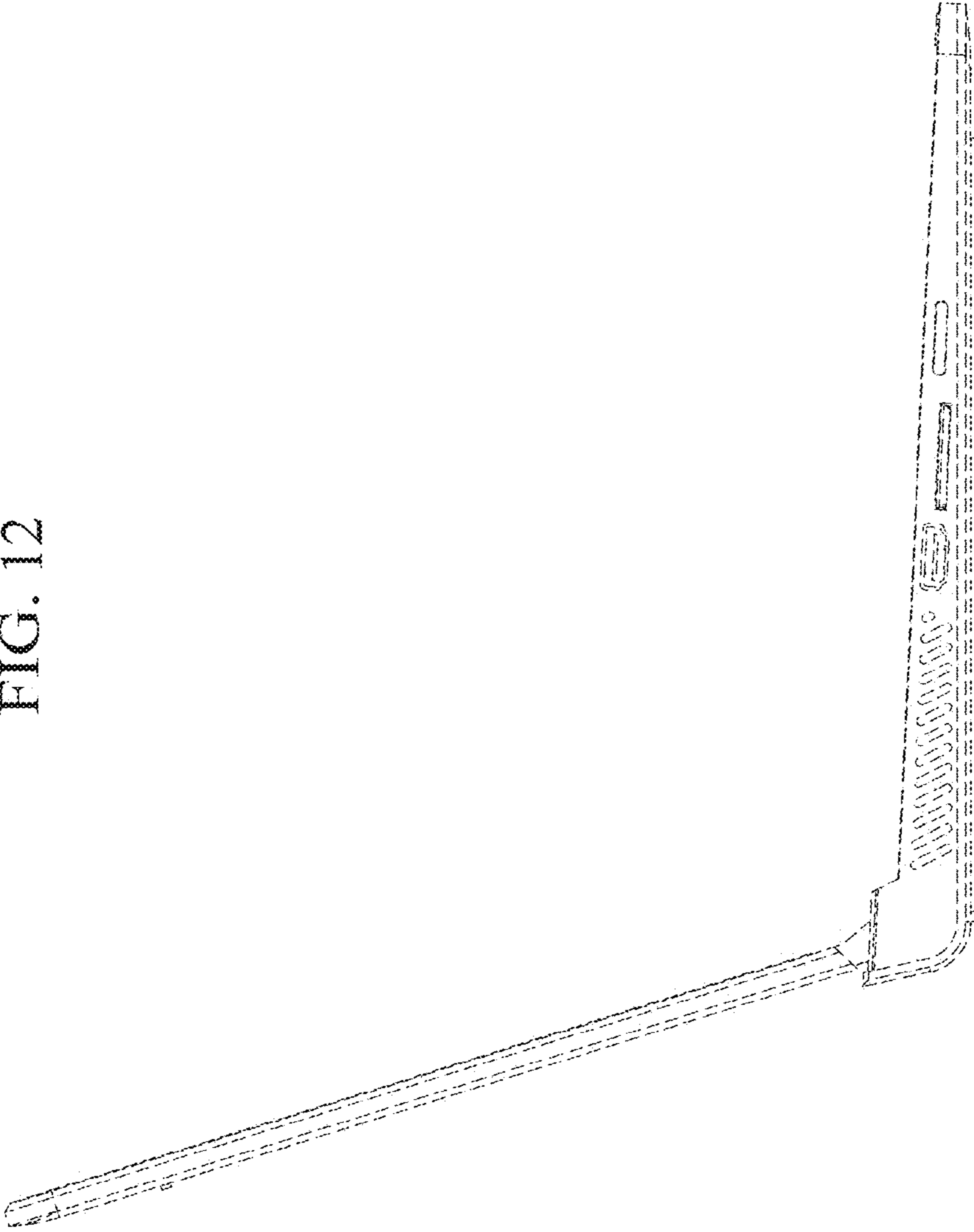


FIG. 13

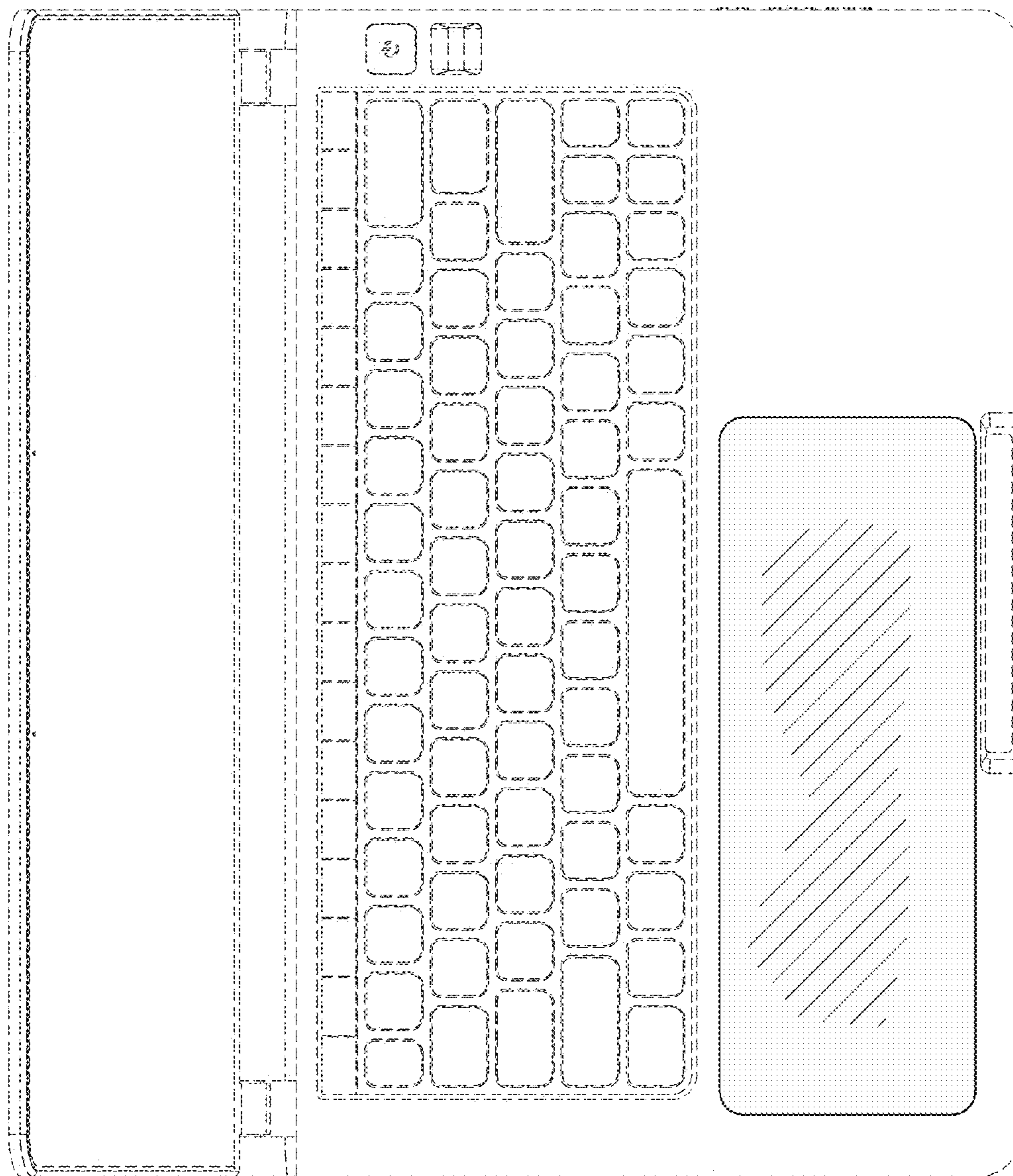
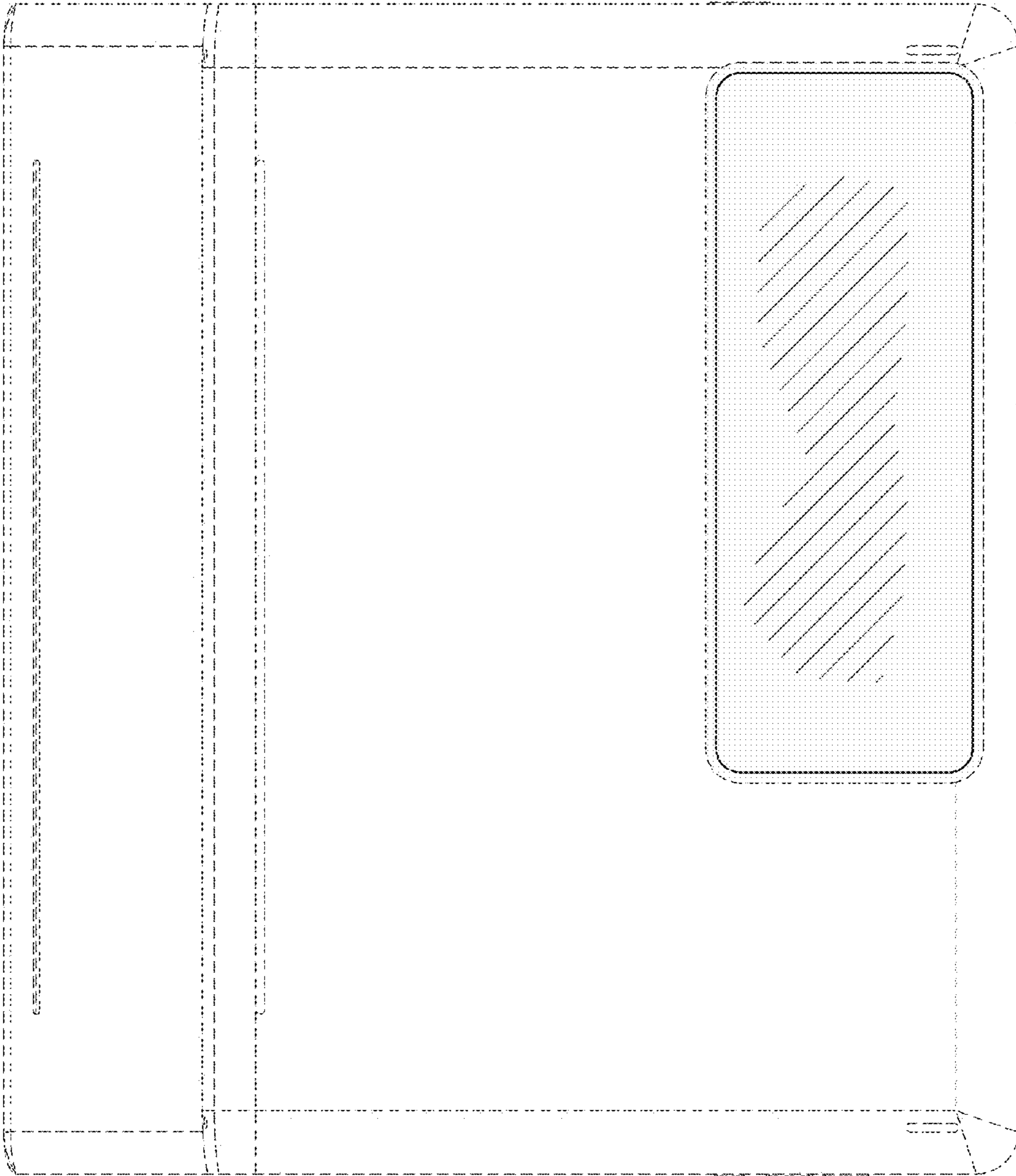


FIG. 14





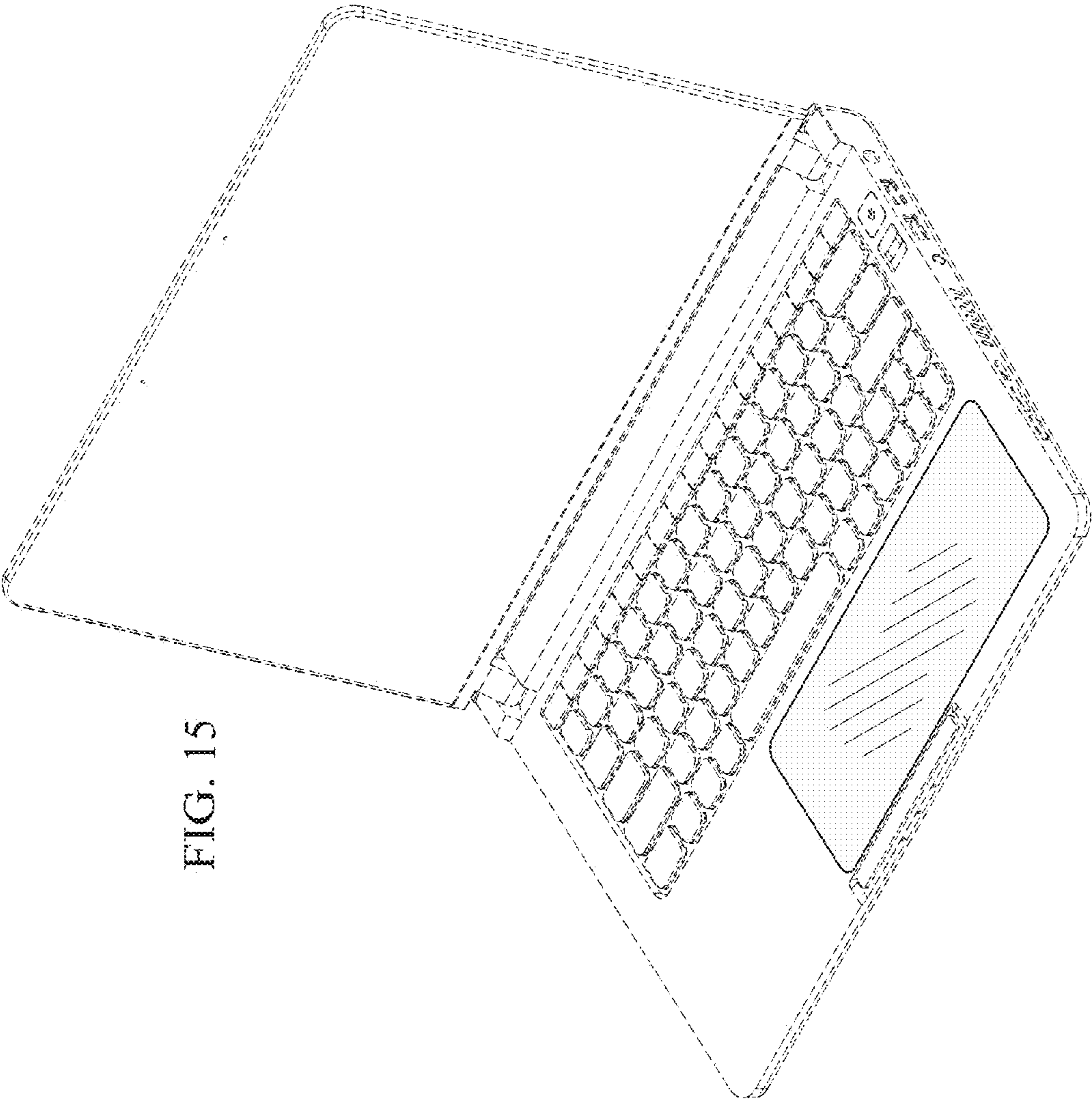


FIG. 15

FIG. 16

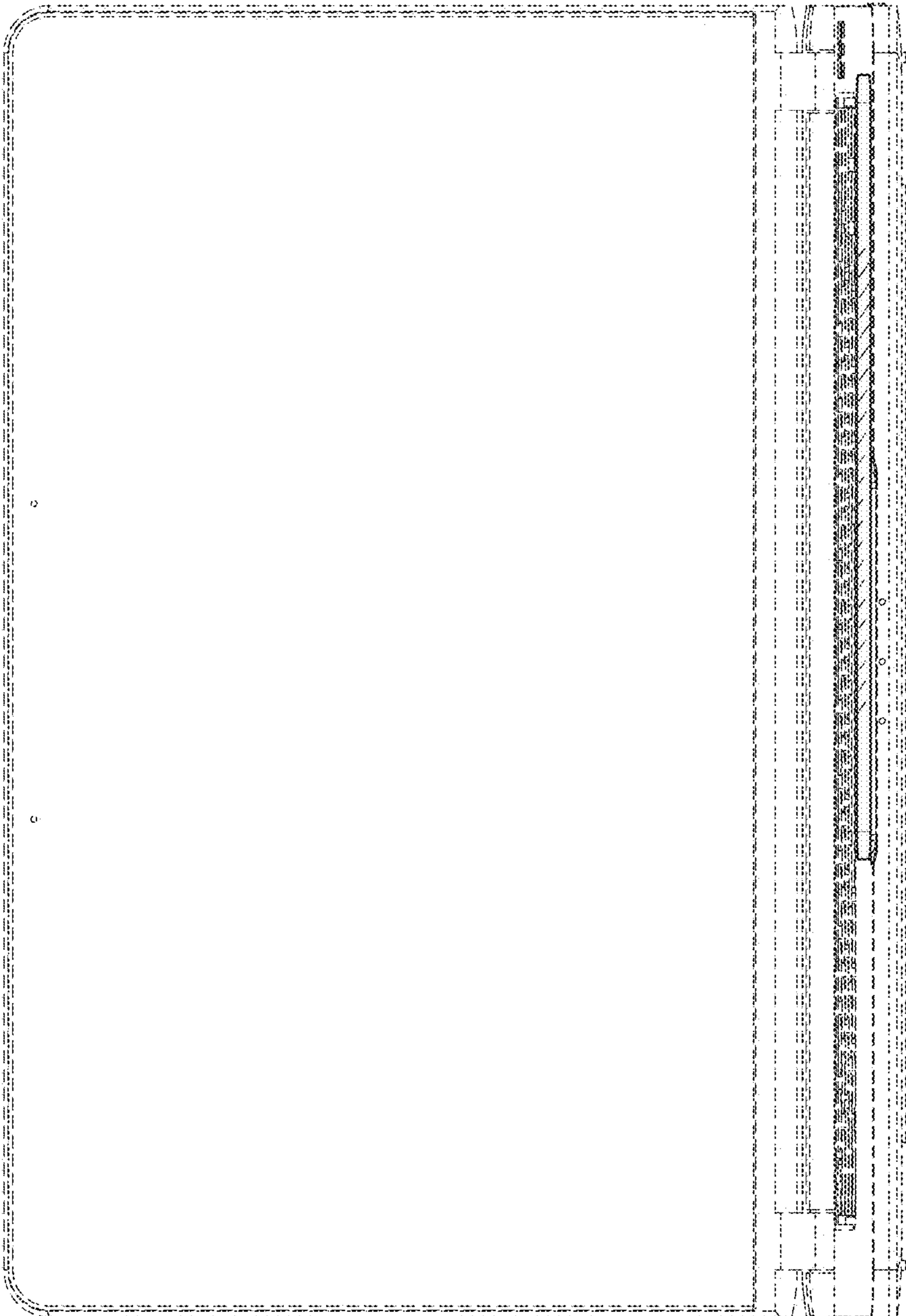
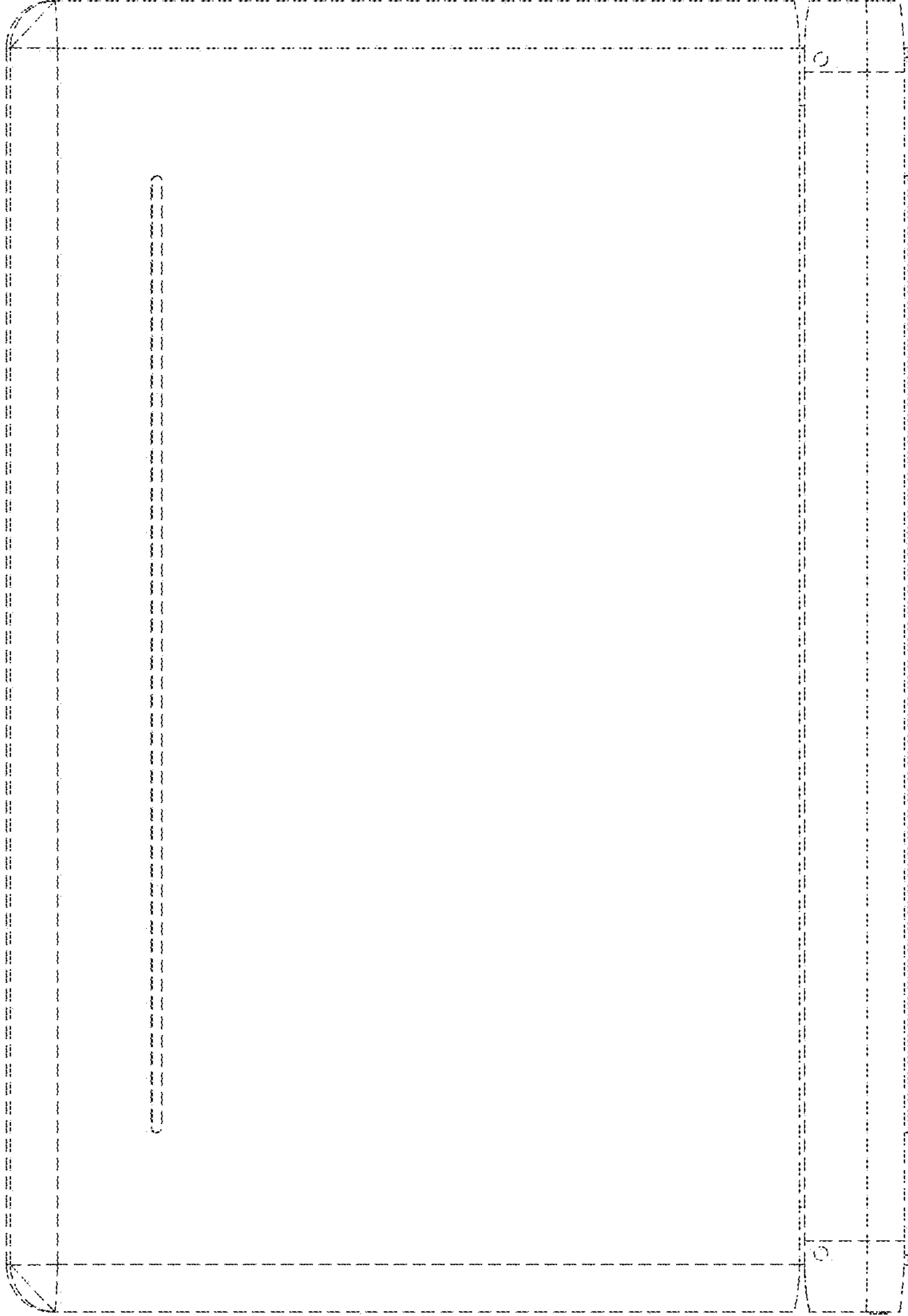


FIG. 17



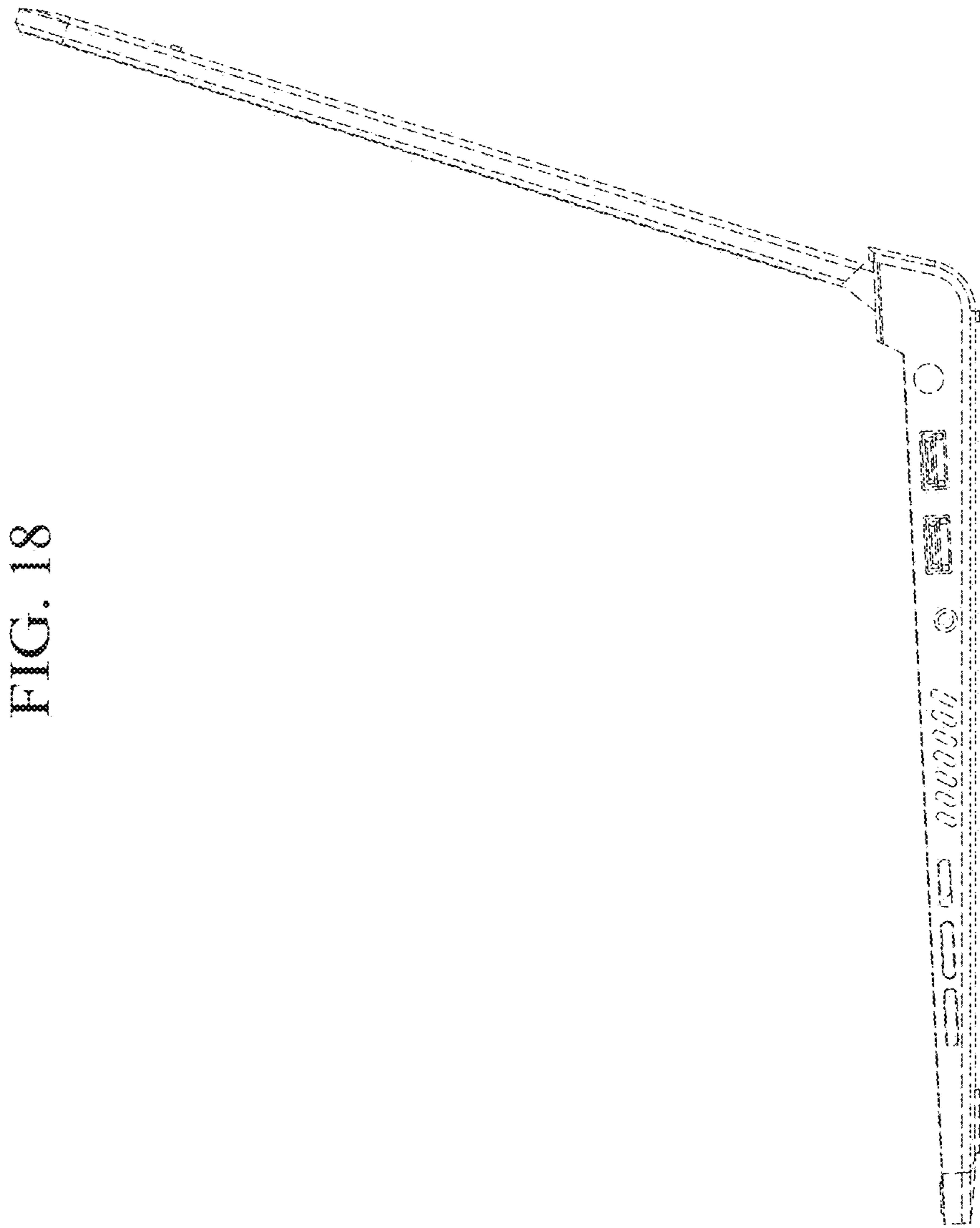


FIG. 18

FIG. 19

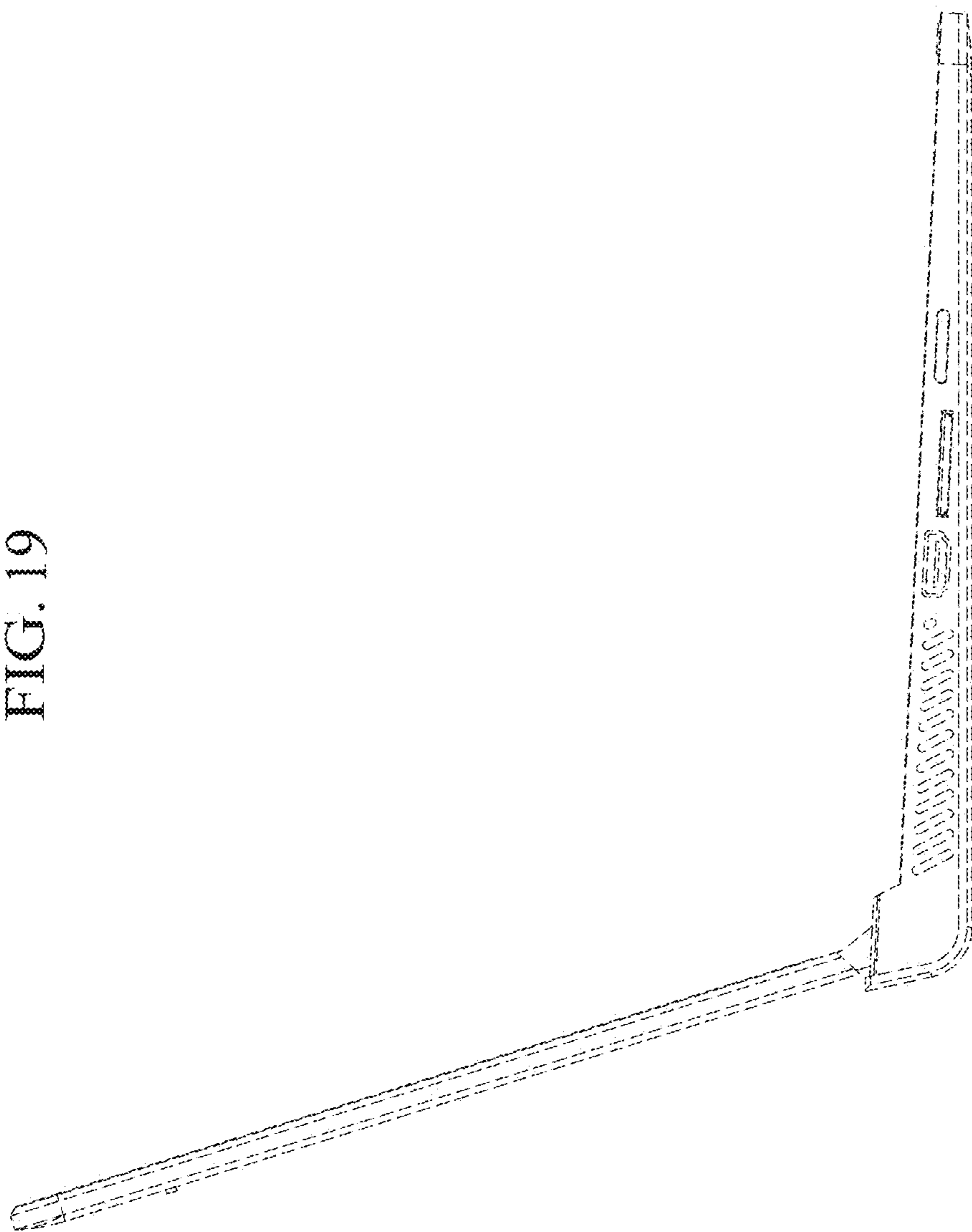


FIG. 20

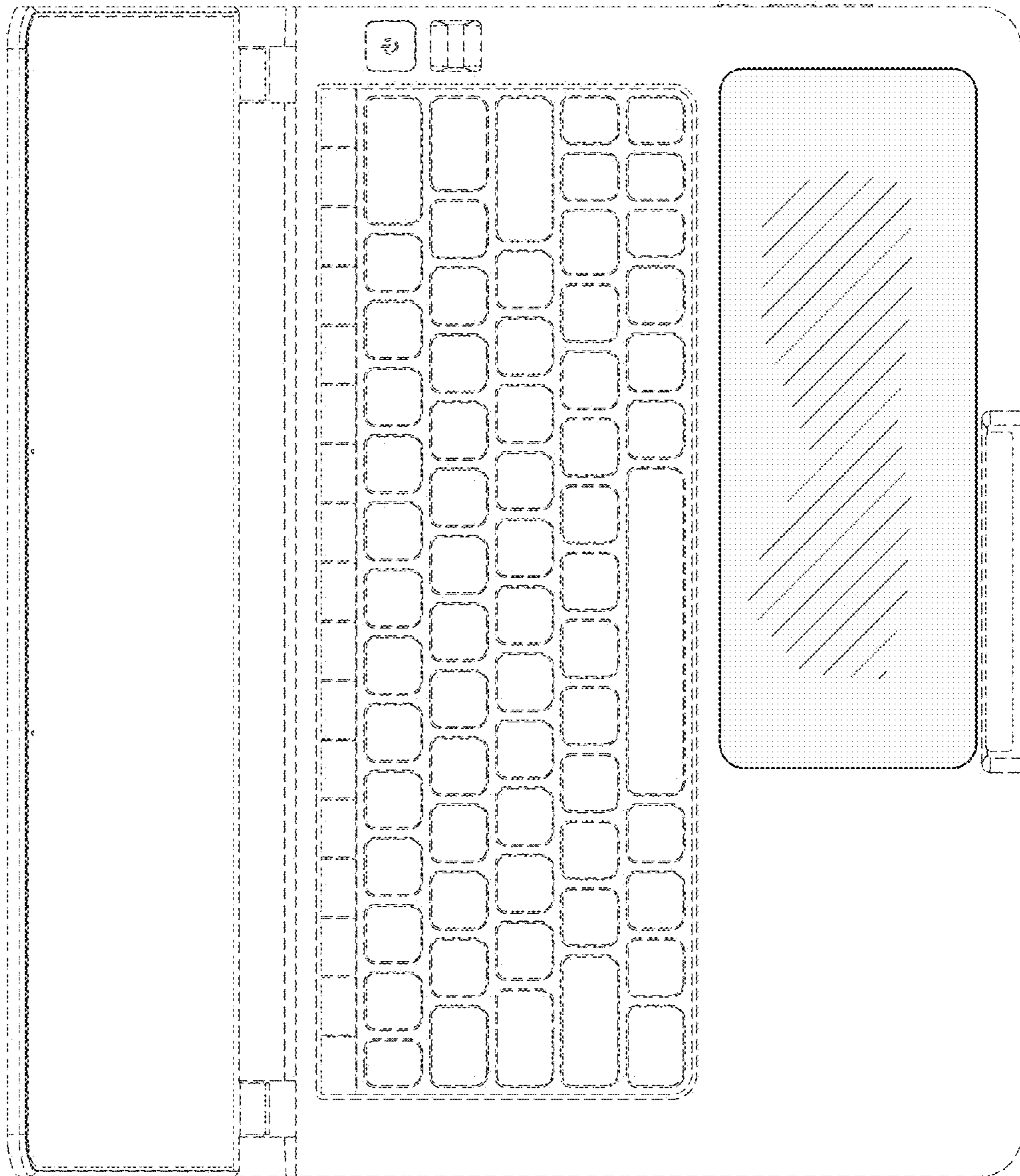
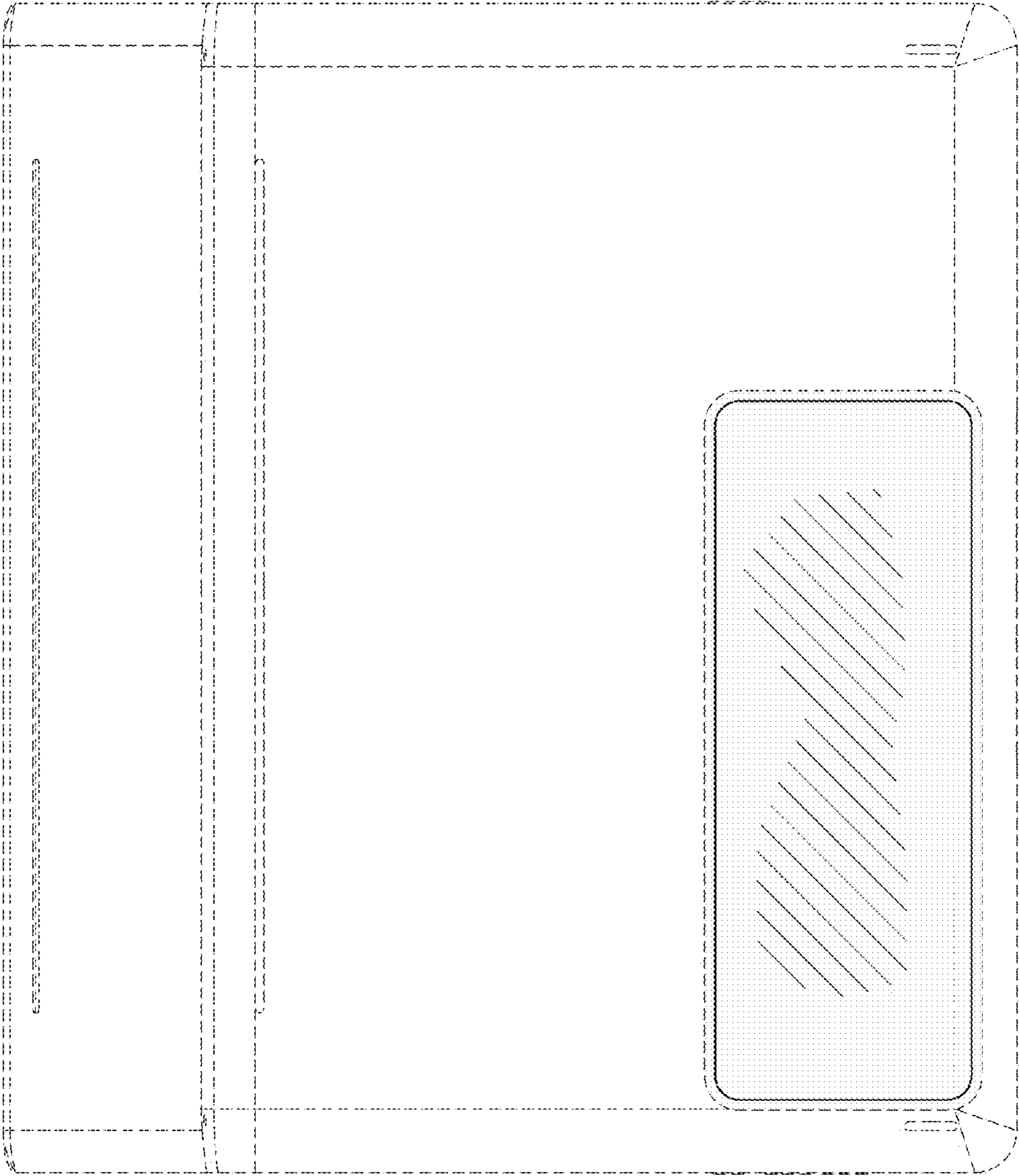


FIG. 21



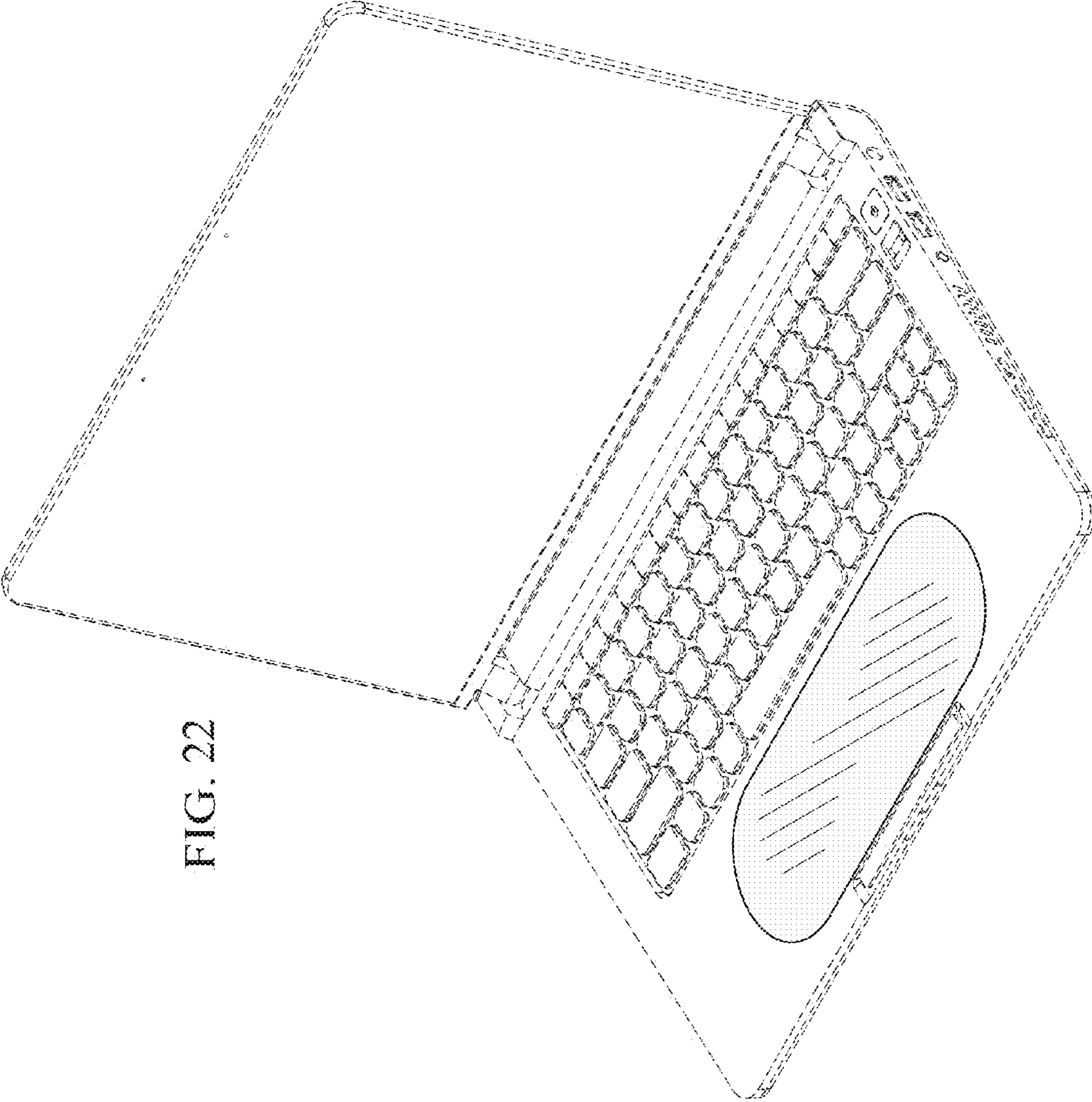


FIG. 22



FIG. 23

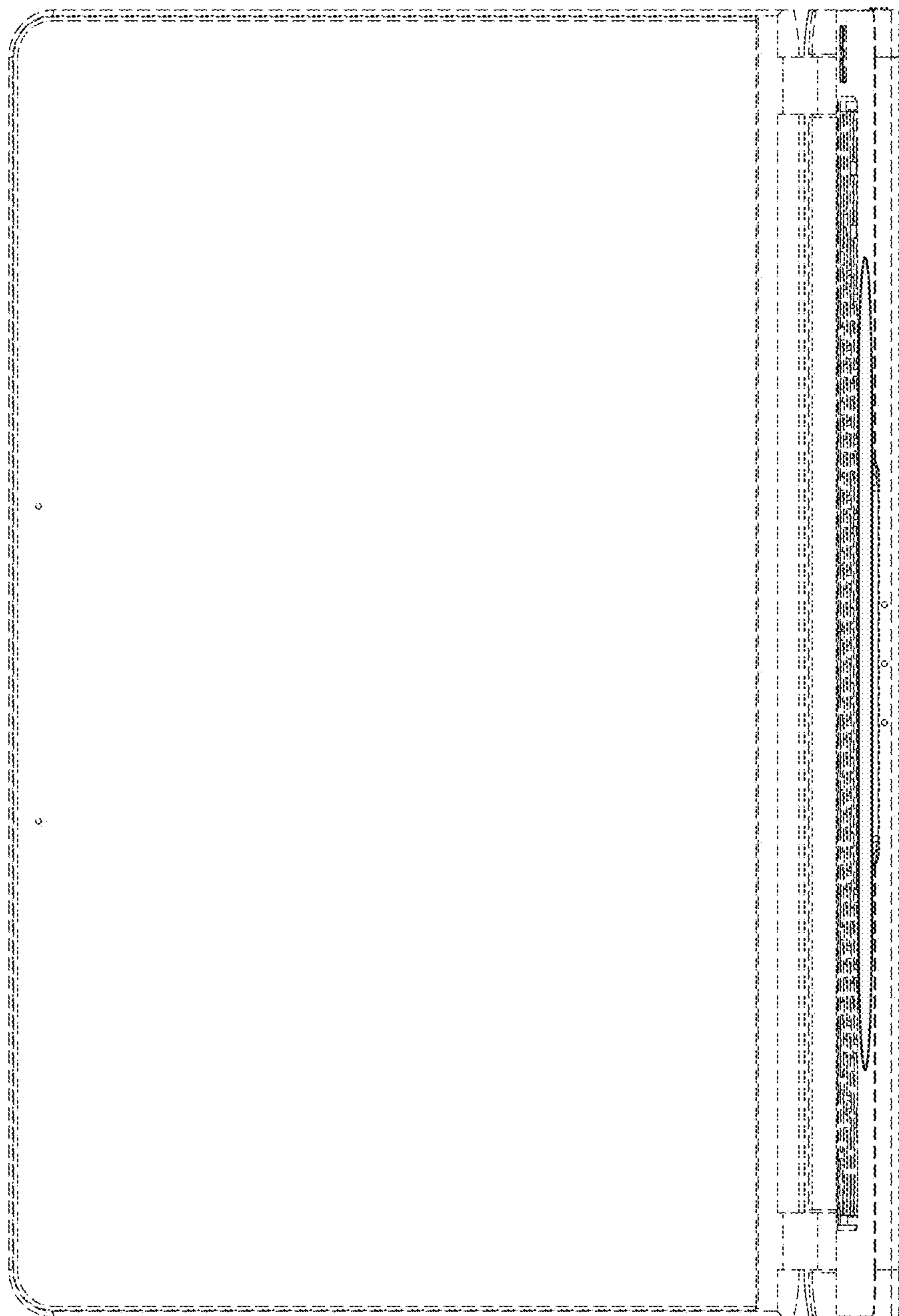


FIG. 24

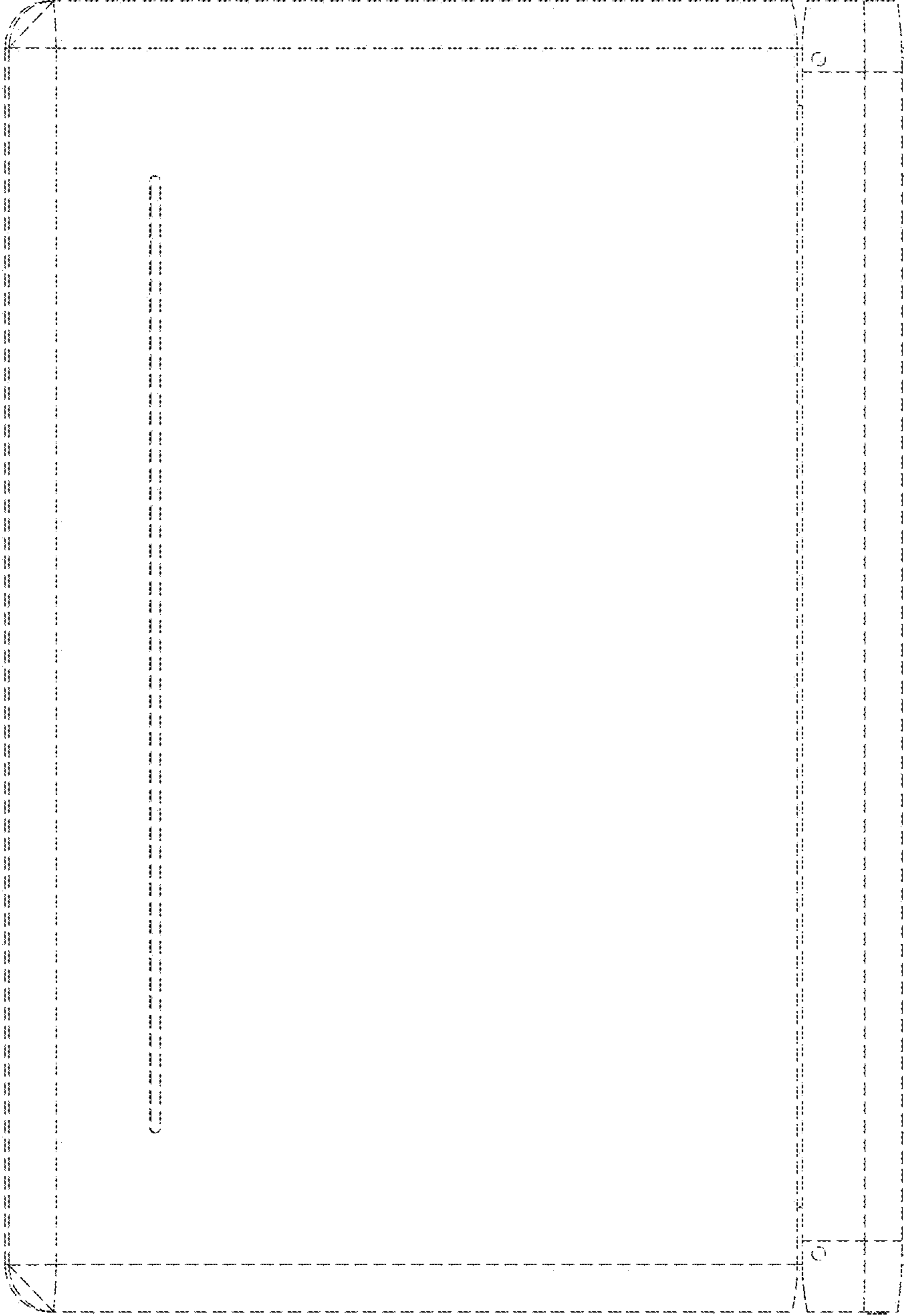


FIG. 25

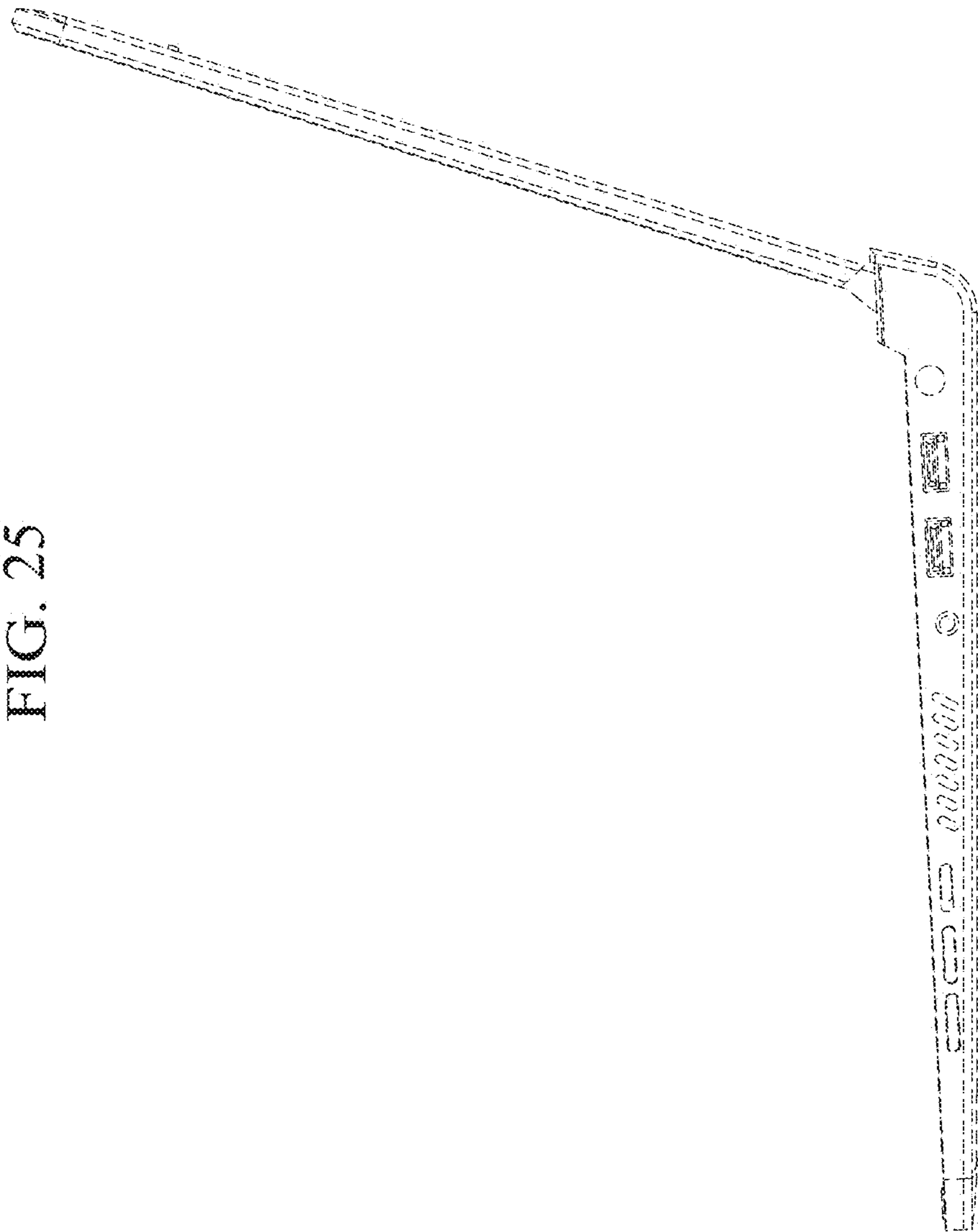


FIG. 26

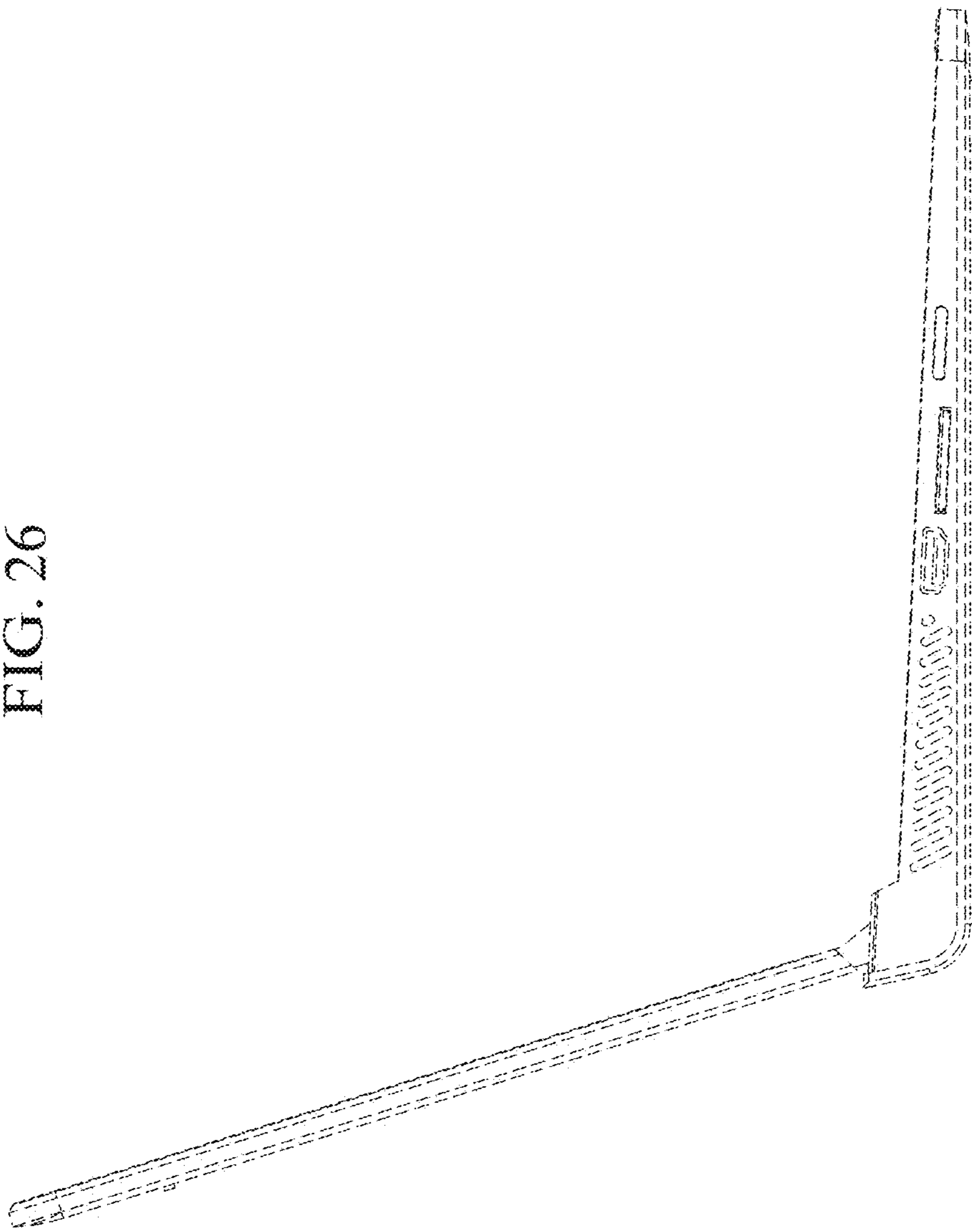


FIG. 27

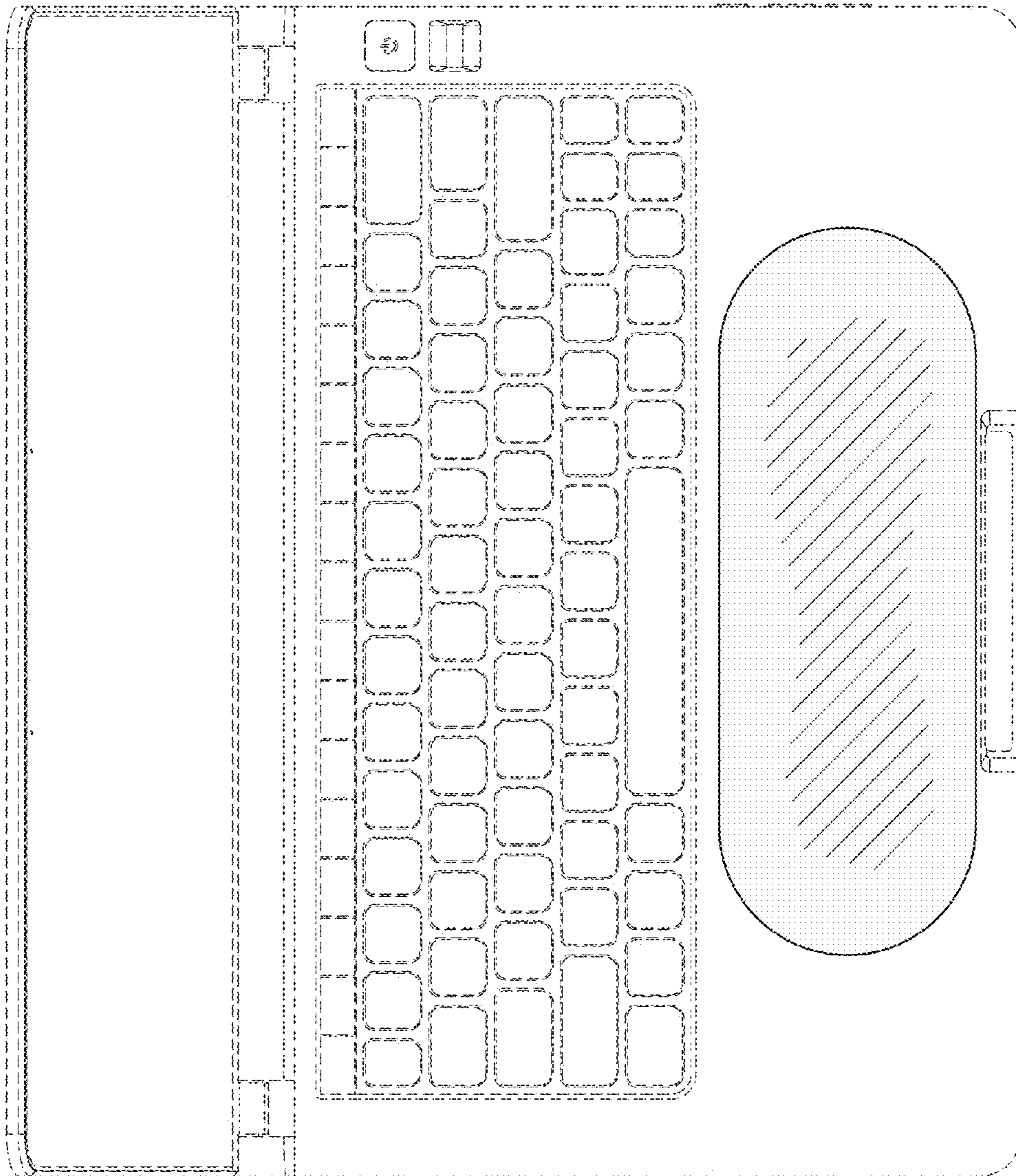
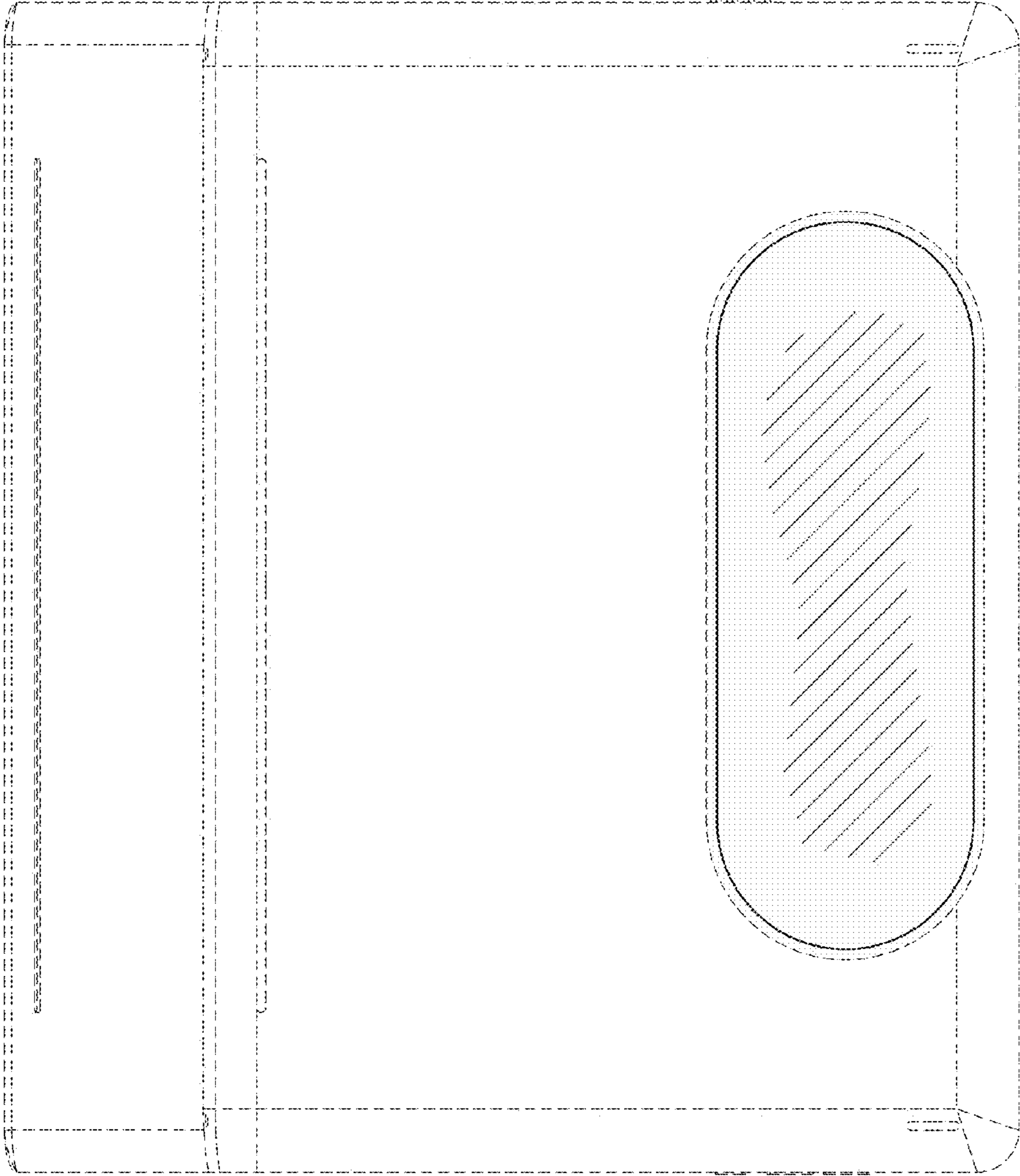


FIG. 28



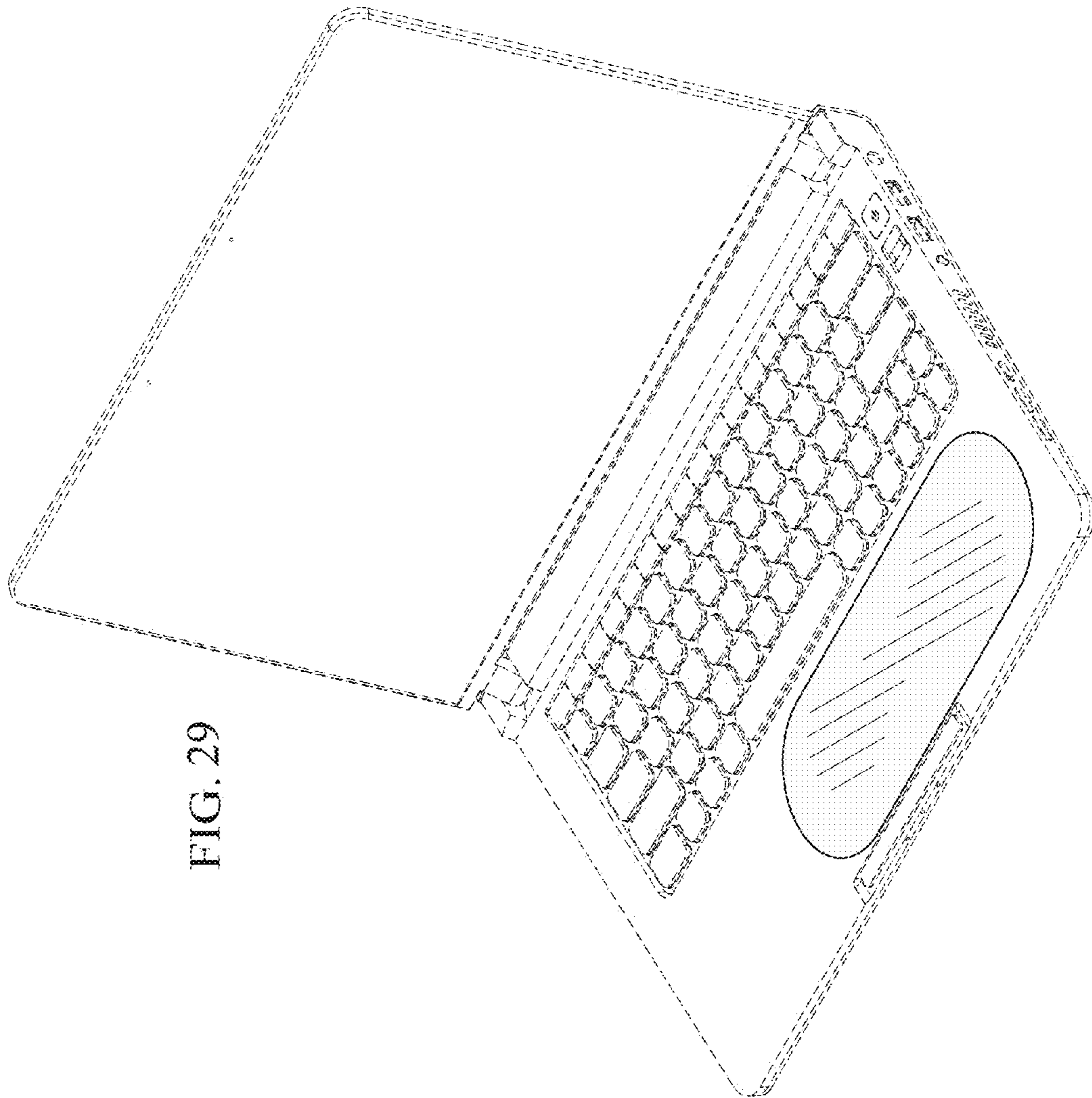


FIG. 29

FIG. 30

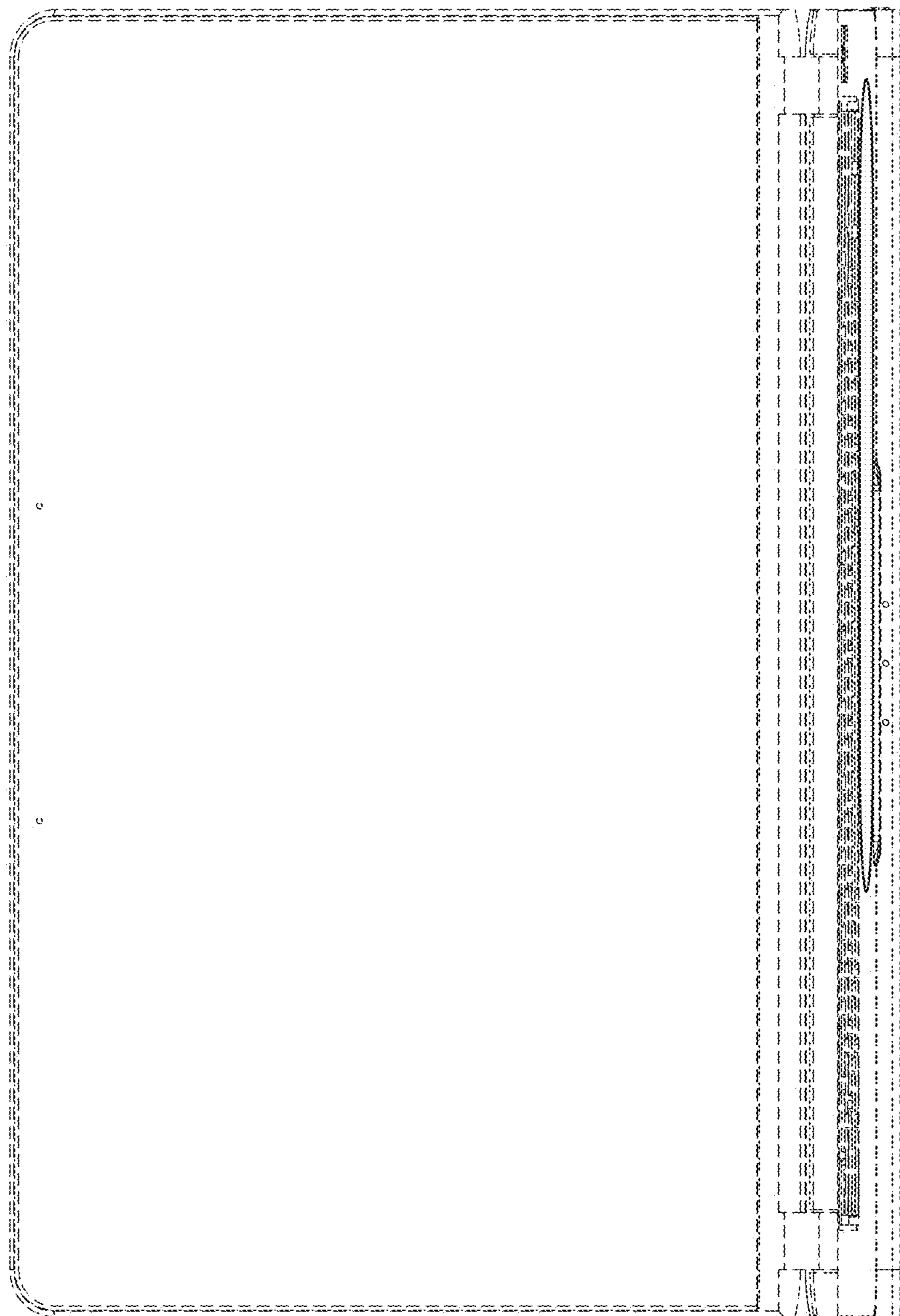




FIG. 31

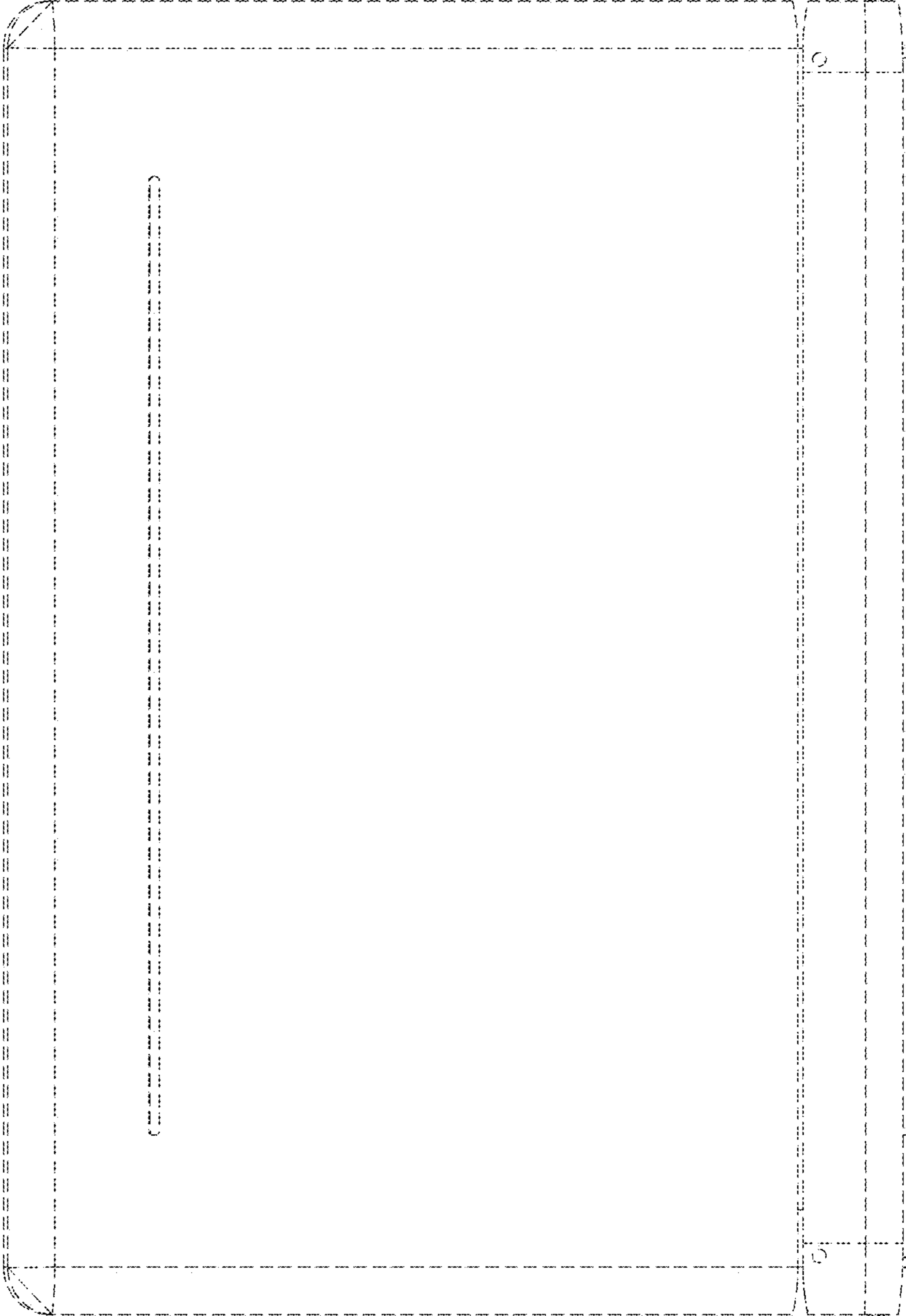


FIG. 32

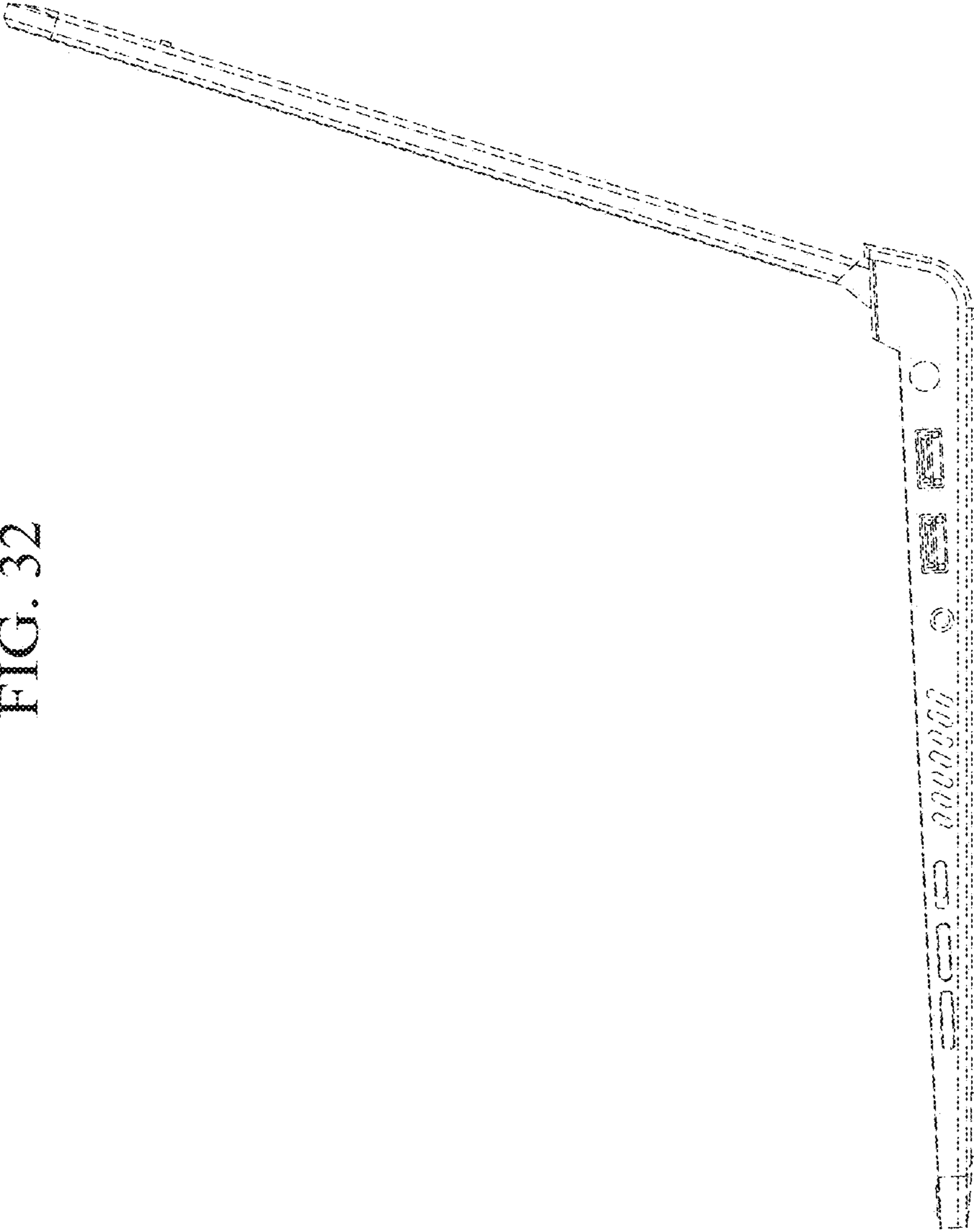


FIG. 33

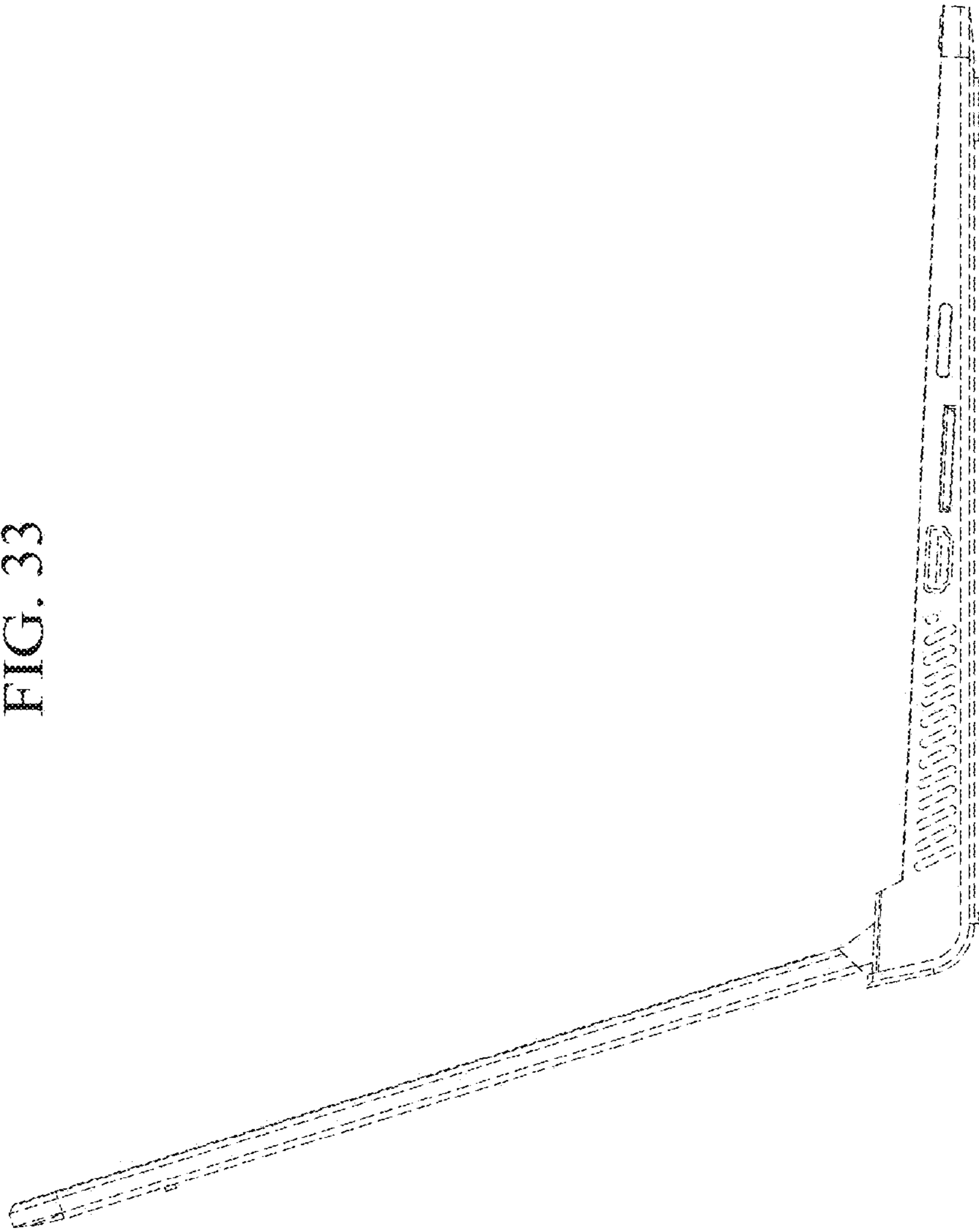


FIG. 34

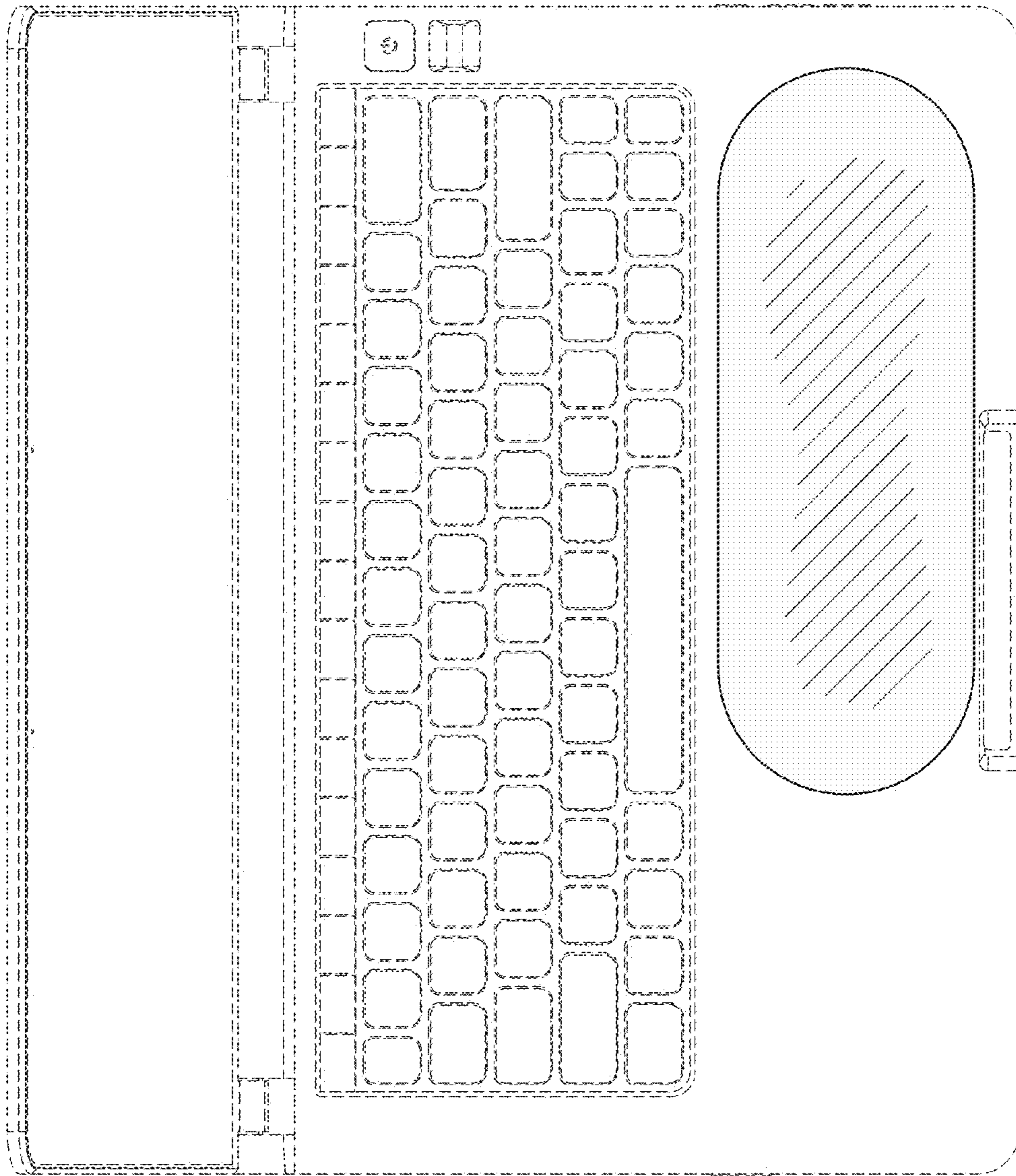
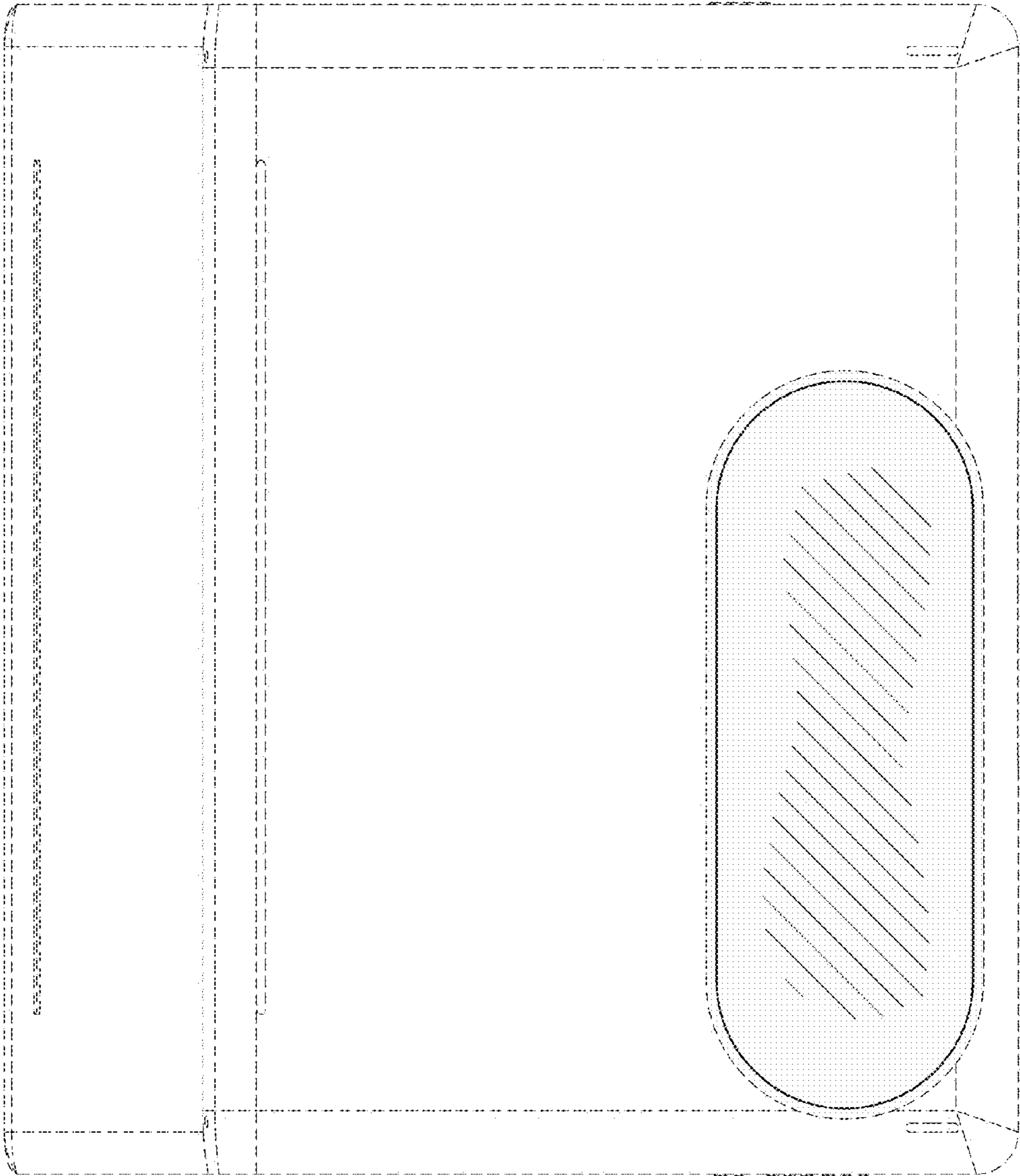


FIG. 35



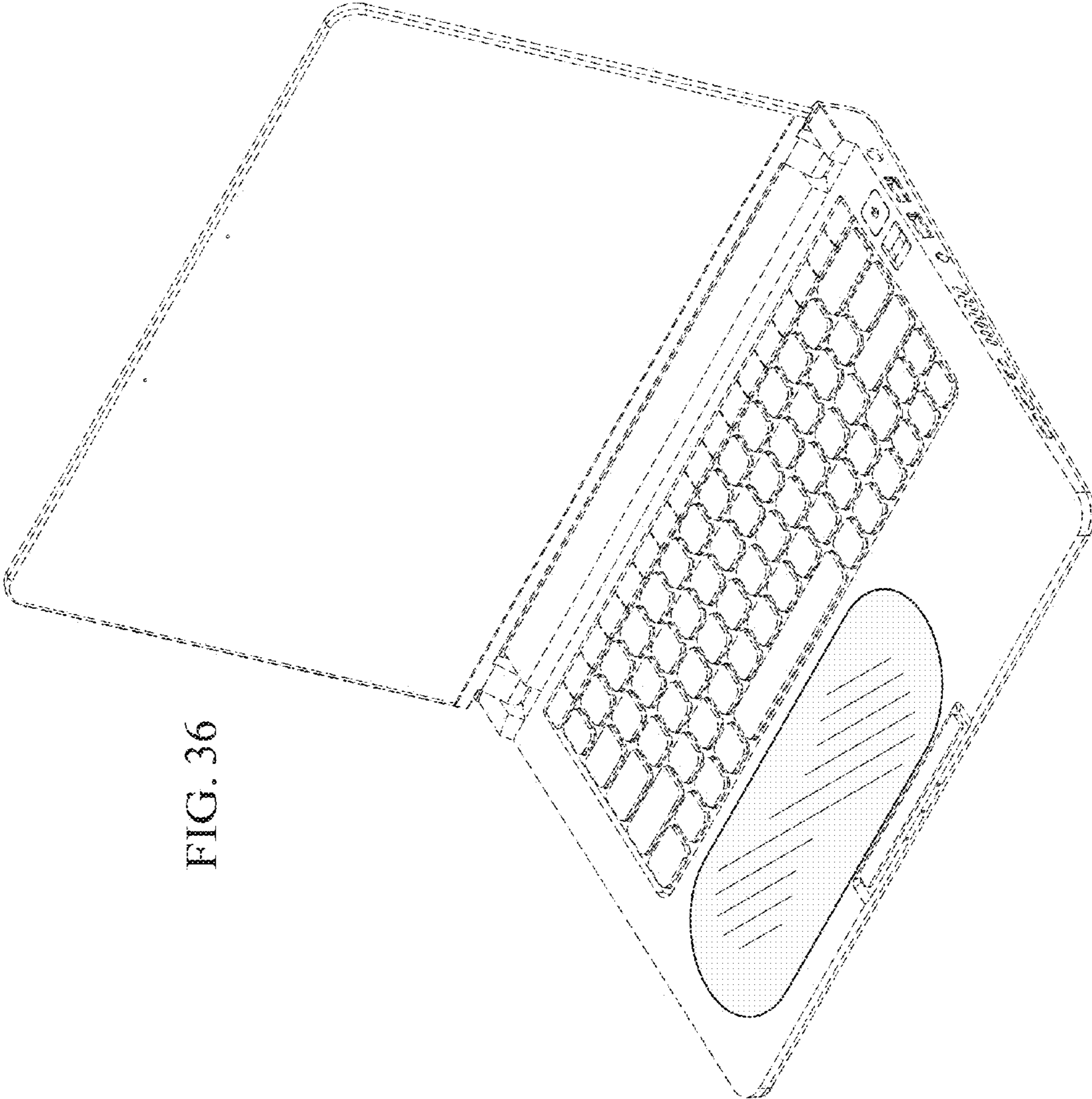


FIG. 36

FIG. 37

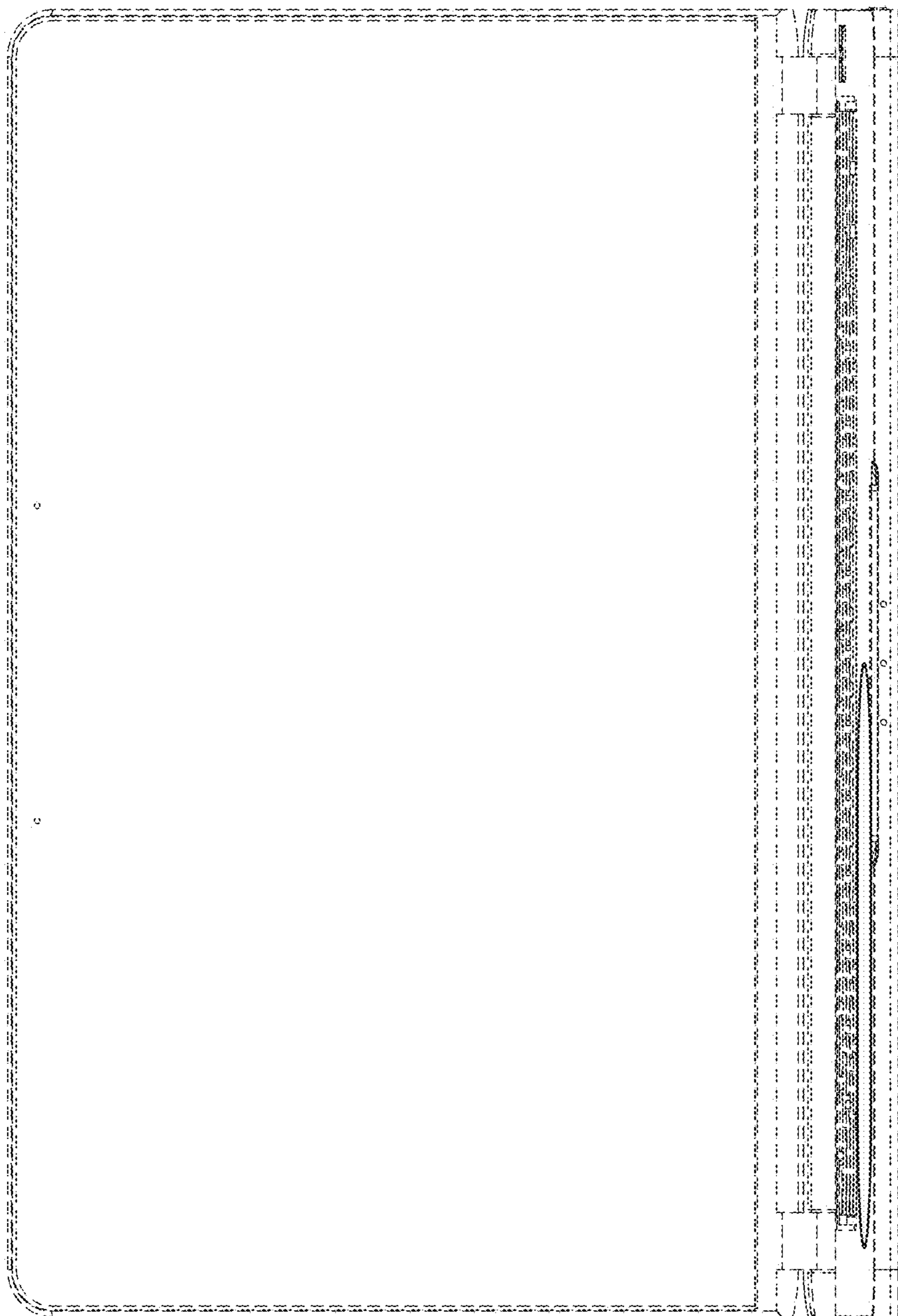
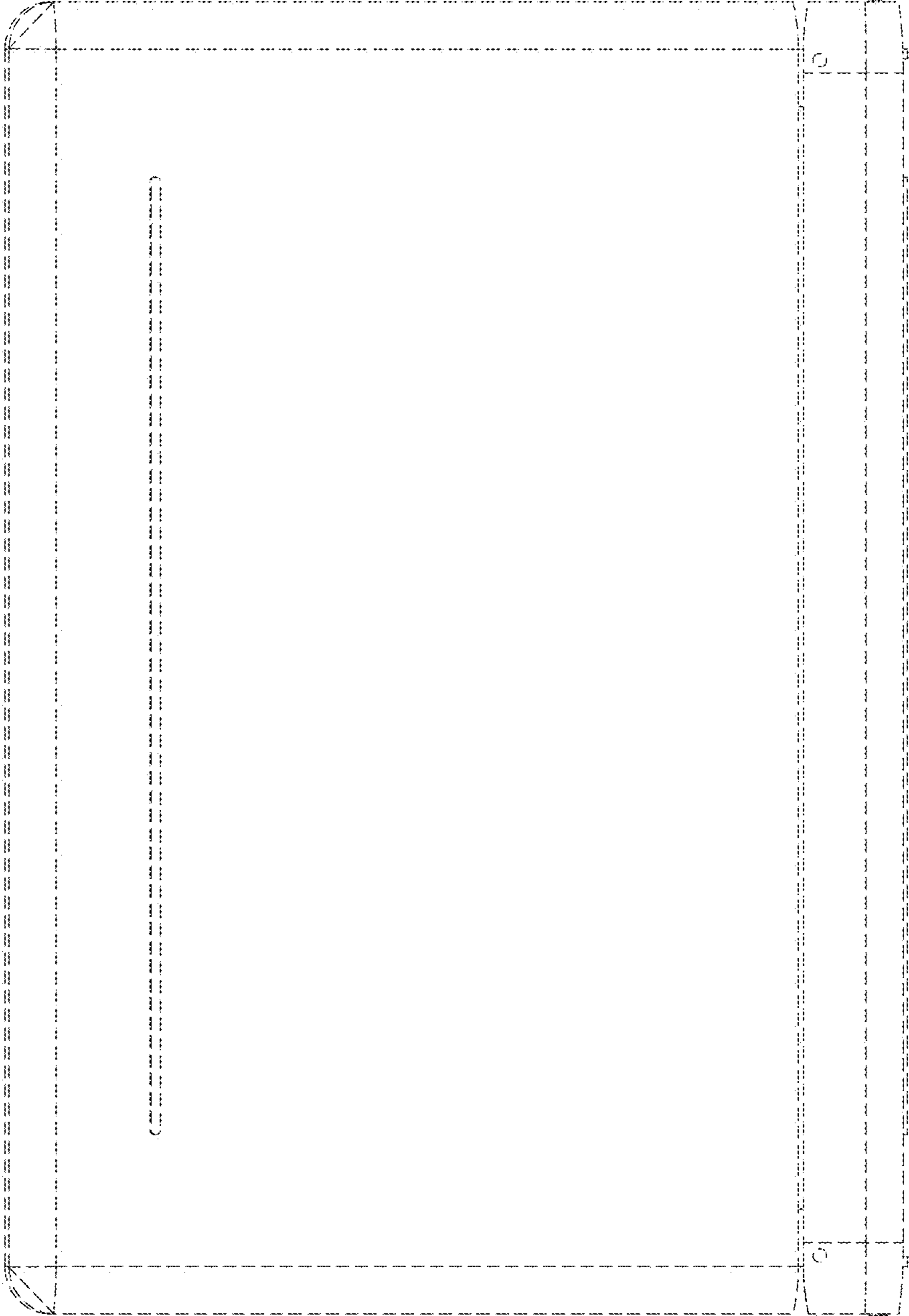


FIG. 38





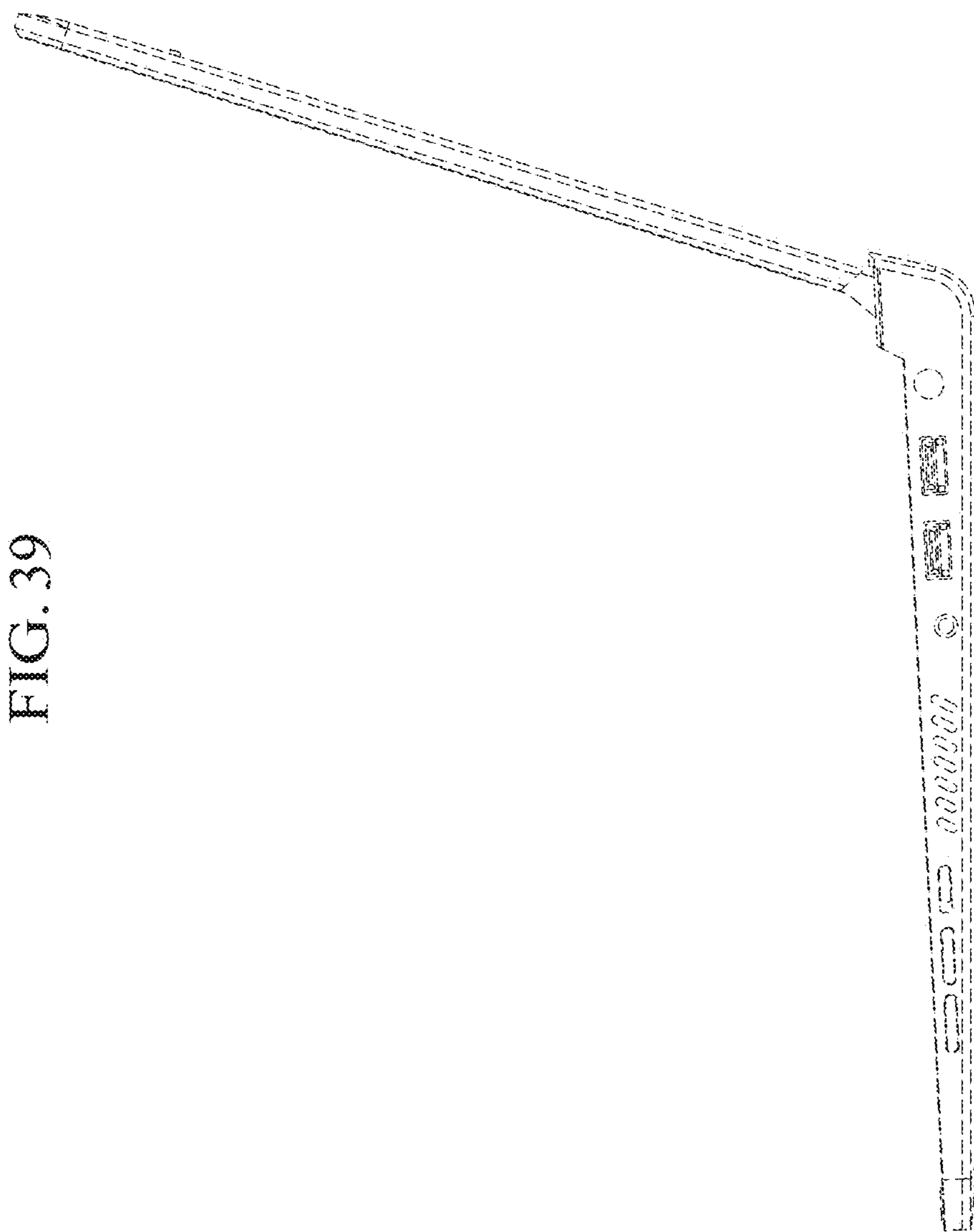


FIG. 39

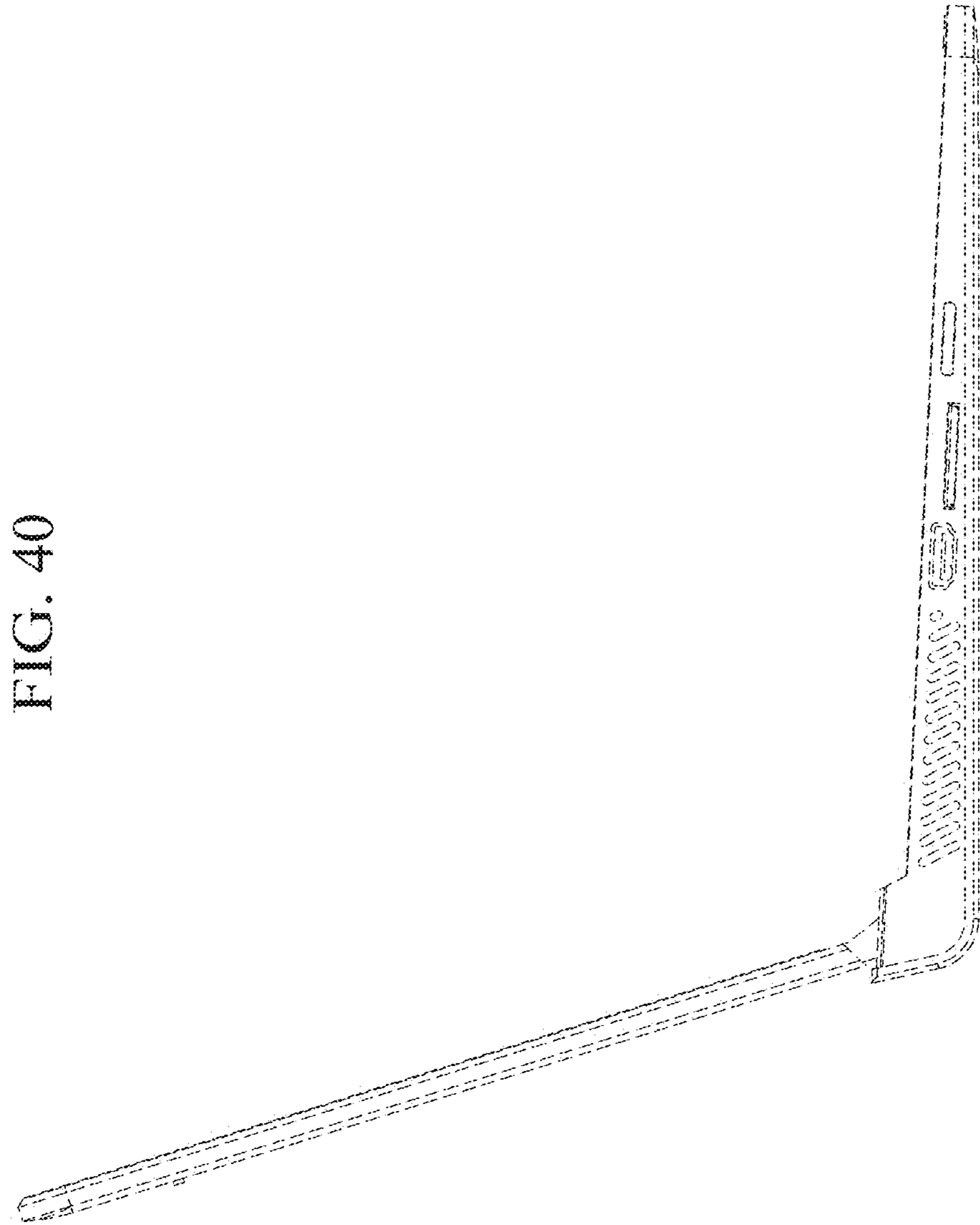


FIG. 40

FIG. 41

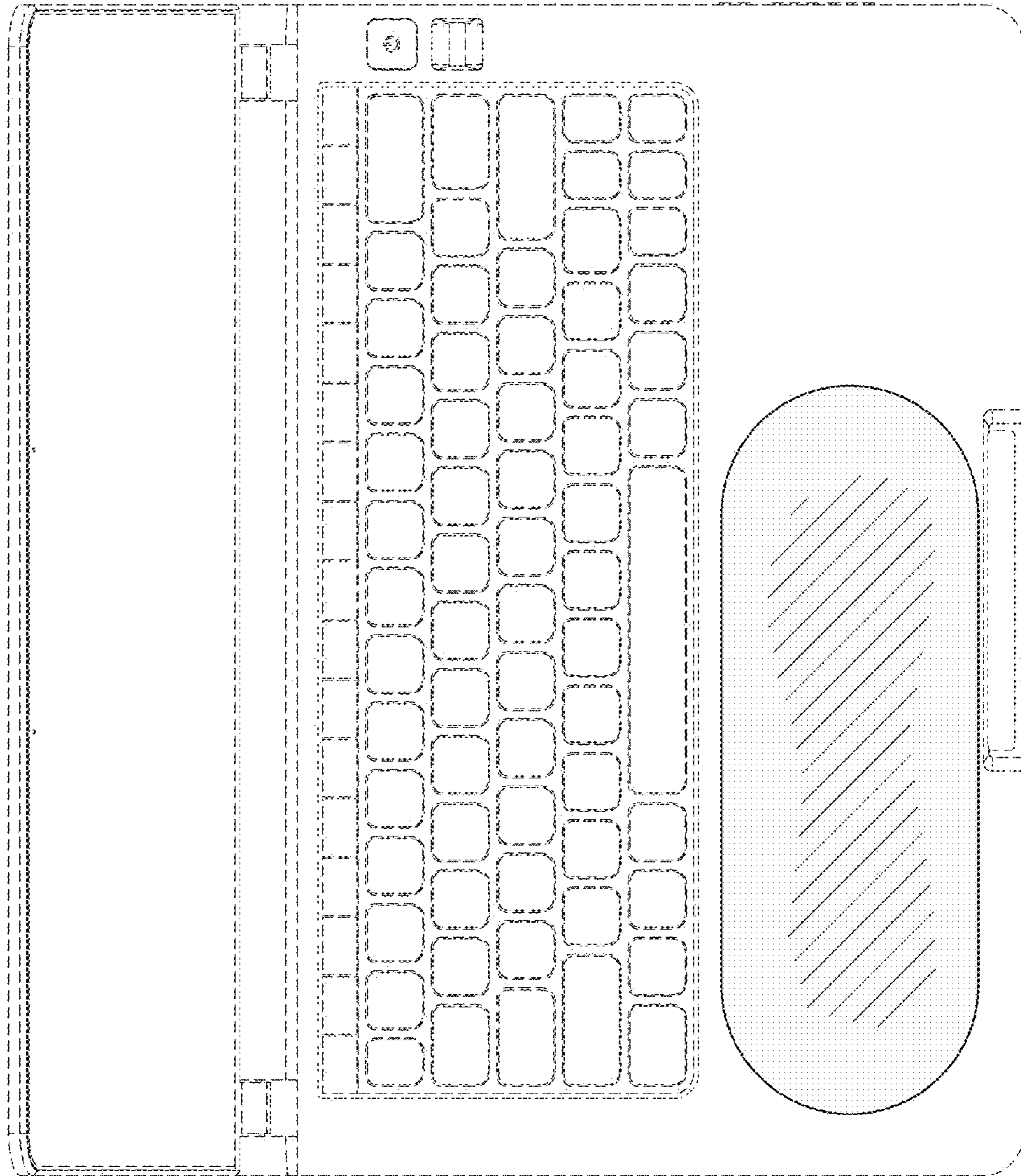


FIG. 42

