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(12) **United States Design Patent**
Adamson et al.

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(54) **ELECTRONIC COMPUTER WITH AN AT LEAST PARTIALLY TRANSPARENT INPUT DEVICE**

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(73) Assignee: **Intel Corporation**, Santa Clara, CA (US)

(*) Notice: This patent is subject to a terminal disclaimer.

(**) Term: **14 Years**

(21) Appl. No.: **29/473,585**

(22) Filed: **Nov. 24, 2013**

Related U.S. Application Data

(62) Division of application No. 29/423,475, filed on May 31, 2012, now Pat. No. Des. 694,232.

(51) **LOC (10) Cl.** **14-02**

(52) **U.S. Cl.**
USPC **D14/318**

(58) **Field of Classification Search**
USPC D14/315-327; D18/1, 2, 7, 11;
235/145 A, 145 R; 341/22, 23; 345/104,
345/156, 168, 169, 173; 361/679.06,
361/679.08, 679.09, 679.11, 679.26, 679.27
See application file for complete search history.

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Primary Examiner — Freda S Nunn

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(57) **CLAIM**

The ornamental design for an electronic computer with an at least partially transparent input device, as shown and described.

DESCRIPTION

FIG. 1 is a top perspective view of electronic computer with an at least partially transparent input device, showing our new design;

FIG. 2 is a front view thereof;

FIG. 3 is a back view thereof;

FIG. 4 is a side view thereof;

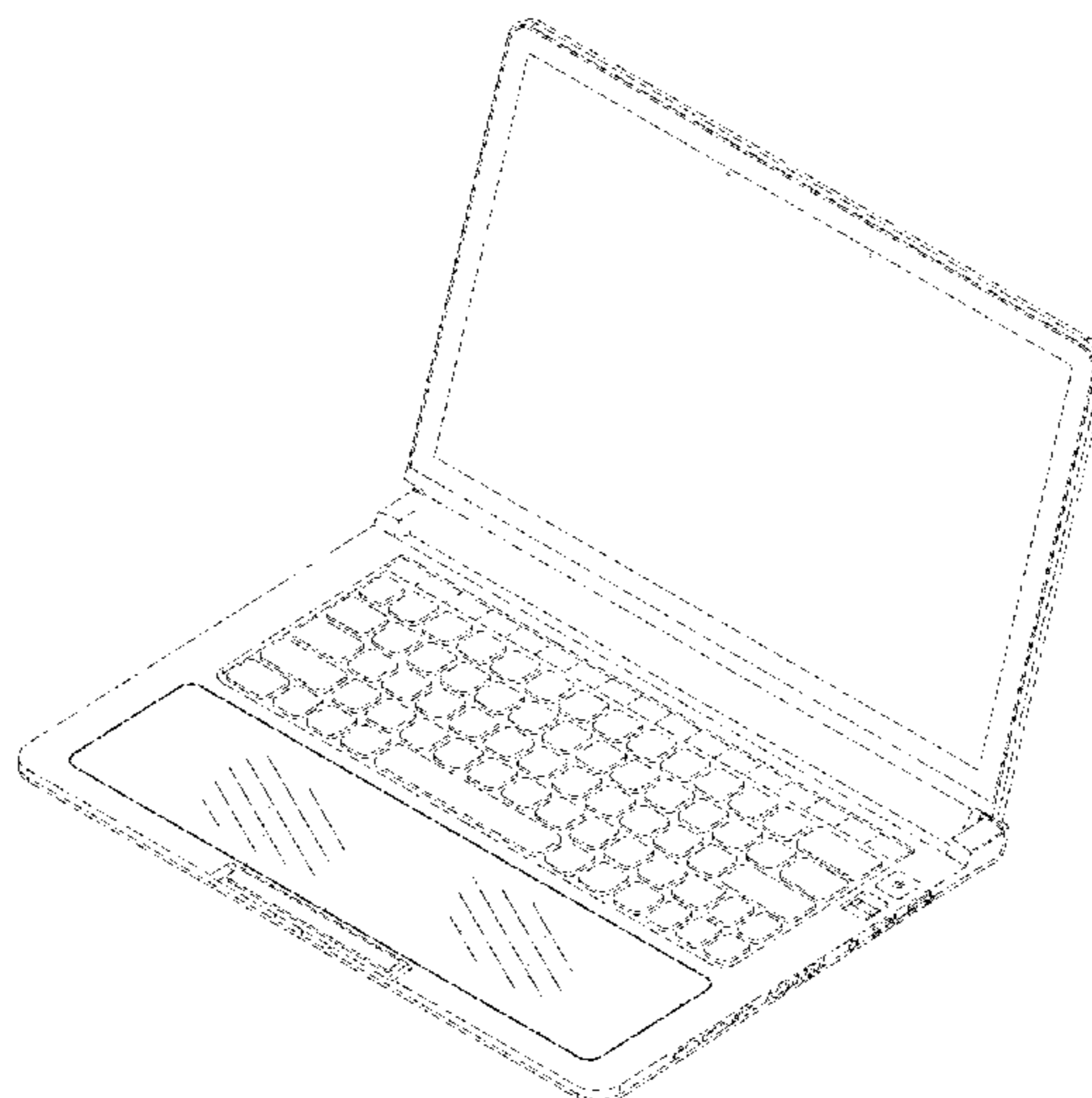
FIG. 5 is another side view thereof;

FIG. 6 is a top view thereof; and,

FIG. 7 is a bottom view thereof.

The-broken lines in the preceding FIGURES illustrate portions of the computer and form no part of the claimed design.

1 Claim, 7 Drawing Sheets



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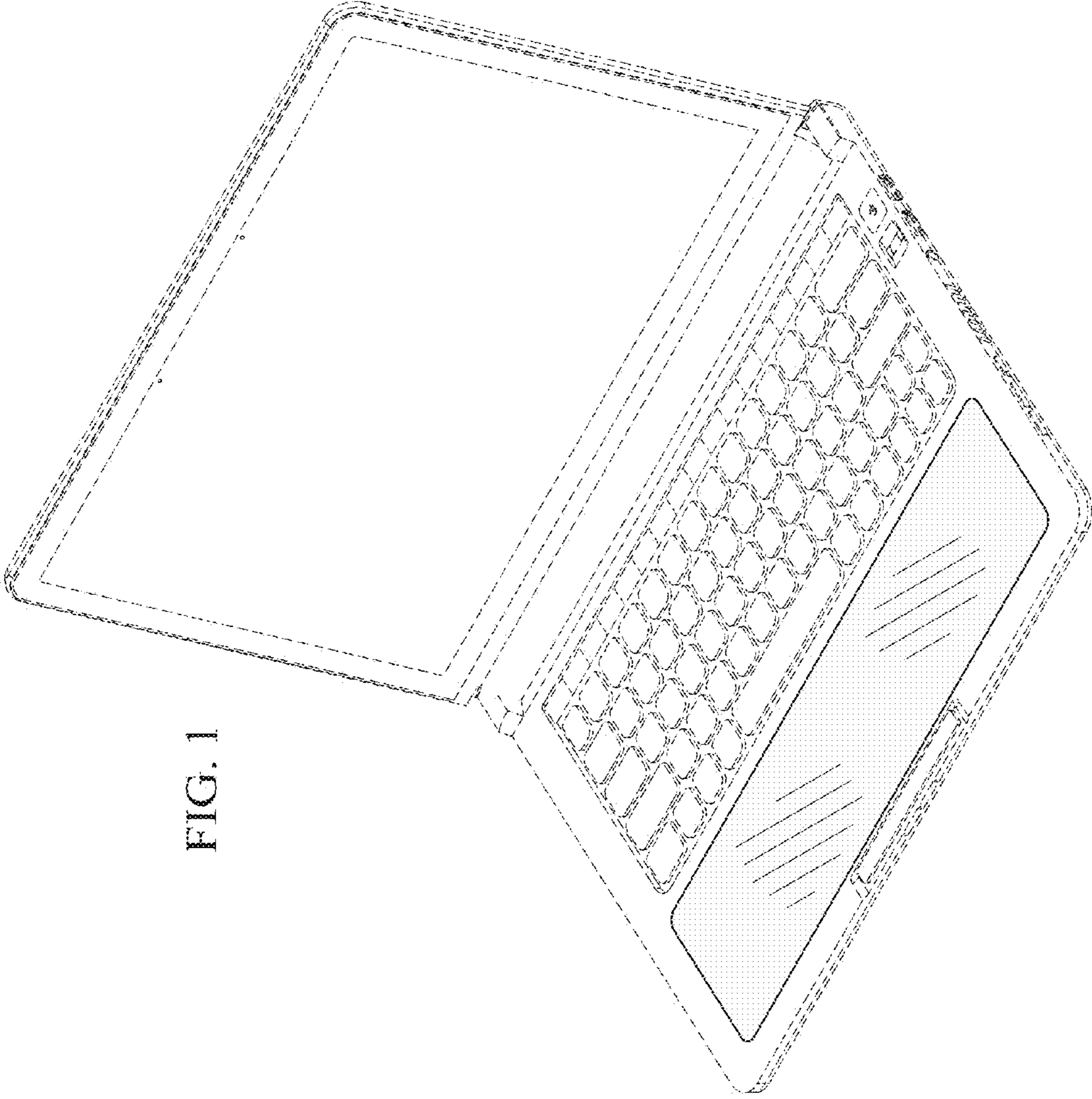


FIG. 1

FIG. 2

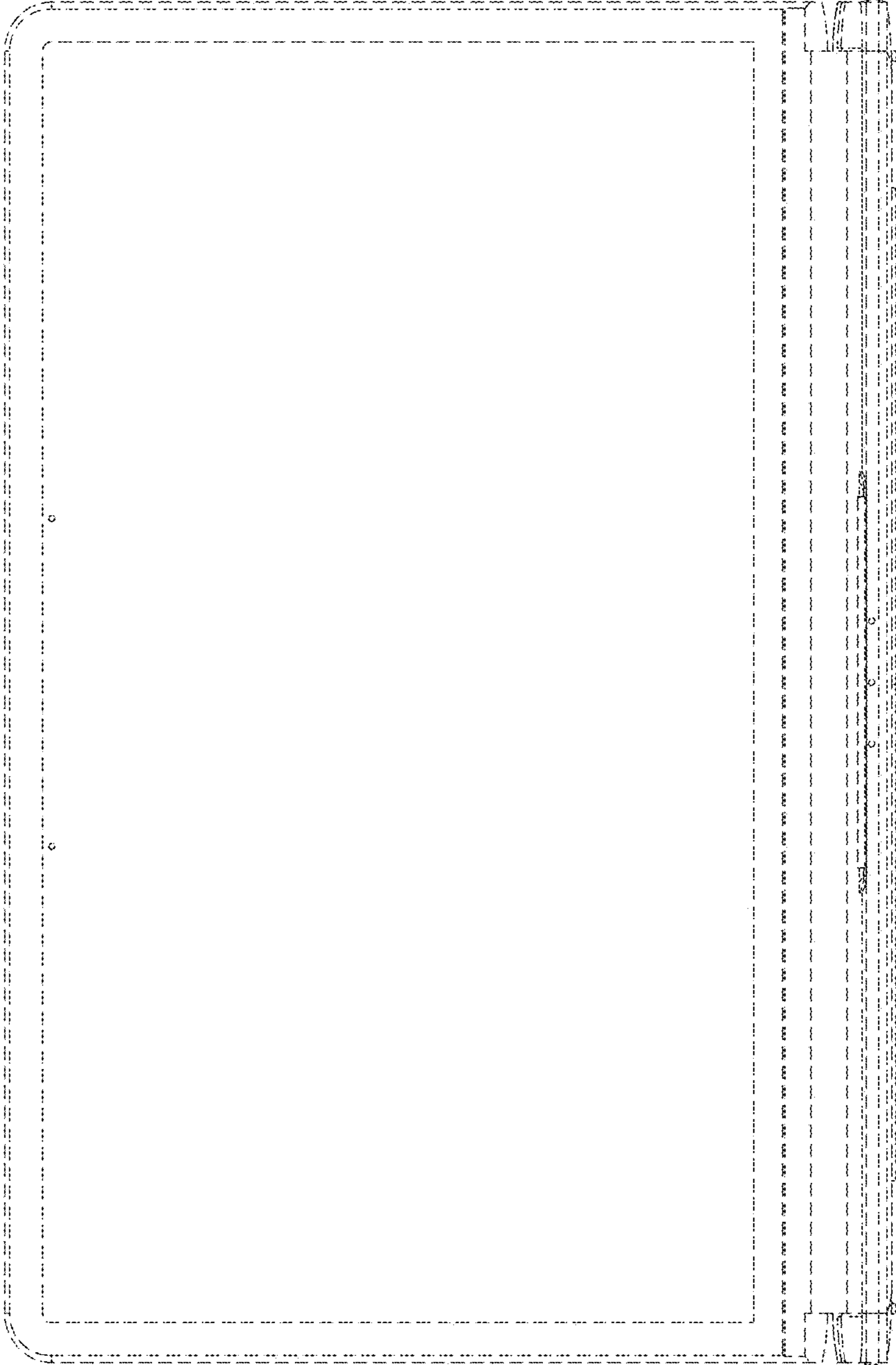


FIG. 3

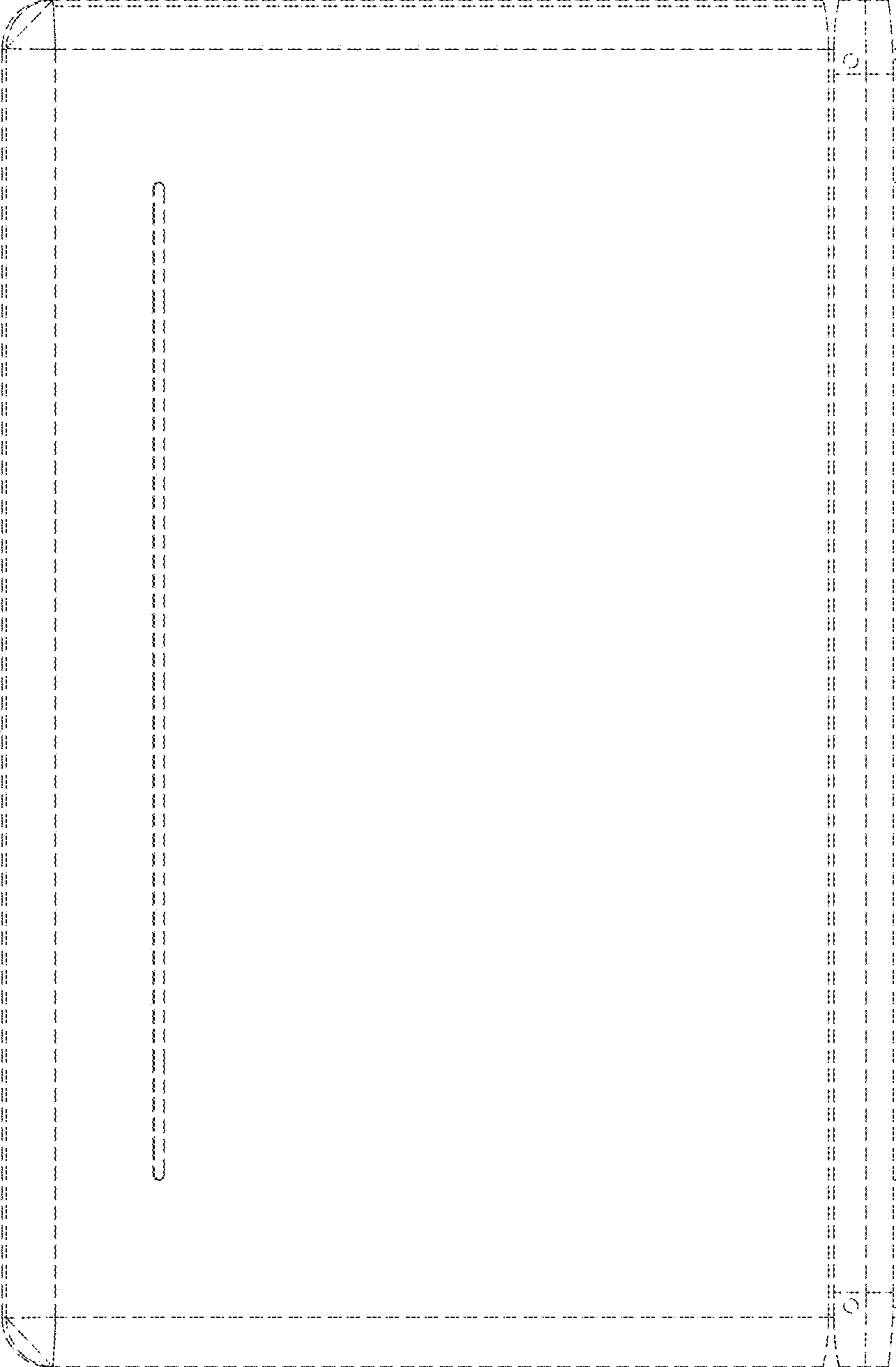


FIG. 4

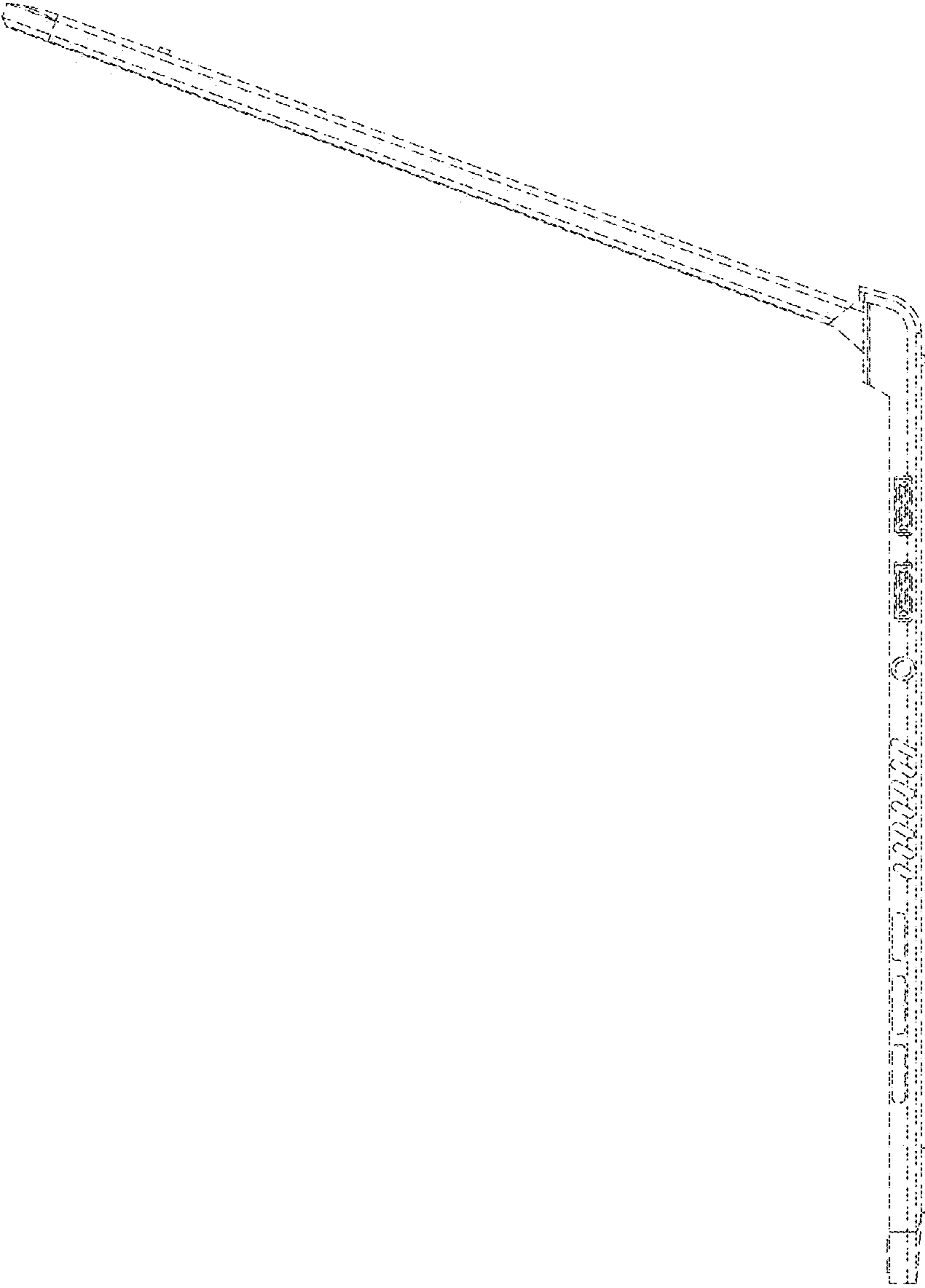


FIG. 5

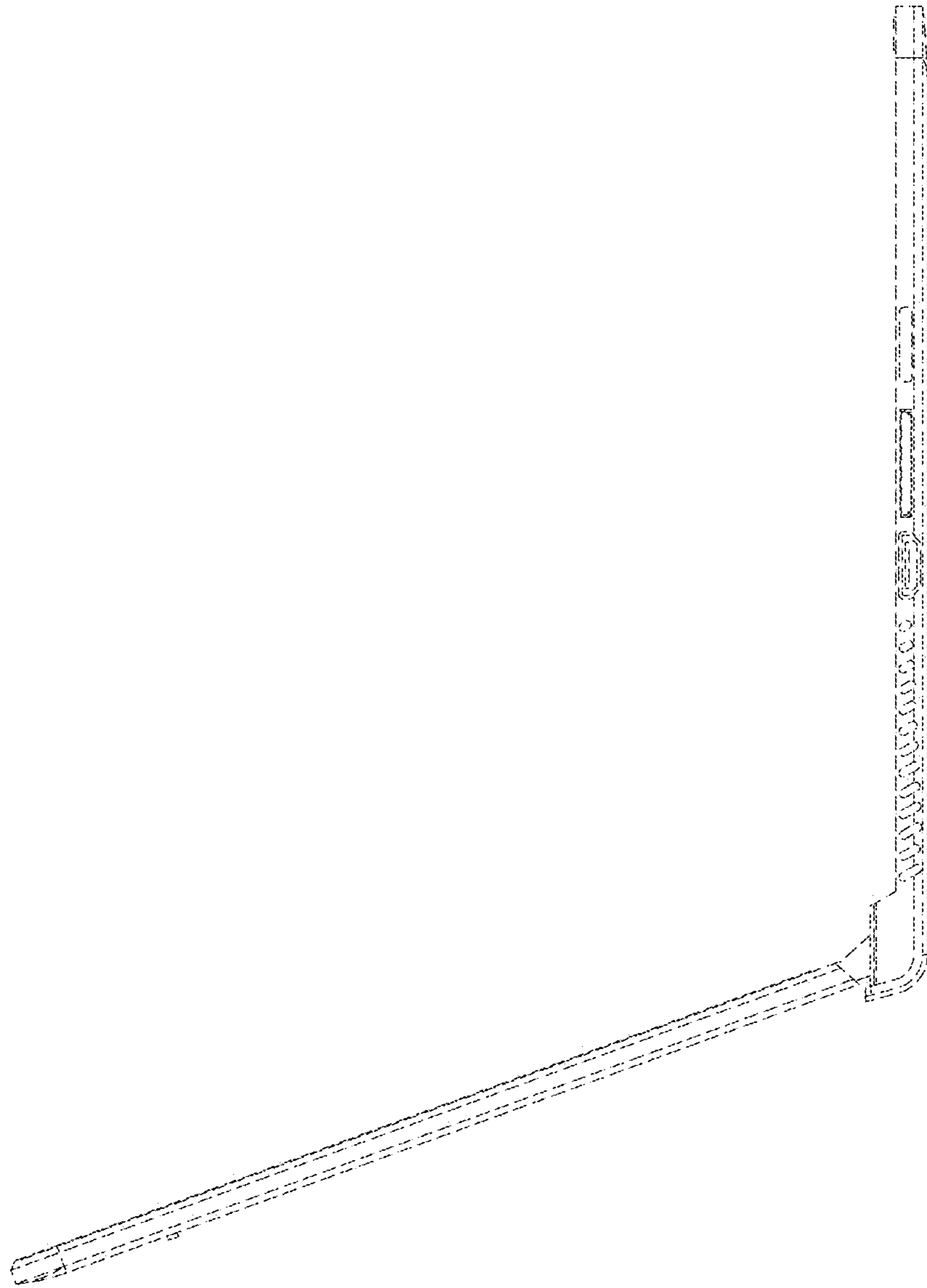


FIG. 6

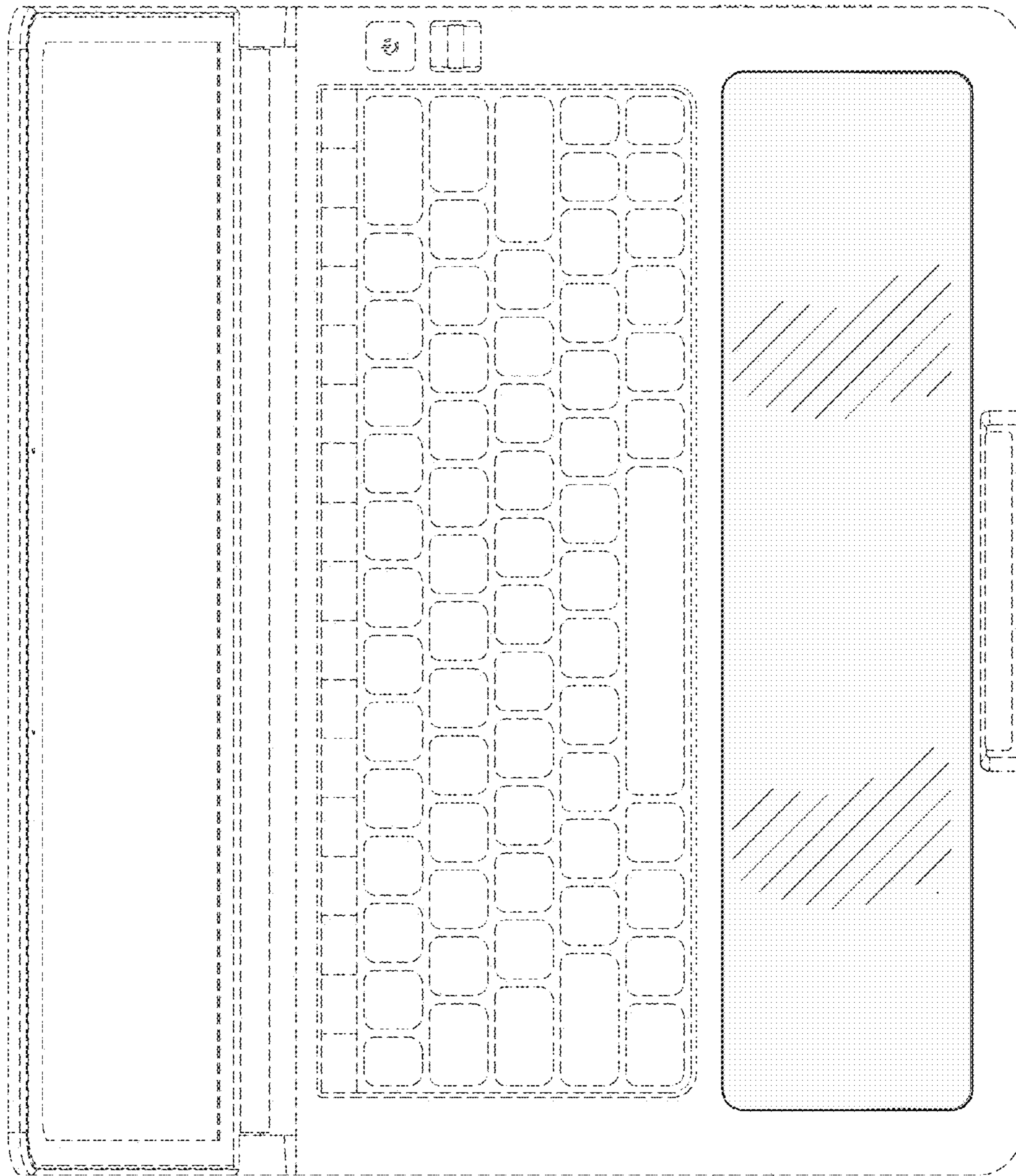


FIG. 7

