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(12) **United States Design Patent**
Quintana

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(54) **CIRCUIT BOARD CUFFLINK**

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(**) Term: **14 Years**

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(52) **U.S. Cl.**
USPC **D13/182**

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439/95; 24/102 R, 102 A, 102 E, 102 SL,
24/102 FC, 102 P, 103, 114.11
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H05K 3/36; H05K 3/361; H05K 3/363;
H05K 3/40; H05K 7/14; H05K 7/1401;
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H05K 1/00; A44B 5/00; A44B 5/002; A44B
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A44B 1/02; A44B 1/18

See application file for complete search history.

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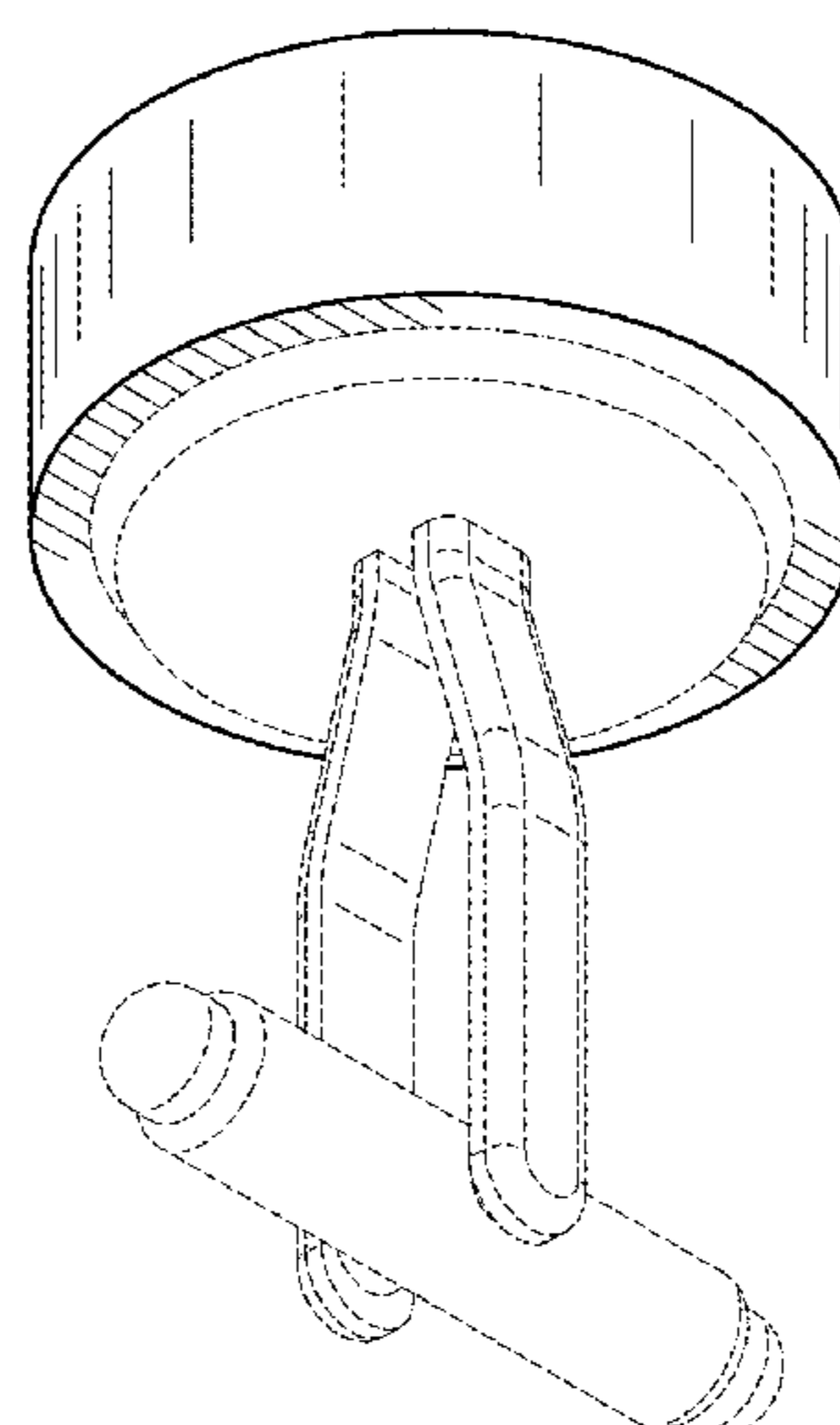
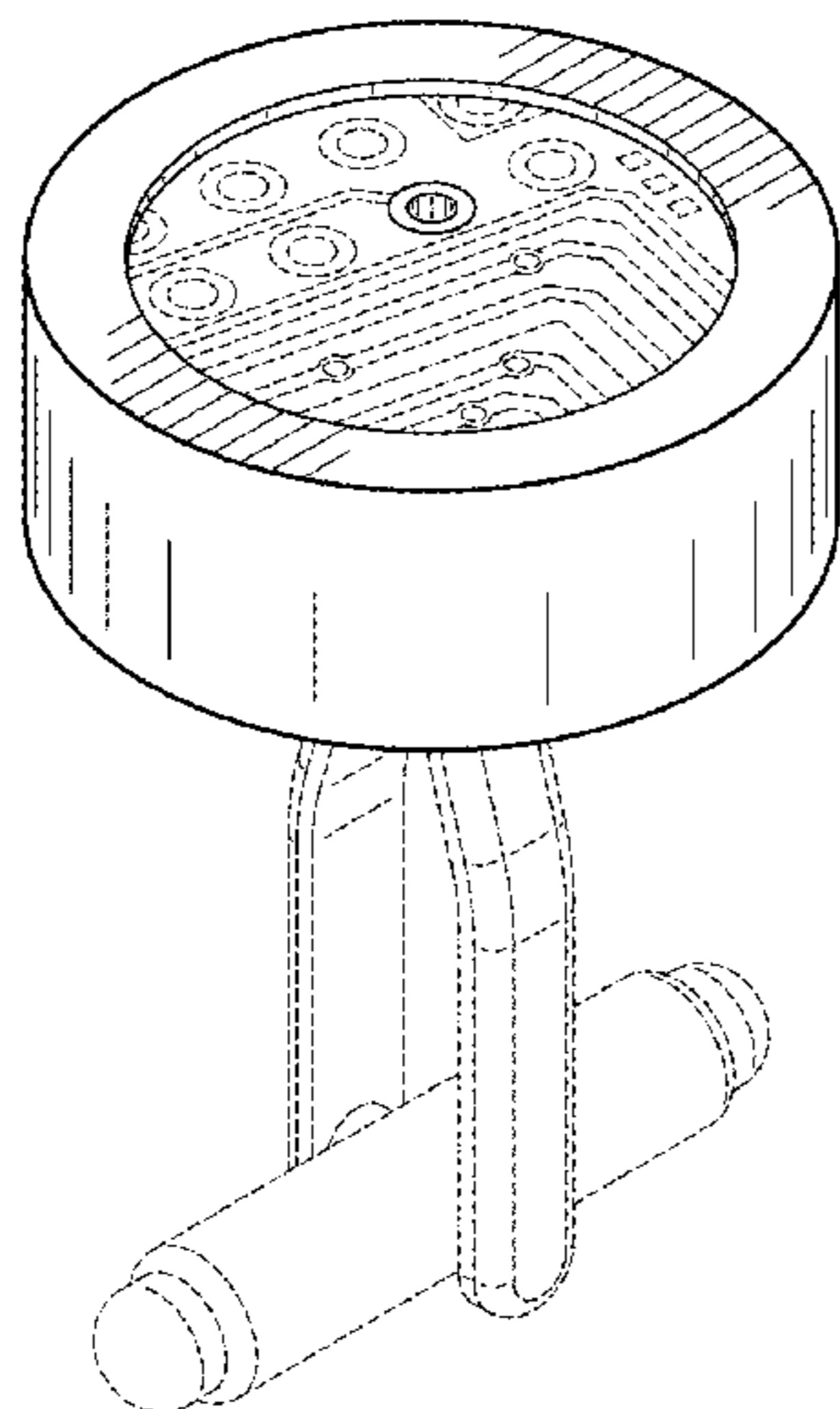
(57) **CLAIM**

The ornamental design for a circuit board cufflink, as shown and described.

DESCRIPTION

FIG. 1 is a top perspective view of a circuit board cufflink showing my new design;
FIG. 2 is a bottom perspective view thereof;
FIG. 3 is a front side elevational view thereof, the rear side elevational view being a mirror image thereof;
FIG. 4 is a right side elevational view thereof, the left side elevational view being a mirror image thereof;
FIG. 5 is a top plan view thereof;
FIG. 6 is a bottom plan view thereof;
FIG. 7 is a perspective view thereof, with the second cufflink in the pair shown in broken lines; and,
FIG. 8 is a perspective view of a cufflink shown in a used condition.
The broken lines in the drawings depict environmental subject matter only and form no part of the claimed design.

1 Claim, 5 Drawing Sheets



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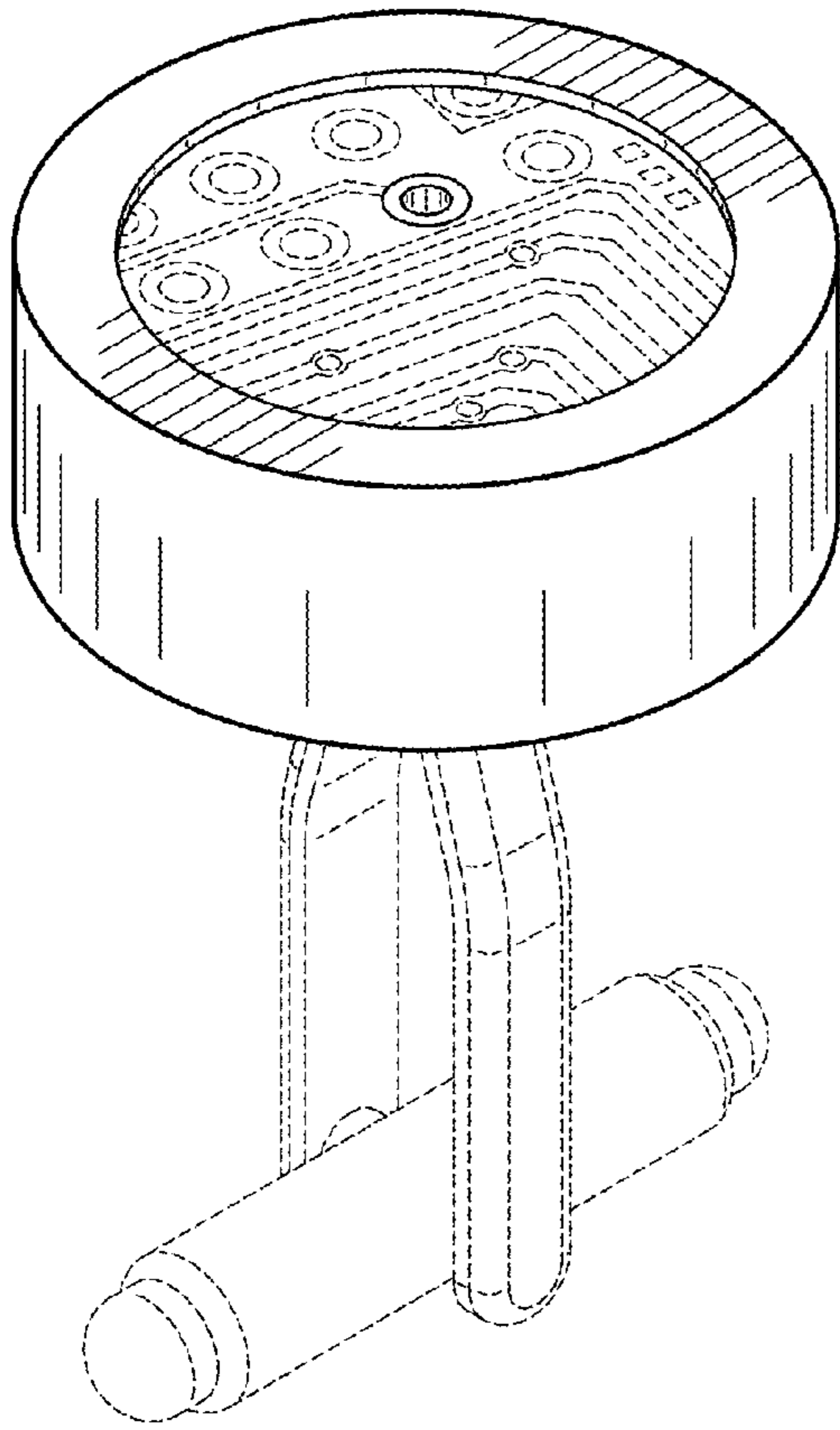


FIG. 1

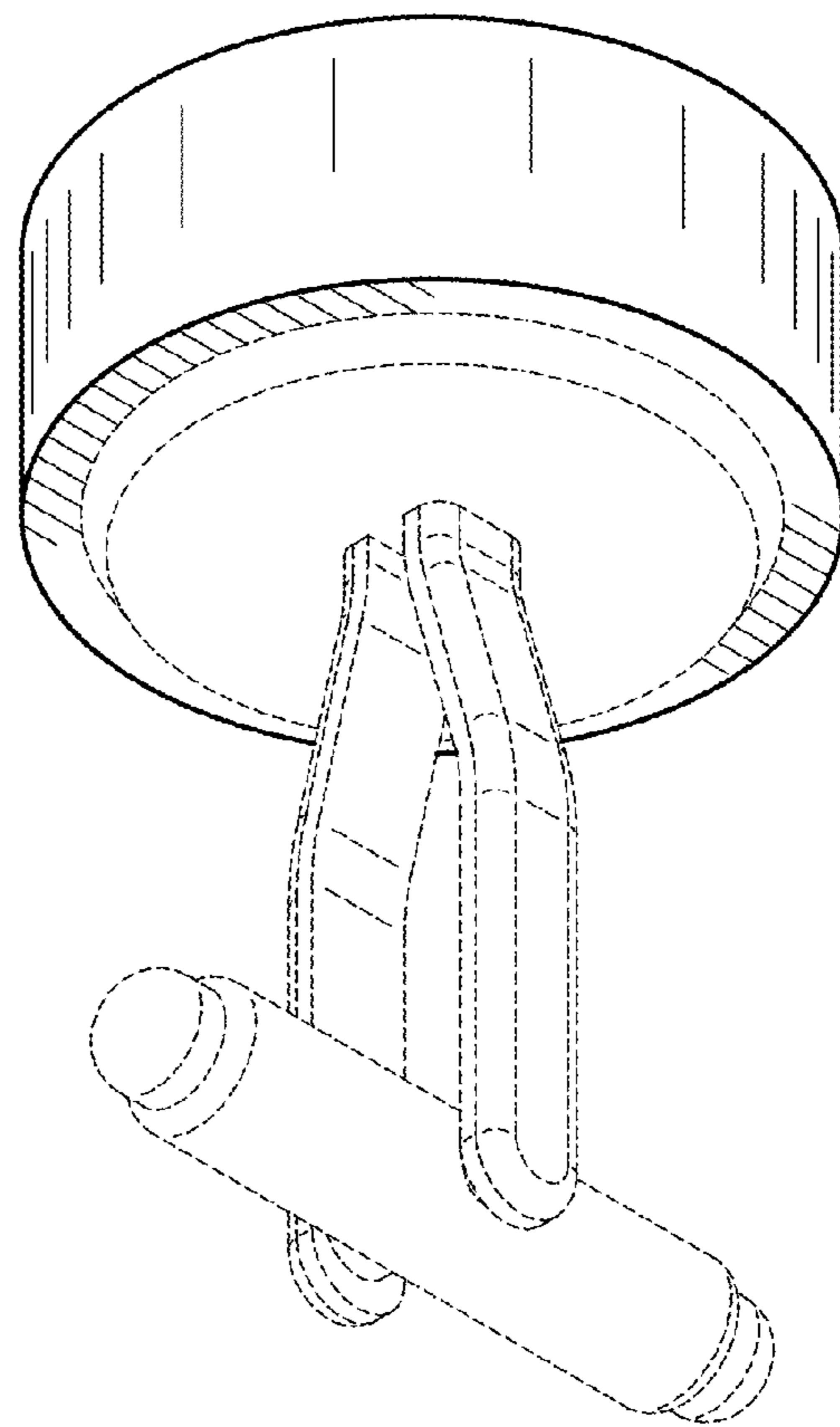


FIG. 2

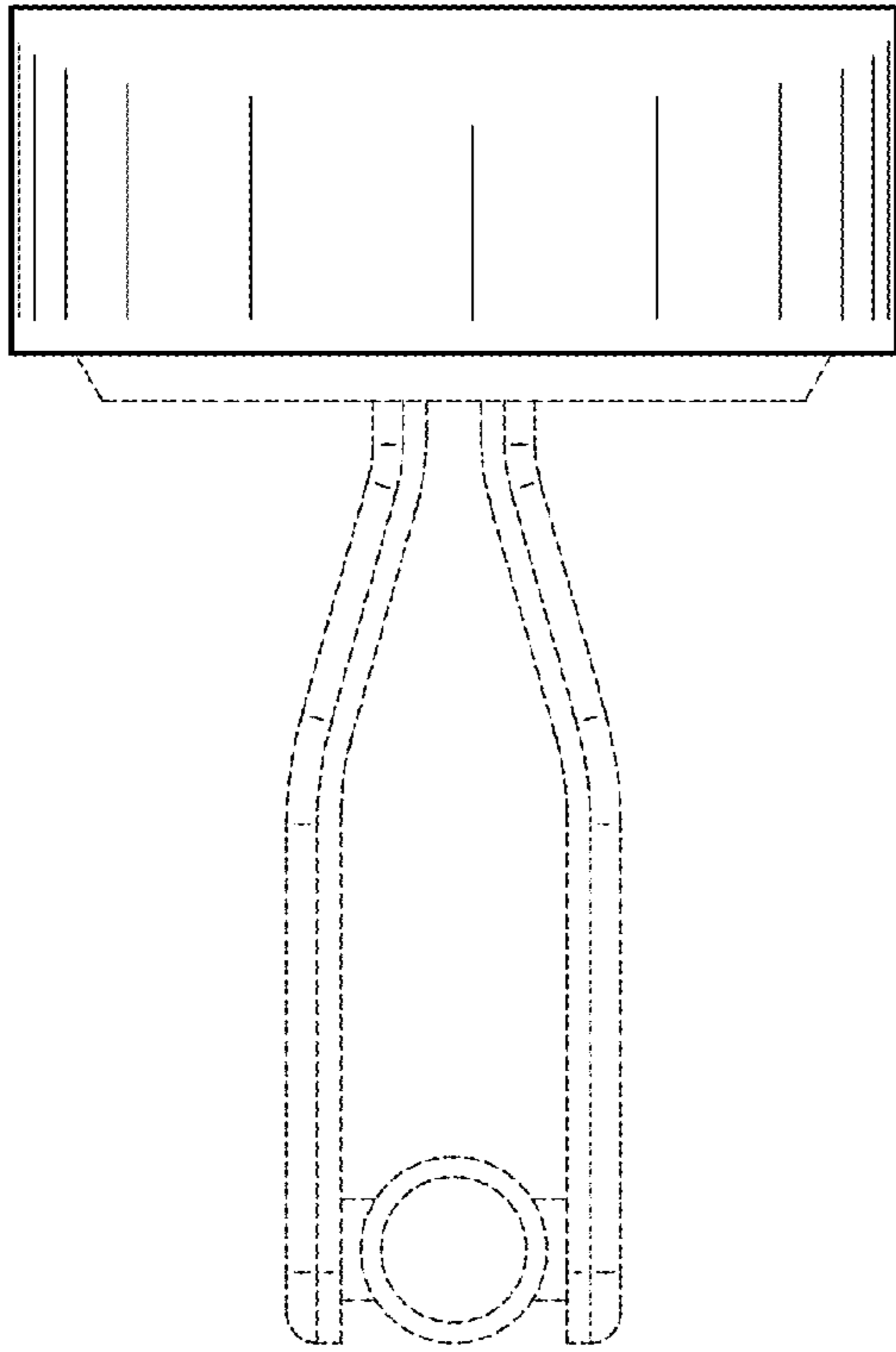


FIG. 3

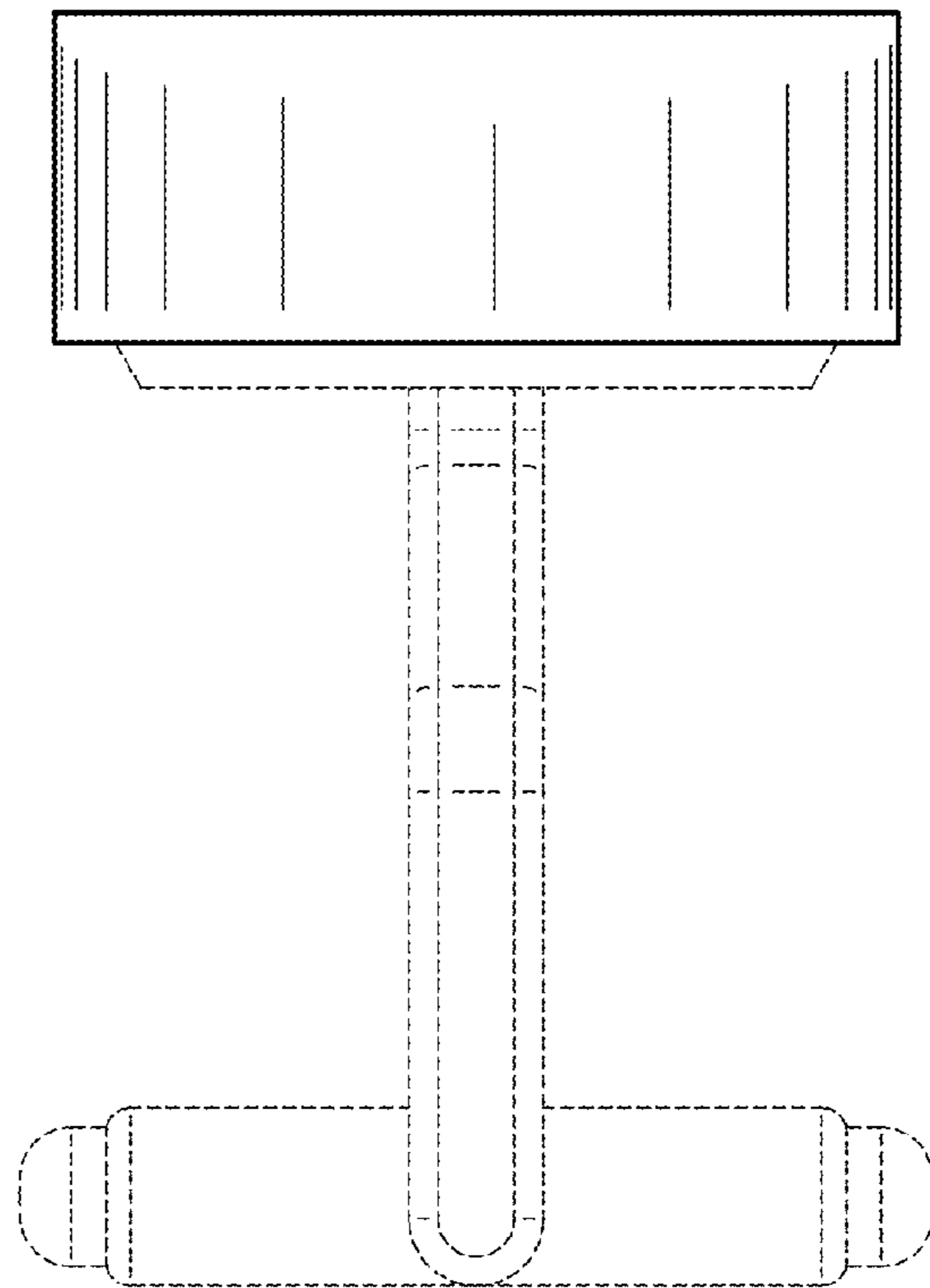


FIG. 4

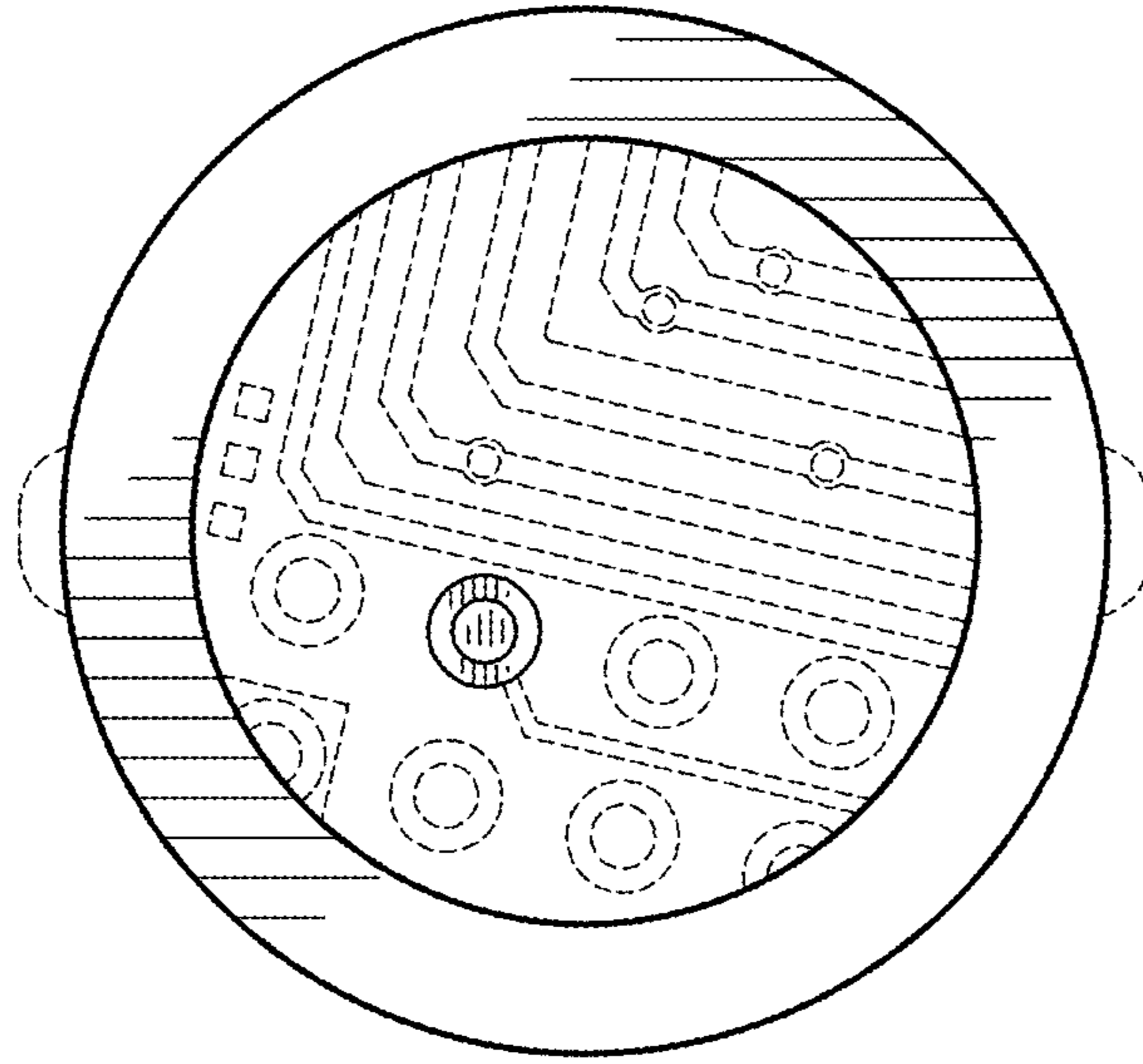


FIG. 5

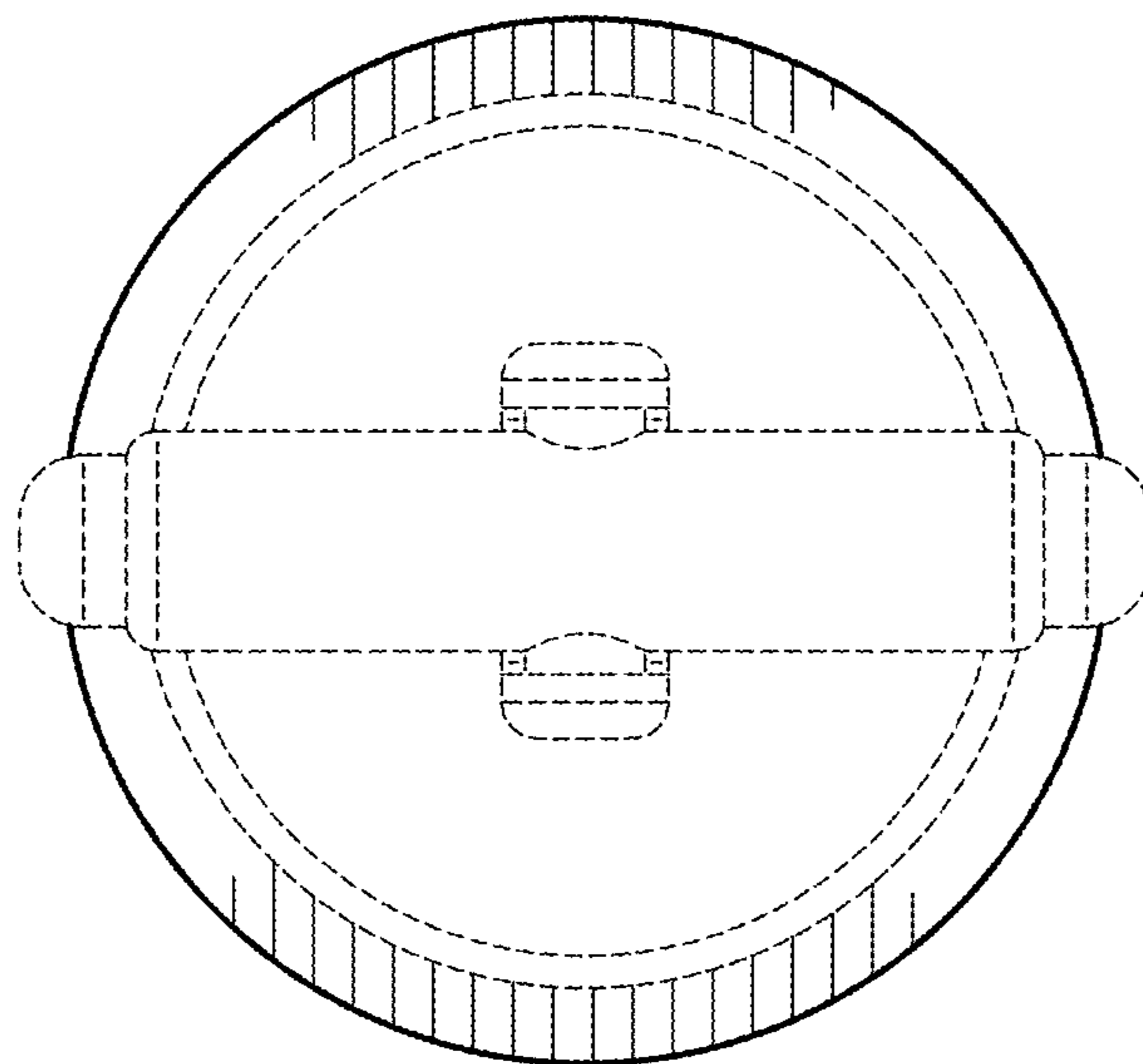


FIG. 6

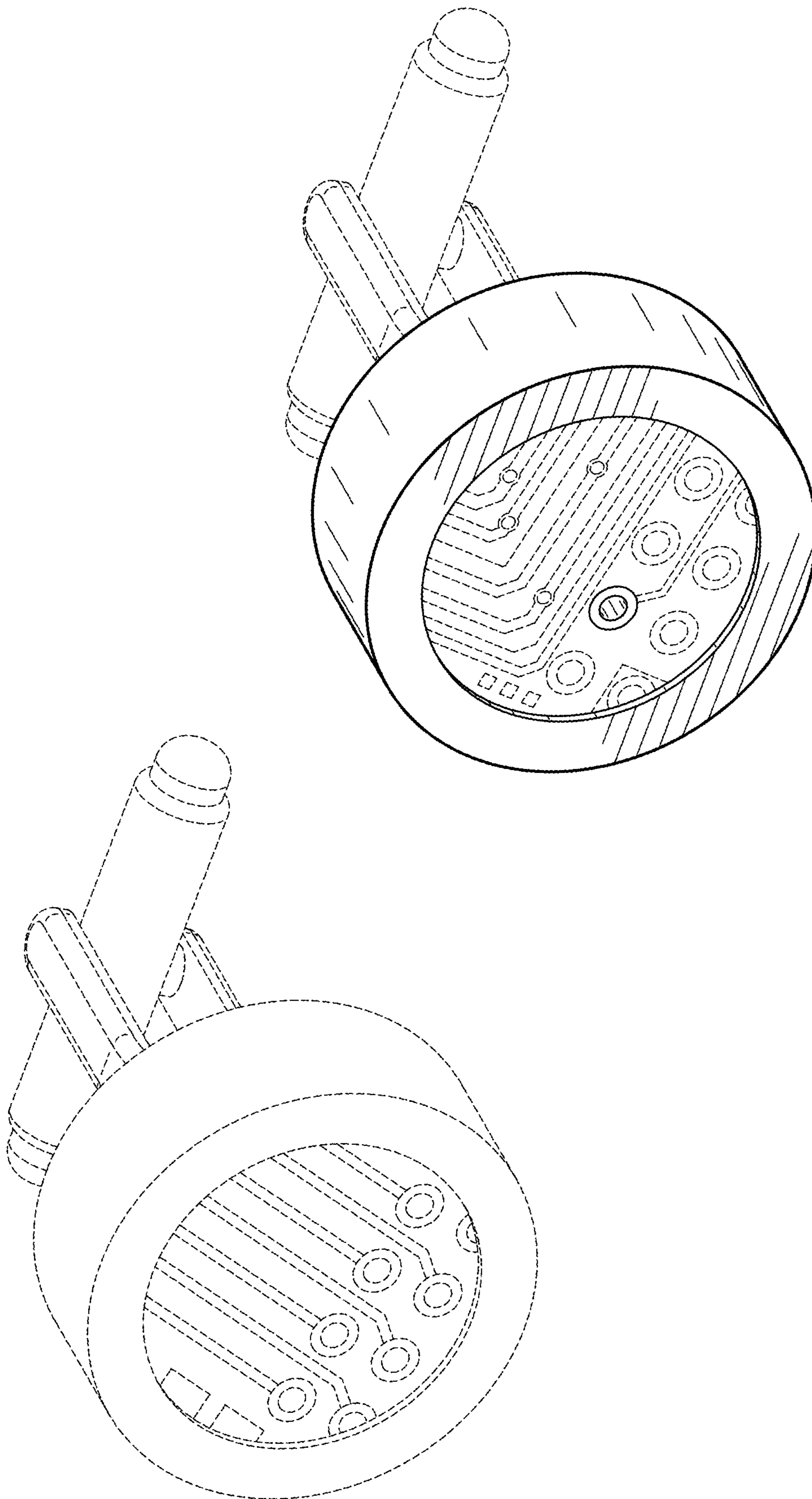


FIG. 7

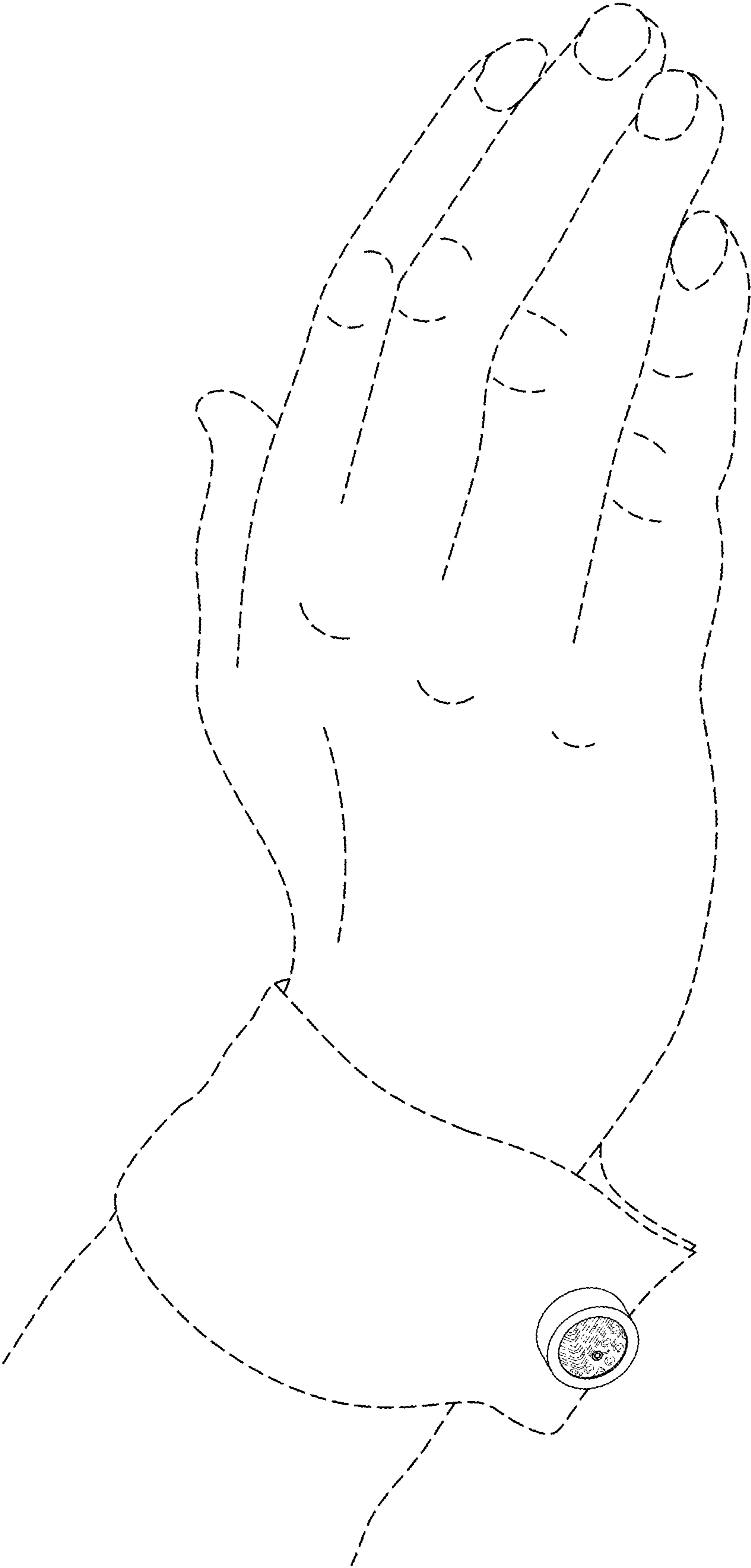


FIG. 8