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(12) **United States Design Patent**  
**Jia et al.**

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(54) **INPUT/OUTPUT CONTROL DEVICE**

1/3975; F15B 15/1428; F15B 11/0365; F15B 15/084; F15B 15/1414; F15B 15/1433; F15B 2211/7056; Y02E 60/13; H01T 13/08; H01T 13/06; H01T 13/20; A61G 7/1019; A61G 7/1044; A61G 7/1076

(71) Applicant: **ABB TECHNOLOGY LTD.**, Zurich (CH)

See application file for complete search history.

(72) Inventors: **Wenlong Jia**, Shanghai (CN); **Zhen Lian**, Shanghai (CN); **Bin Li**, Shanghai (CN)

(56) **References Cited**

(73) Assignee: **ABB TECHNOLOGY LTD.**, Zurich (CH)

U.S. PATENT DOCUMENTS

(\*\*) Term: **14 Years**

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(21) Appl. No.: **29/453,372**

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(30) **Foreign Application Priority Data**

*Primary Examiner* — Thomas Johannes

Dec. 14, 2012 (CN) ..... 2012 3 0630080

*Assistant Examiner* — Shawn T Gingrich

(51) **LOC (10) Cl.** ..... **13-03**

(74) *Attorney, Agent, or Firm* — Robert A. Jefferis; Driggs, Hogg, Daugherty & Del Zoppo Co., LPA

(52) **U.S. Cl.**  
USPC ..... **D13/123**

(58) **Field of Classification Search**  
USPC ..... D13/110, 112, 122–128, 133, 147, D13/158–160, 162, 184, 199; D14/300, D14/308, 311–314, 348, 349, 356; 60/476; 137/343; 200/43.14, 50.01, 200/50.21–50.23; 361/44, 115, 608, 634, 361/679.01

(57) **CLAIM**

The ornamental design of an input/output control device, as shown and described.

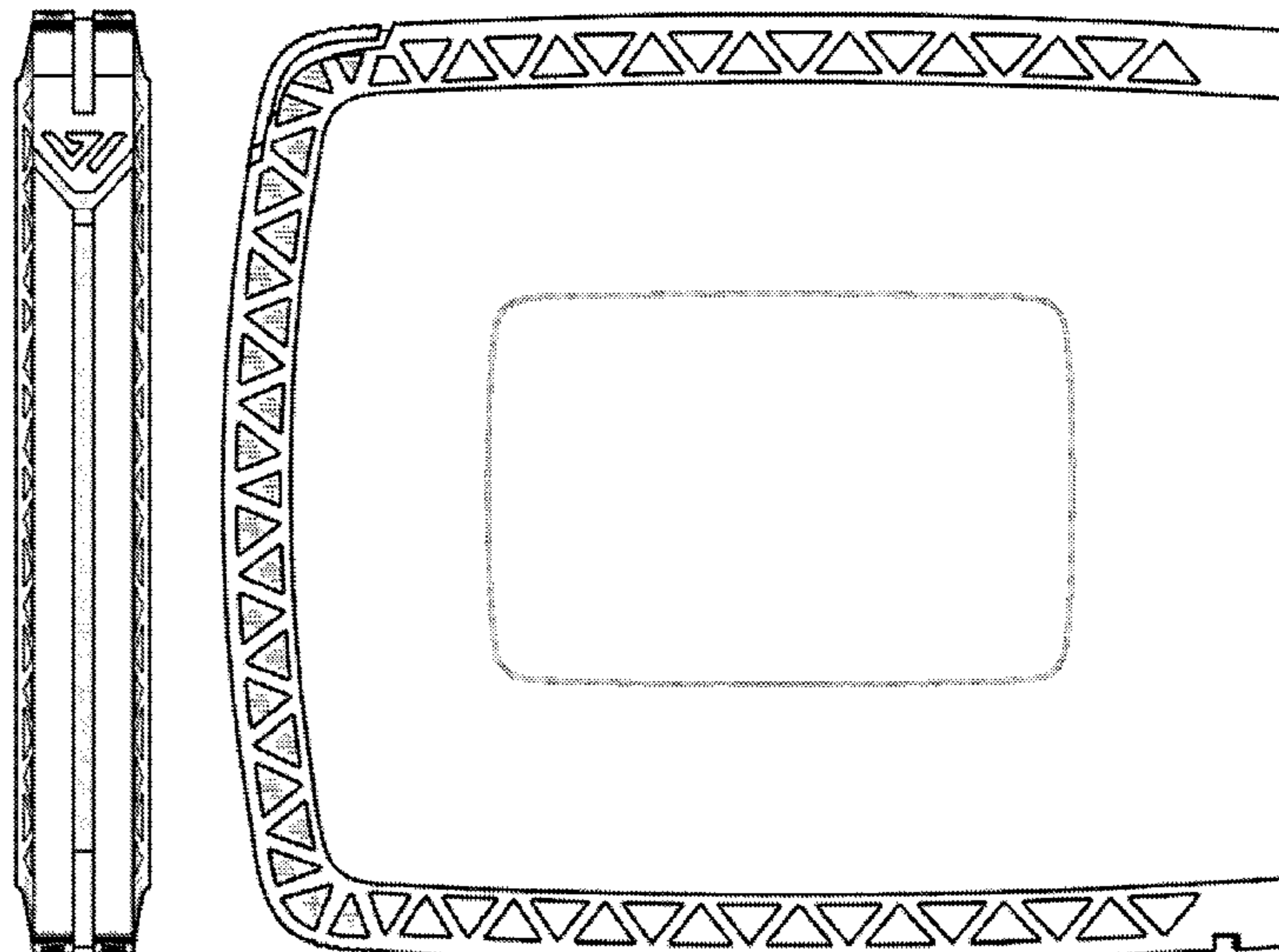
CPC ..... H01H 11/00; H01H 19/02; H01H 19/10; H01H 19/28; H01H 19/36; H01H 19/64; H01H 1/06; H01H 1/2041; H01H 1/42; H01H 2071/0292; H01H 2071/042; H01H 33/08; H01H 33/12; H01H 33/42; H01H 3/30; H01H 3/3015; H01H 71/025; H01H 71/04; H01H 71/1054; H01H 9/167; H01H 9/281; H01H 9/282; H05K 7/1432; H01G 9/008; H01G 4/32; H01G 9/10; H01G 4/28; A61N

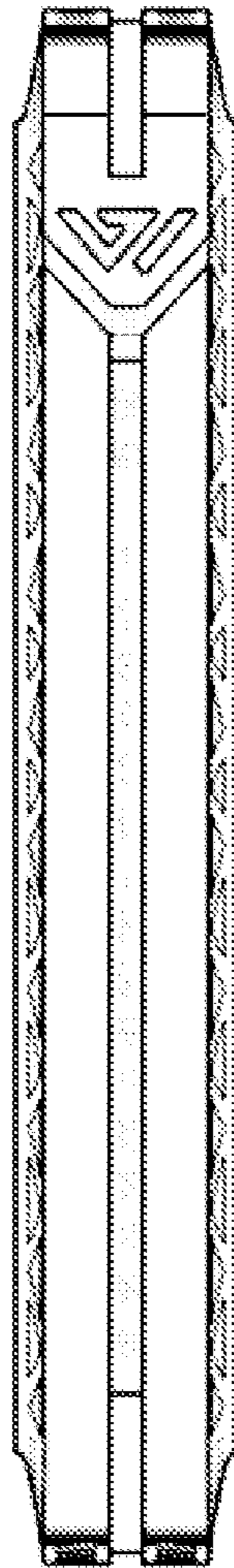
**DESCRIPTION**

FIG. 1 is a front view of an input/output control device showing the new design;  
FIG. 2 is a back view thereof;  
FIG. 3 is a left view thereof;  
FIG. 4 is a right view thereof;  
FIG. 5 is a top view thereof; and,  
FIG. 6 is a bottom view thereof.

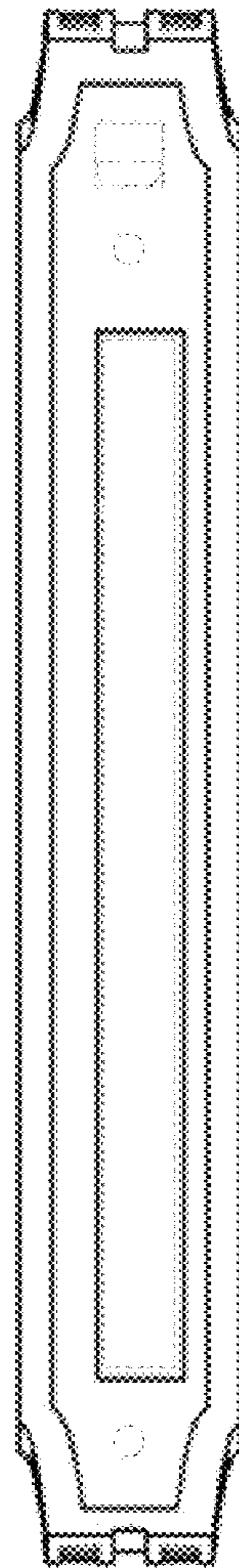
The broken line portion of the figure drawings is included to show unclaimed subject matter only and forms no part of the claimed design.

**1 Claim, 6 Drawing Sheets**

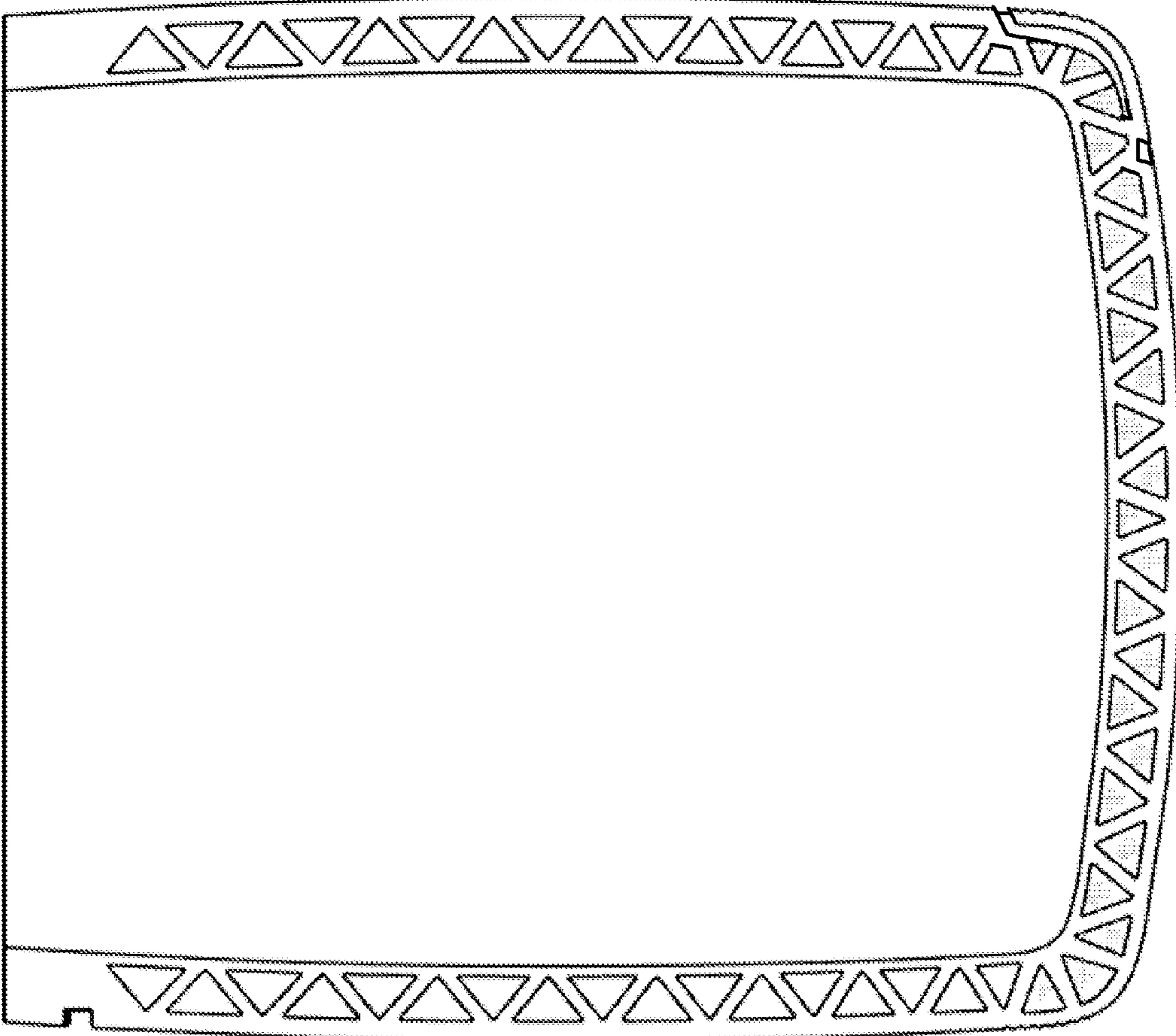




**FIGURE 1**

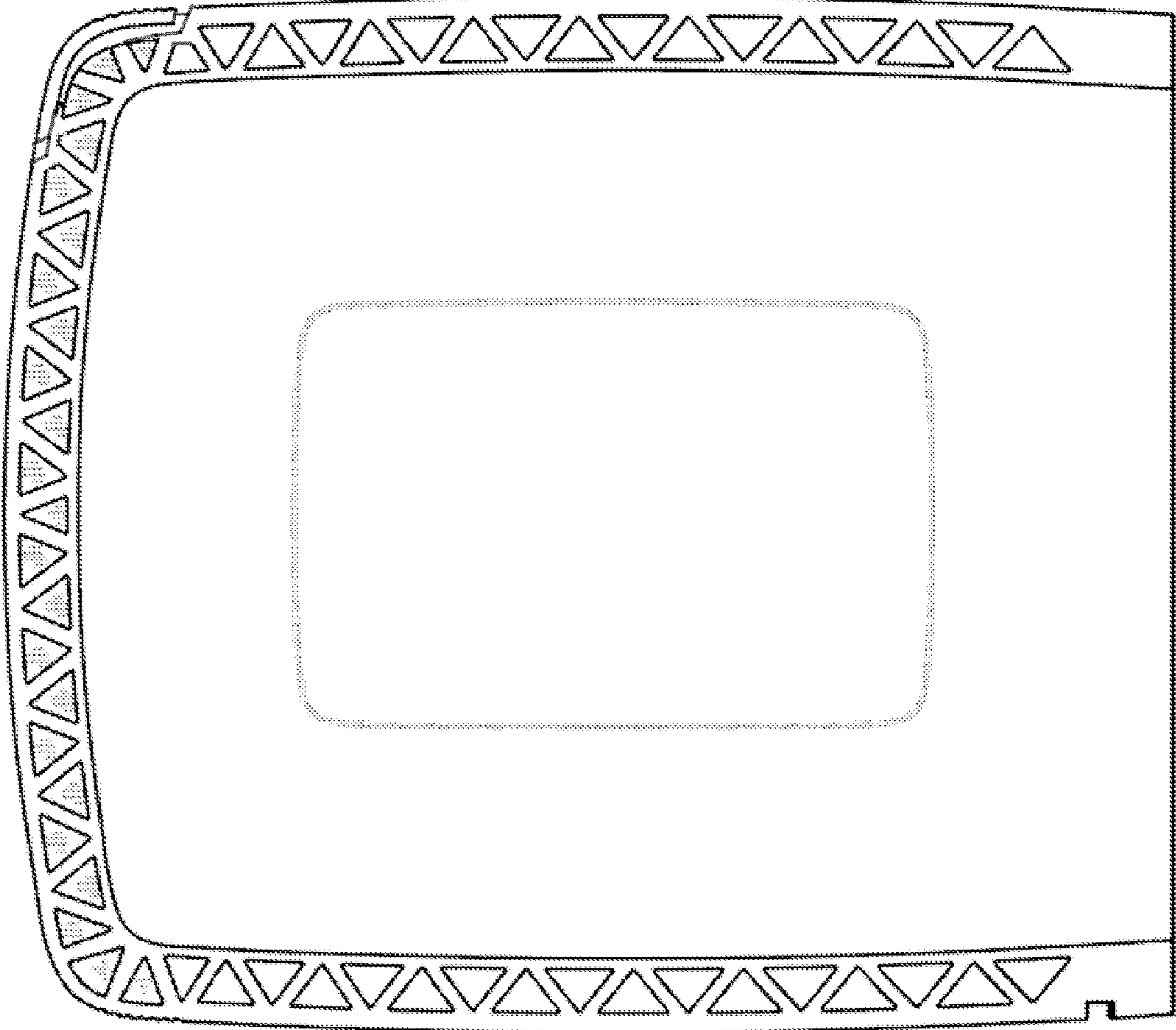


**FIGURE 2**

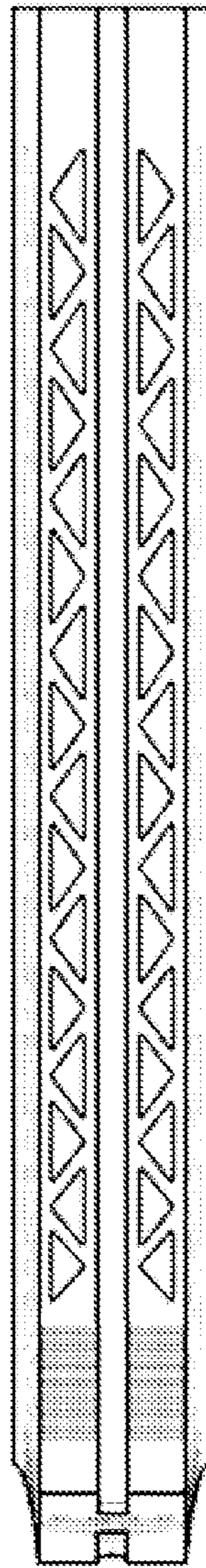


**FIGURE 3**

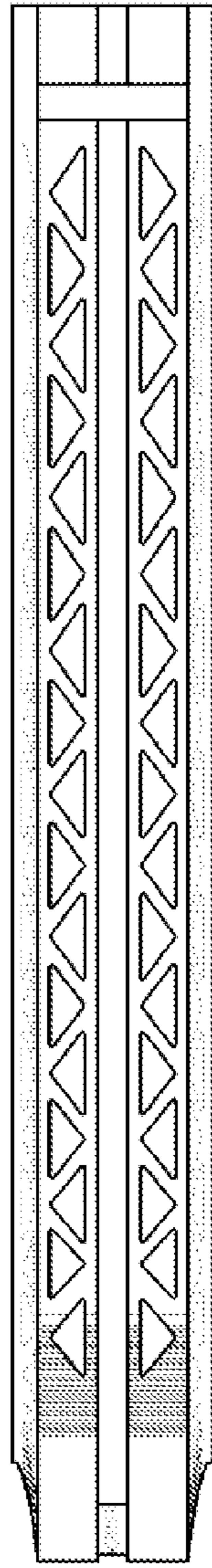




**FIGURE 4**



**FIGURE 5**



**FIGURE 6**