



US00D735139S

(12) **United States Design Patent**  
**Ikenaka**(10) **Patent No.:** **US D735,139 S**  
(45) **Date of Patent:** \*\* Jul. 28, 2015(54) **MULTI-CONDUCTOR CONNECTOR**(71) Applicant: **ITT MANUFACTURING ENTERPRISES LLC**, Wilmington, DE (US)(72) Inventor: **Kazuo Ikenaka**, Tokyo (JP)(73) Assignee: **ITT Manufacturing Enterprise LLC**, Wilmington, DE (US)(\*\*) Term: **14 Years**(21) Appl. No.: **29/466,856**(22) Filed: **Sep. 12, 2013**(30) **Foreign Application Priority Data**

Mar. 13, 2013 (JP) ..... 2013-005524

(51) LOC (10) Cl. ..... **13-03**

(52) U.S. Cl.

USPC ..... **D13/147**(58) **Field of Classification Search**

USPC ..... D13/147, 154, 184, 199

See application file for complete search history.

(56)

**References Cited****U.S. PATENT DOCUMENTS**7,479,017 B1 \* 1/2009 Koopman et al. .... 439/67  
D628,162 S \* 11/2010 Kimura et al. .... D13/147  
D628,163 S \* 11/2010 Kimura et al. .... D13/147

\* cited by examiner

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**(57) CLAIM**

I claim the ornamental design for a multi-conductor connector, as shown and described.

**DESCRIPTION**

FIG. 1 is a front, top and left side perspective view of a multi-conductor connector, showing my new design; FIG. 2 is a front, bottom and left side perspective view thereof;

FIG. 3 is a front view thereof, the opposite side being a mirror image thereof;

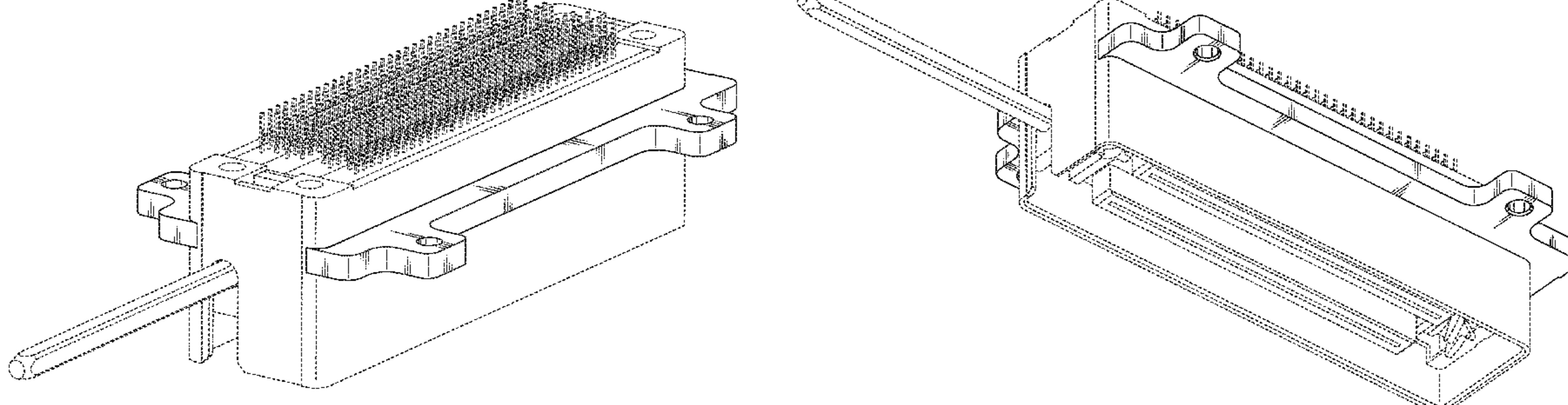
FIG. 4 is a top plan view thereof;

FIG. 5 is a bottom plan view thereof;

FIG. 6 is a right side elevational view thereof; and,

FIG. 7 is a left side elevational view thereof.

The broken line in the figure drawing views is included for the purpose of illustrating environment and forms no part of the claimed design.

**1 Claim, 6 Drawing Sheets**

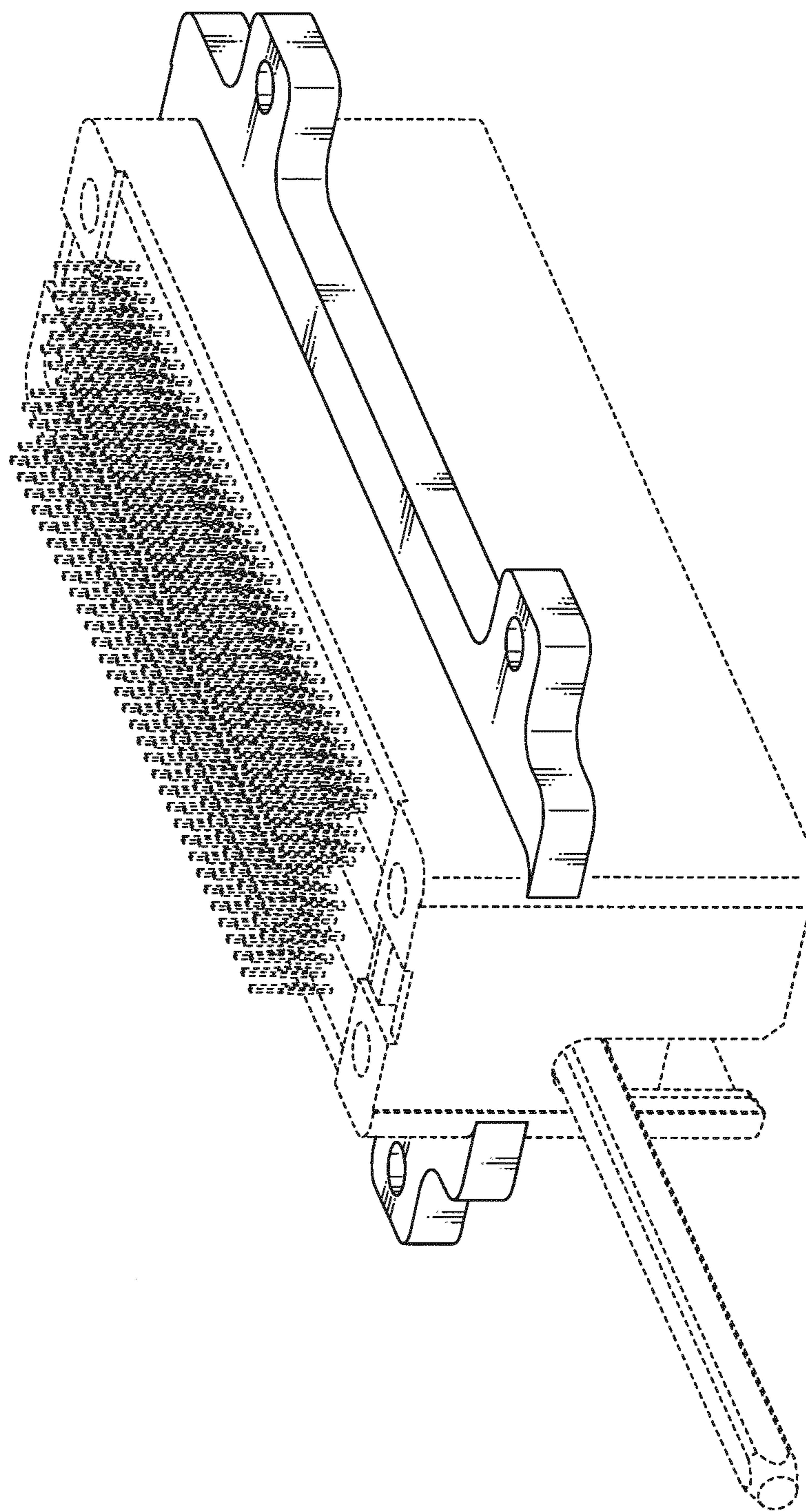


FIG. 1

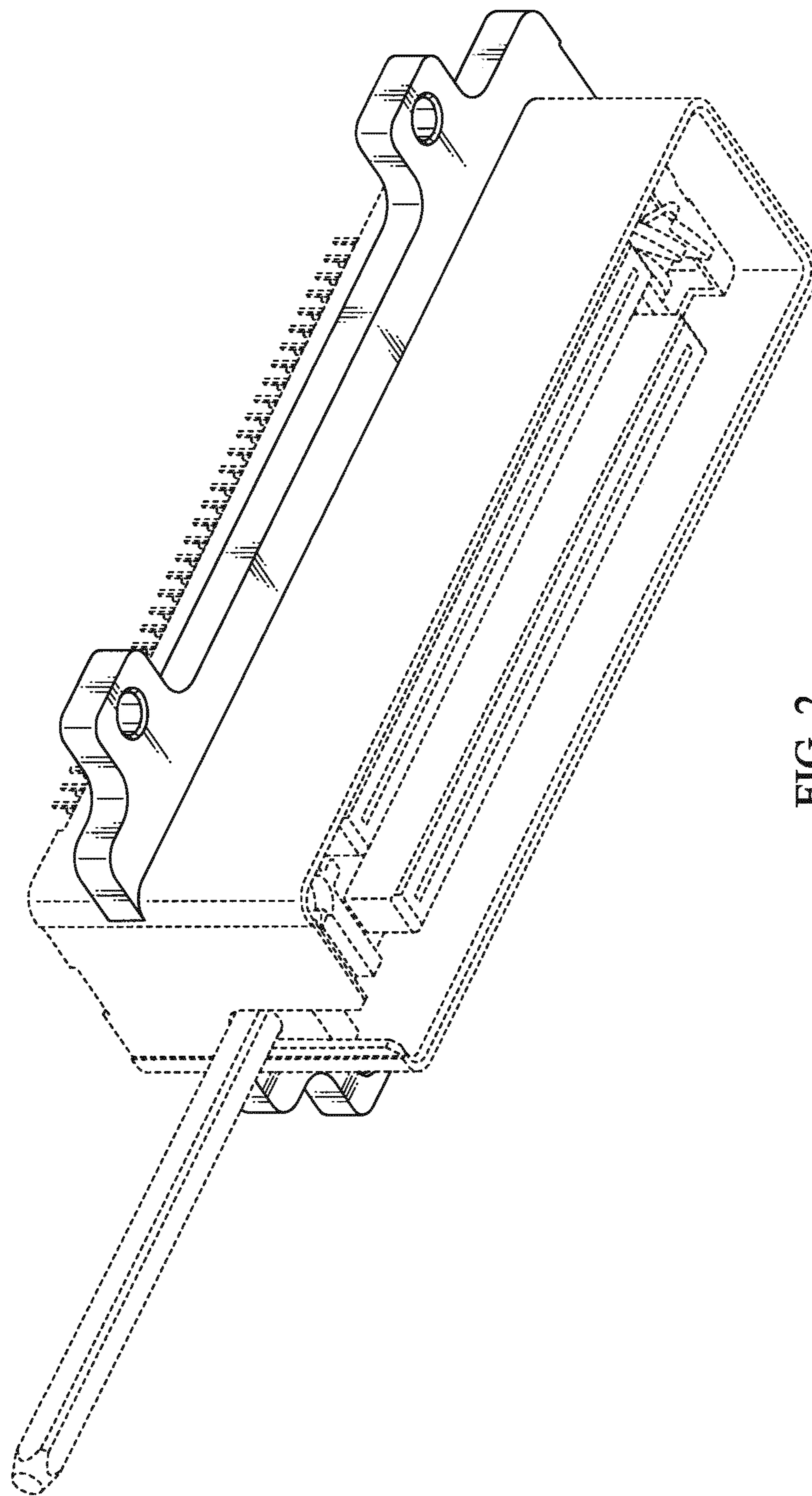
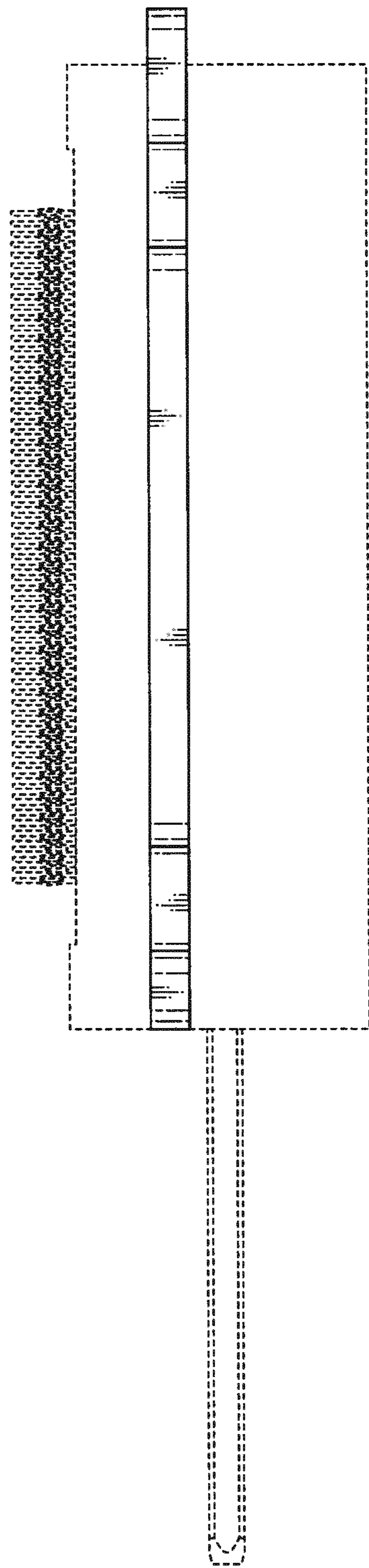


FIG. 2



**FIG. 3**

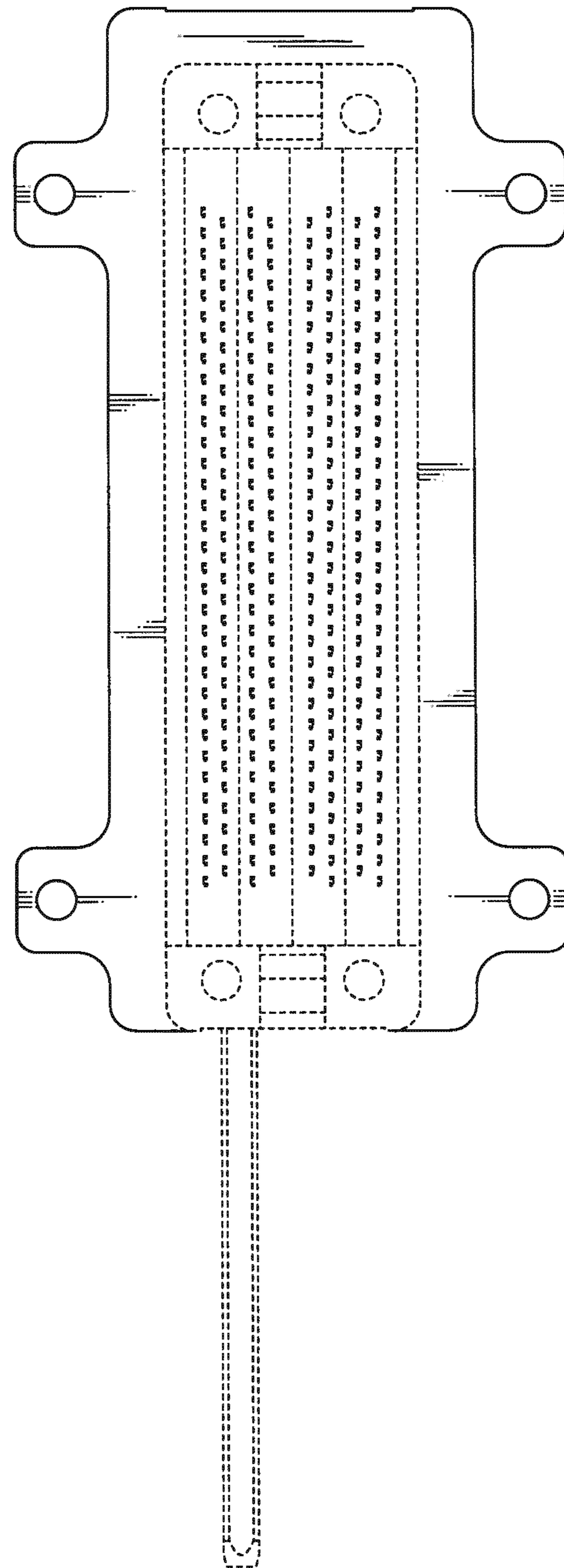


FIG. 4

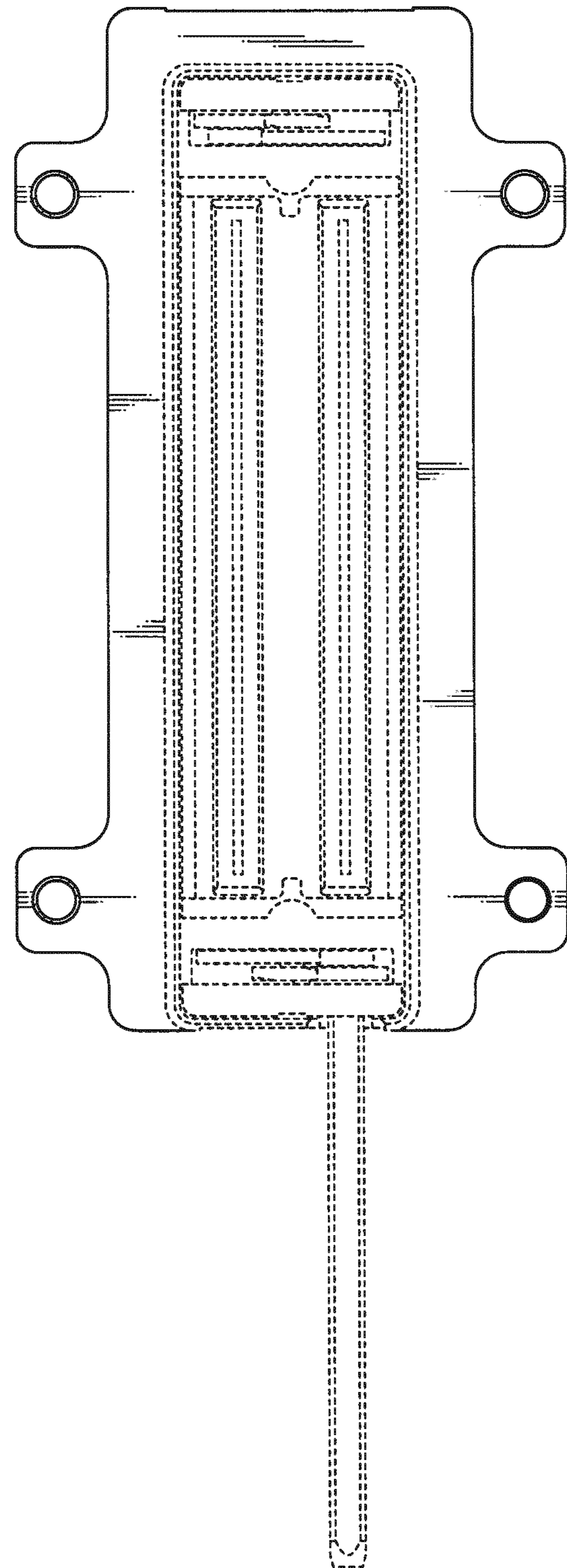


FIG. 5

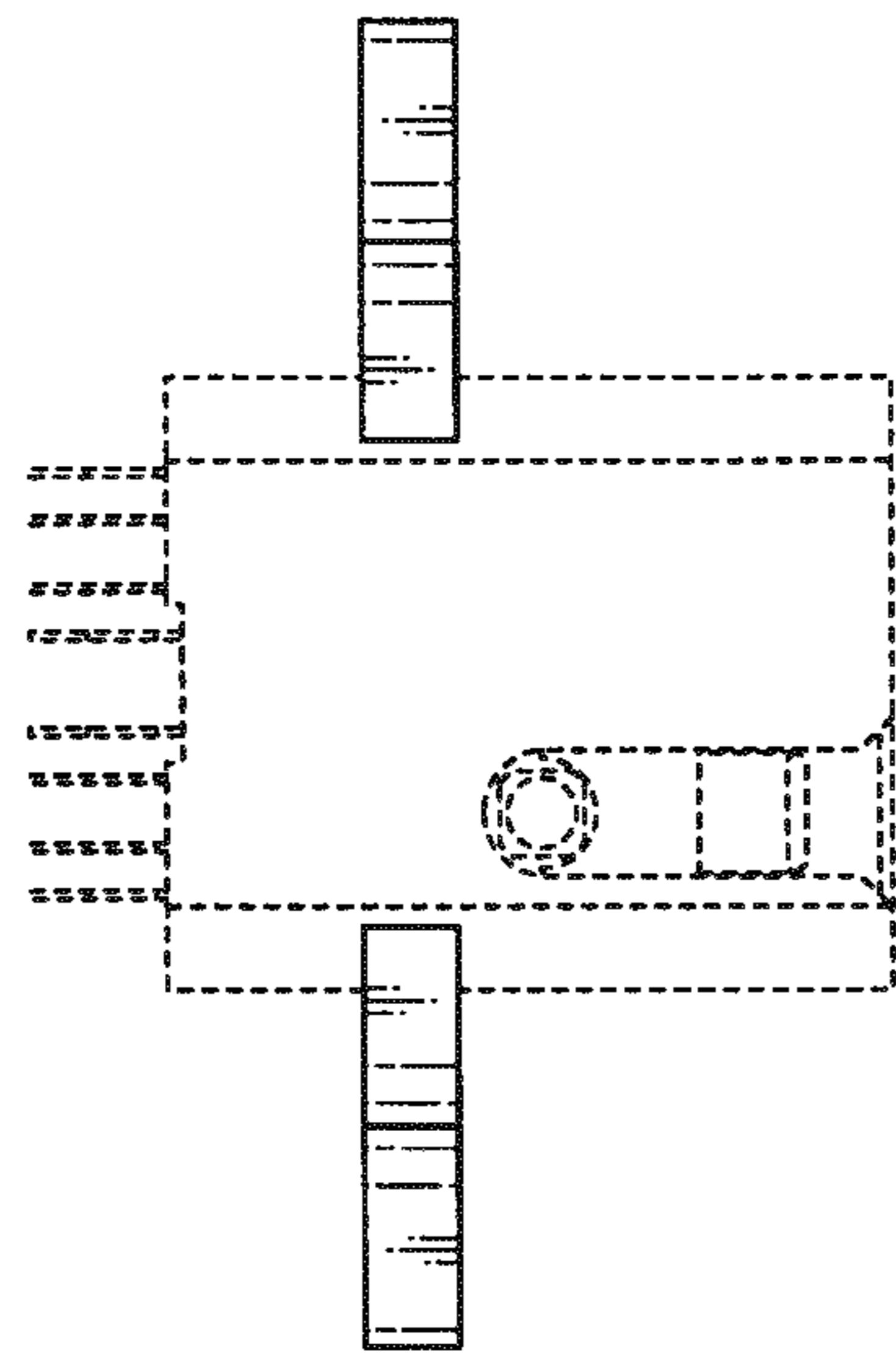


FIG. 7

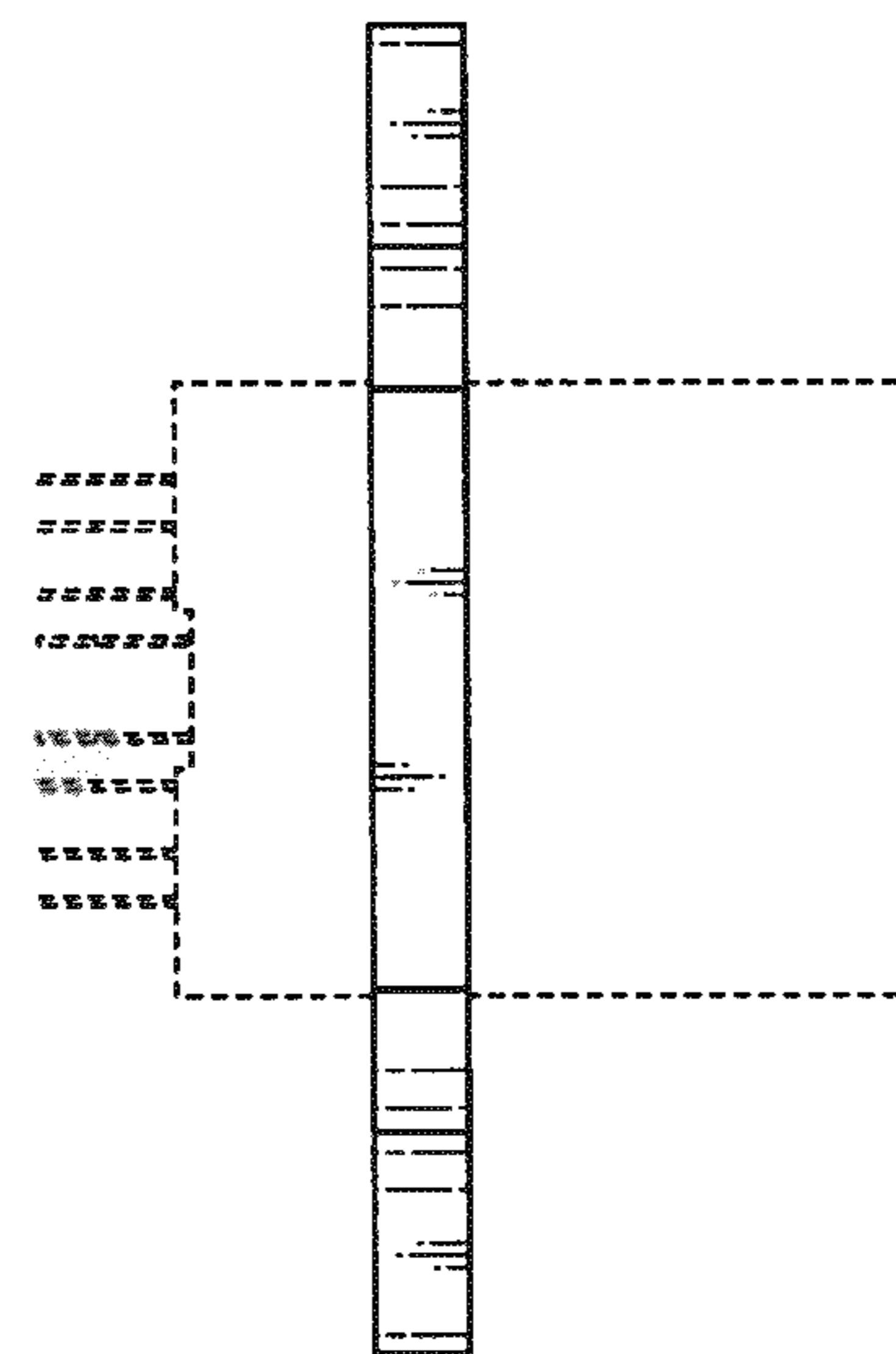


FIG. 6