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(12) **United States Design Patent**
Chaney et al.

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(54) **ELECTRONIC DEVICE ENCLOSURE**

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(**) Term: **14 Years**

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Related U.S. Application Data

(63) Continuation of application No. 29/459,681, filed on Jul. 2, 2013, now Pat. No. Des. 716,285, which is a continuation of application No. 13/736,139, filed on Jan. 8, 2013.

(51) **LOC (10) Cl.** **14-03**

(52) **U.S. Cl.**
USPC **D14/250**

(58) **Field of Classification Search**
USPC D14/138 AD, 138 C, 138 G, 203.7, 230, D14/238.1, 240, 247, 248, 250, 253, 341, D14/345, 346, 439, 440, 447; D13/103, D13/107, 119; D3/201, 218, 228, 269, 273, D3/303; 206/320; 220/662; 361/679.27, 361/679.56, 737, 753; 224/666; 455/456.1, 455/572, 557, 575.1, 575.4, 575.8; D10/65
See application file for complete search history.

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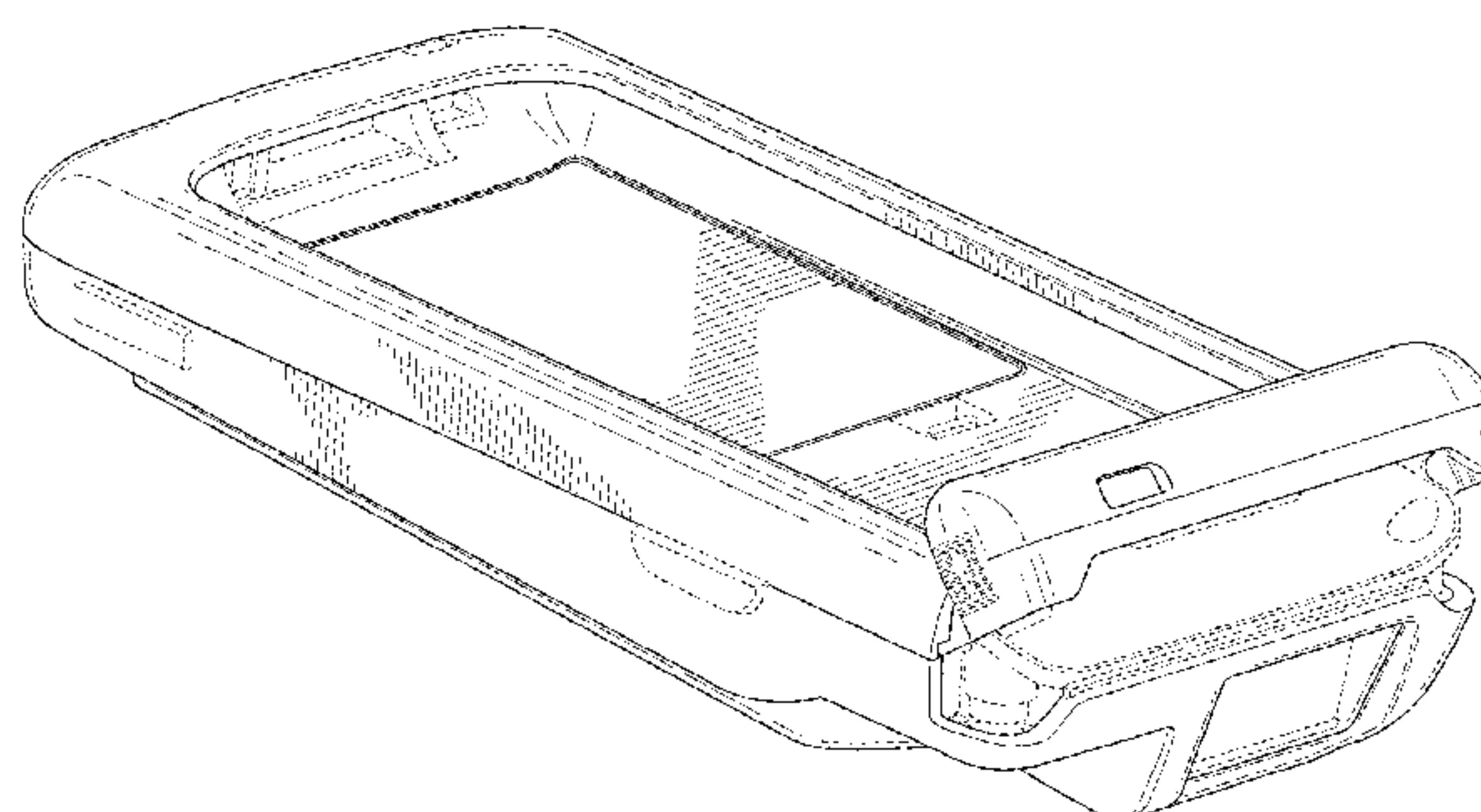
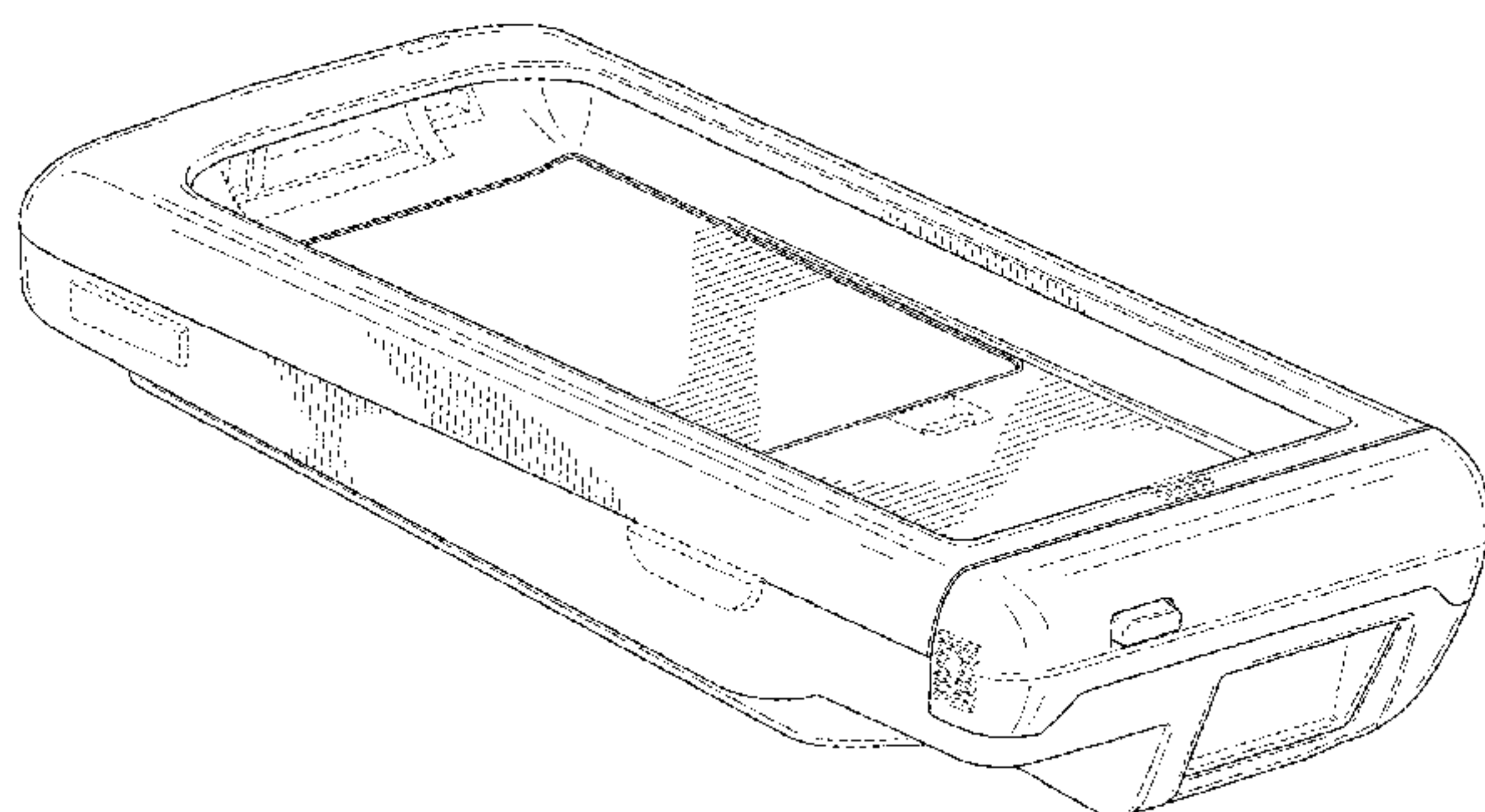
(57) **CLAIM**

The ornamental design for an electronic device enclosure, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of an electronic device enclosure showing our new design;
FIG. 2 is a front elevation view thereof;
FIG. 3 is a rear elevation view thereof;
FIG. 4 is a left side elevation view thereof;
FIG. 5 is a right side elevation view thereof;
FIG. 6 is a top plan view thereof;
FIG. 7 is a bottom plan view thereof;
FIG. 8 is another perspective view thereof; and,
FIG. 9 is another perspective view thereof, with the electronic device enclosure's door in an open position.
The broken lines indicate portions of the electronic device enclosure that form no part of the claimed design.

1 Claim, 9 Drawing Sheets



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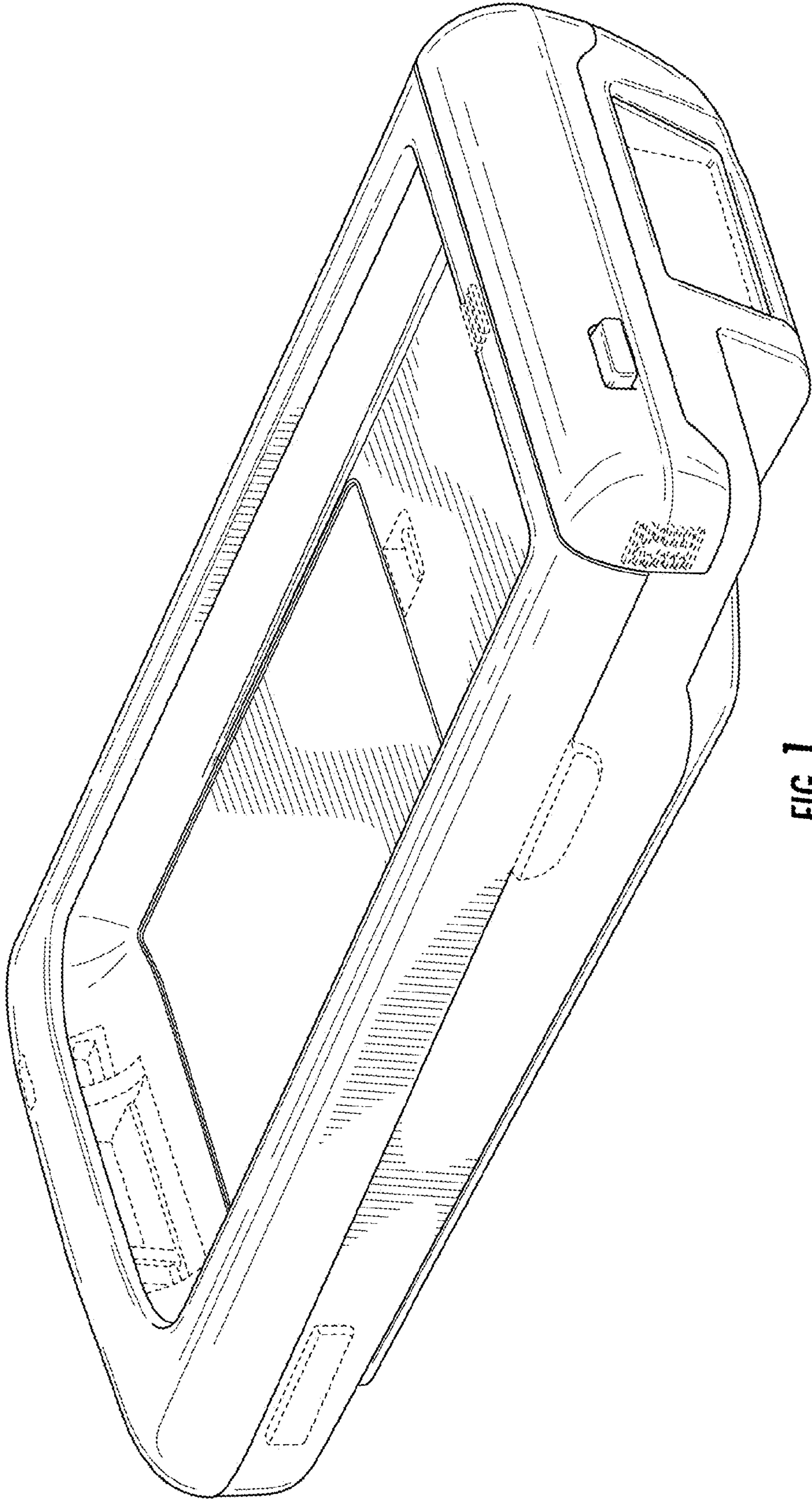


FIG. 1

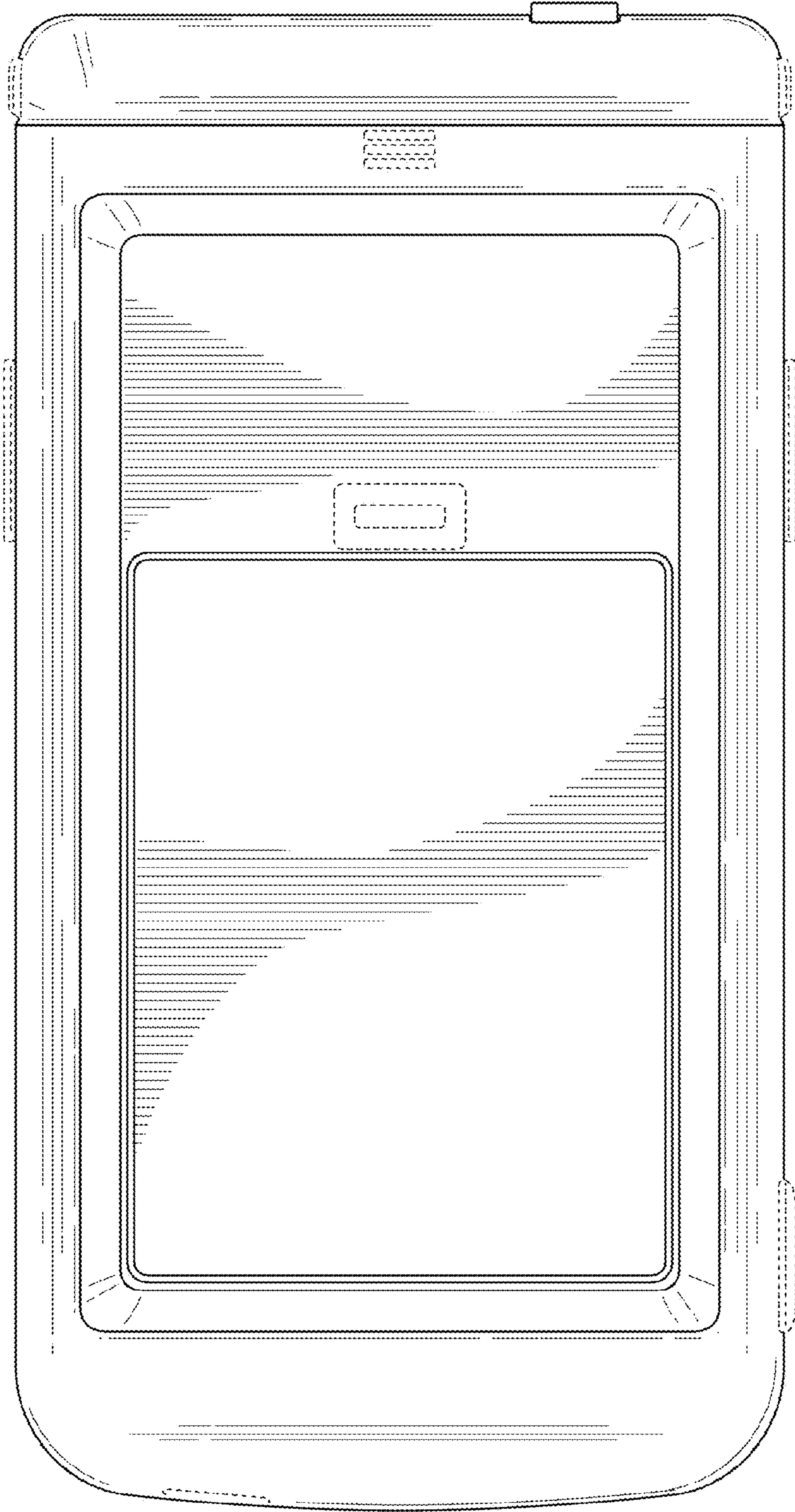


FIG. 2

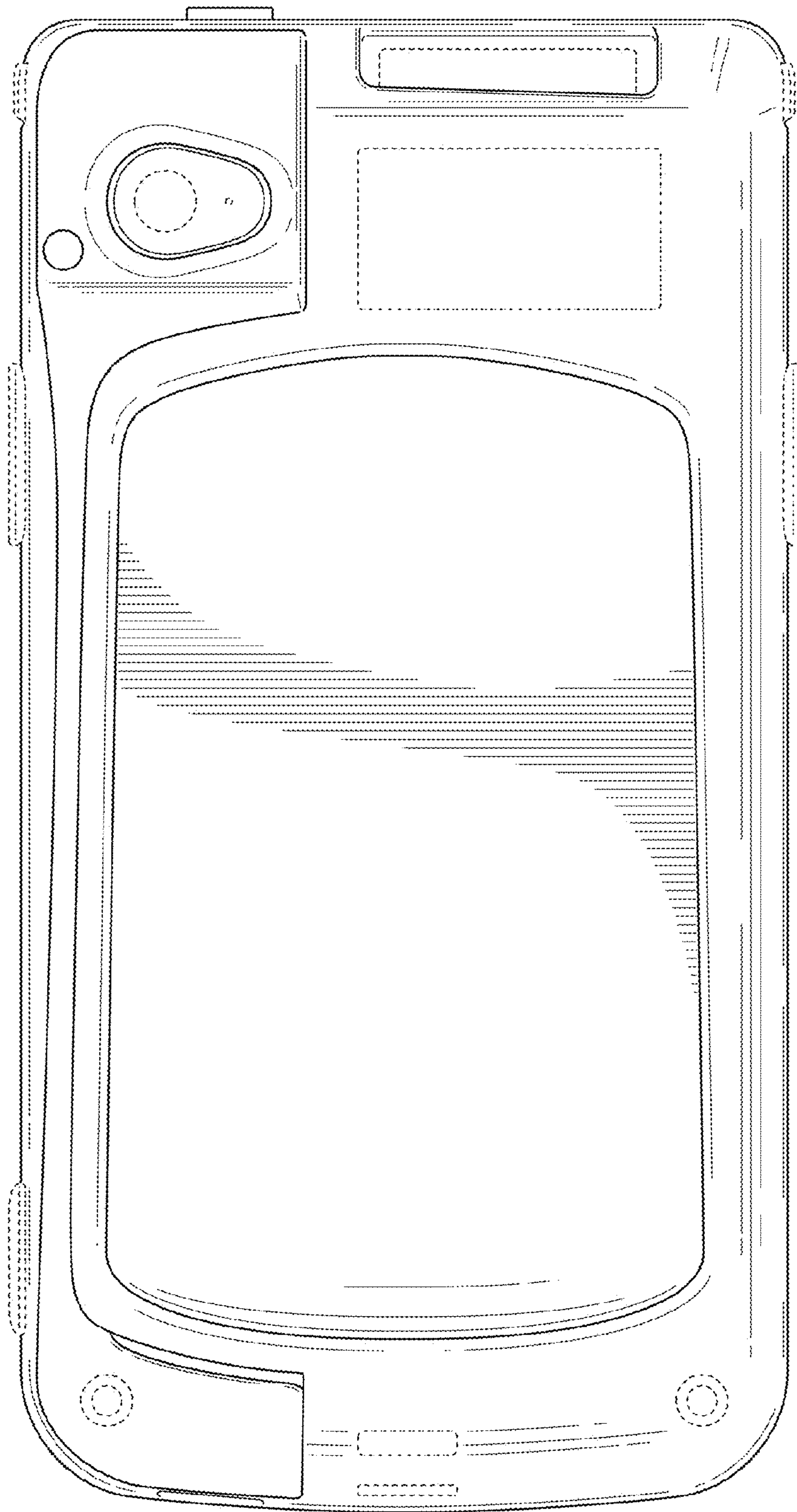


FIG. 3

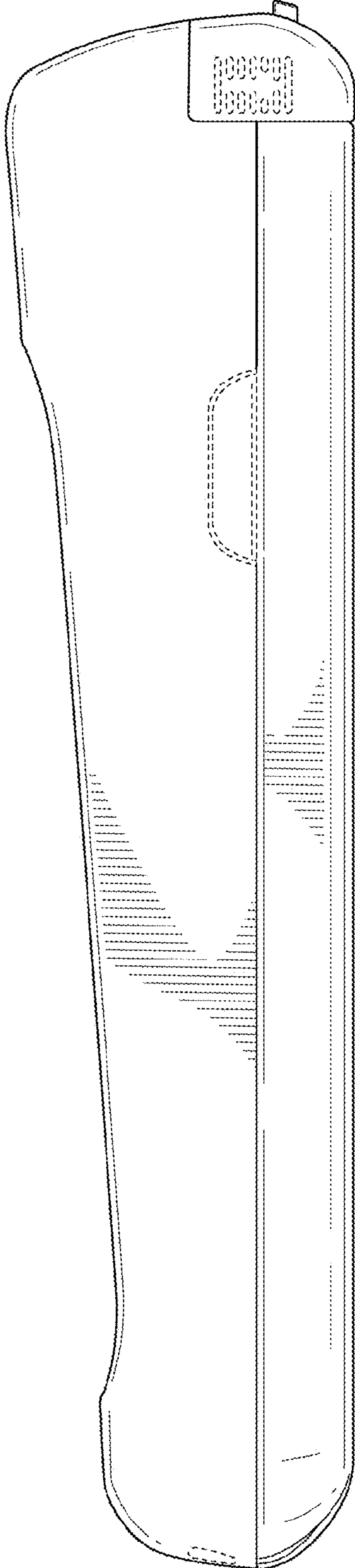


FIG. 4

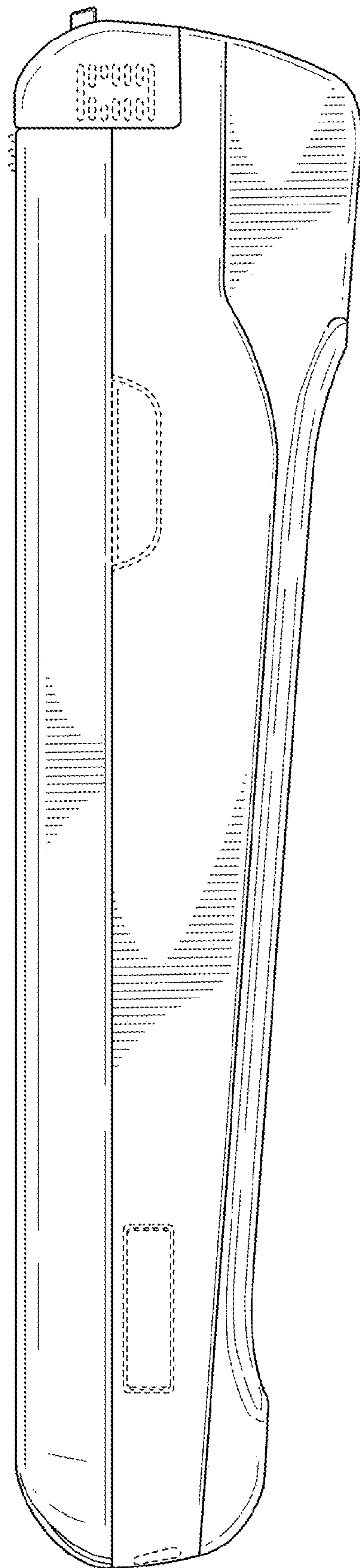


FIG. 5

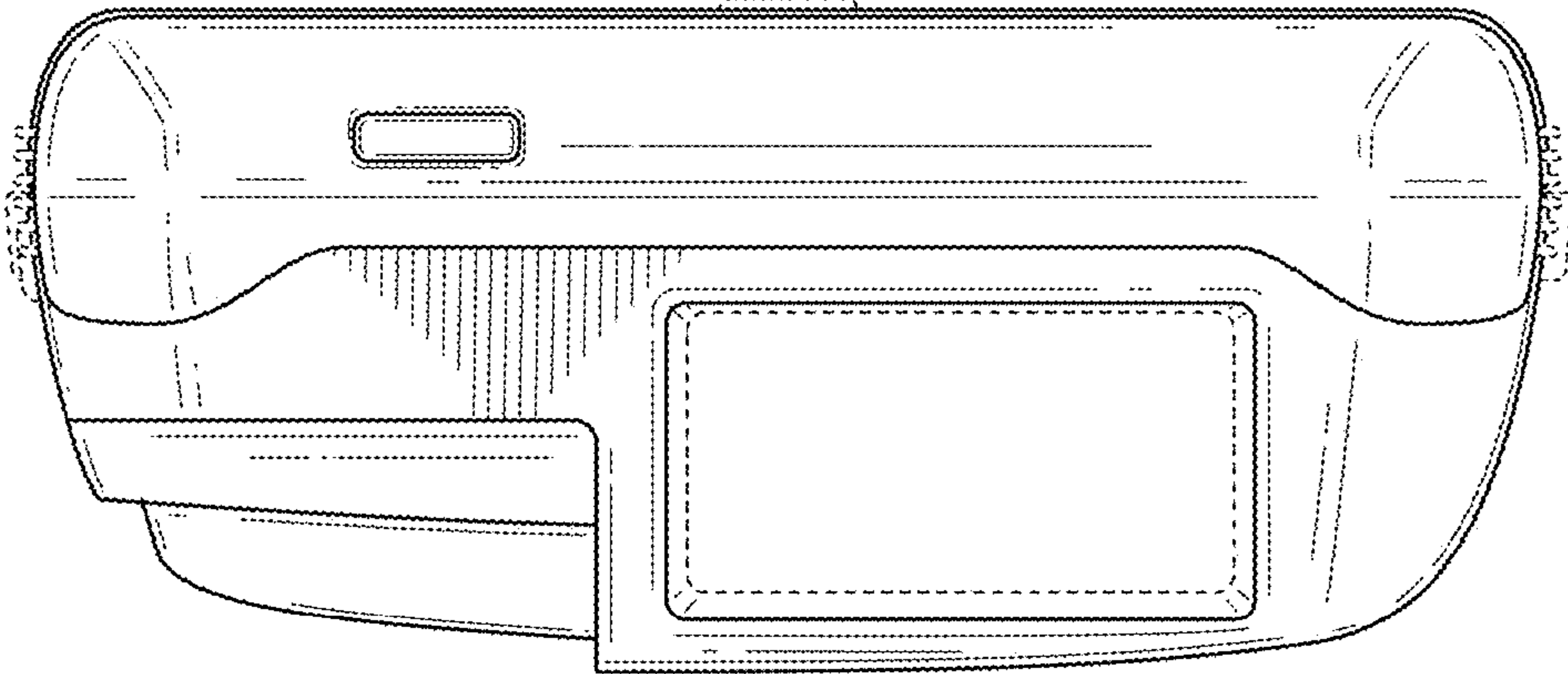


FIG. 6

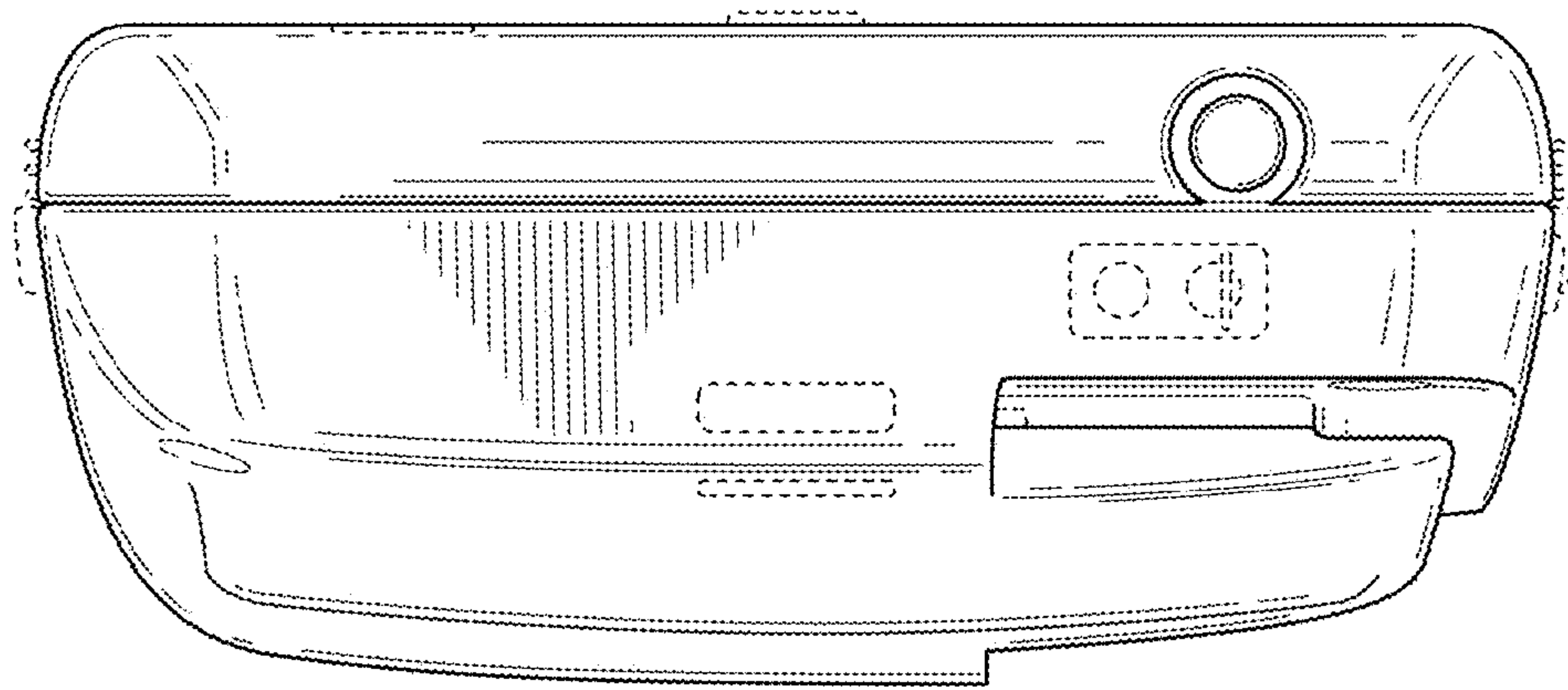


FIG. 7

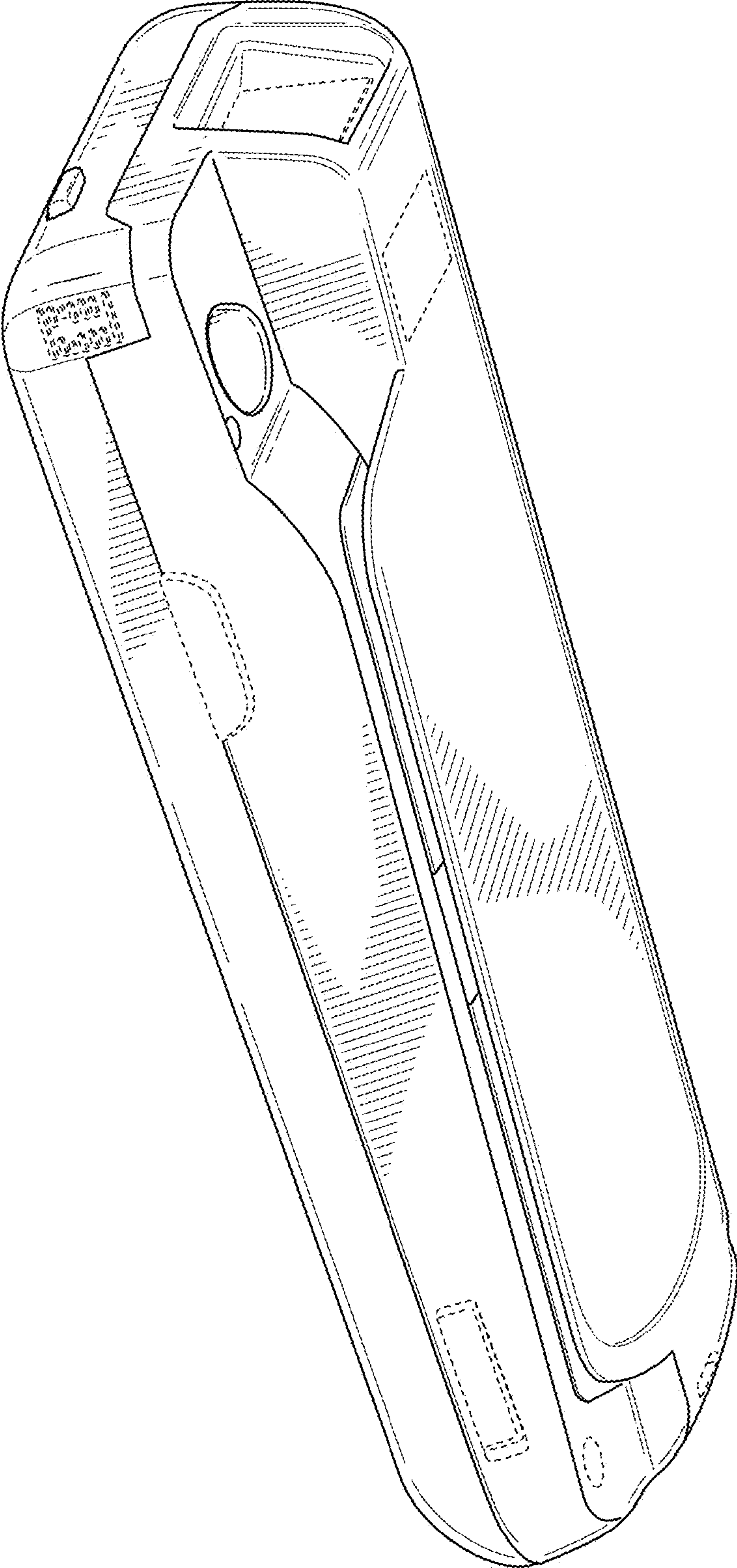


FIG. 8

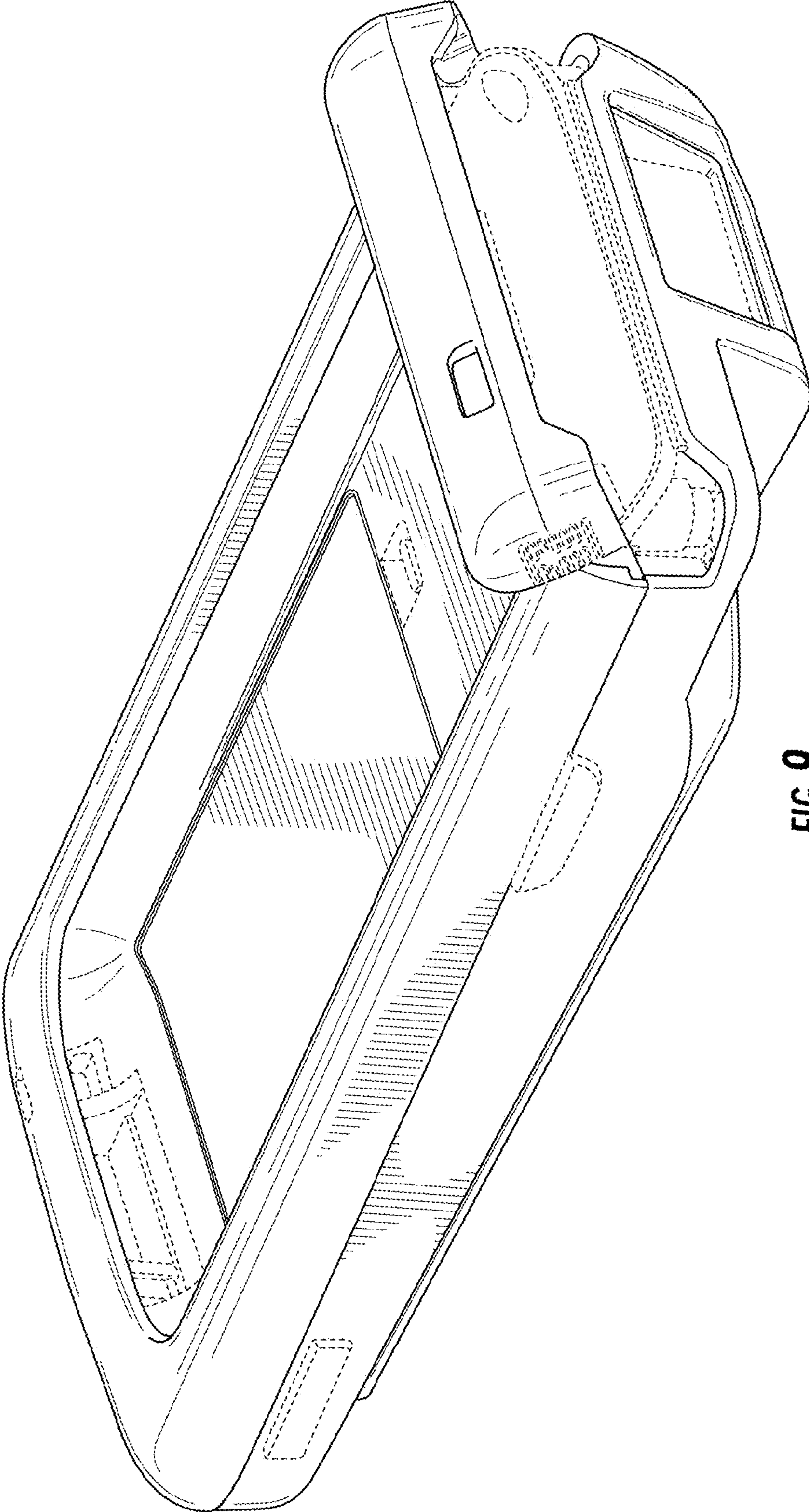


FIG. 9