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(12) **United States Design Patent**  
**Matchung**

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(54) **INTERCONNECTABLE FLOOR PANEL**

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(57) **CLAIM**

The ornamental design for interconnectable floor panel, as shown and described.

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**DESCRIPTION**

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/460,800**

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(51) **LOC (10) Cl.** ..... **25-01**

(52) **U.S. Cl.**  
USPC ..... **D25/138**

(58) **Field of Classification Search**  
USPC ..... D25/138, 163; 52/250, 563, 568, 588.1,  
52/586.2, 789.1

See application file for complete search history.

FIG. 1 is a top perspective view of an interconnectable floor panel according to my new design, with portions that are not considered part of the claimed design shown in broken lines; FIG. 2 is a bottom perspective view of the interconnectable floor panel of FIG. 1, with portions that are not considered part of the claimed design shown in broken lines; FIG. 3 is a top plan view of the interconnectable floor panel of FIG. 1, with portions that are not considered part of the claimed design shown in broken lines; FIG. 4 is a right side view of the interconnectable floor panel of FIG. 1, with portions that are not considered part of the claimed design shown in broken lines; FIG. 5 is a left side view of the interconnectable floor panel of FIG. 1, with portions that are not considered part of the claimed design shown in broken lines; FIG. 6 is a front view of the interconnectable floor panel of FIG. 1, with portions that are not considered part of the claimed design shown in broken lines; FIG. 7 is a bottom view of the interconnectable floor panel of FIG. 1, with portions that are not considered part of the claimed design shown in broken lines; FIG. 8 is a top perspective view of the interconnectable floor panel of FIG. 1, with the environment and portions that are not considered part of the claimed design shown in broken lines; and, FIG. 9 is a top perspective view of the interconnectable floor panel of FIG. 1, with the environment and portions that are not considered part of the claimed design shown in broken lines.

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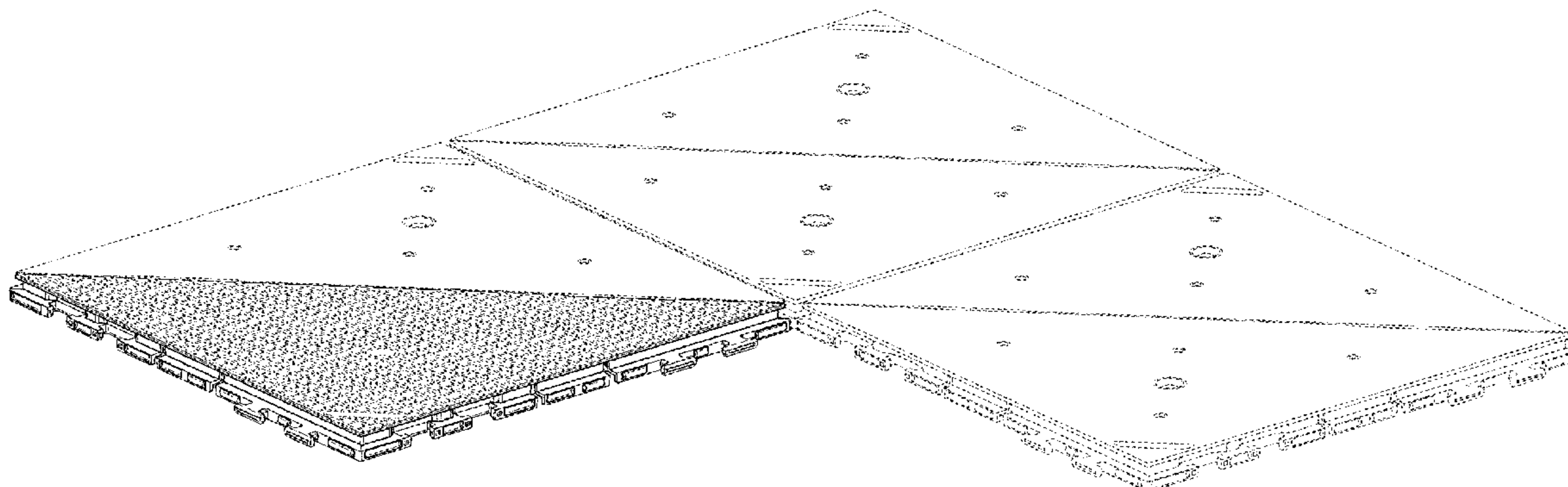
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*Primary Examiner* — Doris Clark

**1 Claim, 6 Drawing Sheets**



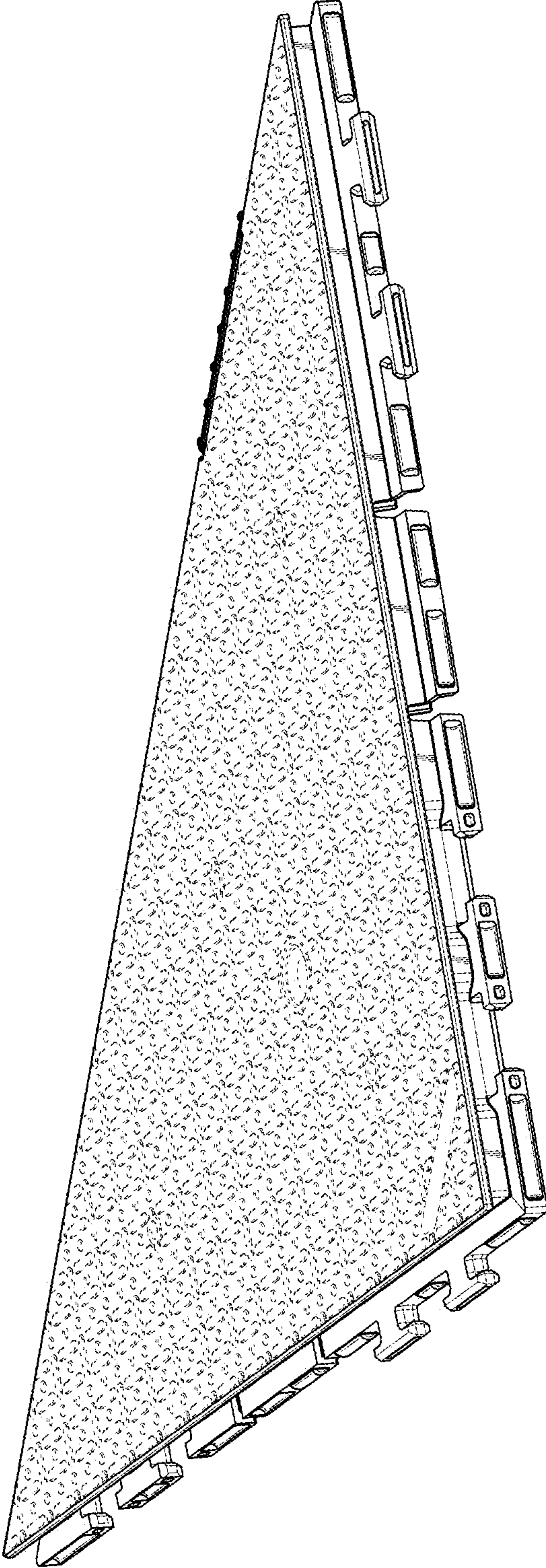
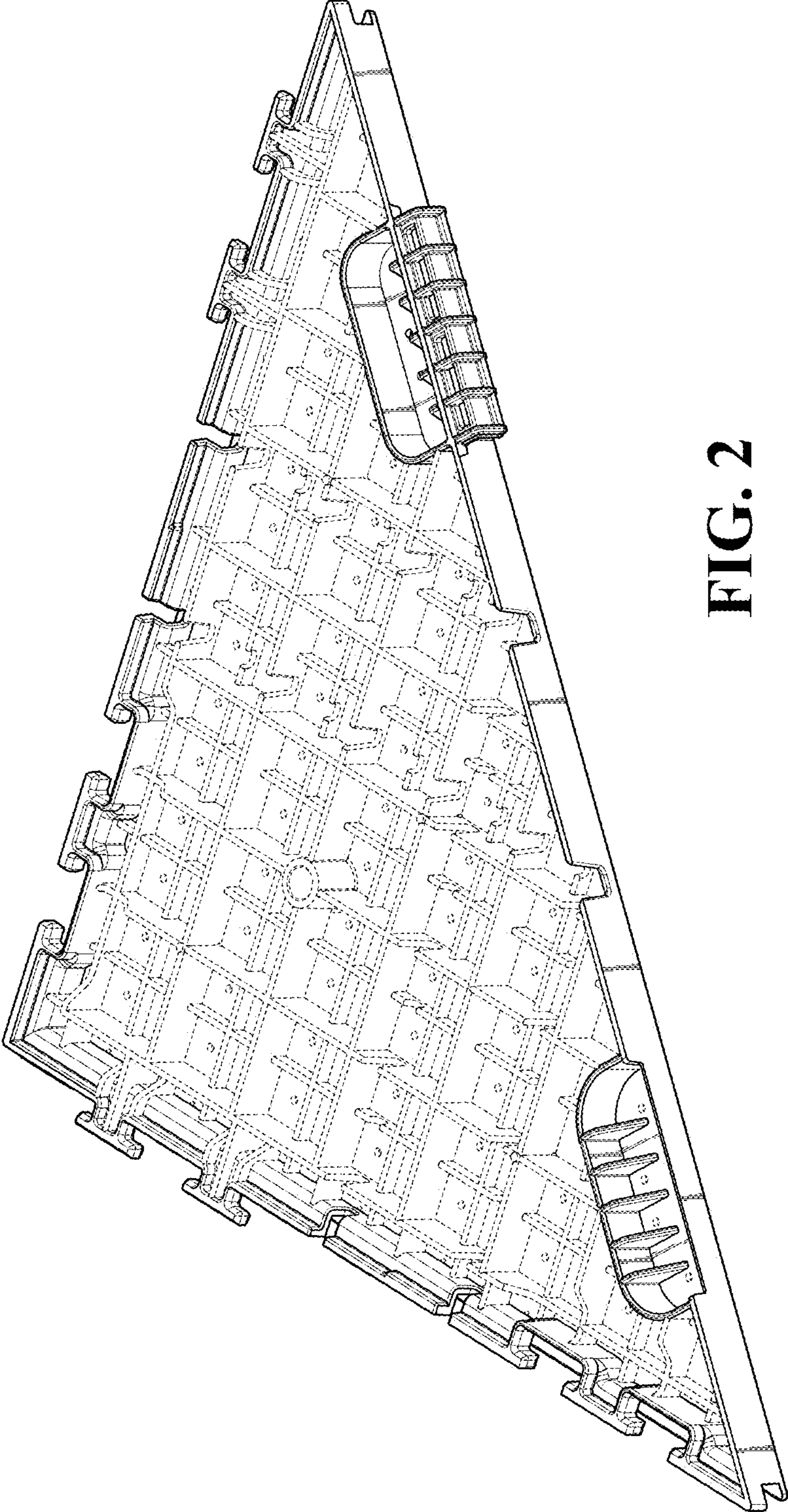


FIG. 1





**FIG. 2**

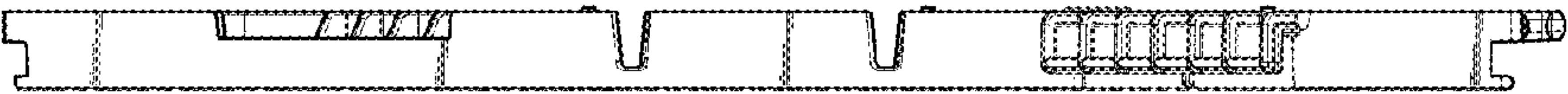


FIG. 5

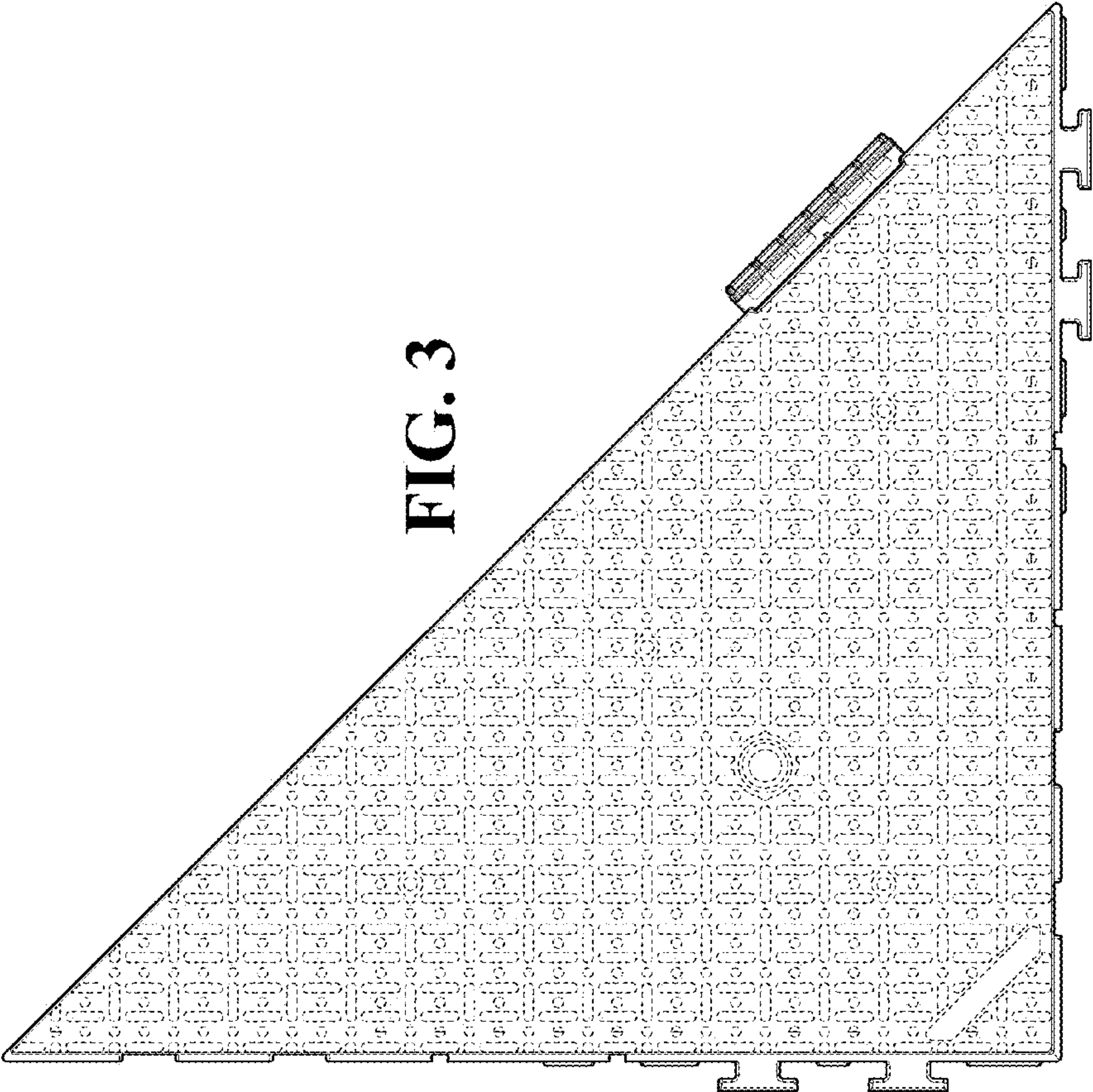


FIG. 3

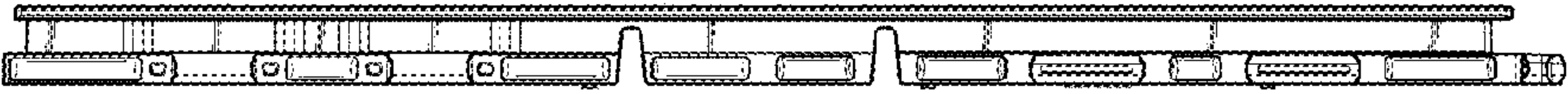


FIG. 4



FIG. 6

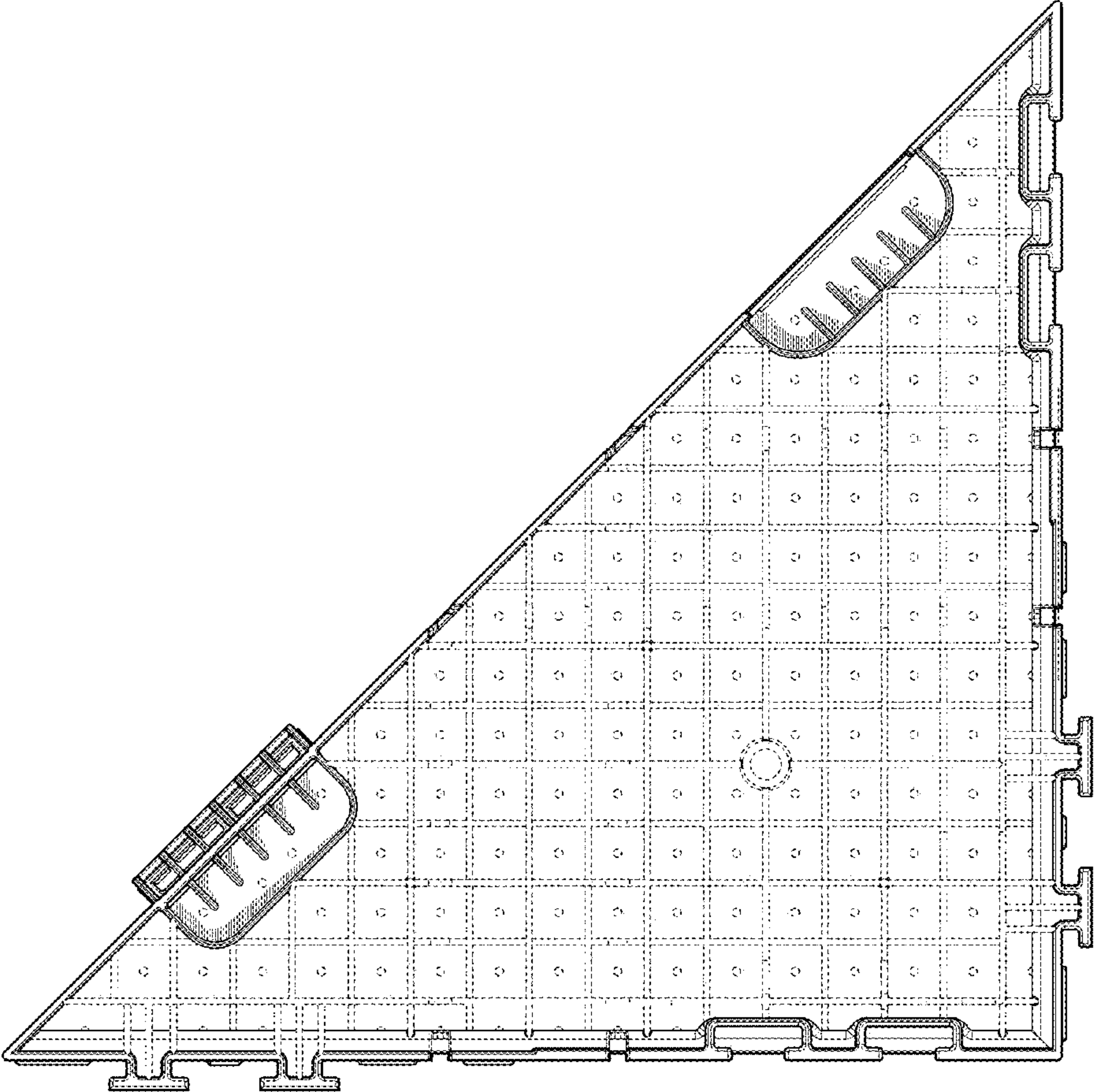


FIG. 7



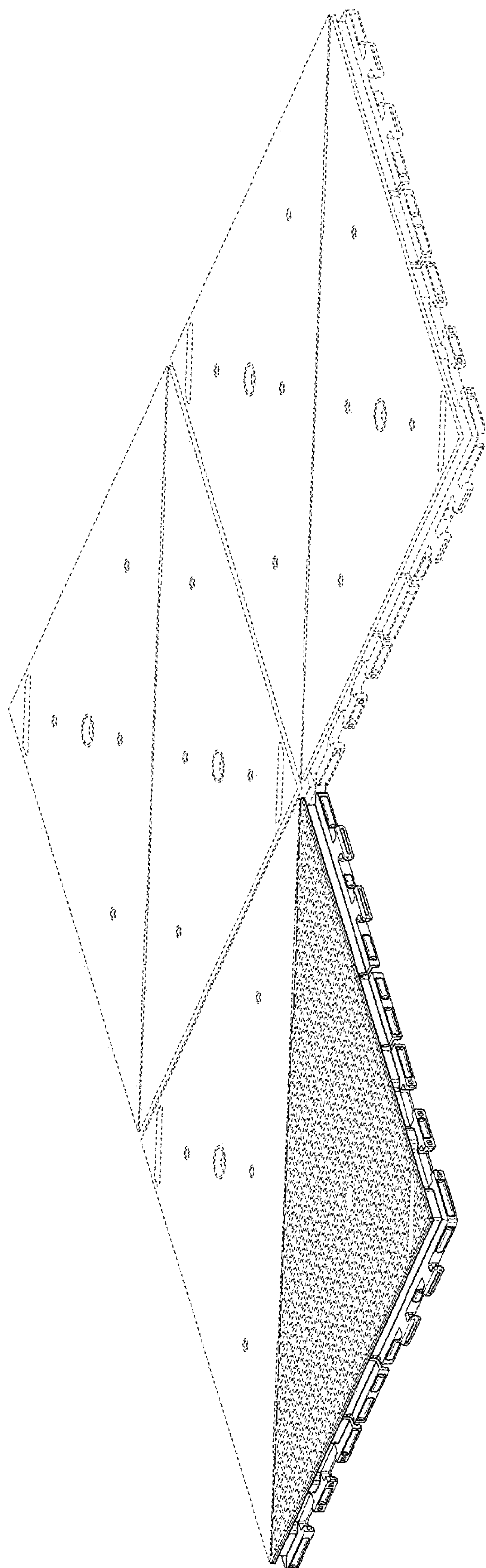
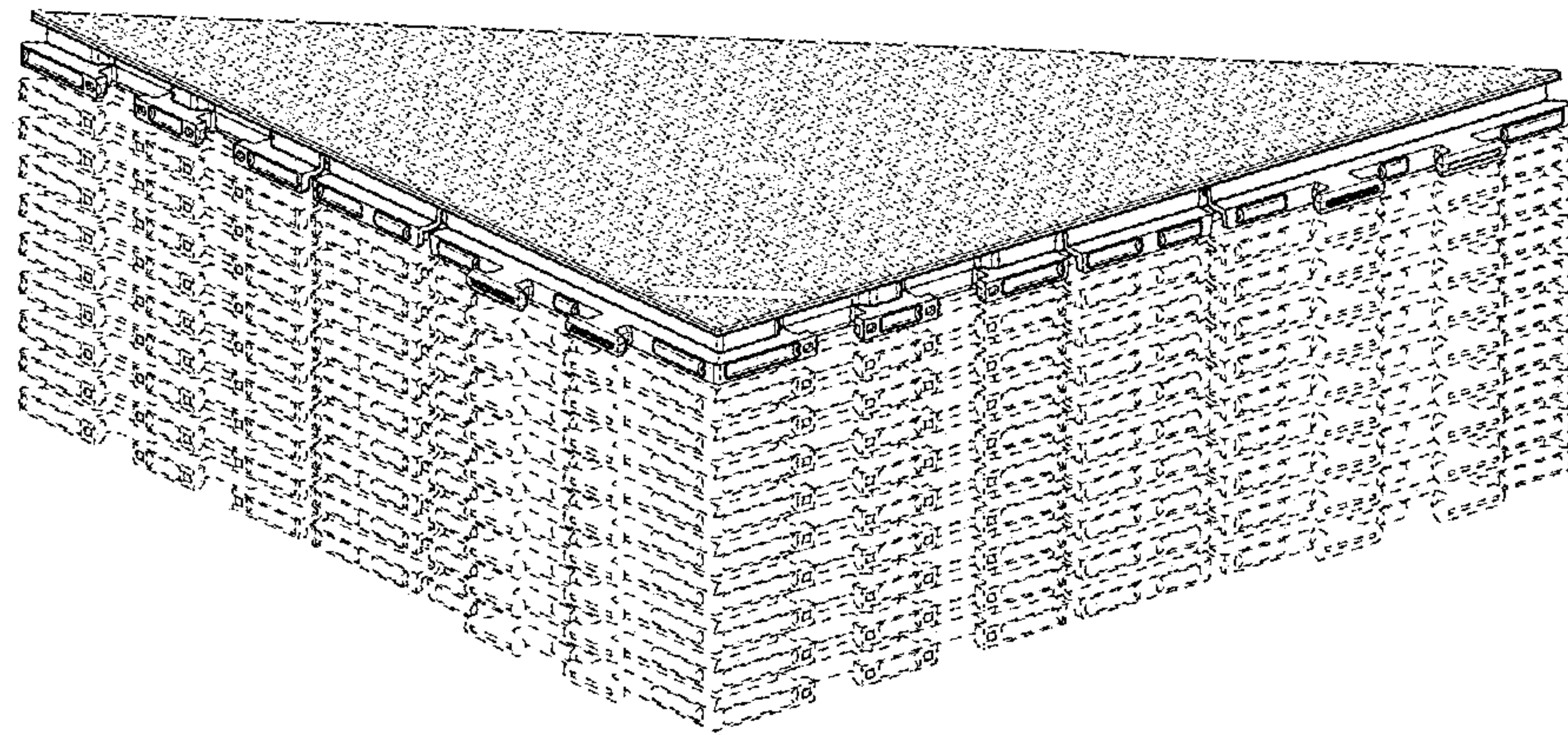


FIG. 8



**FIG. 9**