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(12) **United States Design Patent**
Gao et al.

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(45) **Date of Patent:** **** Jun. 9, 2015**

(54) **ELECTRONICS MODULE**

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(73) Assignee: **Eaton Corporation, Cleveland, OH (US)**

(**) Term: **14 Years**

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(30) **Foreign Application Priority Data**

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Jun. 21, 2013 (CN) 2013 3 0272421

(51) **LOC (10) Cl.** **13-02**

(52) **U.S. Cl.**
USPC **D13/103**

(58) **Field of Classification Search**
USPC D13/102-104, 110, 118-121, 184, 199;
429/96-100, 163, 176
See application file for complete search history.

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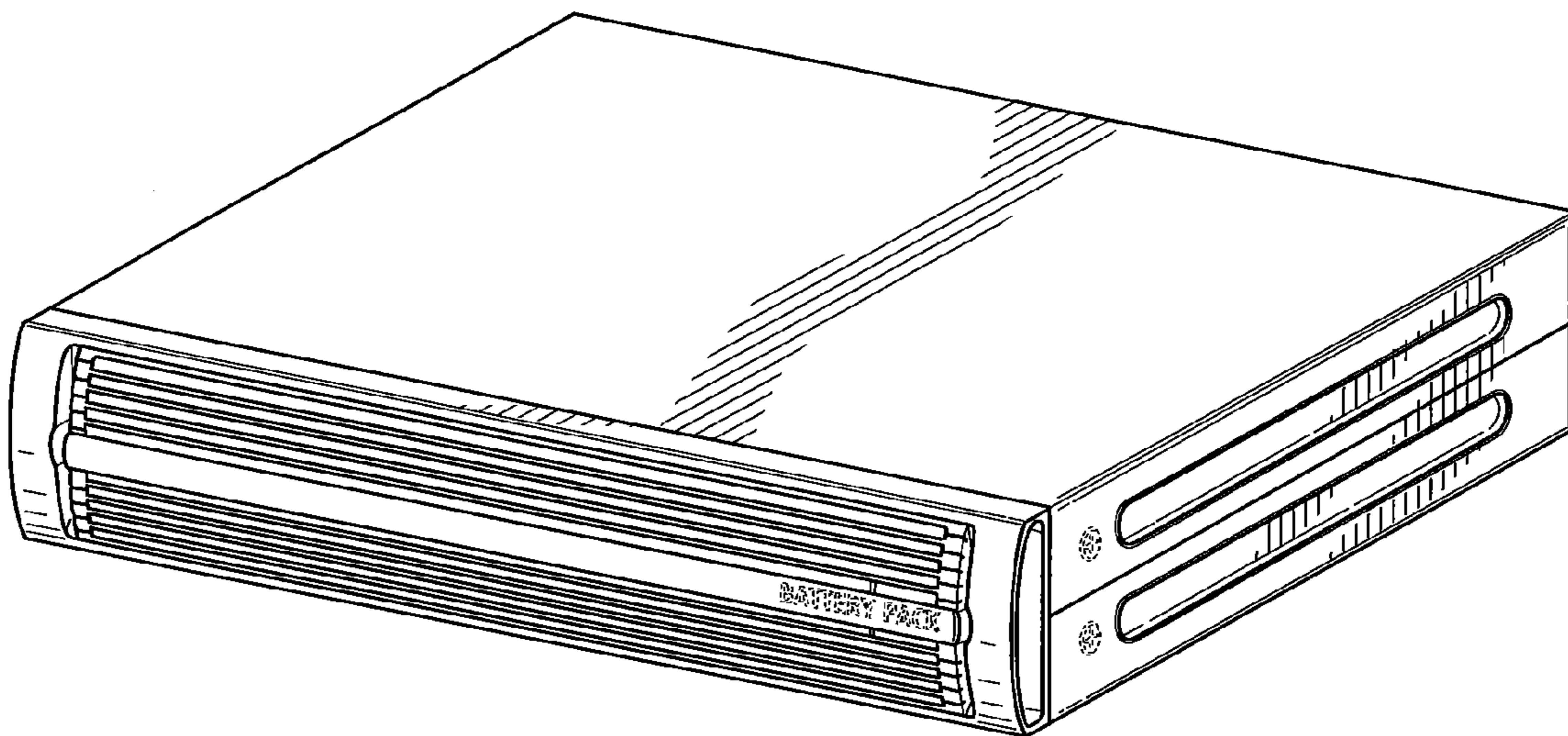
(57) **CLAIM**

The ornamental design for an electronics module, as shown and described.

DESCRIPTION

FIG. 1 is a front perspective view of an electronics module according to our design;
FIG. 2 is a front view thereof;
FIG. 3 is a back view thereof;
FIG. 4 is a side view thereof;
FIG. 5 is an opposing side view thereof;
FIG. 6 is top view thereof;
FIG. 7 is a front perspective view of another embodiment of an electronics module according to our design;
FIG. 8 is a front view thereof;
FIG. 9 is a back view thereof;
FIG. 10 is a side view thereof;
FIG. 11 is an opposing side view thereof; and,
FIG. 12 is top view thereof.
The broken lines shown herein are for illustrative purposes only and form no part of the claimed design. The bottom views are disclaimed.

1 Claim, 8 Drawing Sheets



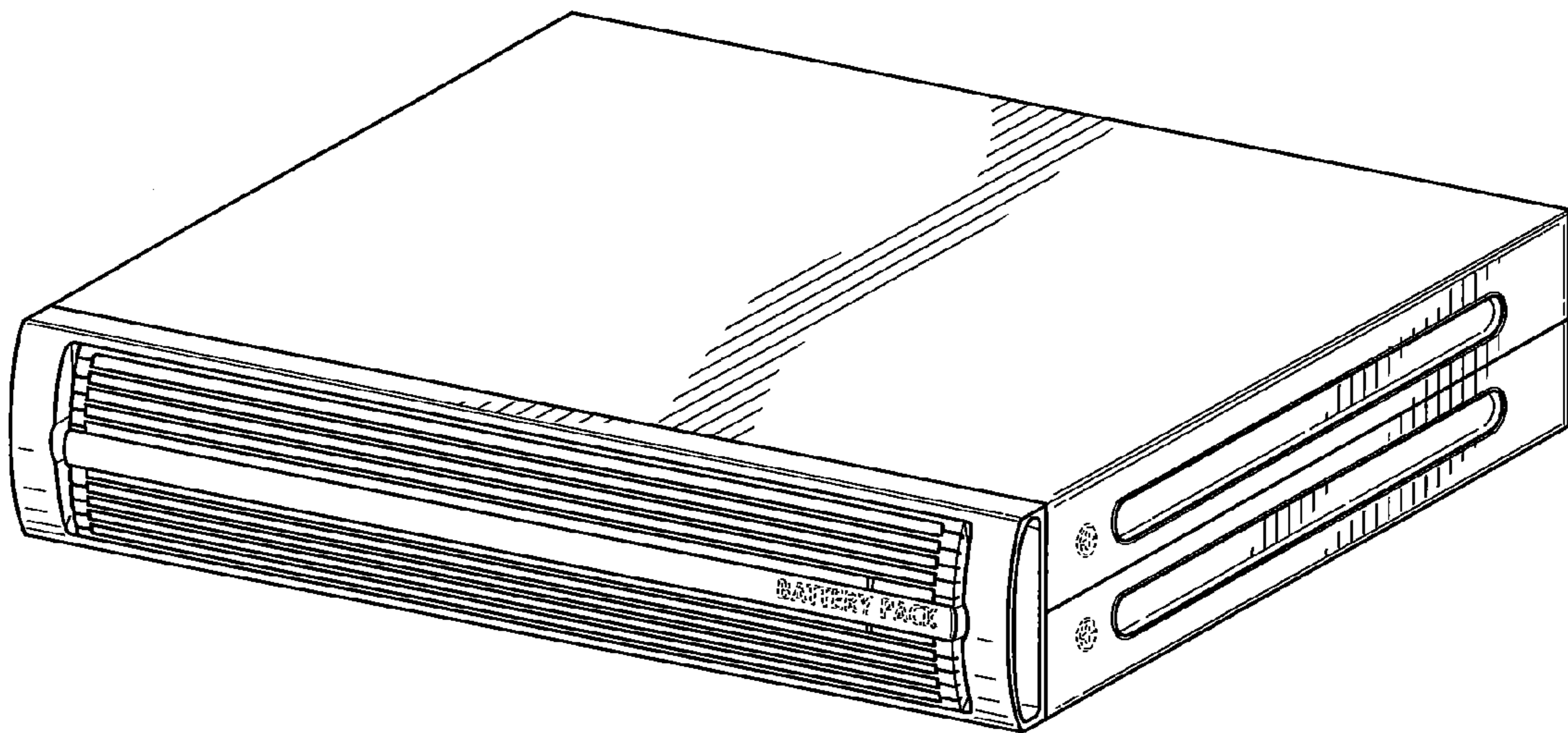


FIG. 1

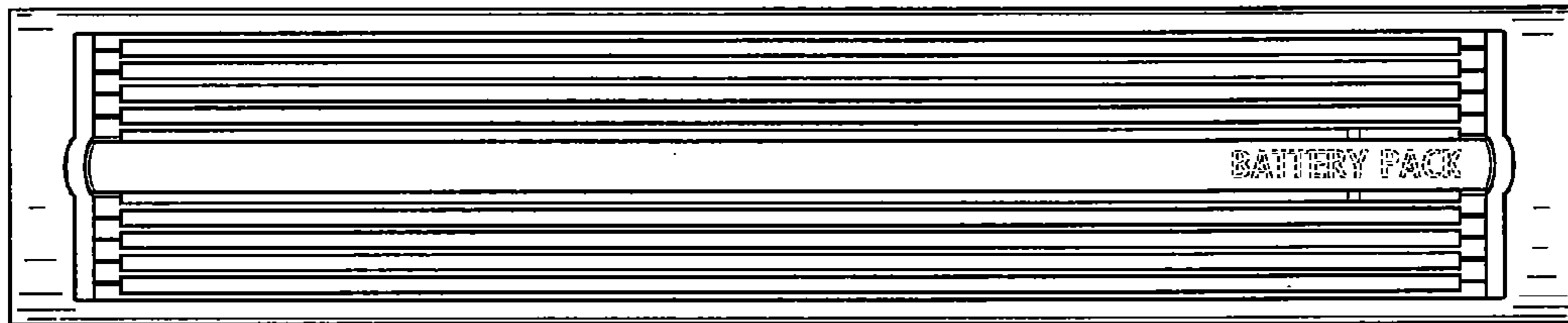


FIG. 2

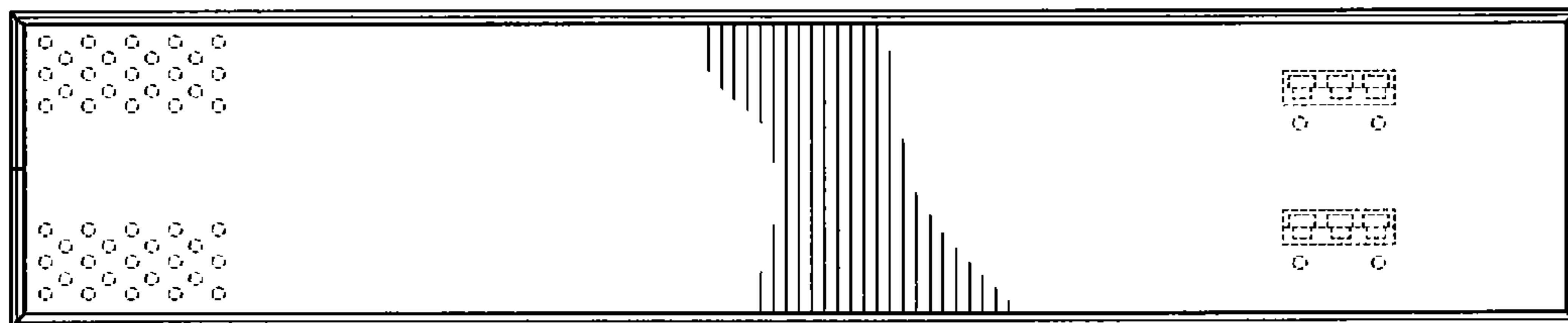


FIG. 3



FIG. 4

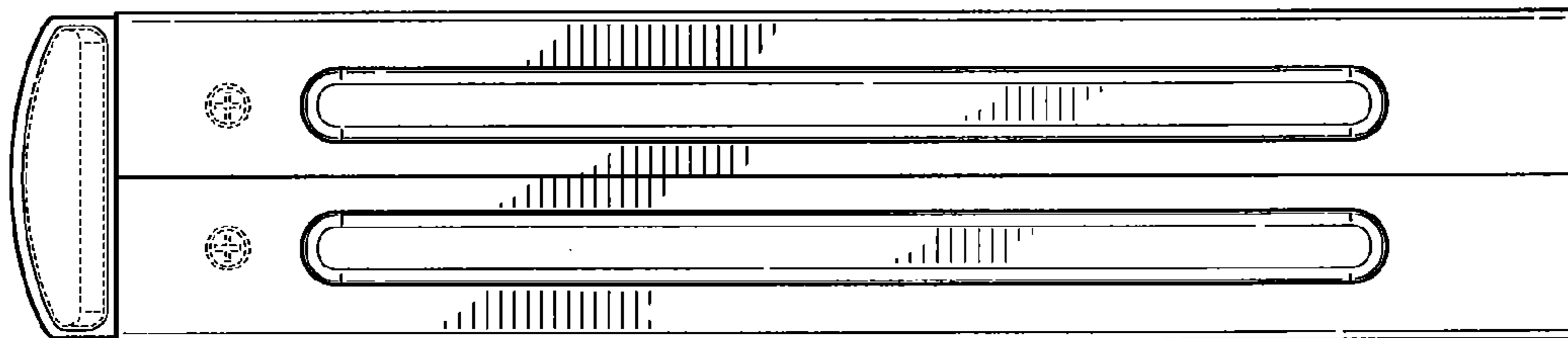


FIG. 5

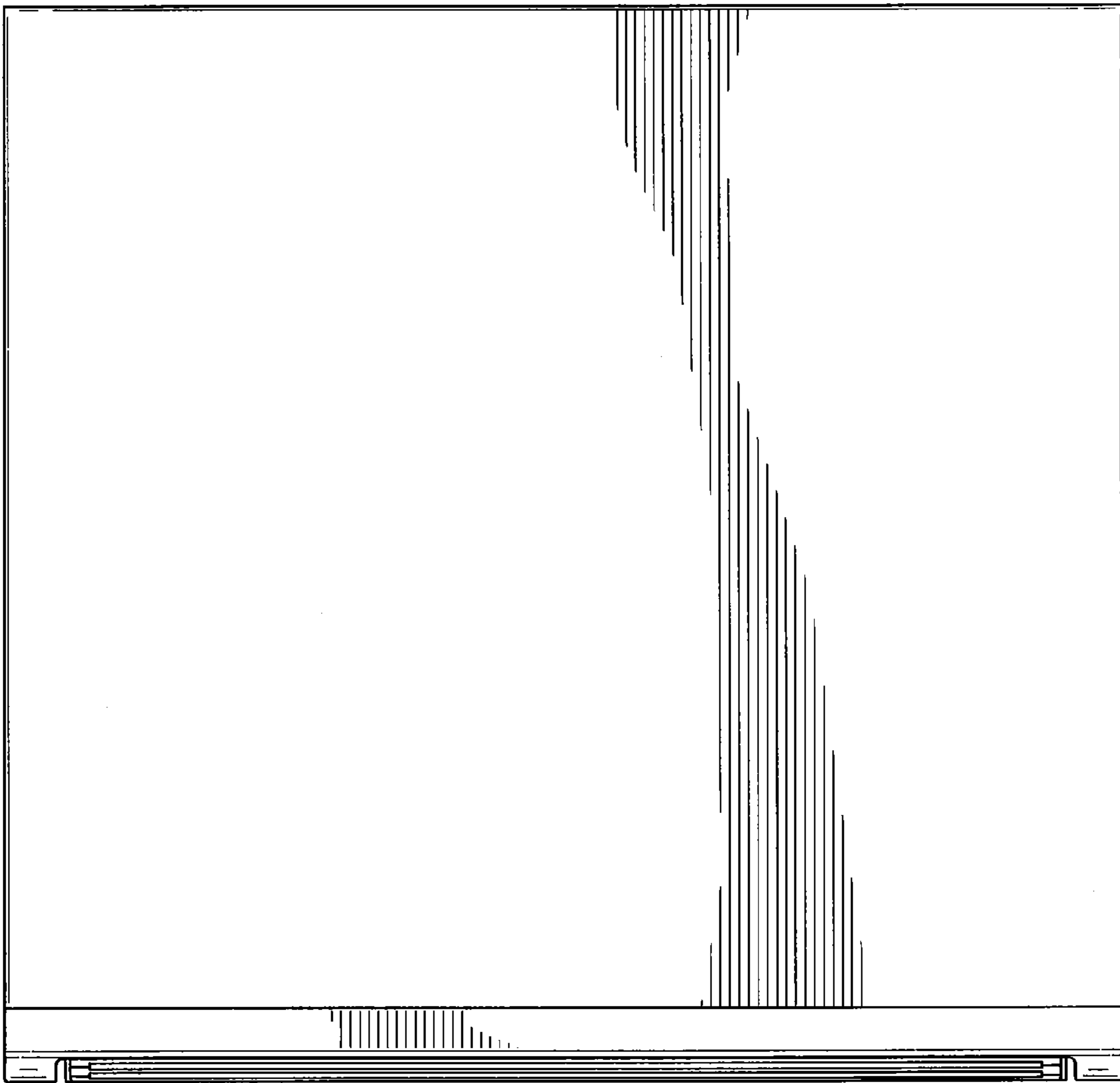


FIG. 6

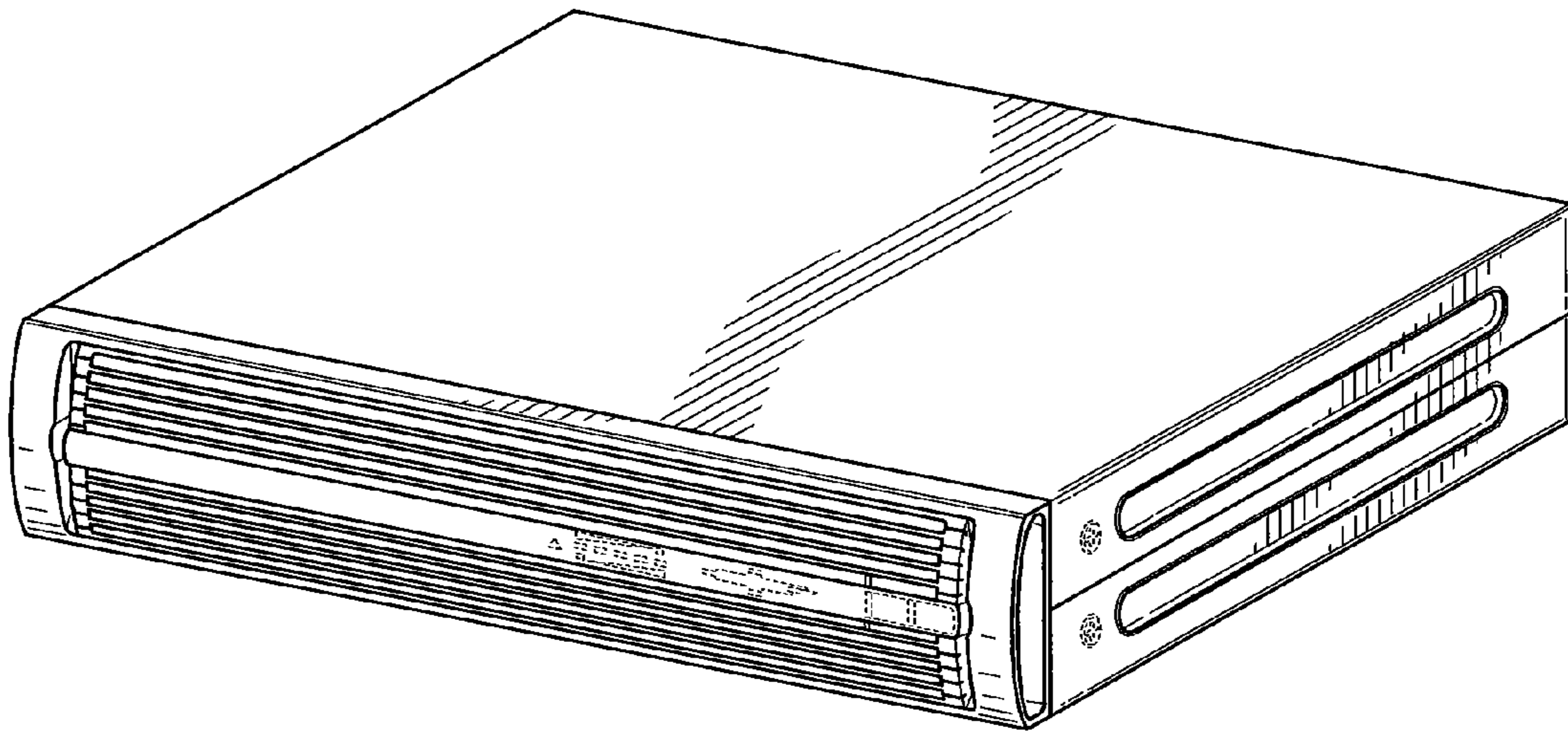


FIG. 7

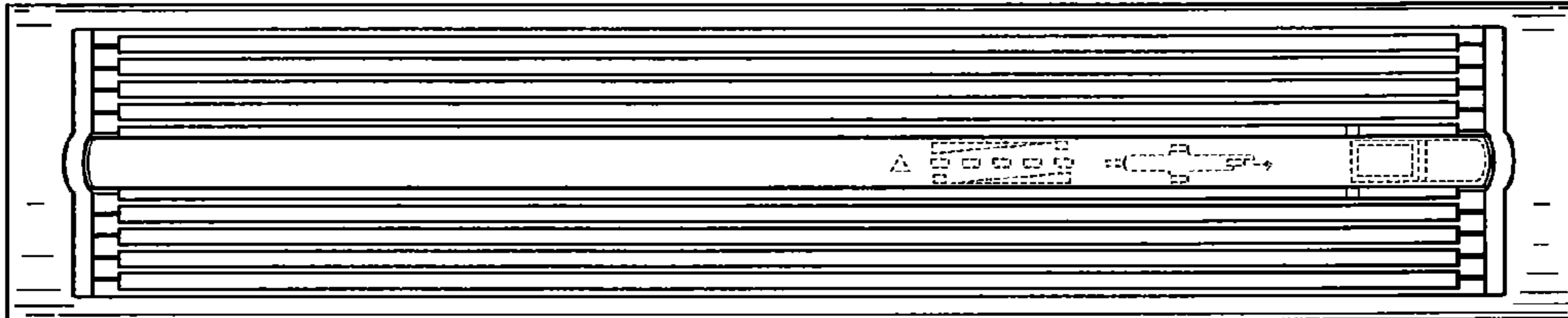


FIG. 8

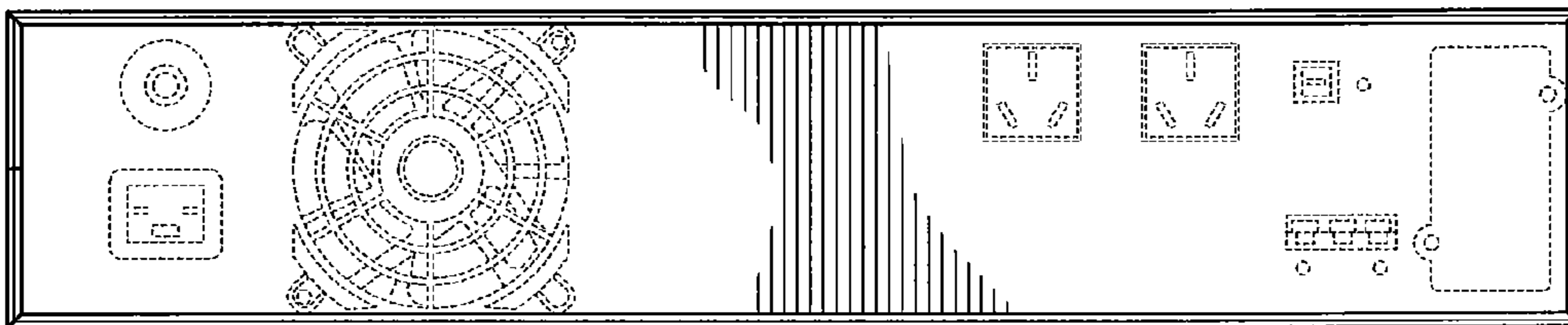


FIG. 9

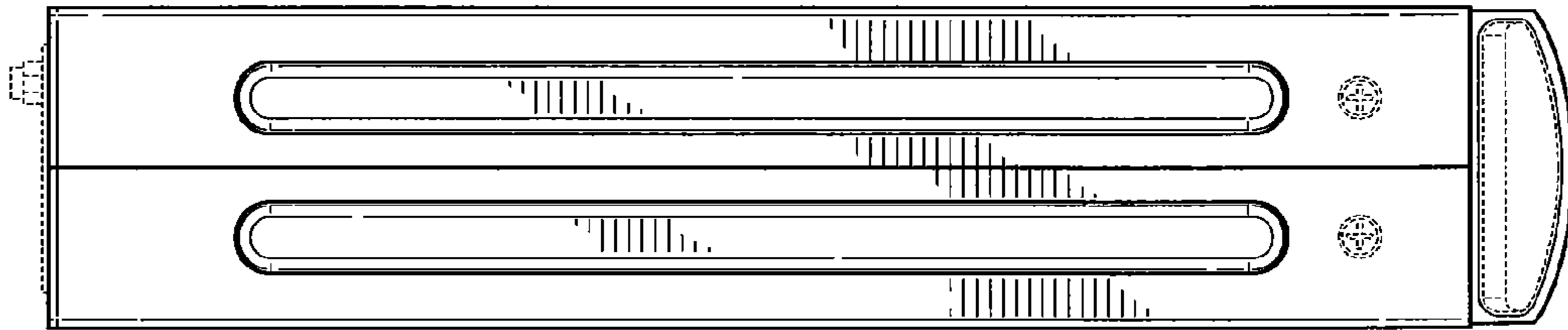


FIG. 10

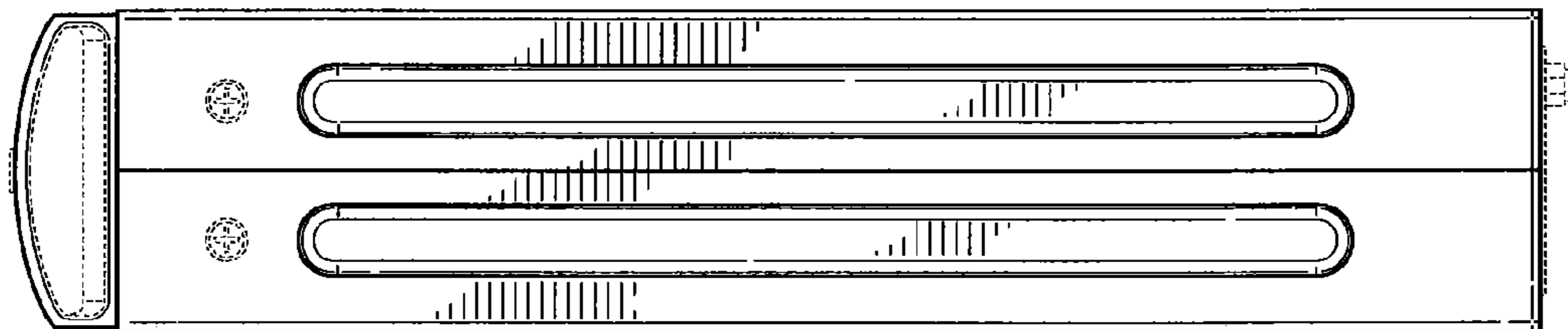


FIG. 11

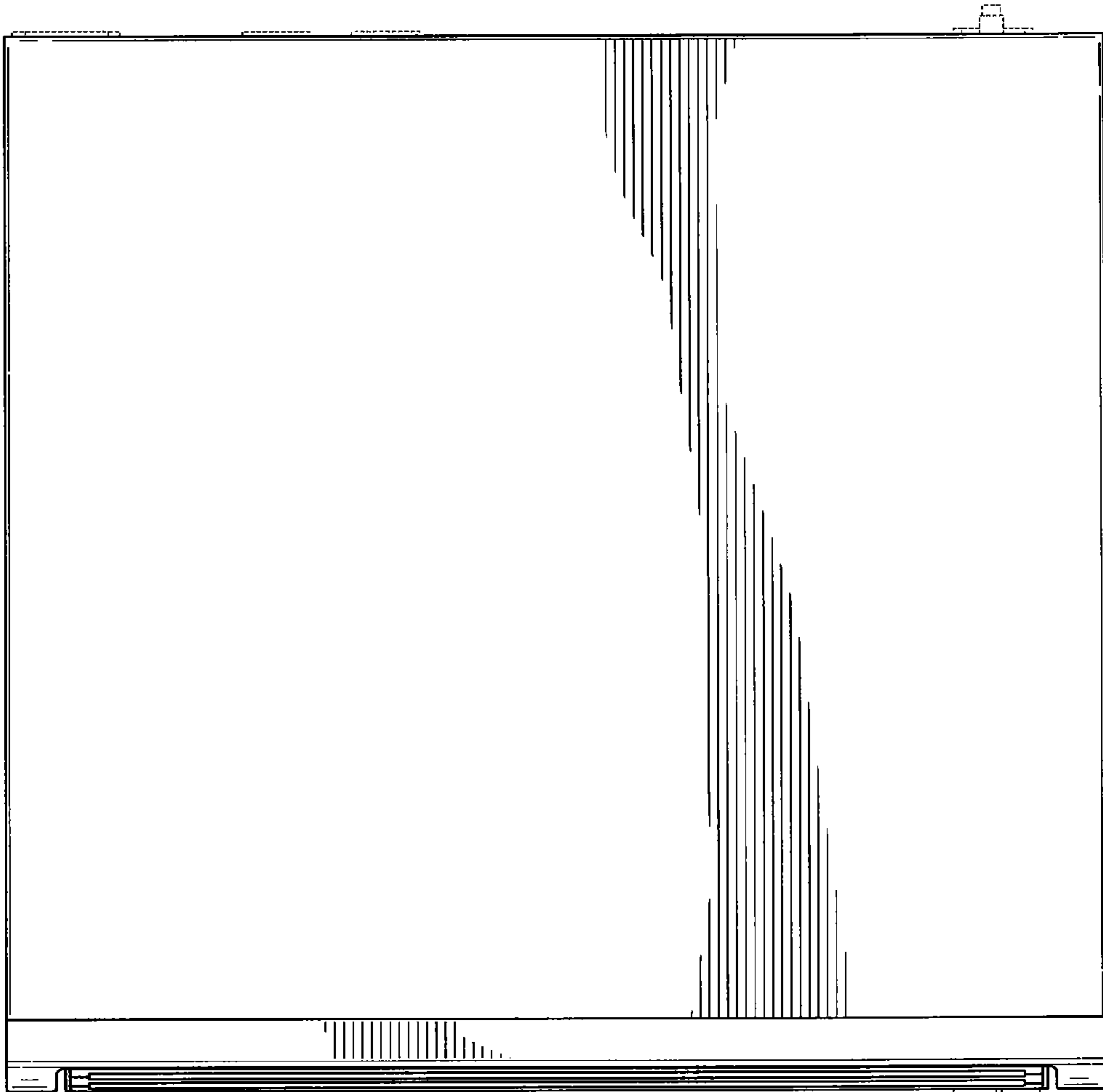


FIG. 12