



US00D725097S

(12) **United States Design Patent**
Ashcroft et al.

(10) **Patent No.:** **US D725,097 S**
(45) **Date of Patent:** **** Mar. 24, 2015**

(54) **COMPUTING DEVICE**

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(73) Assignee: **Hewlett-Packard Development Company, L.P.**, Houston, TX (US)

(**) Term: **14 Years**

(21) Appl. No.: **29/375,626**

(22) Filed: **Sep. 24, 2010**

(51) **LOC (10) Cl.** **14-02**

(52) **U.S. Cl.**
USPC **D14/315**

(58) **Field of Classification Search**

USPC D14/315-326, 341-347, 353-356, 432,
D14/439, 441-446, 125, 138,
D14/138 AA-138 AD, 138 C, 138 G; 341/22,
341/23; 345/168, 169; 361/679.01, 679.02,
361/679.08, 679.09, 679.1, 679.18, 679.26,
361/679.3, 679.31, 679.4, 679.55, 679.56
See application file for complete search history.

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(Continued)

Primary Examiner — Deanna L Pratt

(74) *Attorney, Agent, or Firm* — Rathe Lindenbaum LLP

(57) **CLAIM**

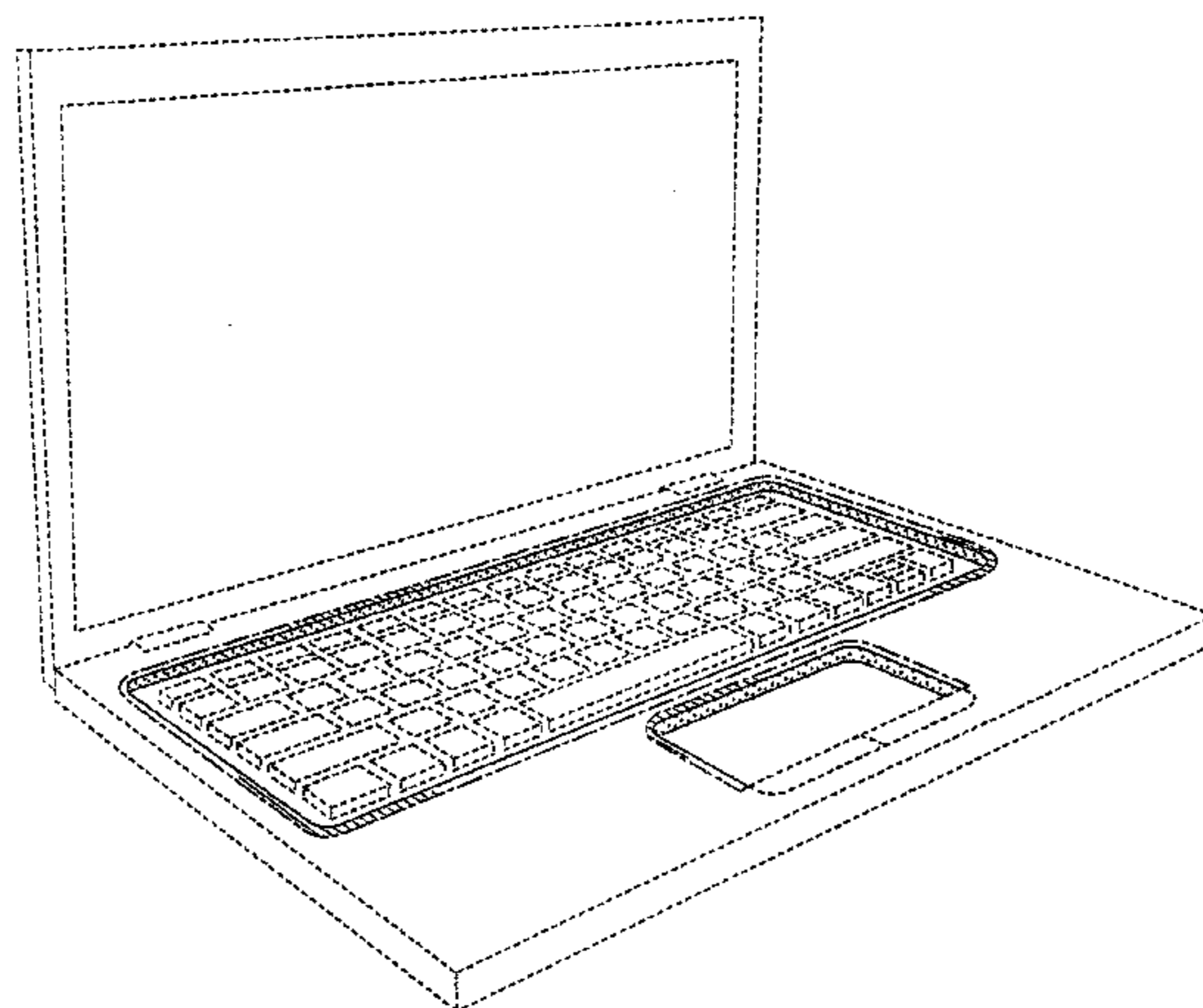
We claim the ornamental design for a computing device, as shown and described.

DESCRIPTION

FIG. 1 is a front perspective view of a computing device;
FIG. 2 is a rear perspective view of the computing device;
FIG. 3 is a top plan view of the computing device;
FIG. 4 is a sectional view of the computing device of FIG. 3 taken along line 4--4;
FIG. 5 is a sectional view of the computing device of FIG. 3 taken along line 5--5;

FIG. 6 is a sectional view of the computing device of FIG. 3 taken along line 6--6;
FIG. 7 is a front perspective view of a second embodiment of the computing device;
FIG. 8 is a rear perspective view of the second embodiment of the computing device;
FIG. 9 is a top plan view of the second embodiment of the computing device;
FIG. 10 is a sectional view of the second embodiment of the computing device of FIG. 9 taken along line 10-10;
FIG. 11 is a sectional view of the second embodiment of the computing device of FIG. 9 taken along line 11--11;
FIG. 12 is a sectional view of the second embodiment of the computing device of FIG. 9 taken along line 12--12;
FIG. 13 is a front perspective view of a third embodiment of the computing device;
FIG. 14 is a rear perspective view of the third embodiment of the computing device;
FIG. 15 is a top plan view of the third embodiment of the computing device;
FIG. 16 is a sectional view of the third embodiment of the computing device of FIG. 15 taken along line 16--16;
FIG. 17 is a sectional view of the third embodiment of the computing device of FIG. 15 taken along line 17--17;
FIG. 18 is a sectional view of the third embodiment of the computing device of FIG. 15 taken along line 18--18;
FIG. 19 is a front perspective view of a fourth embodiment of the computing device;
FIG. 20 is a rear perspective view of the fourth embodiment of the computing device;
FIG. 21 is a top plan view of the fourth embodiment of the computing device;
FIG. 22 is a sectional view of the fourth embodiment of the computing device of FIG. 21 taken along line 22--22;
FIG. 23 is a sectional view of the fourth embodiment of the computing device of FIG. 21 taken along line 23--23; and,
FIG. 24 is a sectional view of the fourth embodiment of the computing device of FIG. 21 taken along line 24--24.
The dash-dash broken lines in the drawings are environmental lines while the short dash-long dash lines are boundary lines, neither of which are part of the claimed design. The stippling in the drawings indicates portions of the claimed design that have an appearance that contrasts with the unstippled portions of the claimed design.

1 Claim, 16 Drawing Sheets



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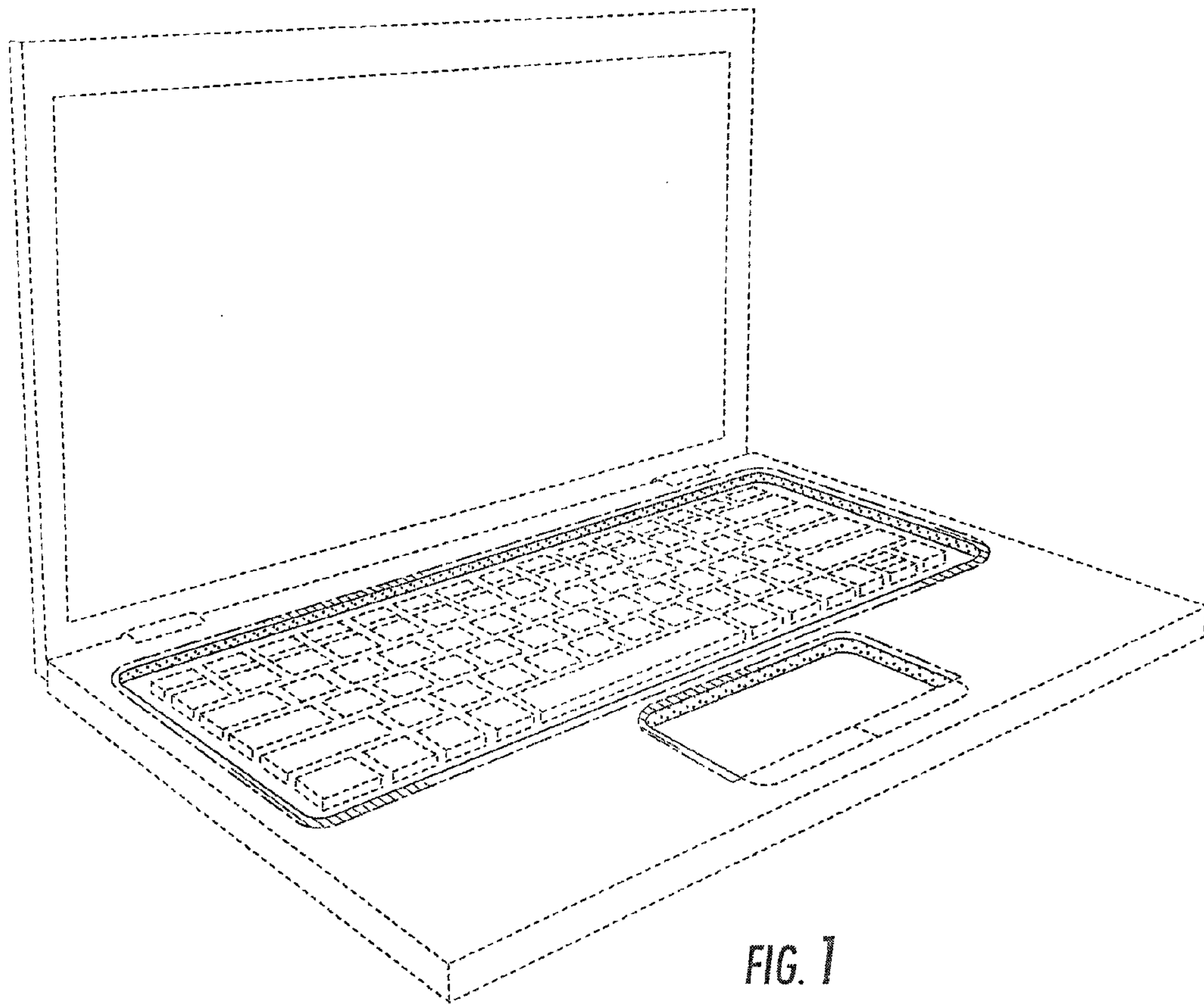


FIG. 1

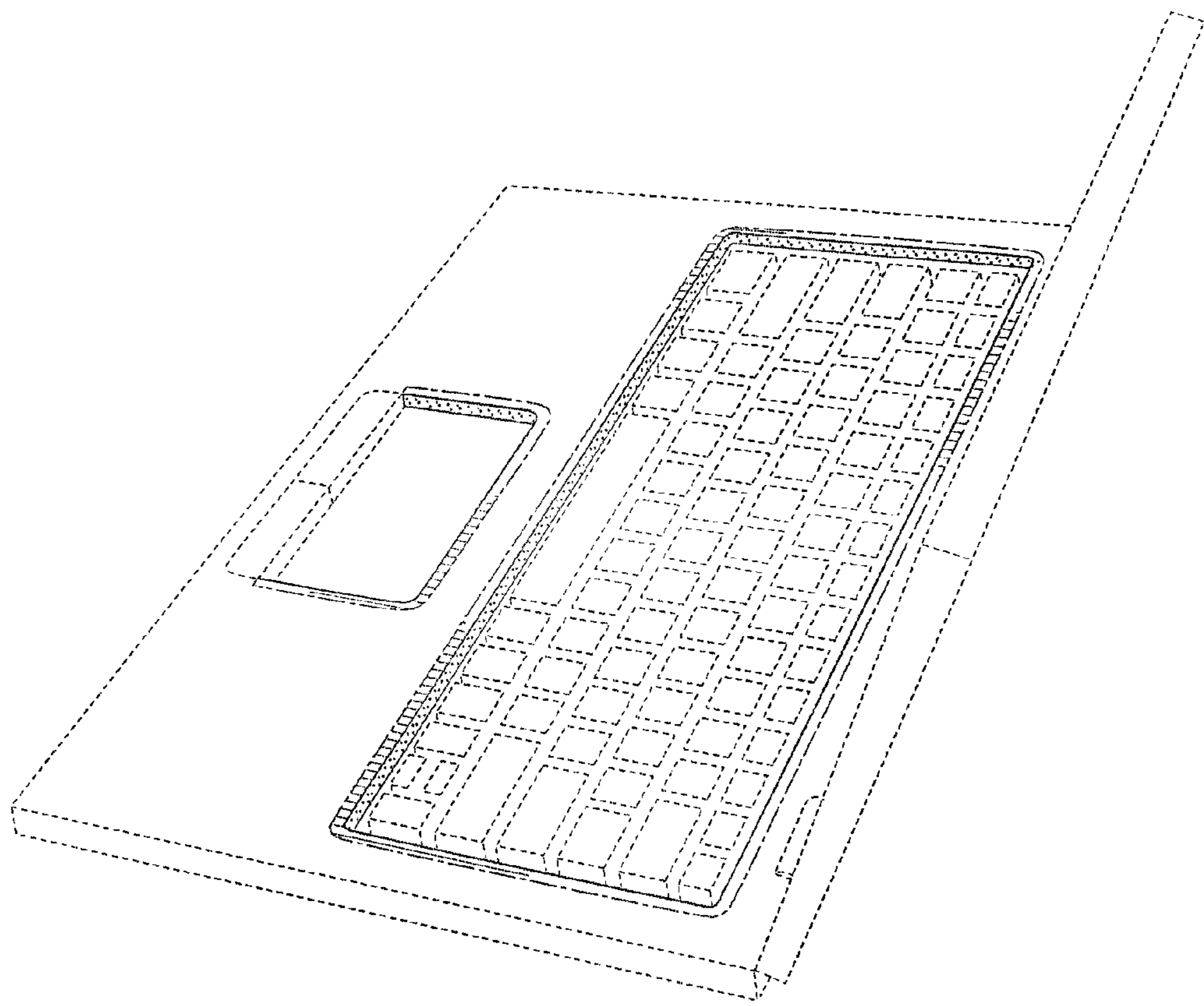


FIG. 2

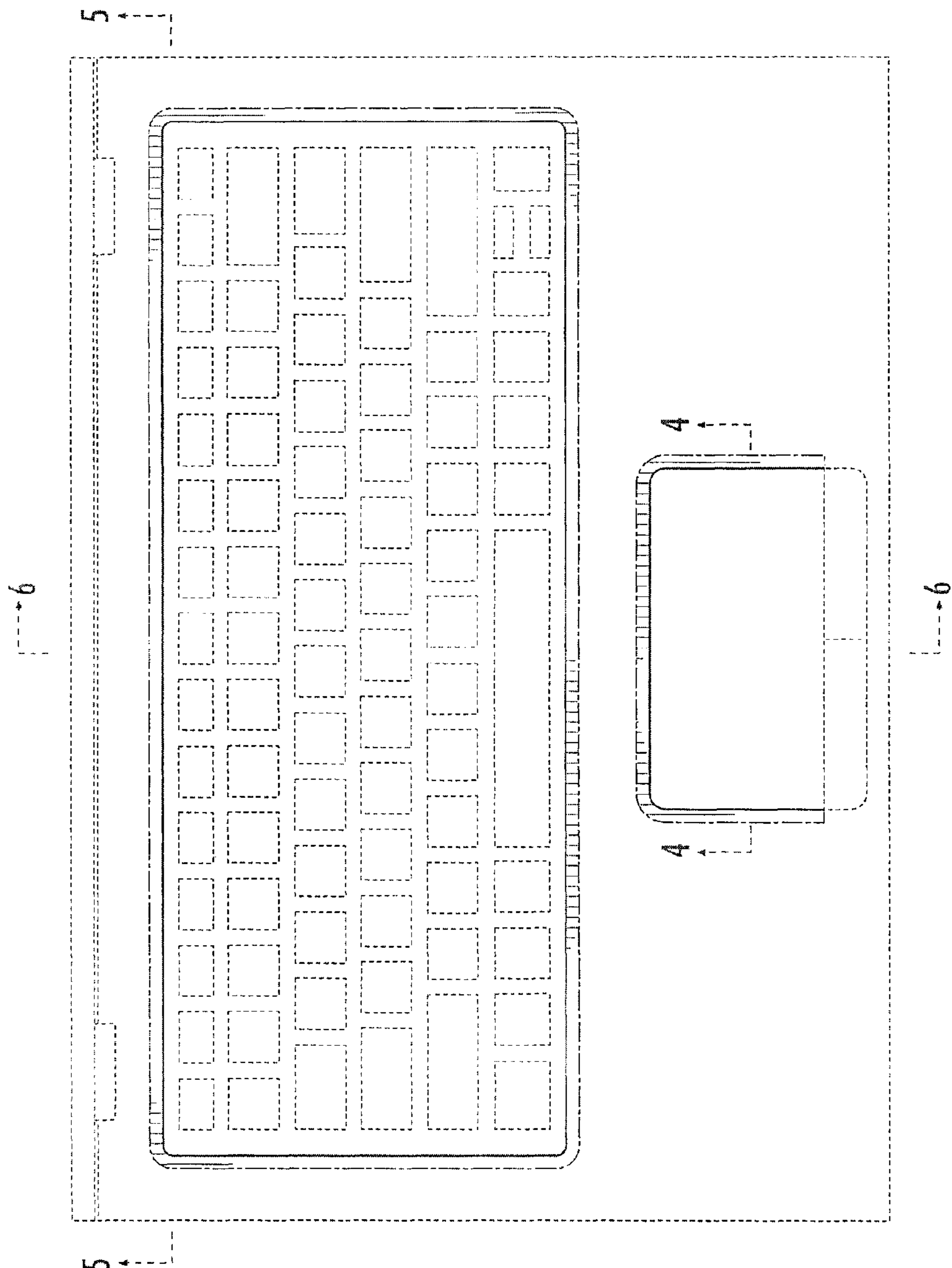


FIG. 3



FIG. 4



FIG. 5

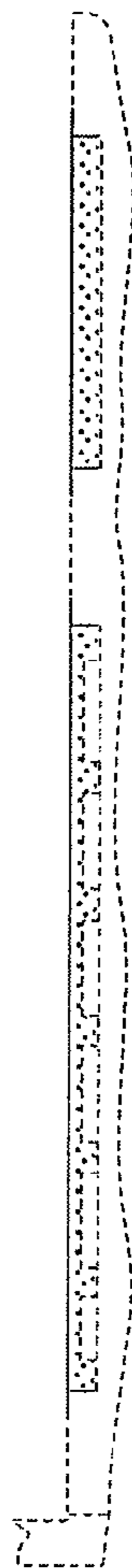


FIG. 6

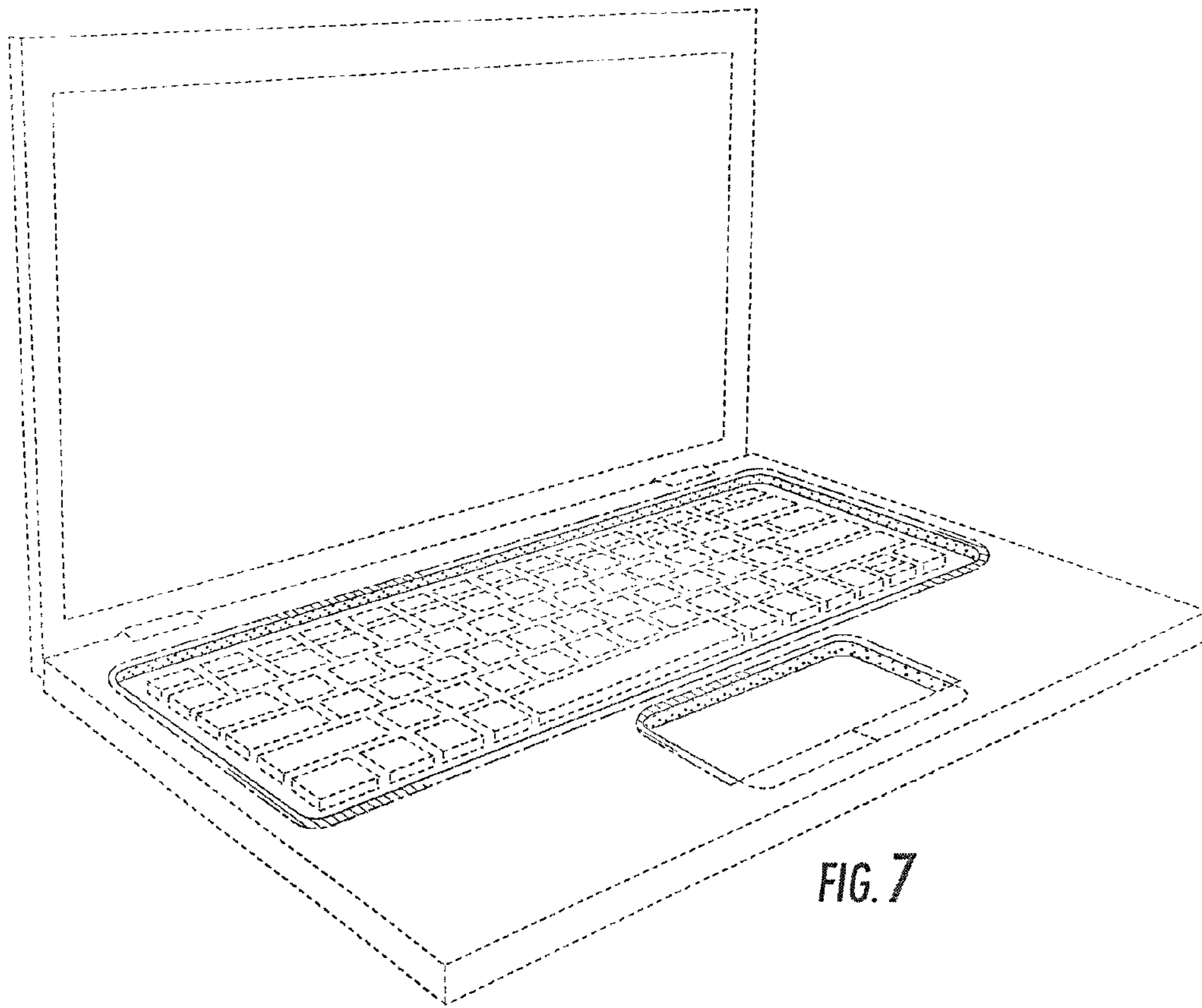


FIG. 7

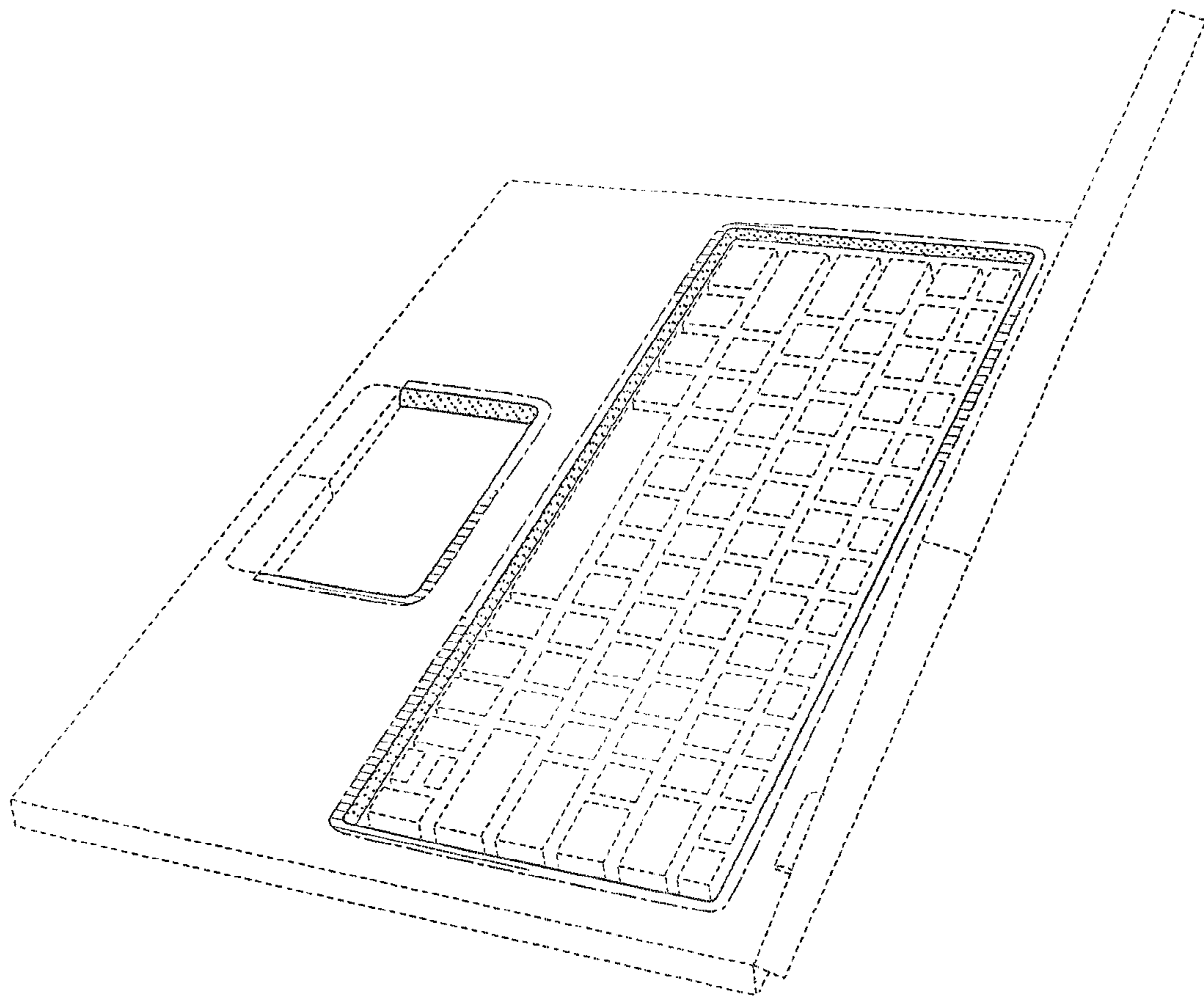


FIG. 8

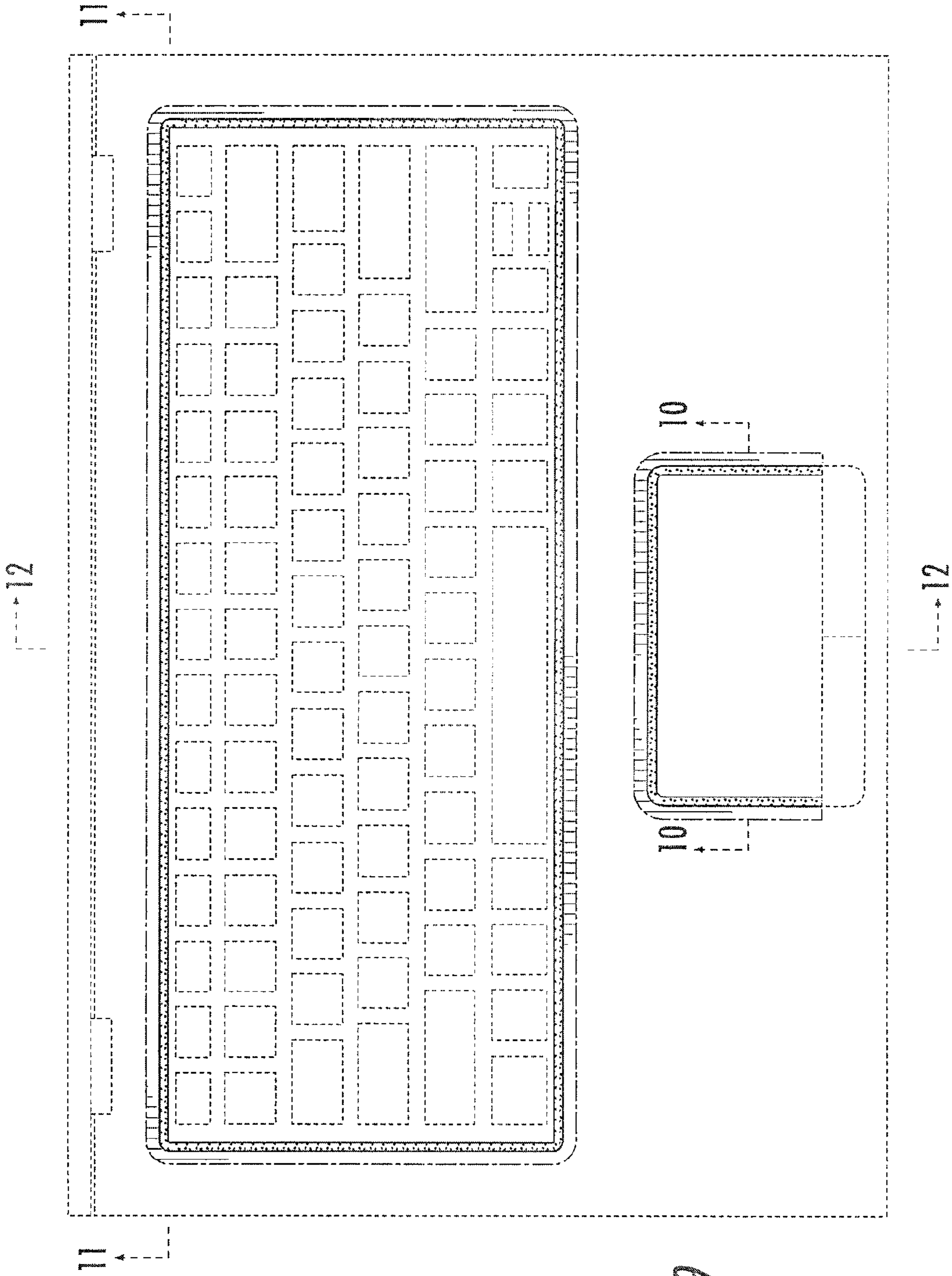


FIG. 9



FIG. 10

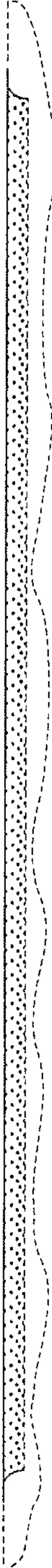


FIG. 11

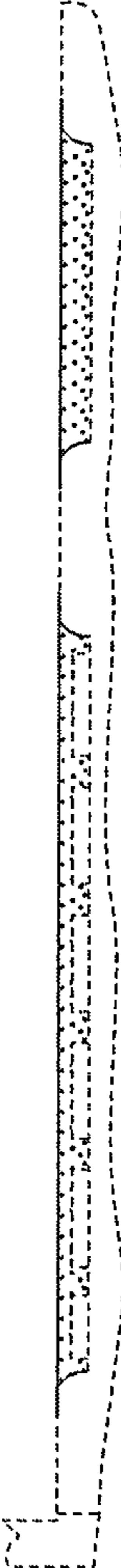


FIG. 12

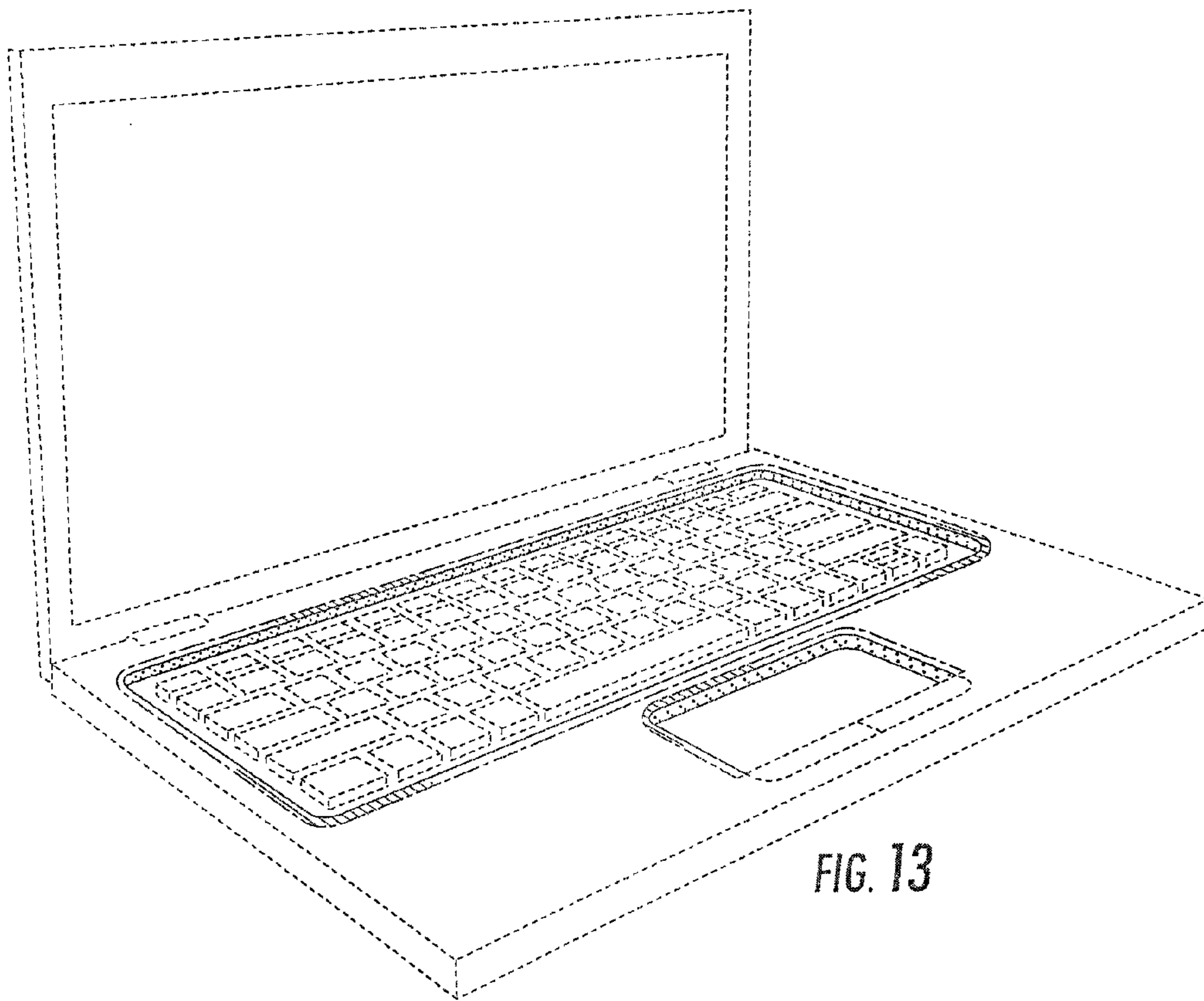


FIG. 13

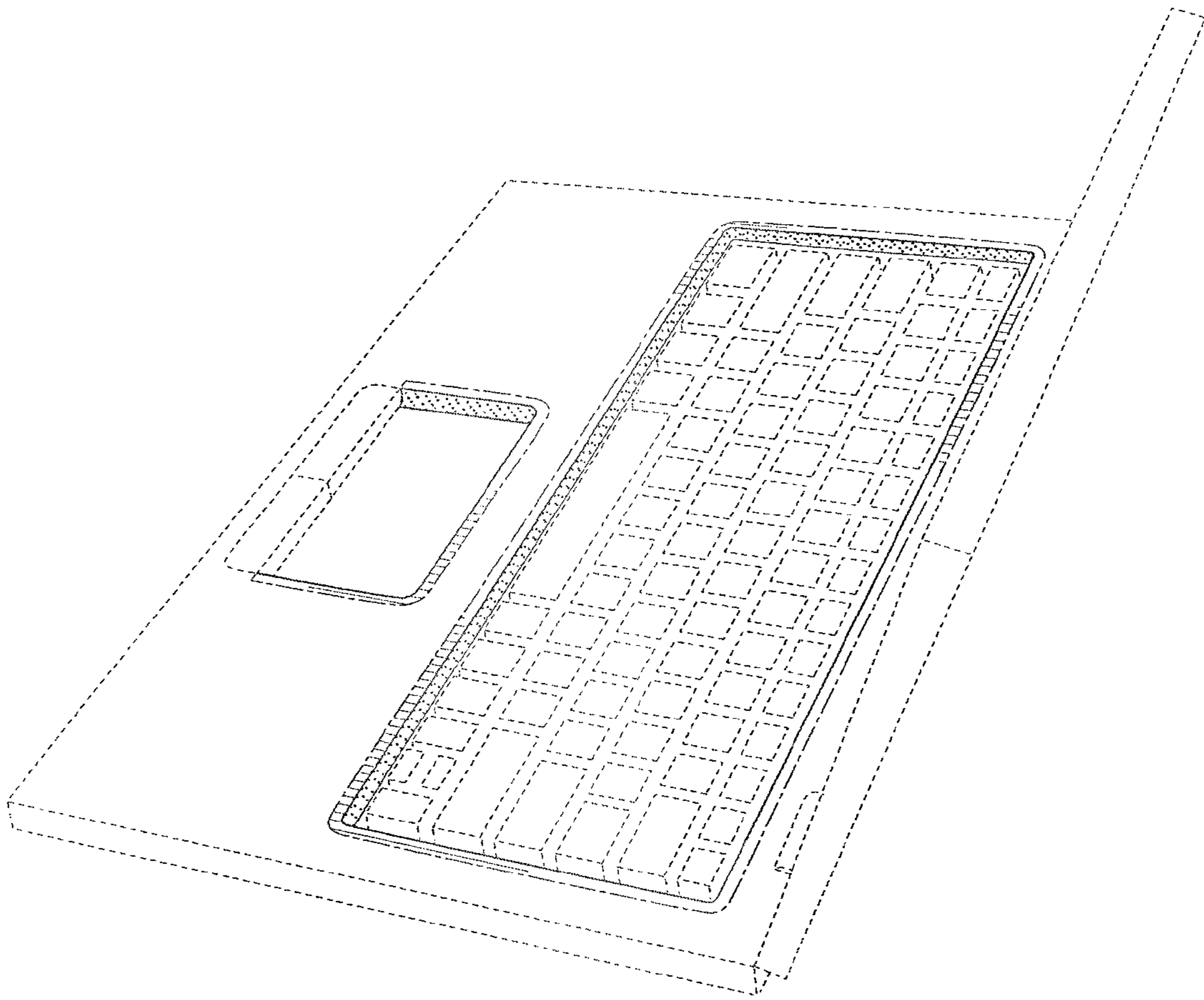


FIG. 14

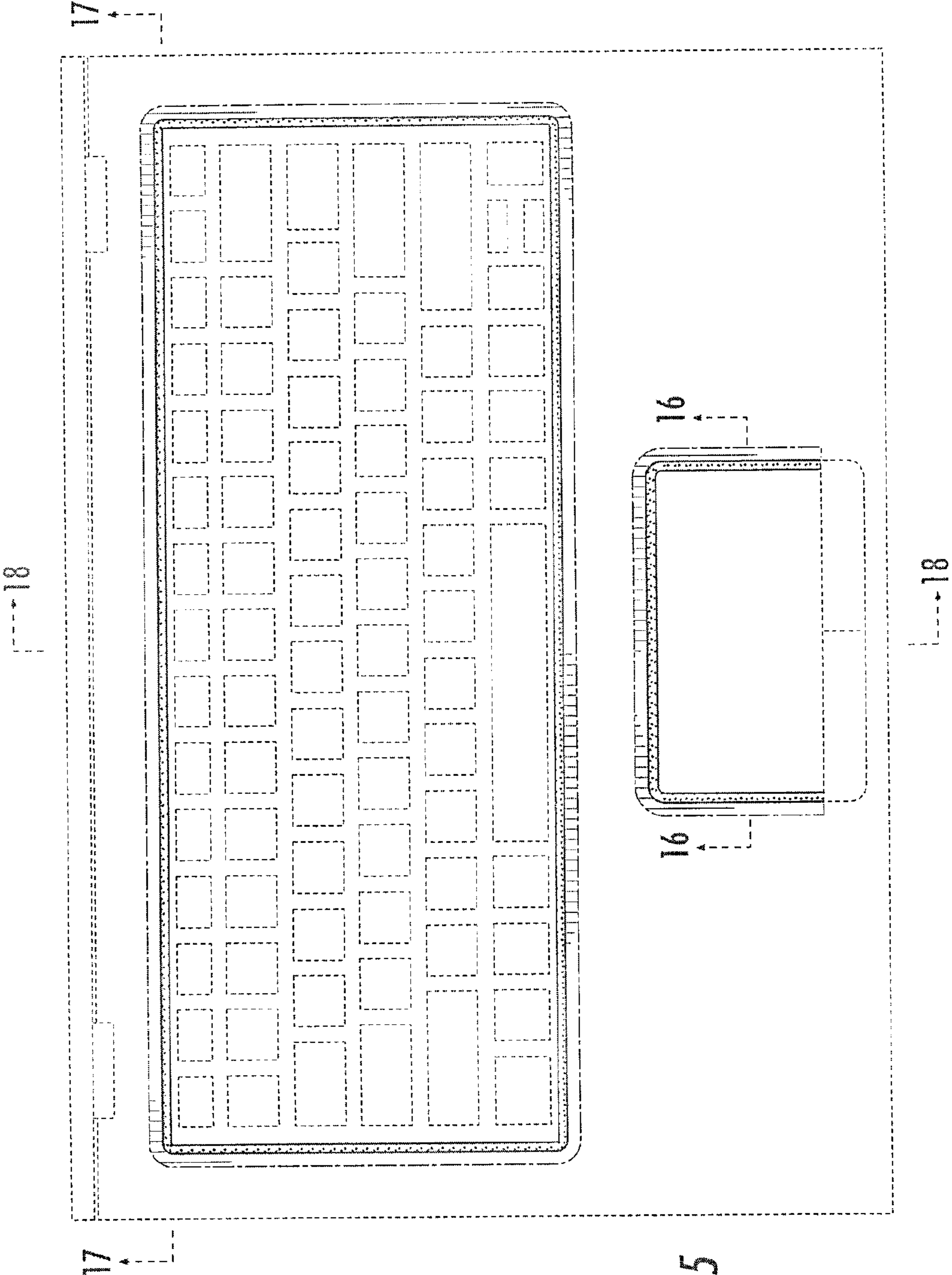


FIG. 15



FIG. 16

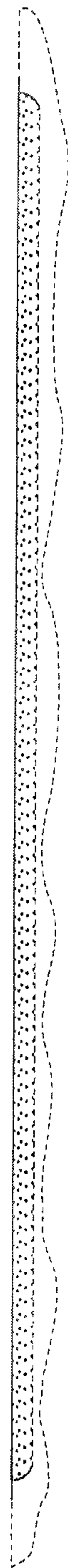


FIG. 17

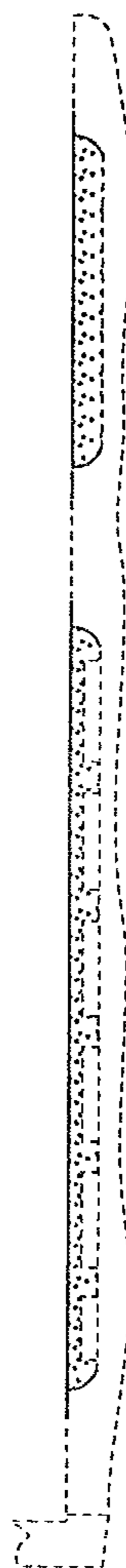


FIG. 18

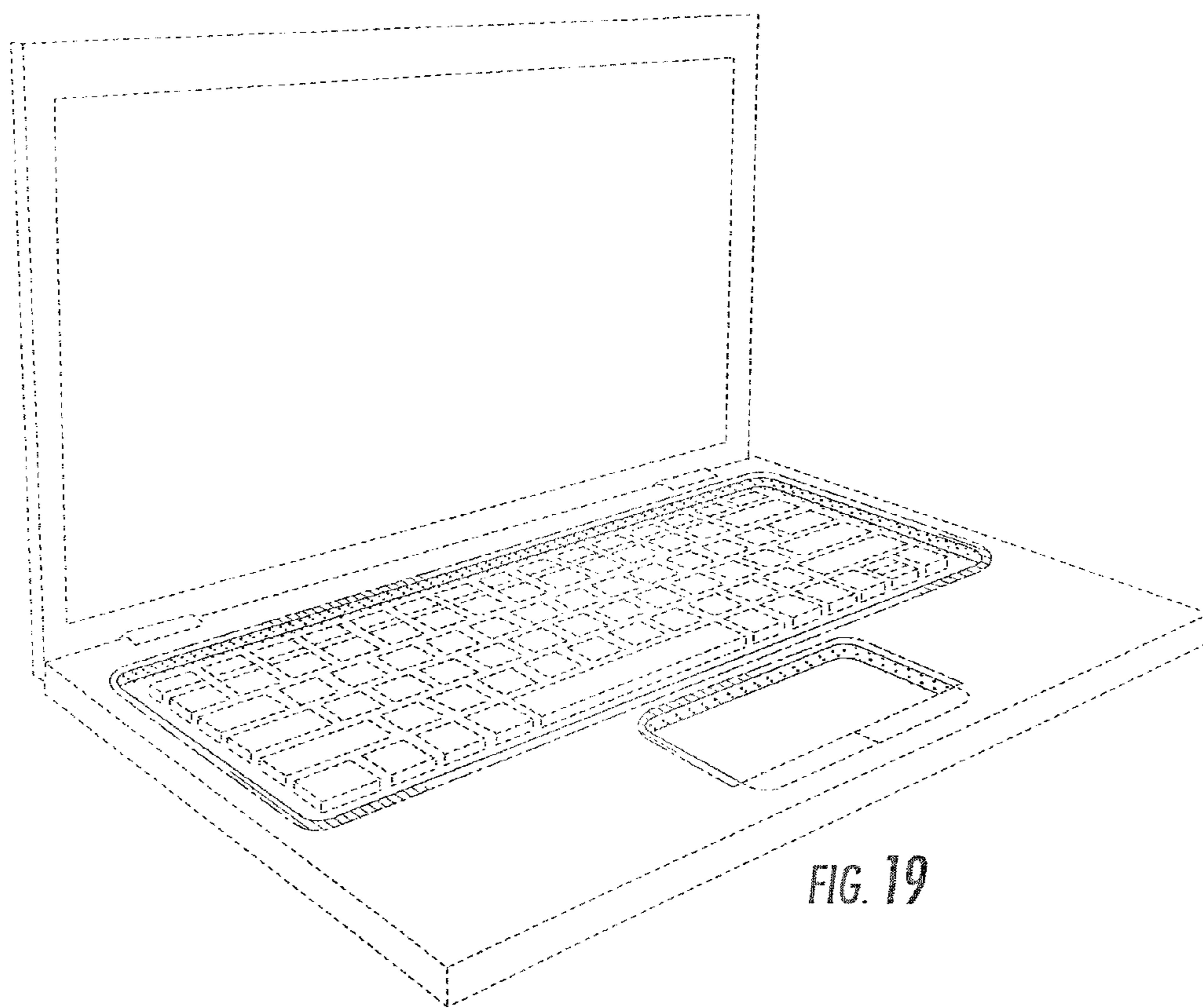


FIG. 19

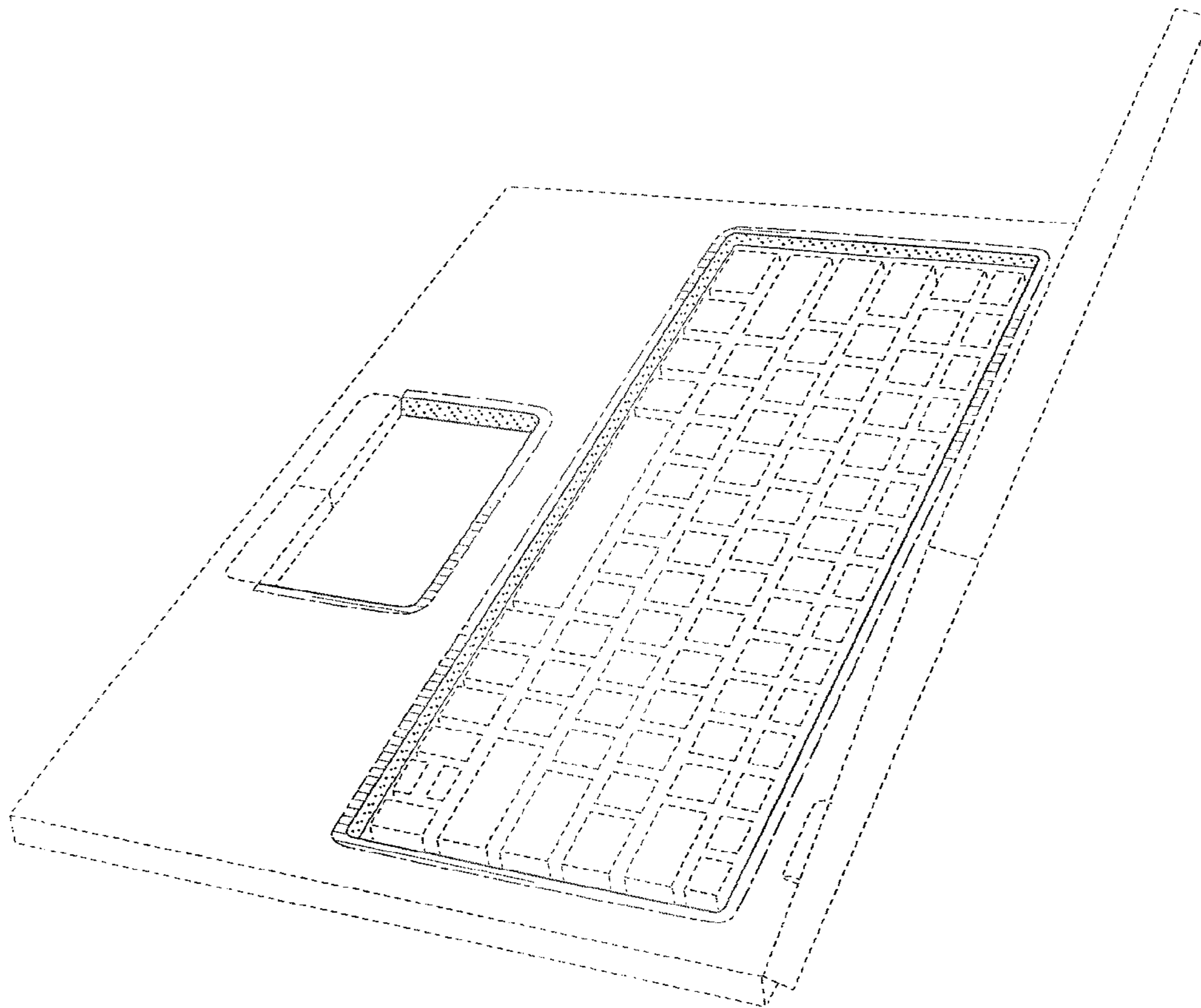


FIG. 20

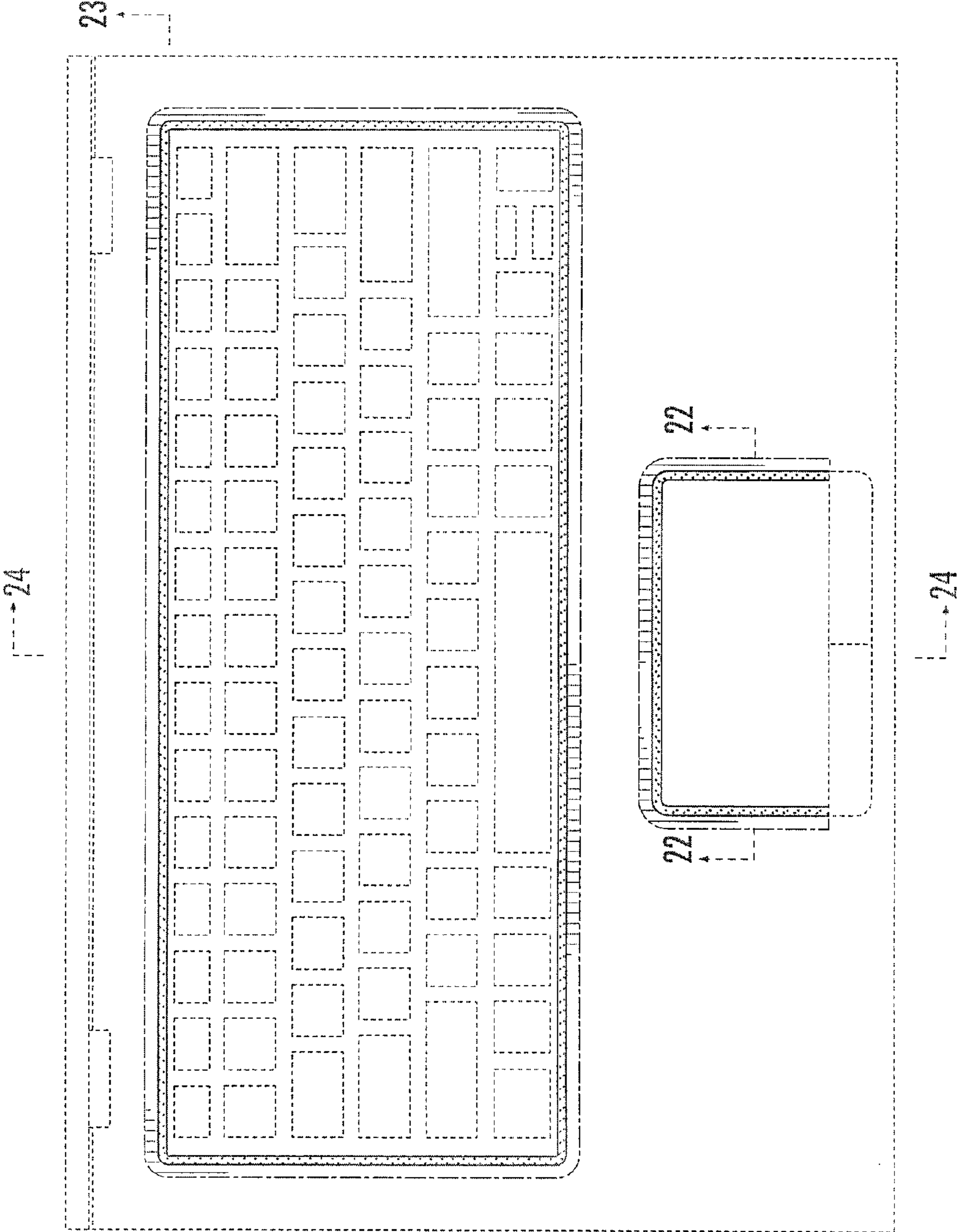


FIG. 21



FIG. 22

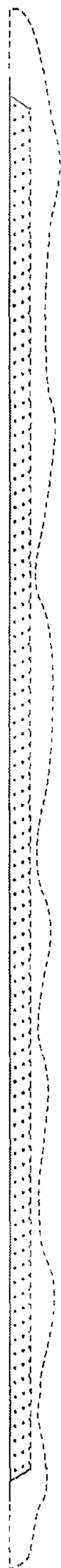


FIG. 23

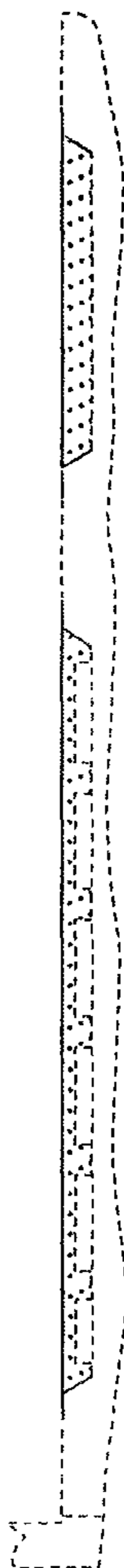


FIG. 24

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : D725,097 S
APPLICATION NO. : 29/375626
DATED : March 24, 2015
INVENTOR(S) : Britt C. Ashcroft et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page, in item (74), Attorney, in column 1, line 1, after "Rathe Lindenbaum LLP" insert
-- Hewlett-Packard Patent Department --, therefor.

Signed and Sealed this
Fourteenth Day of July, 2015



Michelle K. Lee
Director of the United States Patent and Trademark Office