



US00D722563S

(12) **United States Design Patent**  
**Byrne et al.**

(10) **Patent No.:** **US D722,563 S**  
(45) **Date of Patent:** **\*\* Feb. 17, 2015**

(54) **LOW VOLTAGE ELECTRICAL RECEPTACLE**

(71) Applicants: **Norman R. Byrne**, Ada, MI (US);  
**Daniel P. Byrne**, Lowell, MI (US)

(72) Inventors: **Norman R. Byrne**, Ada, MI (US);  
**Daniel P. Byrne**, Lowell, MI (US)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/490,843**

(22) Filed: **May 14, 2014**

(51) **LOC (10) Cl.** ..... **13-03**

(52) **U.S. Cl.**  
USPC ..... **D13/146**

(58) **Field of Classification Search**  
USPC ..... D13/147, 154, 184, 199  
See application file for complete search history.

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*Primary Examiner* — Daniel Bui

(74) *Attorney, Agent, or Firm* — Gardner, Linn, Burkhardt & Flory, LLP

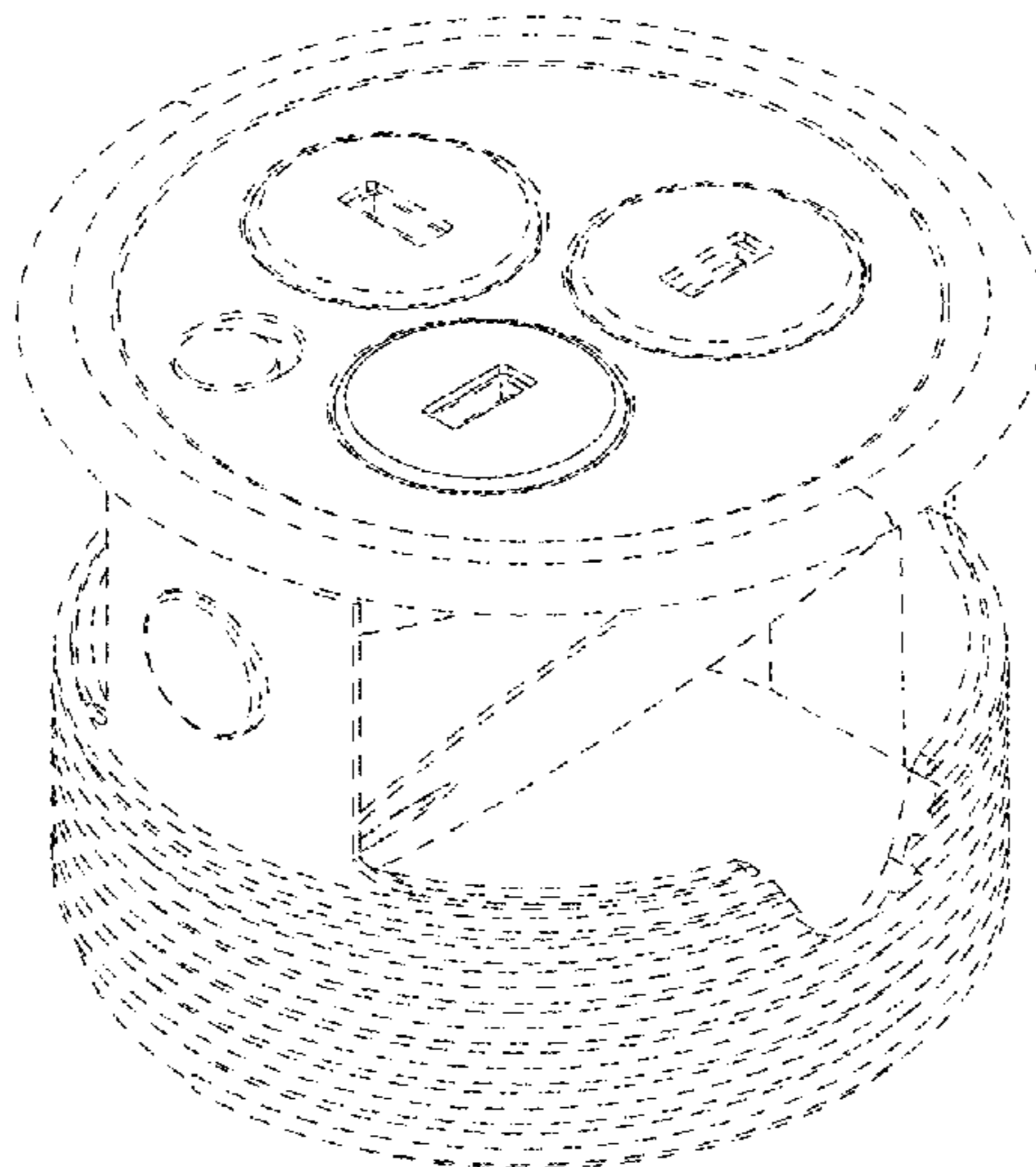
(57) **CLAIM**

The ornamental design for a low voltage electrical receptacle, as shown and described.

**DESCRIPTION**

FIG. 1 is a perspective view of a low voltage electrical receptacle showing our new design; FIG. 2 is a top plan view thereof; FIG. 3 is a bottom plan view thereof; FIG. 4 is a front elevation thereof; FIG. 5 is a rear elevation thereof; FIG. 6 is a right side elevation thereof; FIG. 7 is a left side elevation thereof; and, FIG. 8 is a perspective view thereof, in a use condition. In the drawings, the broken lines depict environmental subject matter only and form no part of the claimed design.

**1 Claim, 3 Drawing Sheets**



(56)

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Co-pending Design U.S. Appl. No. 29/488,043, filed Apr. 15, 2014, entitled Power Center for a Work Surface.

Co-pending Design U.S. Appl. No. 29/490,844, filed May 14, 2014, entitled Low Voltage Electrical Receptacle.

Co-pending Design U.S. Appl. No. 29/490,845, filed May 14, 2014, entitled Electrical Receptacle.

Co-pending Design U.S. Appl. No. 29/490,846, filed May 14, 2014, entitled Electrical Receptacle.

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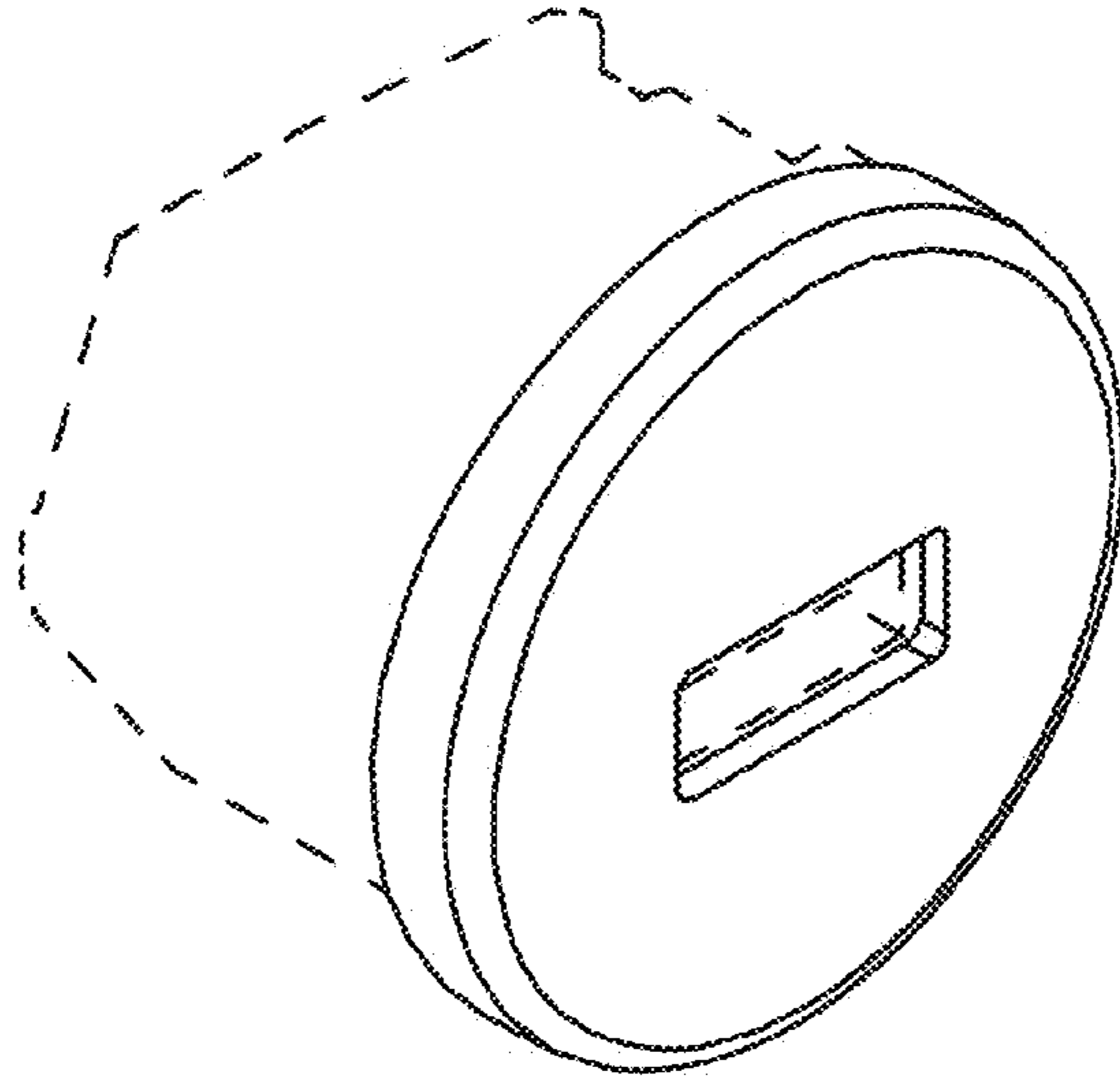


FIG. 1

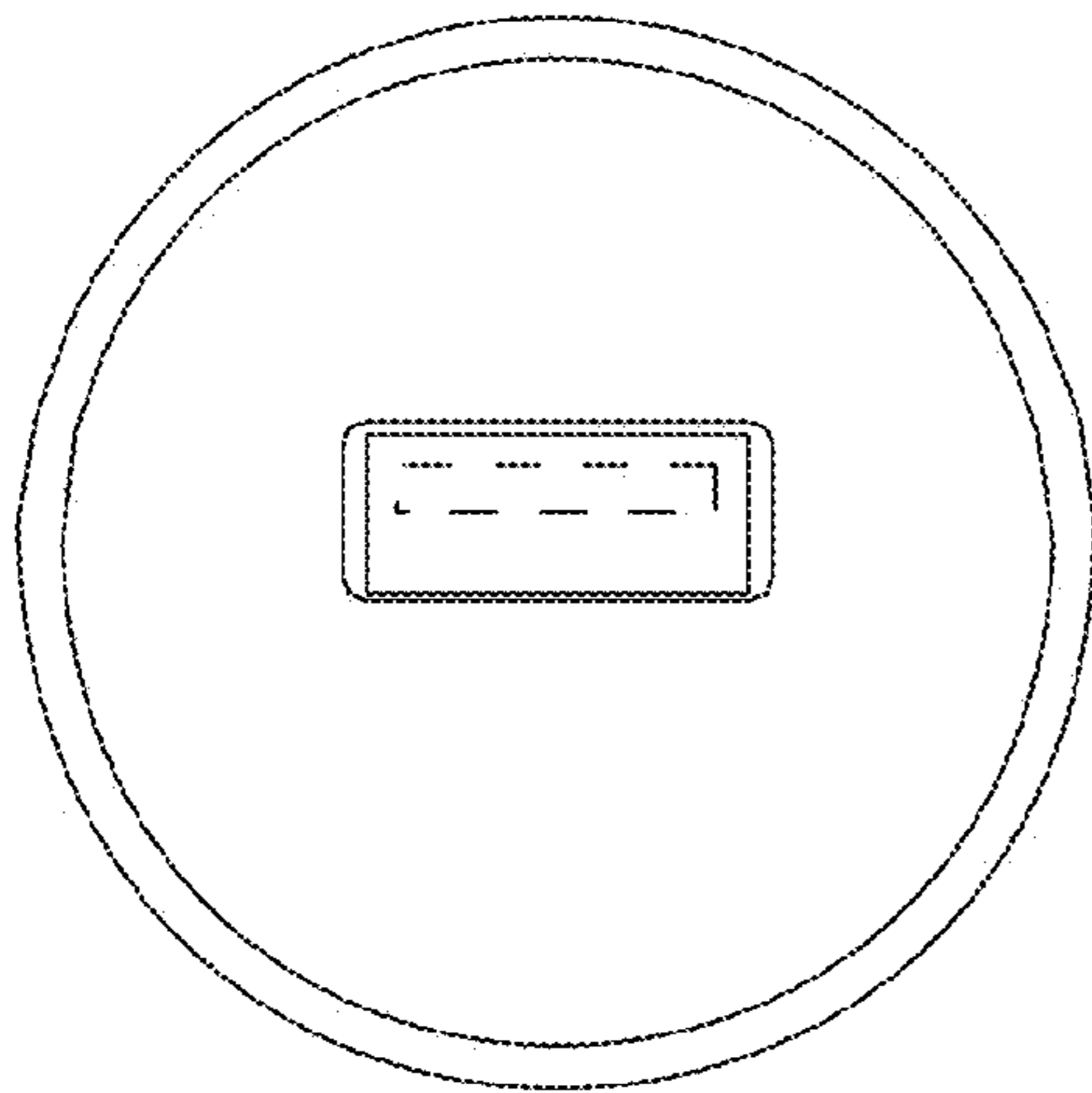


FIG. 2

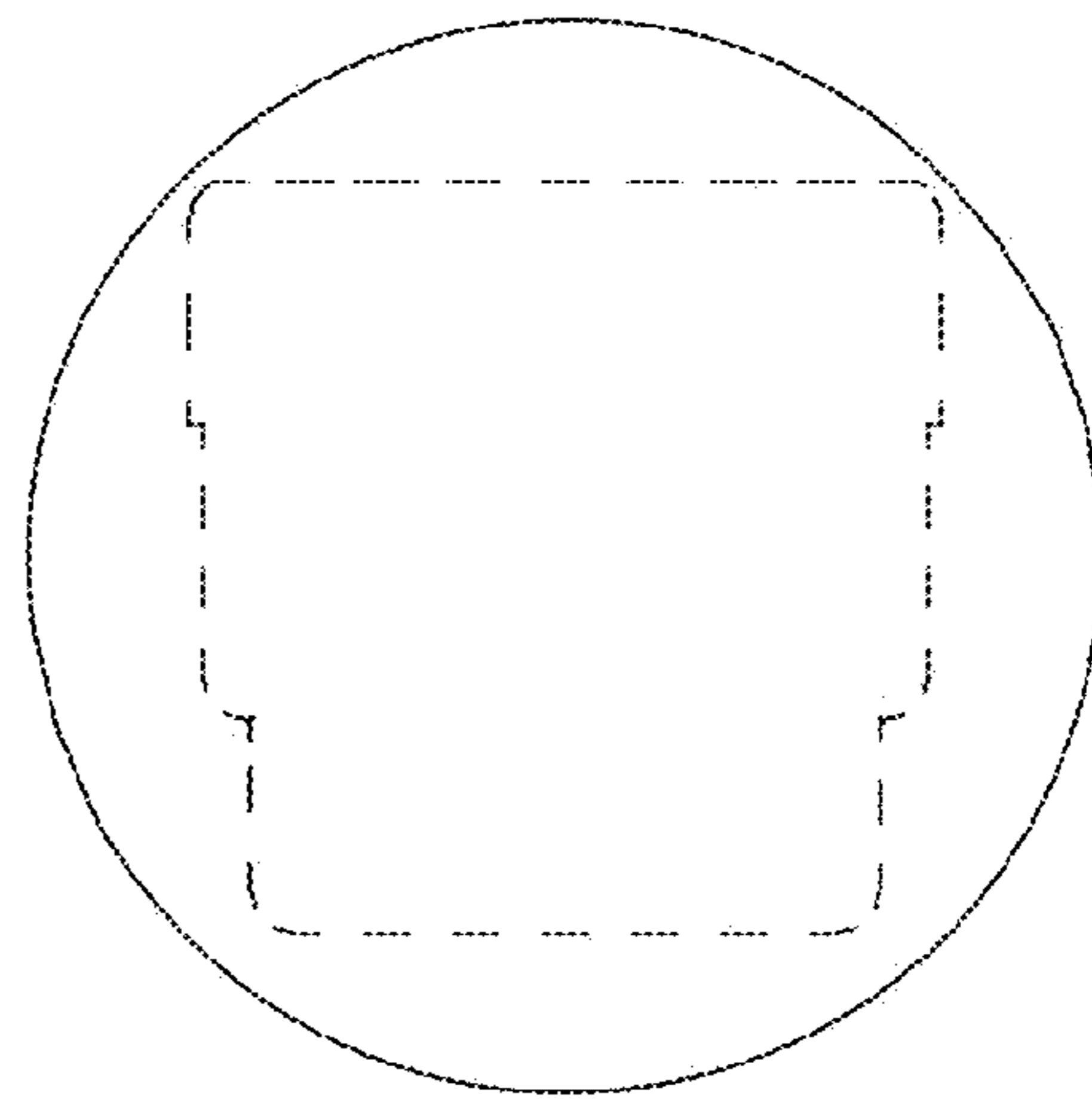


FIG. 3

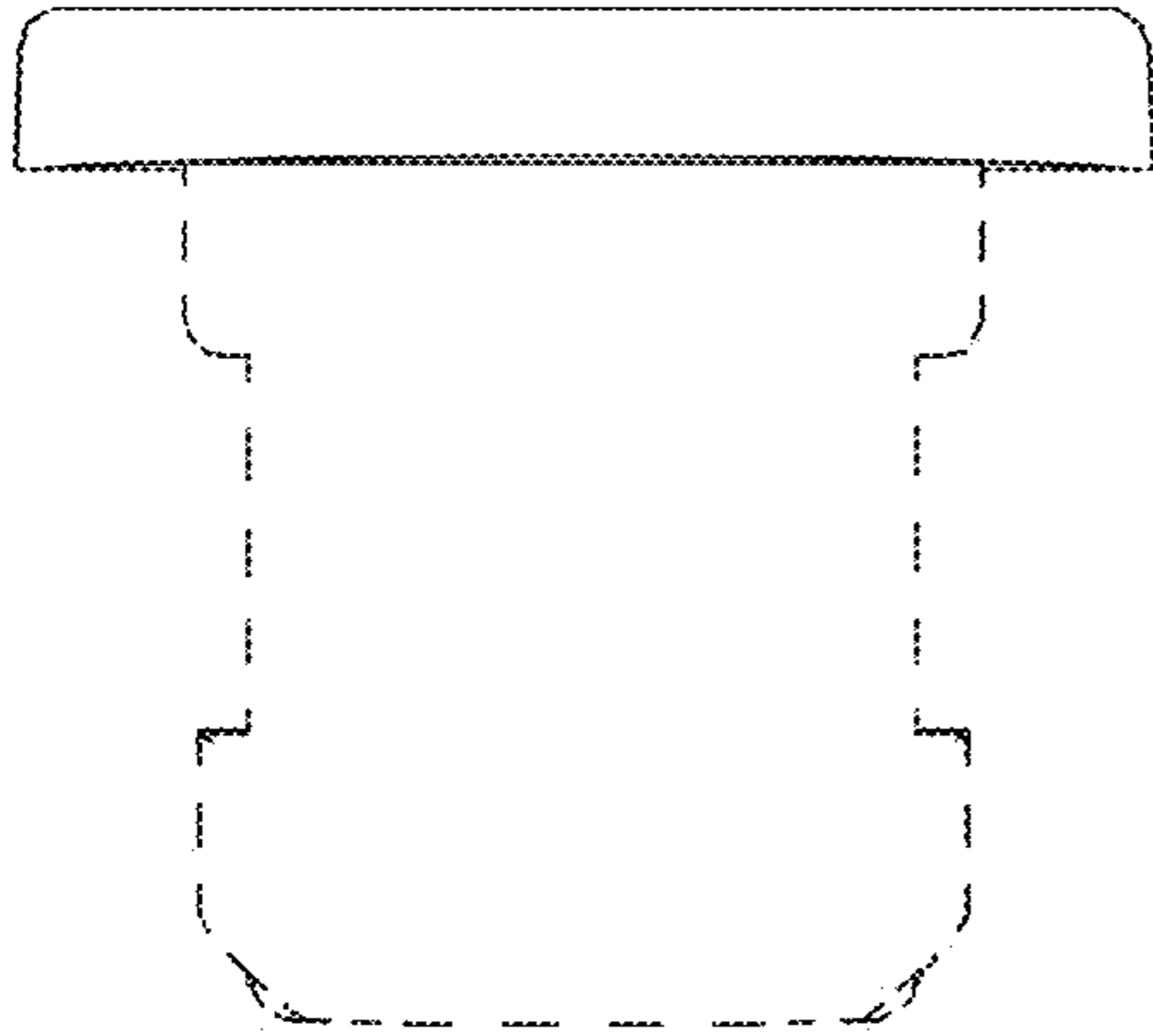


FIG. 4

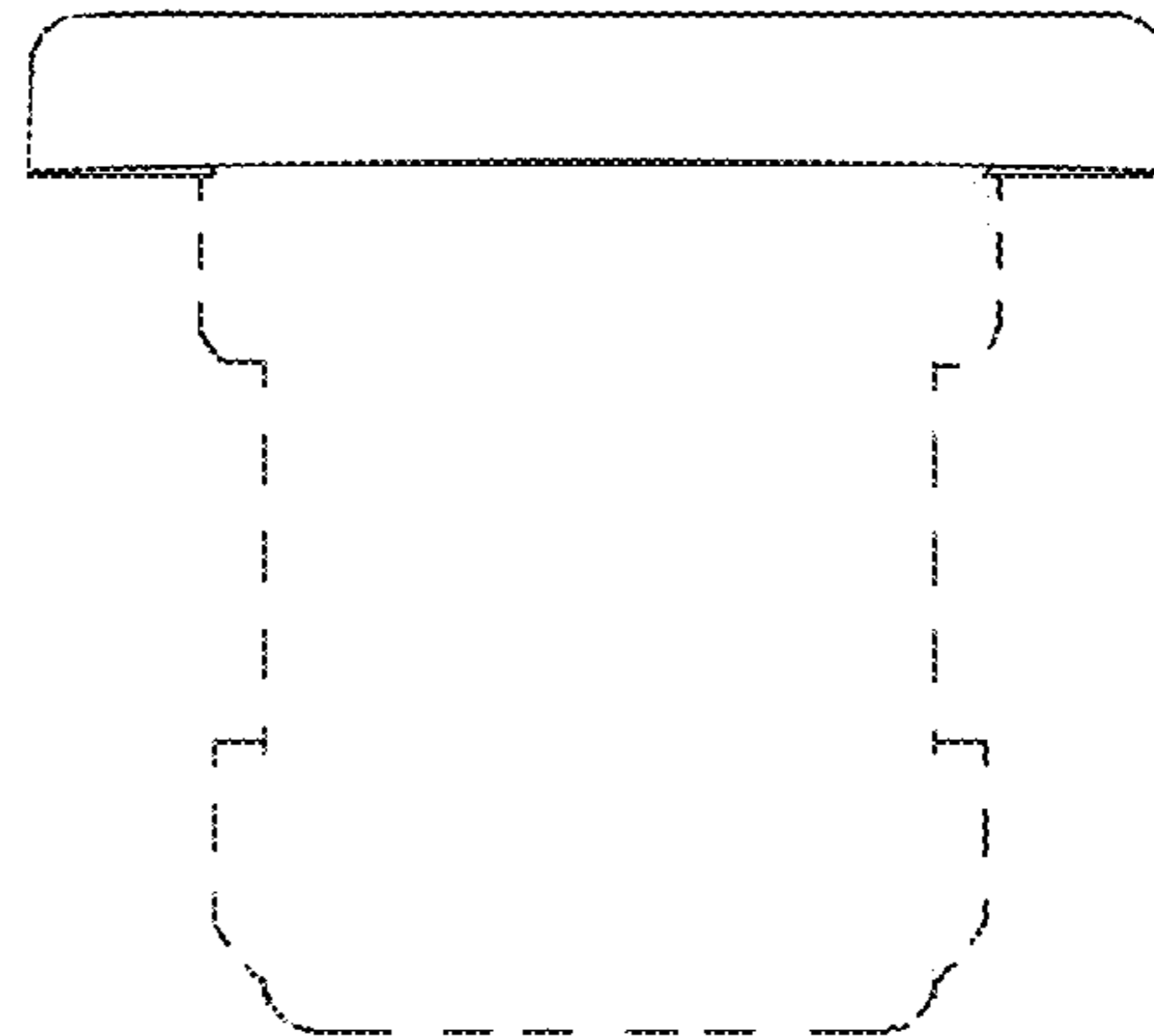


FIG. 5

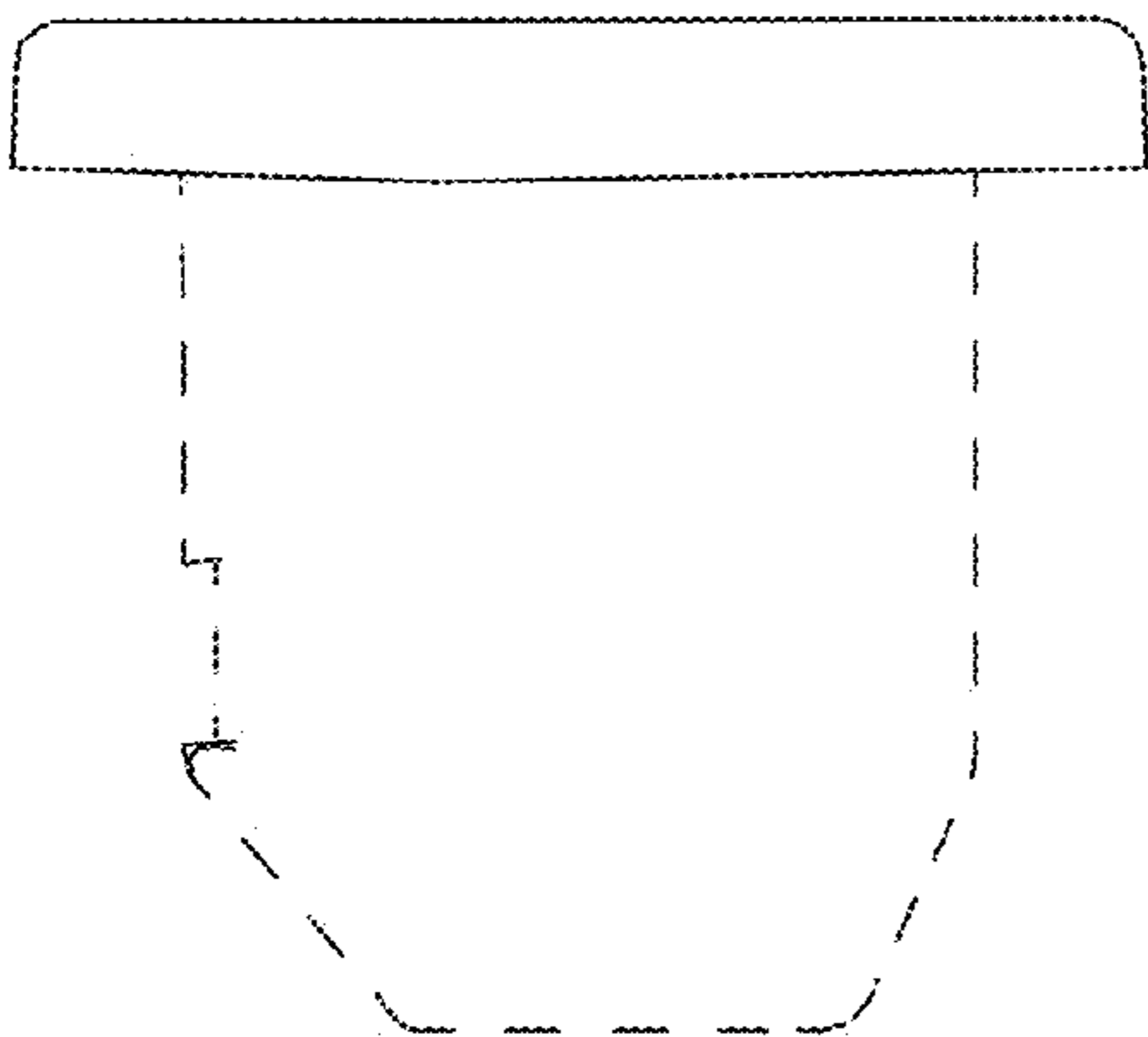


FIG. 6

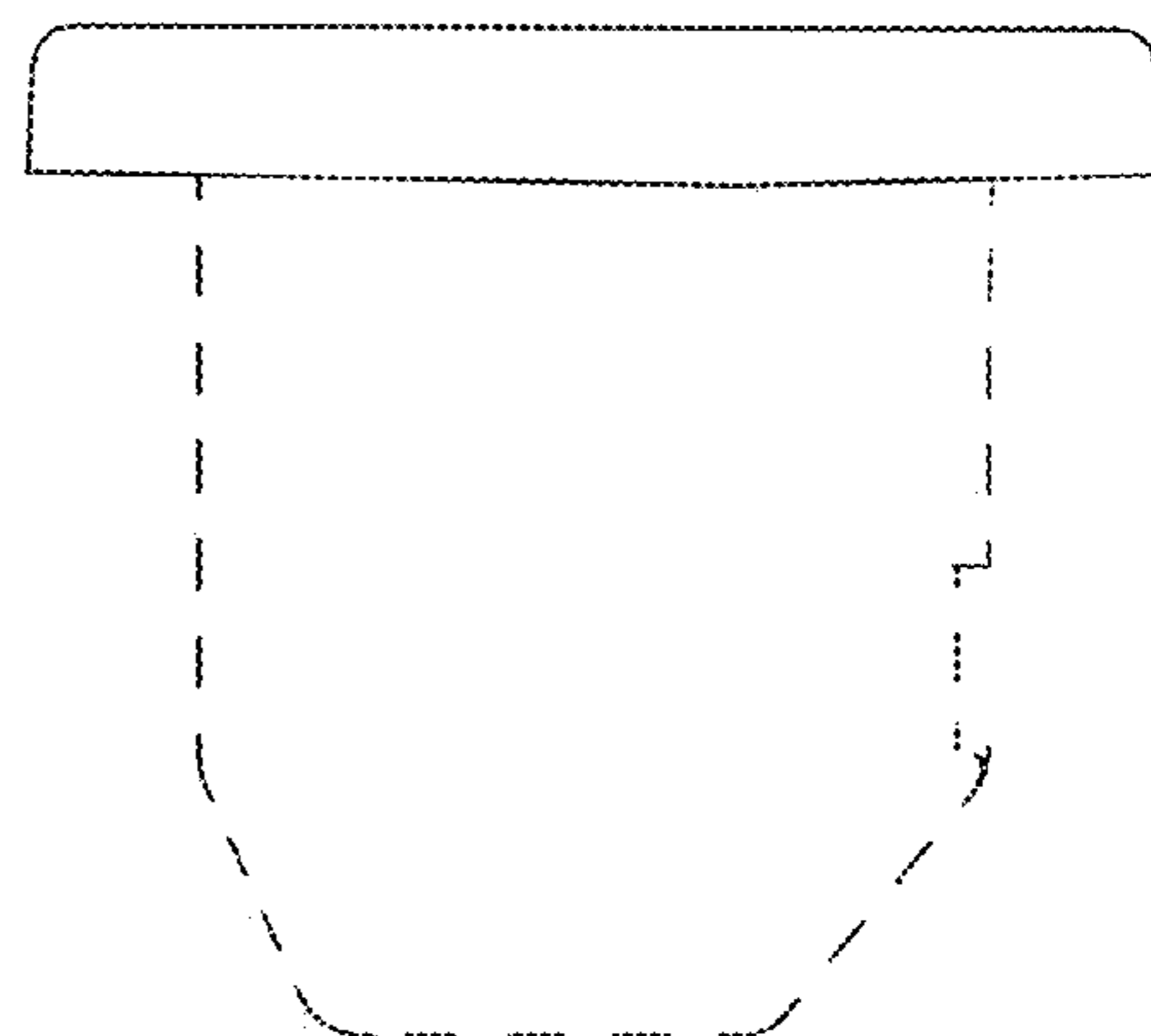


FIG. 7

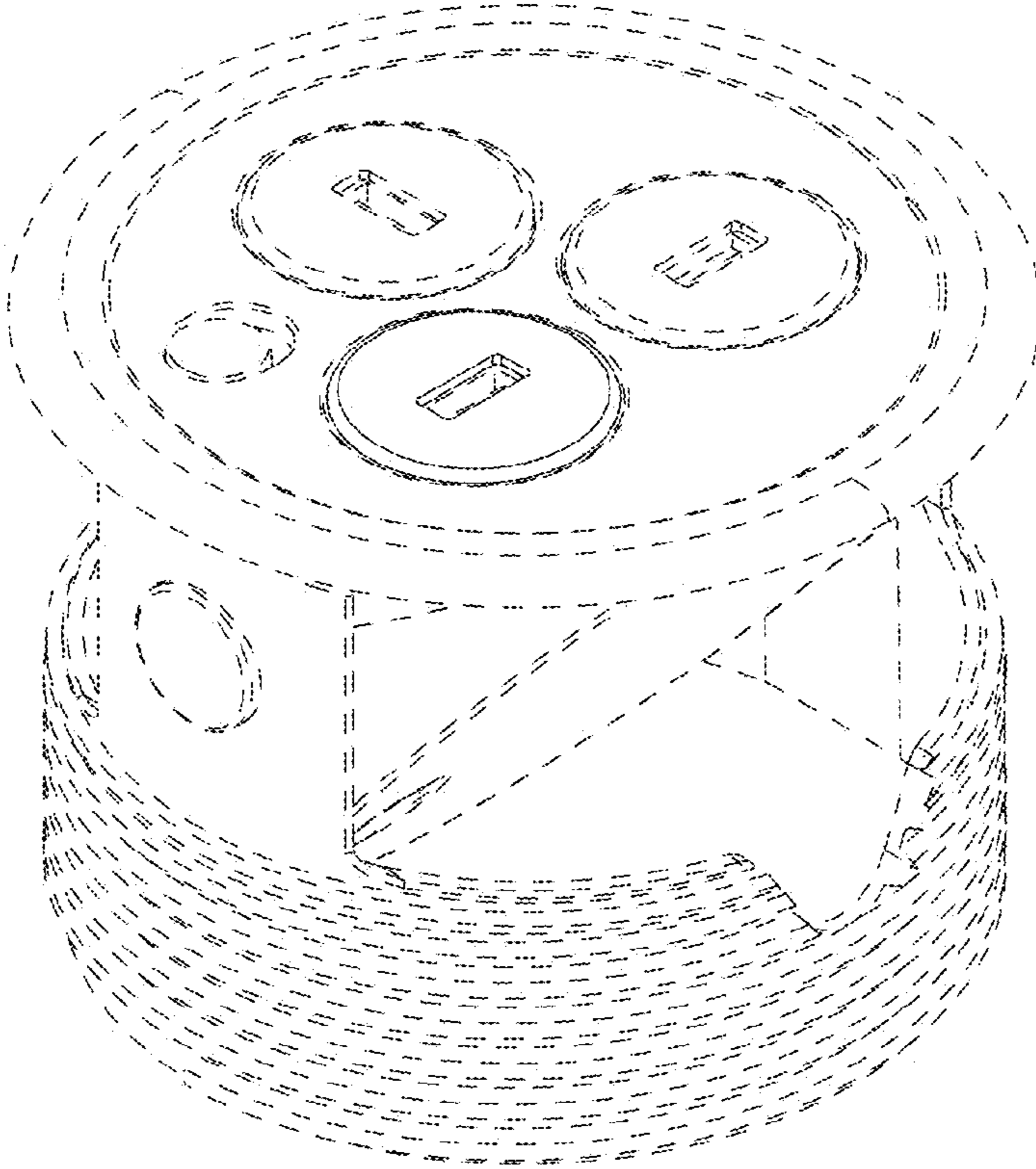


FIG. 8

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : D722,563 S  
APPLICATION NO. : 29/490843  
DATED : February 17, 2015  
INVENTOR(S) : Byrne et al.

Page 1 of 5

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

The title page, showing the illustrative figure, should be deleted and substitute therefor the attached title page.

In the drawing sheets, consisting of figs. 1-8, should be deleted to be replaced with the drawing sheets, consisting of figs. 1-8, as shown on the attached page.

Signed and Sealed this  
Seventh Day of June, 2016



Michelle K. Lee  
*Director of the United States Patent and Trademark Office*

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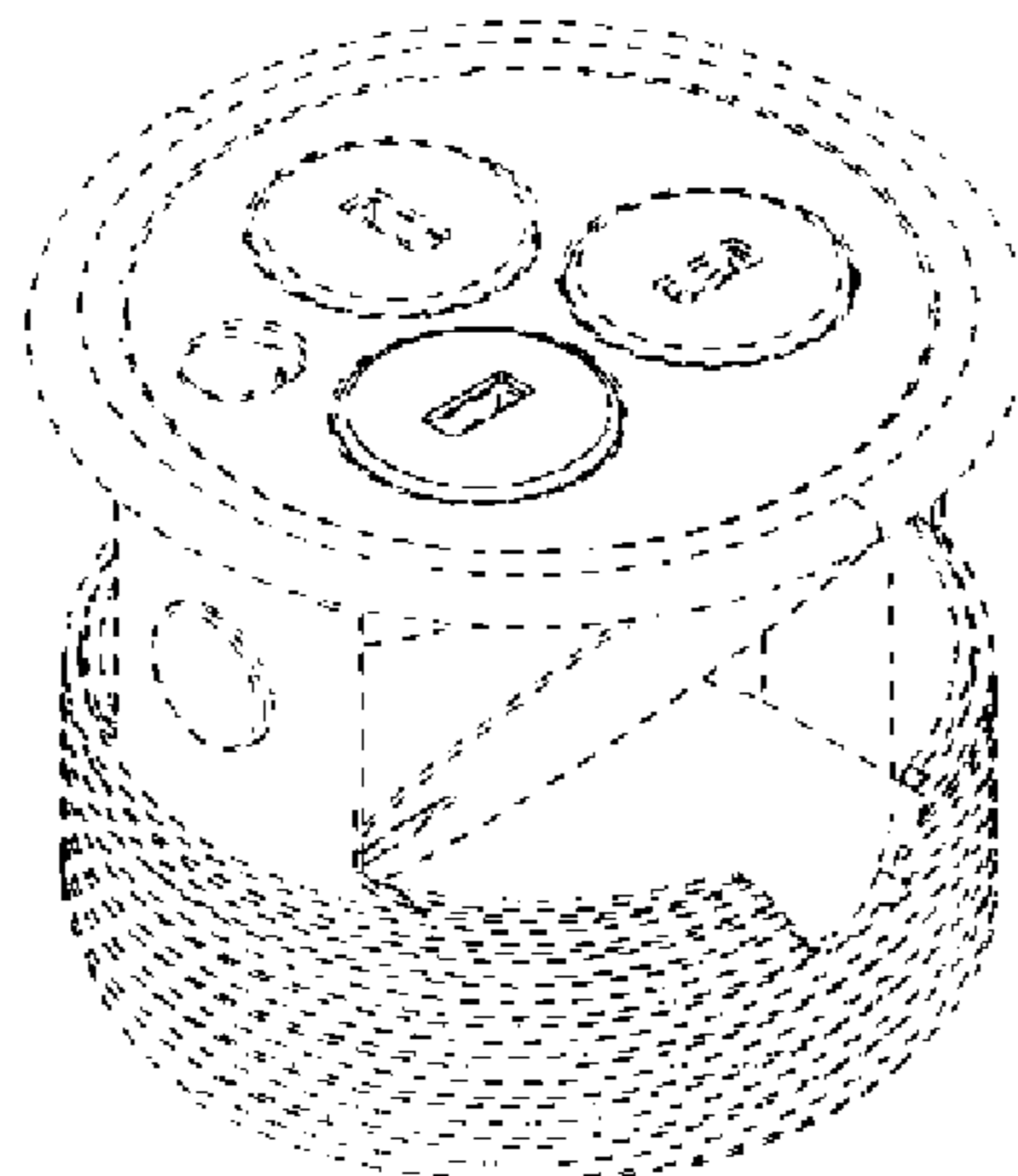
*Primary Examiner* Daniel Bui  
(74) *Attorney, Agent, or Firm* — Gardner, Linn, Burkhardt & Flory, LLP

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**1 Claim, 3 Drawing Sheets**



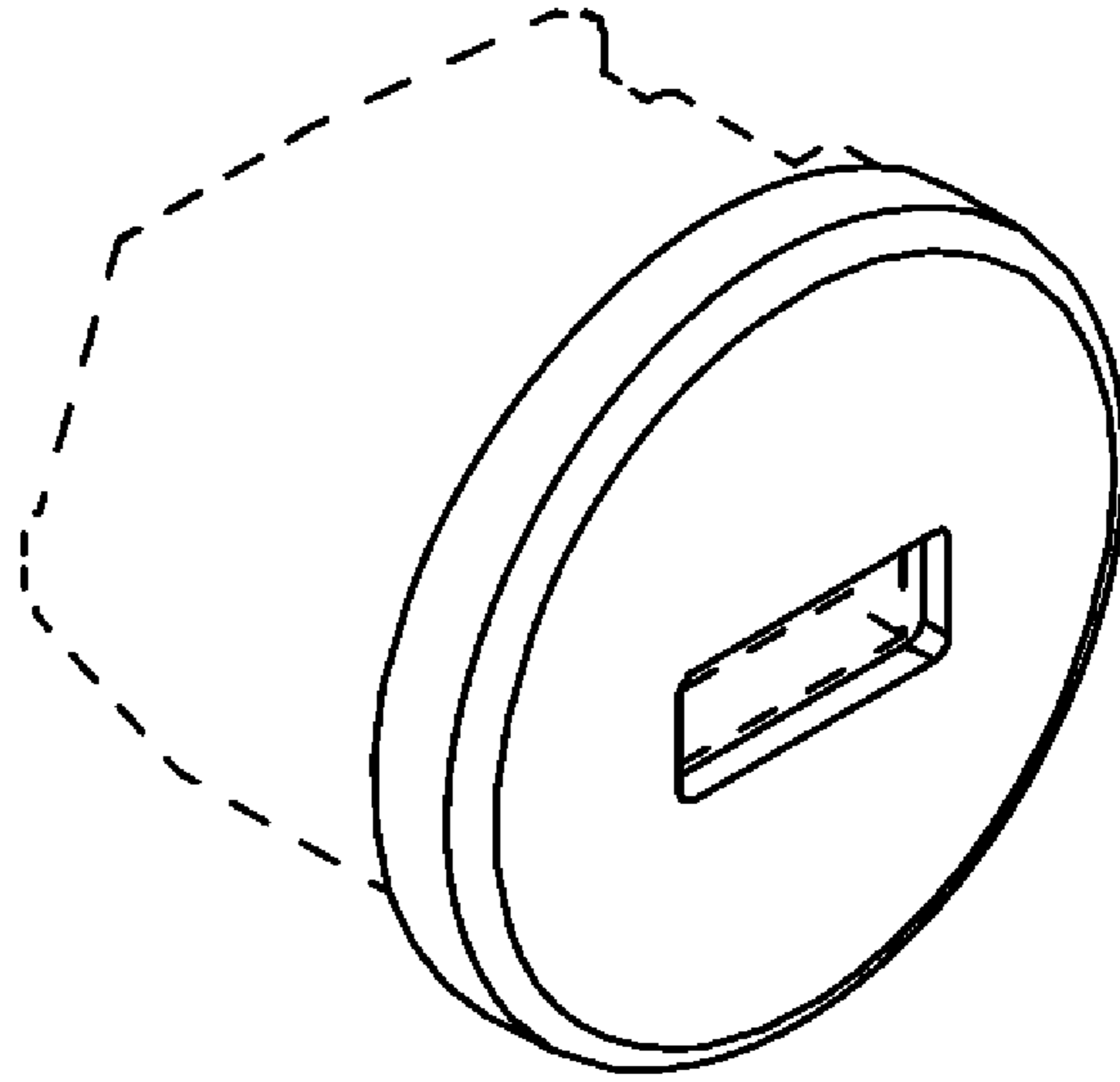


FIG. 1

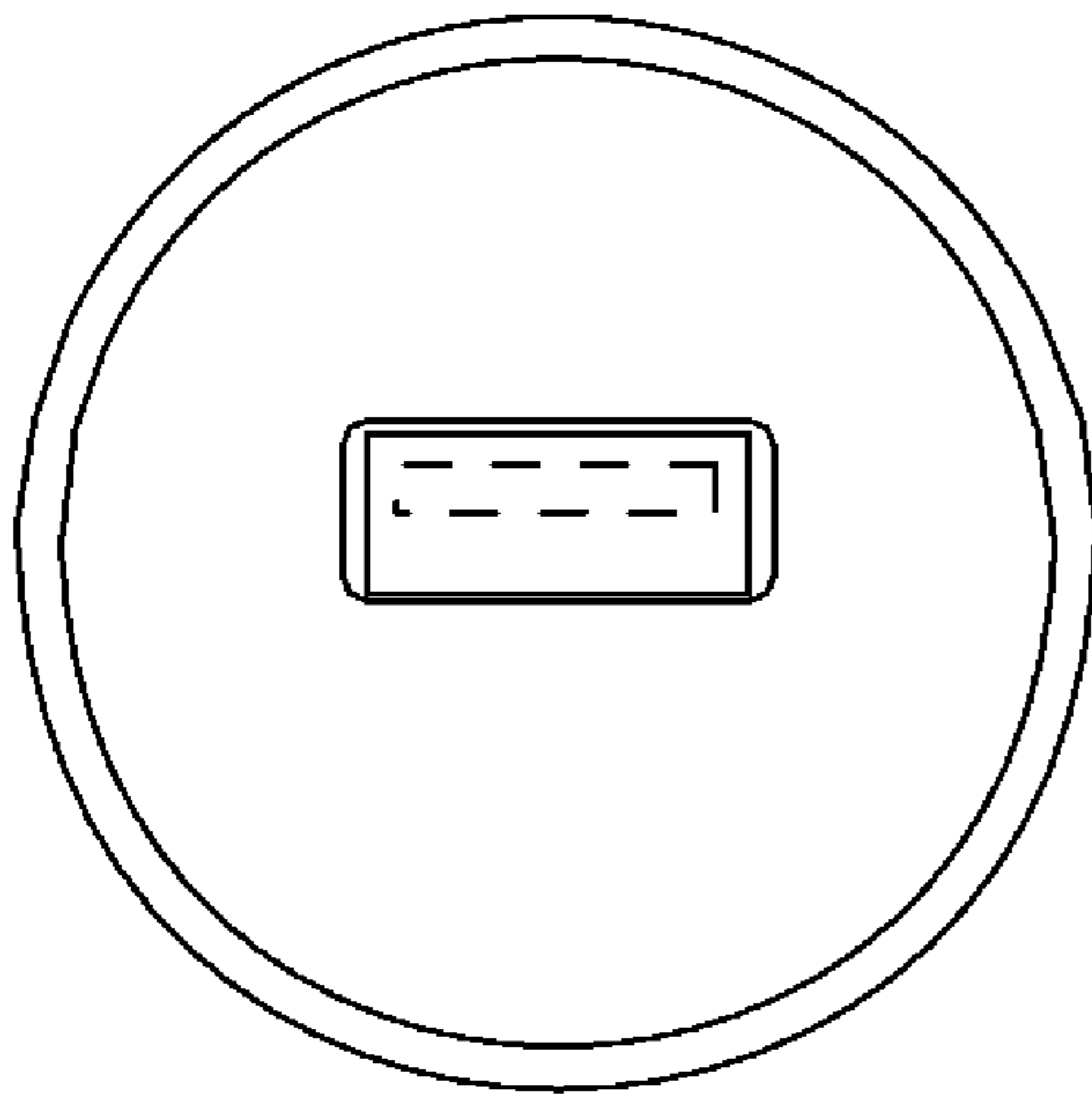


FIG. 2

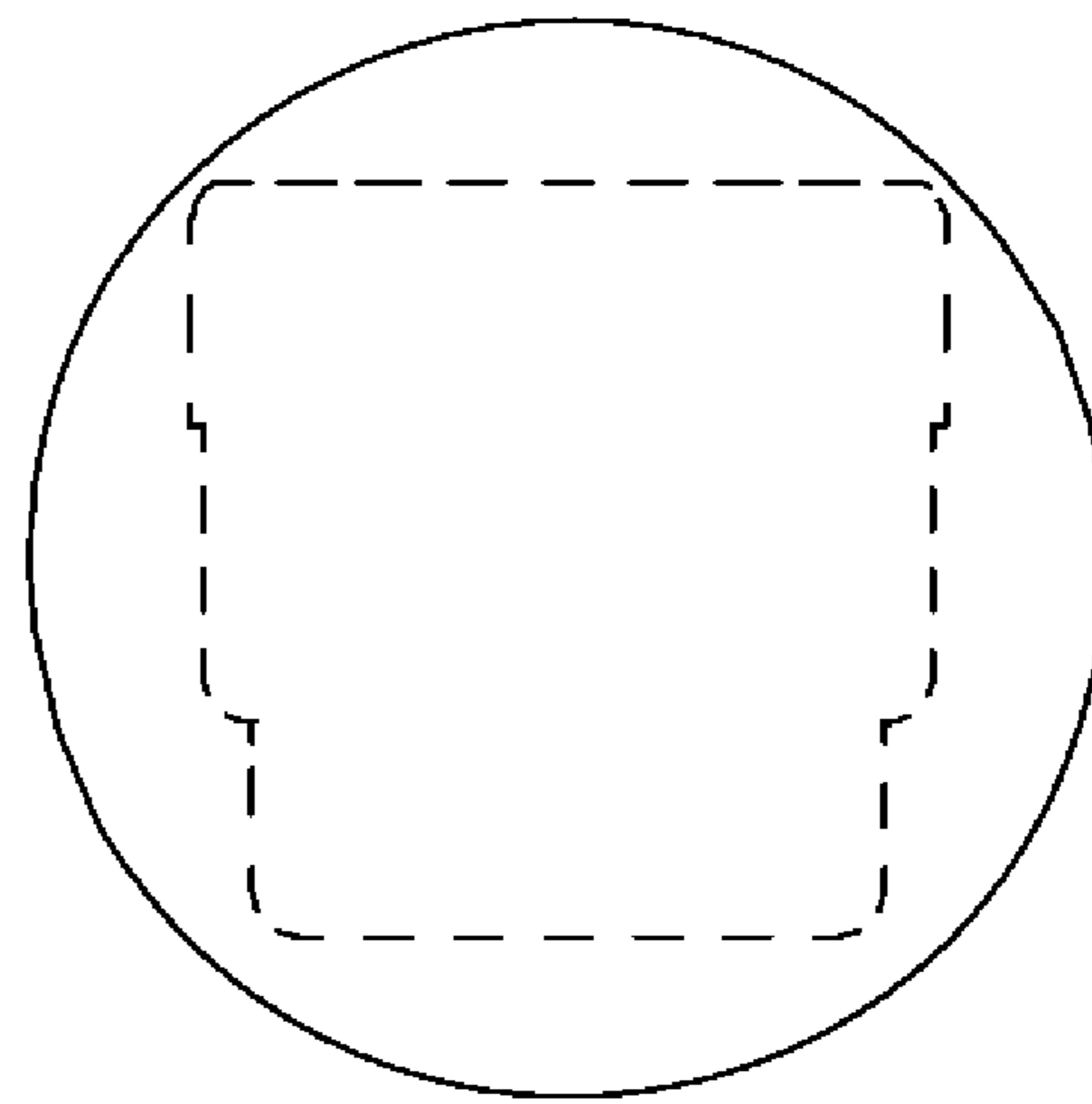


FIG. 3



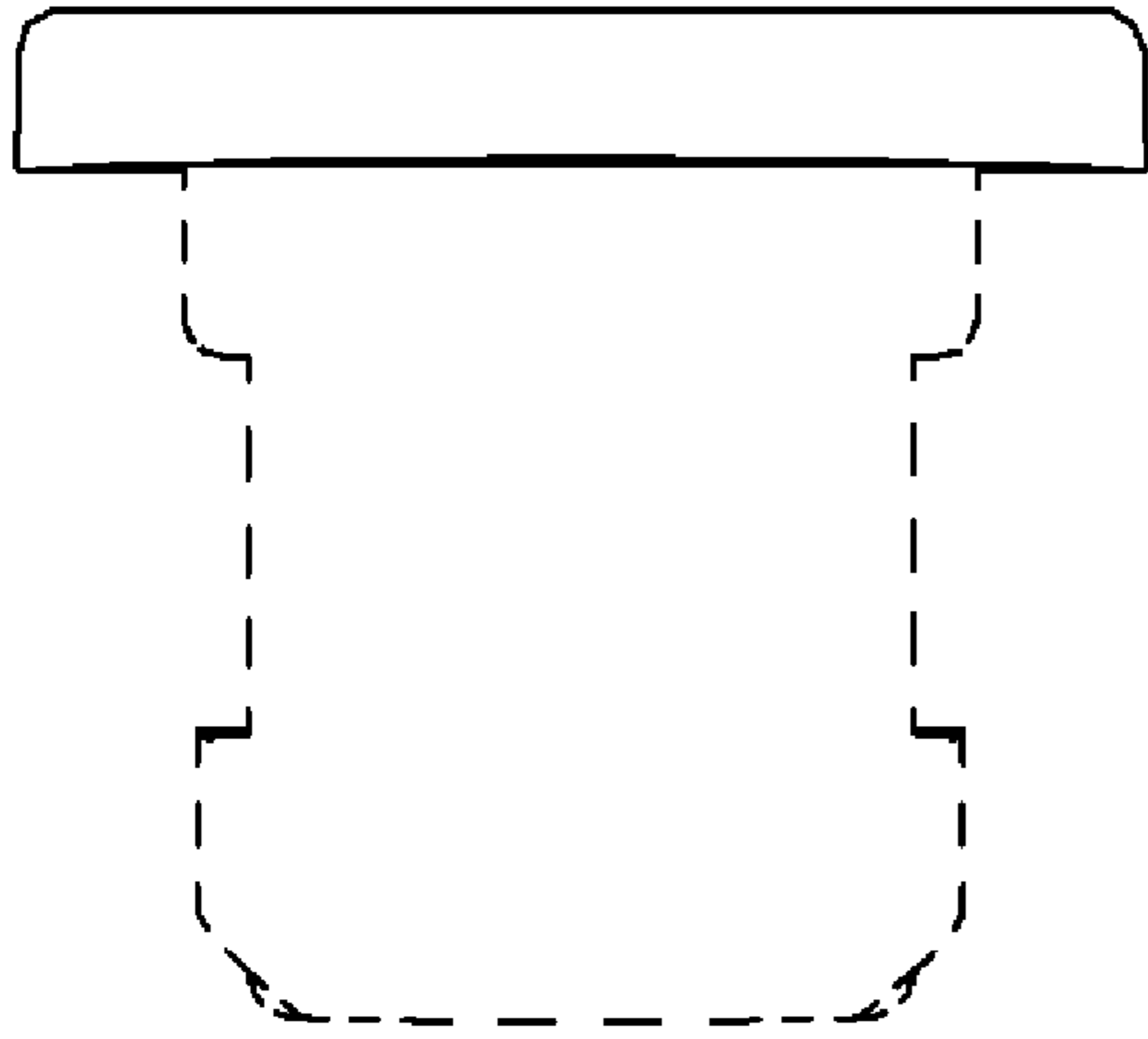


FIG. 4

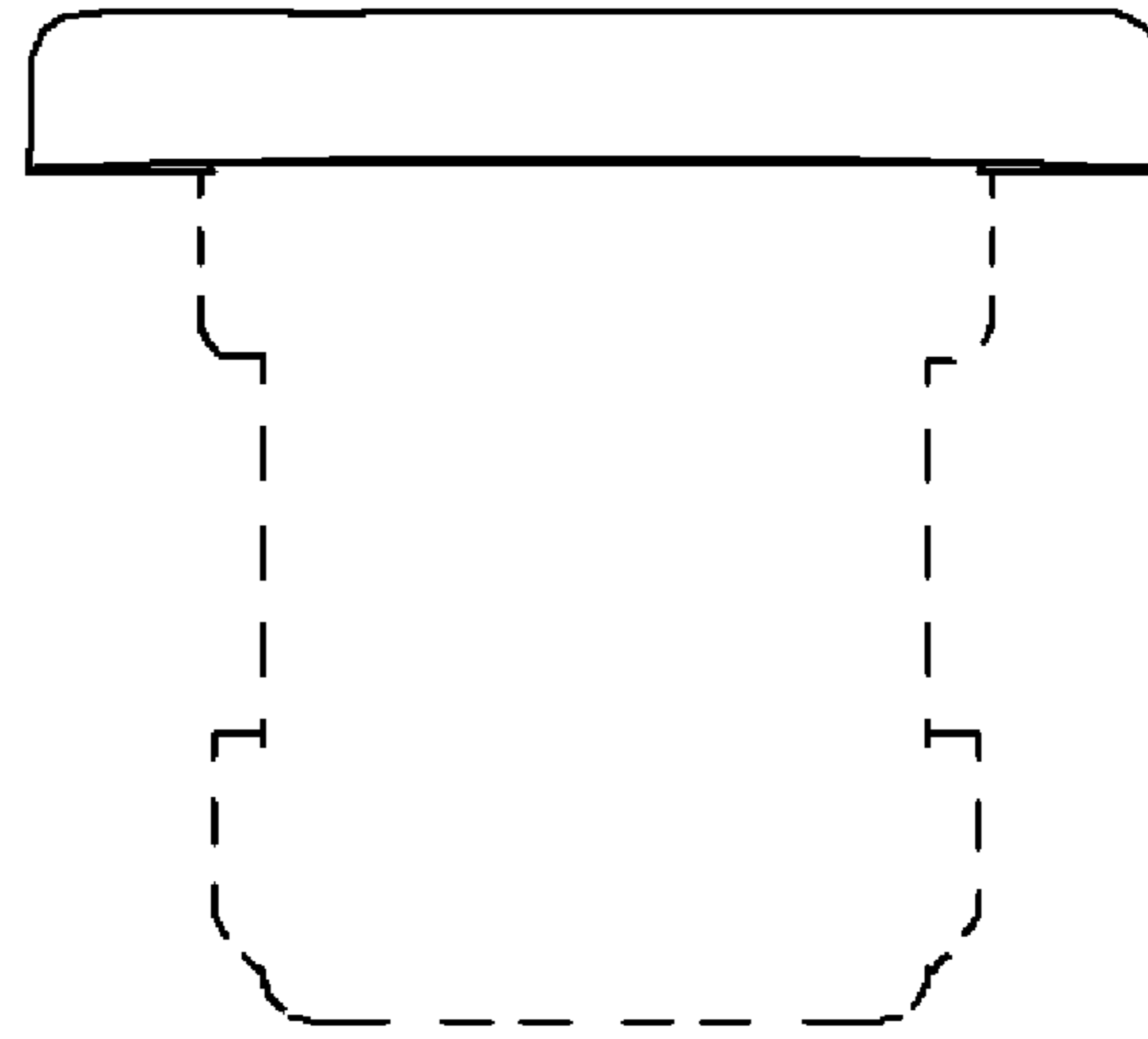


FIG. 5

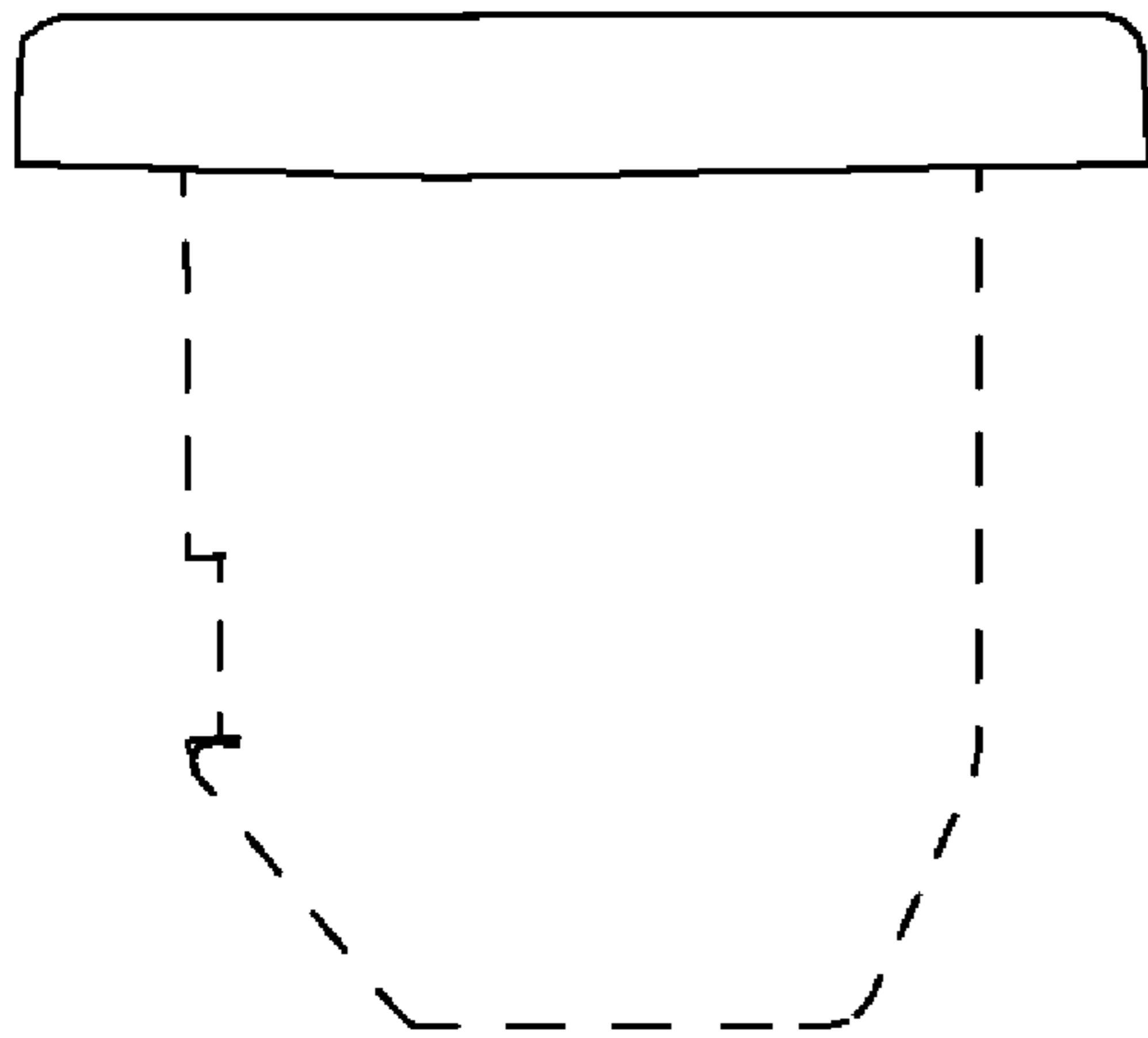


FIG. 6

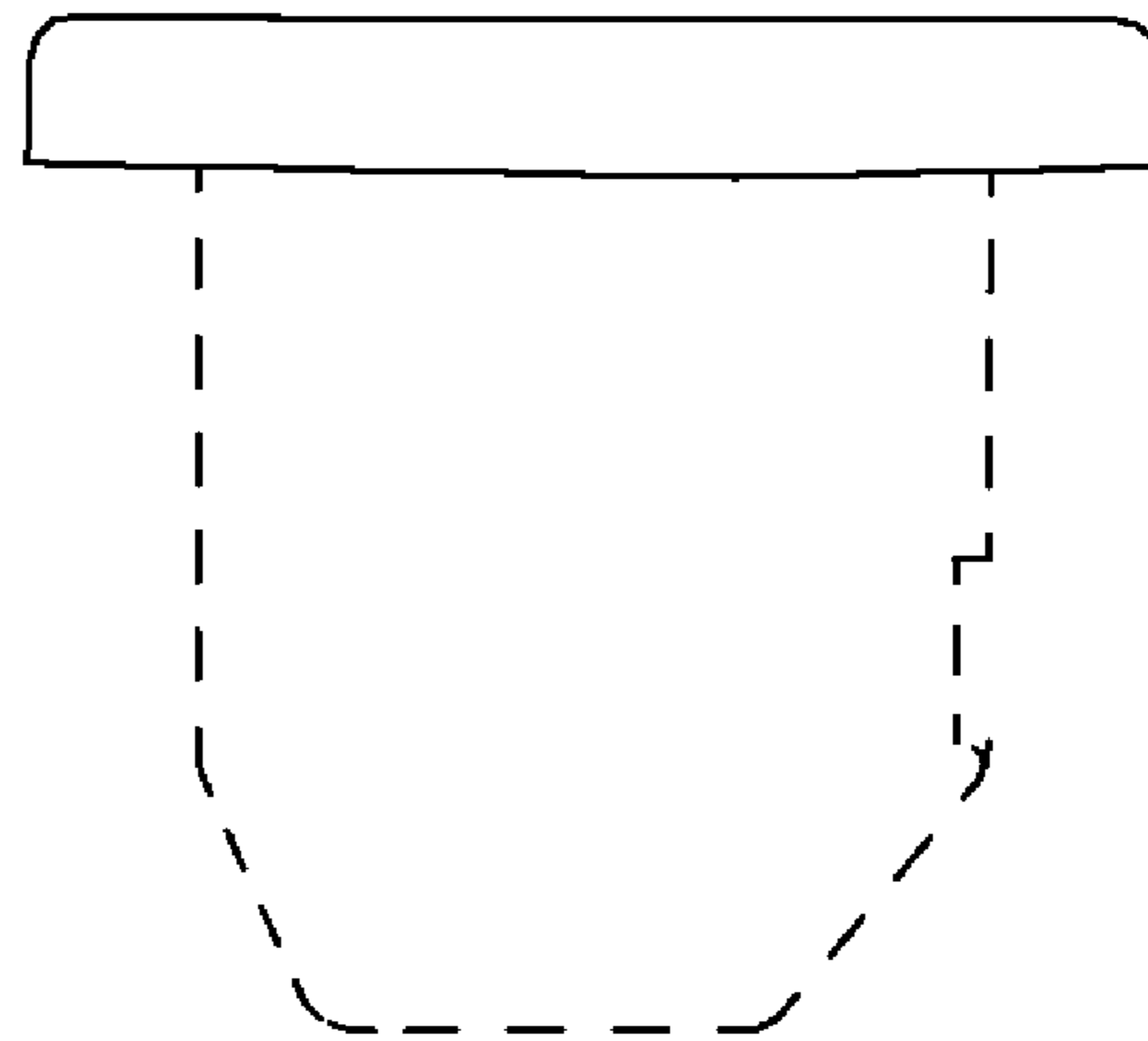


FIG. 7

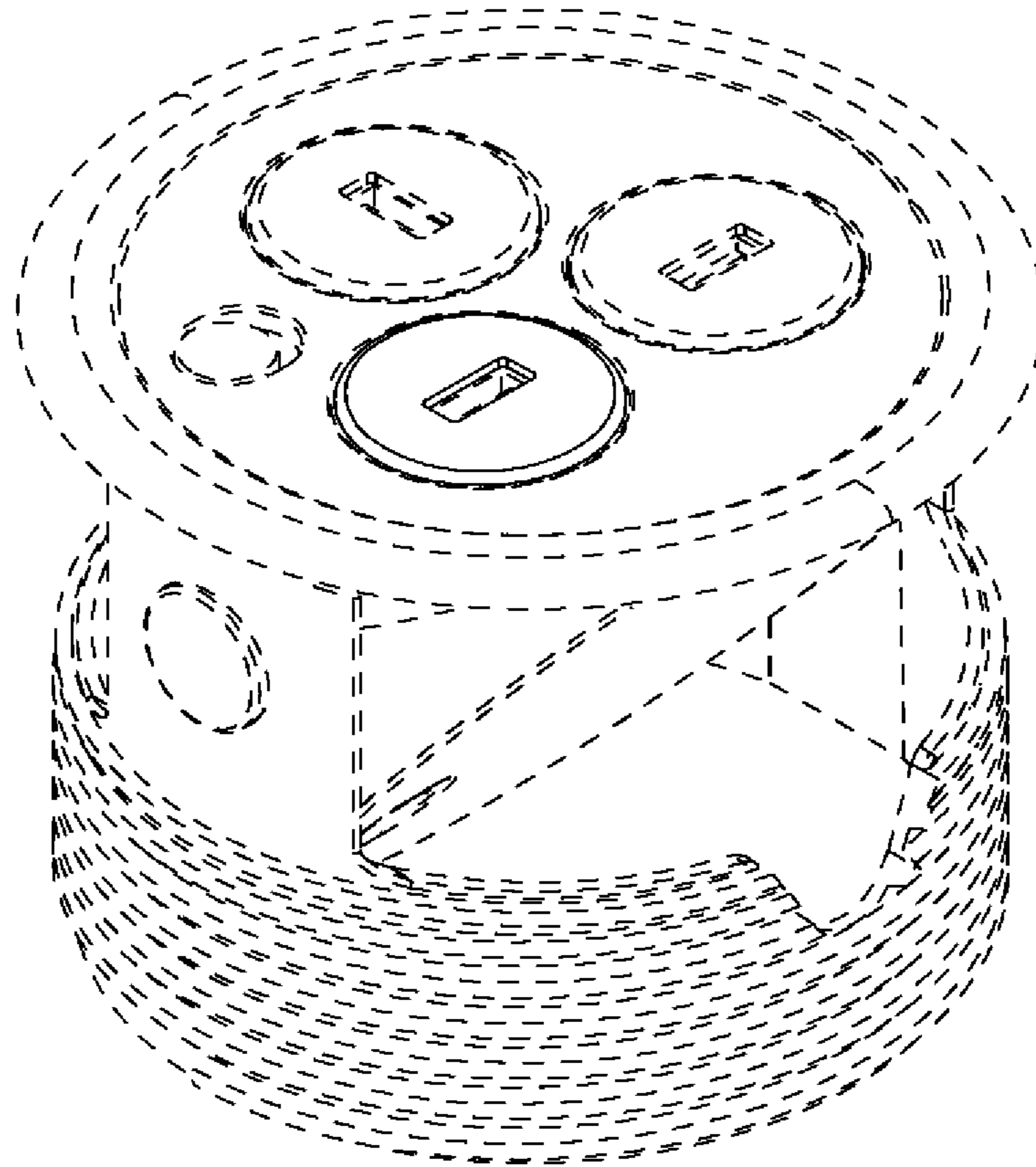


FIG. 8