



US00D722031S

(12) **United States Design Patent**
Dunklee et al.

(10) **Patent No.:** **US D722,031 S**

(45) **Date of Patent:** **** Feb. 3, 2015**

(54) **TOP CONTACT LAYOUT BOARD IN AN ELECTRICAL SYSTEM**

(71) Applicant: **Celadon Systems, Inc.**, Apple Valley, MN (US)

(72) Inventors: **John L. Dunklee**, Tigard, OR (US);
William A. Funk, Eagan, MN (US);
Matthew Page, Apple Valley, MN (US);
Dennis Flanders, Apple Valley, MN (US);
Bryan J. Root, Apple Valley, MN (US)

(73) Assignee: **Celadon Systems, Inc.**, Apple Valley, MN (US)

(**) Term: **14 Years**

(21) Appl. No.: **29/478,155**

(22) Filed: **Dec. 31, 2013**

(51) **LOC (10) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/182**

(58) **Field of Classification Search**
USPC D13/182; D7/585; 174/250, 253, 255;
257/690, 692, 697, 695, 748, 786, 773;
361/777; 29/829

See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D225,434	S	*	12/1972	Cleeland	D7/585
3,795,845	A	*	3/1974	Cass et al.	257/786
D320,361	S	*	10/1991	Karasawa	D10/103
5,091,822	A	*	2/1992	Takashima	361/695
5,210,682	A	*	5/1993	Takashima	361/744

5,491,364	A	*	2/1996	Brandenburg et al.	257/786
6,310,398	B1	*	10/2001	Katz	257/773
6,556,454	B1	*	4/2003	D'Amato et al.	361/777
6,664,620	B2	*	12/2003	Siu et al.	257/692
6,793,500	B1	*	9/2004	Budell et al.	439/55
D616,389	S	*	5/2010	Takahashi	D13/182
D639,755	S	*	6/2011	Root et al.	D13/182
D639,757	S	*	6/2011	Root et al.	D13/182
D649,126	S	*	11/2011	Takahashi	D13/182
D654,033	S	*	2/2012	Root et al.	D13/182
D654,883	S	*	2/2012	Honma et al.	D13/182
D654,884	S	*	2/2012	Honma et al.	D13/182
D655,257	S	*	3/2012	Honma et al.	D13/182
D655,259	S	*	3/2012	Honma et al.	D13/182
D664,249	S	*	7/2012	Wang et al.	D23/388
2002/0060318	A1	*	5/2002	Katz	257/24

* cited by examiner

Primary Examiner — Elizabeth J Oswecki

(74) *Attorney, Agent, or Firm* — Hamre, Schumann, Mueller & Larson, P.C.

(57) **CLAIM**

The ornamental design for a top contact layout board in an electrical system, as shown and described.

DESCRIPTION

FIG. 1 is an enlarged front view of a top contact layout board in an electrical system showing our new design, the mother-board is shown in broken lines as unclaimed environment and forms no part of the claimed design;

FIG. 2 is a front view thereof;

FIG. 3 is a rear view thereof;

FIG. 4 is a top view thereof;

FIG. 5 is a bottom view thereof;

FIG. 6 is a right side view thereof; and,

FIG. 7 is a left side view thereof.

1 Claim, 4 Drawing Sheets

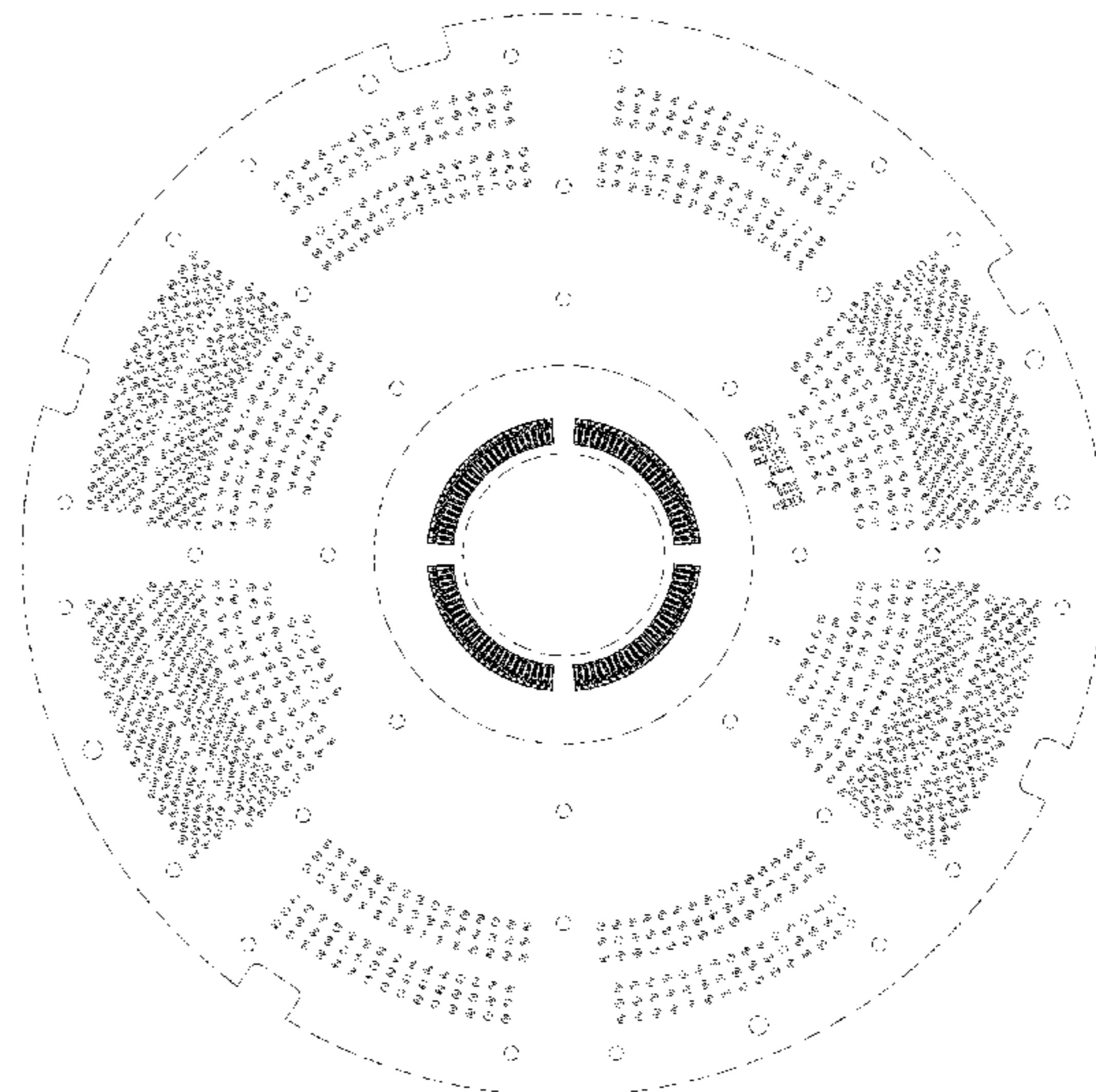
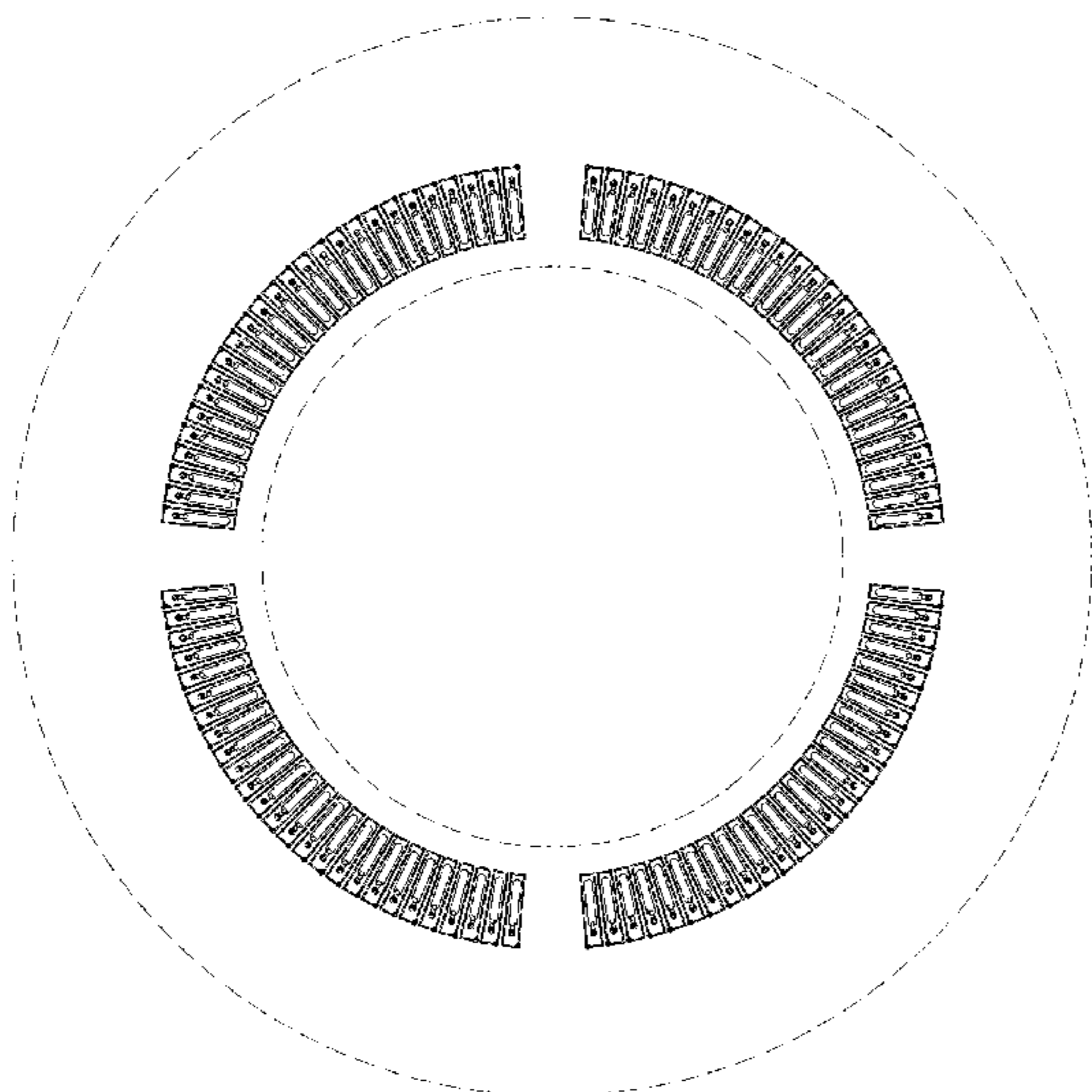


Fig. 1

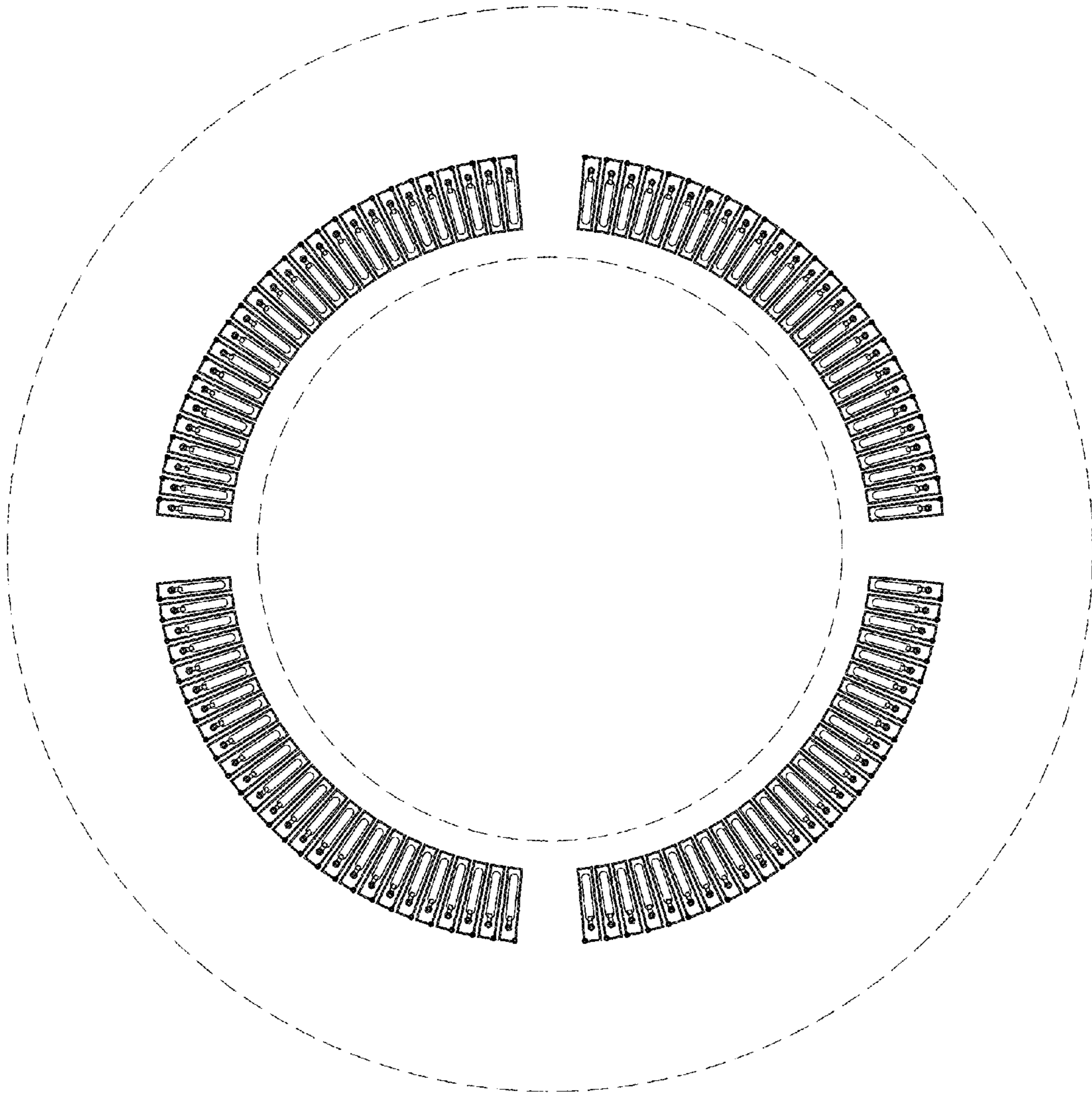


Fig. 2

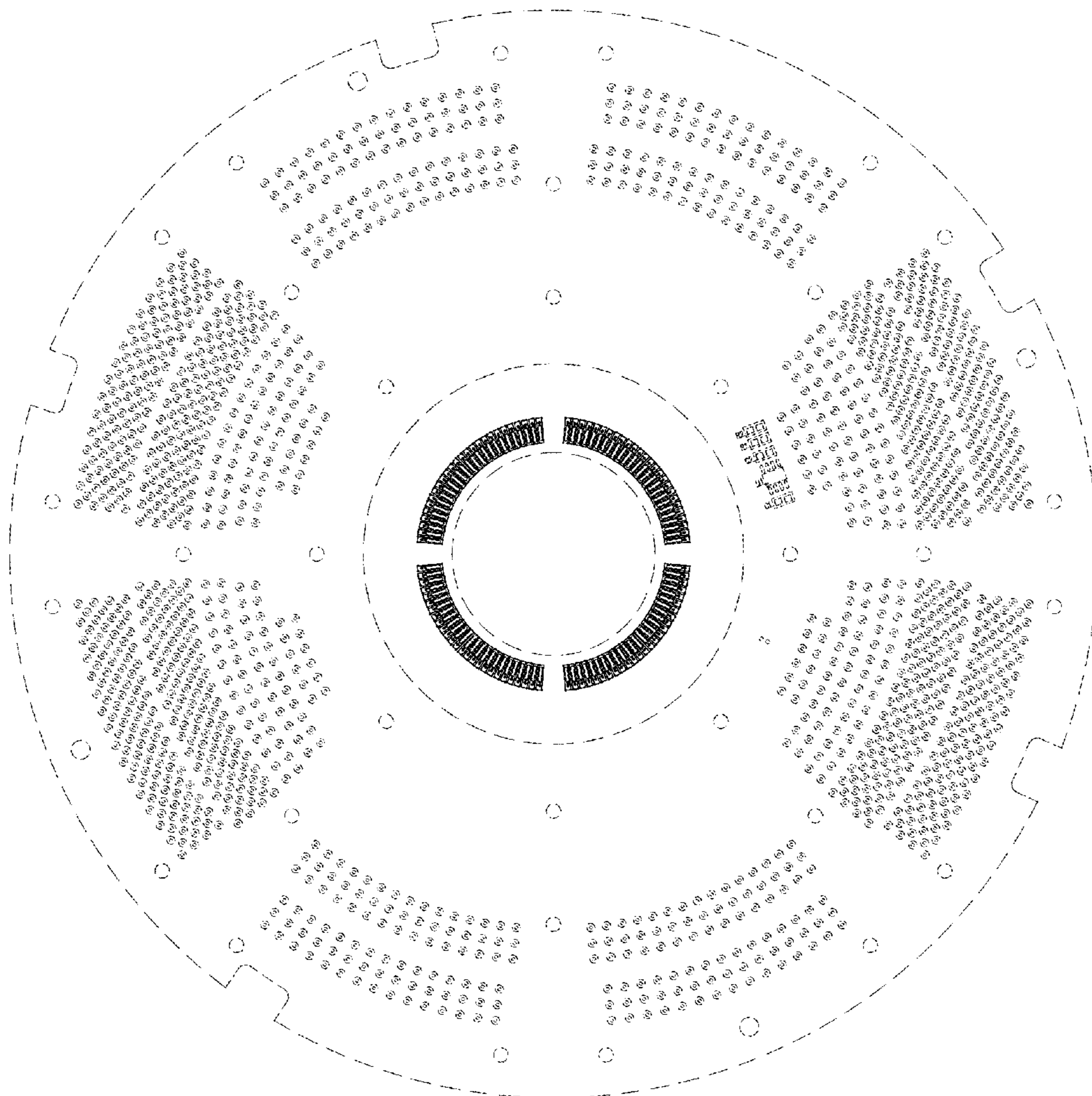


Fig. 3

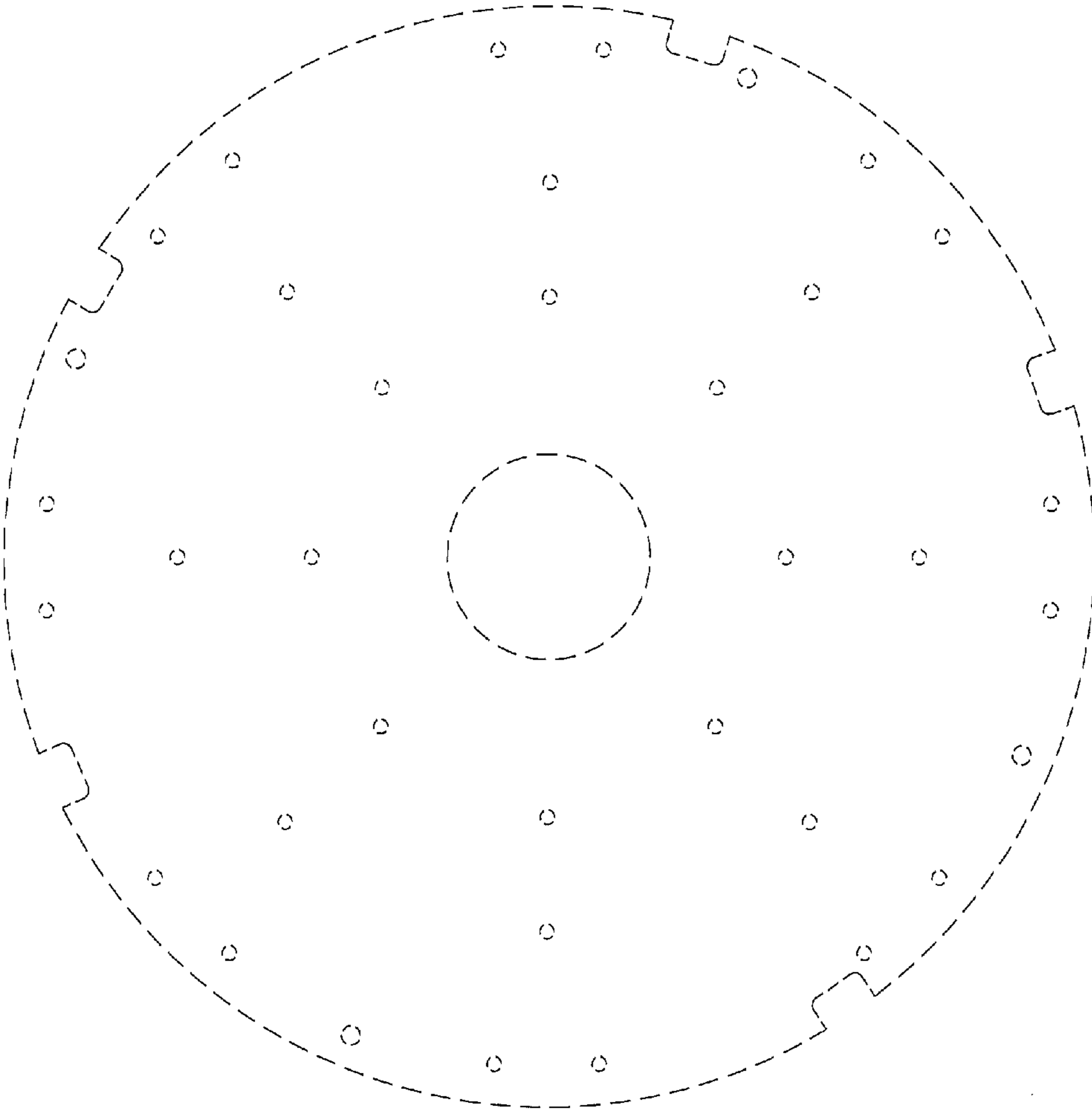


Fig. 4

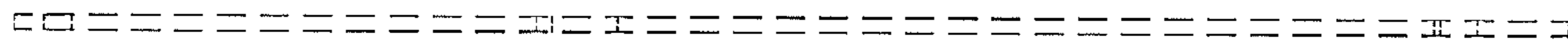


Fig. 5

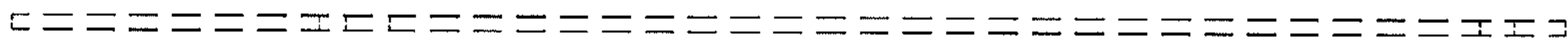


Fig. 6



Fig. 7

