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(12) **United States Design Patent**
Benedict et al.

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(45) **Date of Patent:** **** Feb. 3, 2015**

(54) **CIRCUIT ANALYZER**

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(**) Term: **14 Years**

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(51) **LOC (10) Cl.** **13-02**

(52) **U.S. Cl.**
USPC **D13/110**

(58) **Field of Classification Search**
USPC D13/110, 107, 108, 118, 119, 123, 133,
D13/137.1, 138.1, 138.2, 139.1, 184, 199;
D14/251, 432, 434; 174/53, 54, 59, 61;
307/11, 150, 151; 320/107, 111, 112,
320/113, 114, 115; 361/679.01; 363/142,
363/146; 439/131, 172, 626, 628, 638
See application file for complete search history.

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Primary Examiner — Derrick Holland

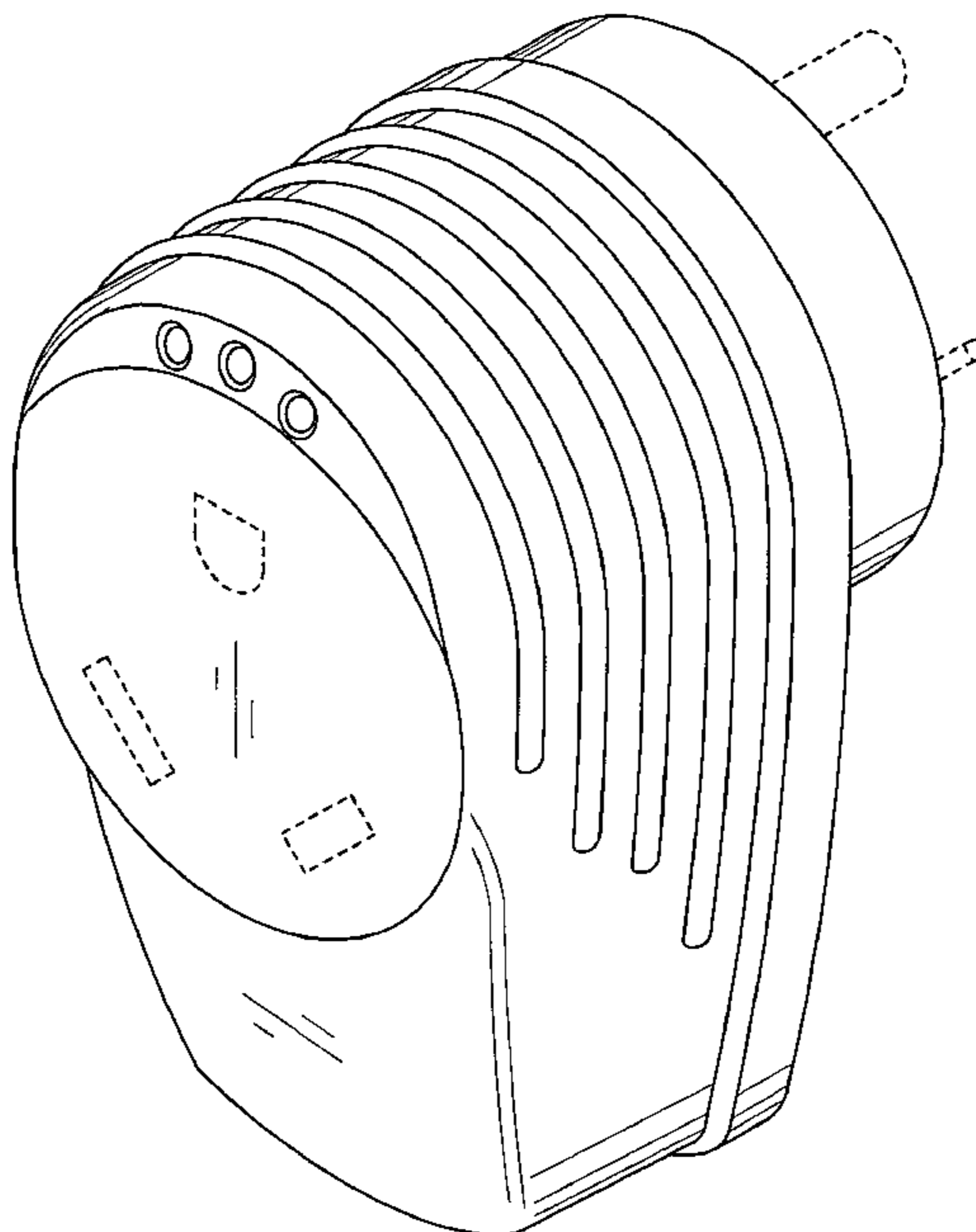
(57) **CLAIM**

The ornamental design for a circuit analyzer, as shown and
described.

DESCRIPTION

FIG. 1 is a front, top, right perspective view of a circuit
analyzer according to the invention;
FIG. 2 is a front elevation view thereof;
FIG. 3 is a rear elevation view thereof;
FIG. 4 is a top plan view thereof;
FIG. 5 is a bottom plan view thereof;
FIG. 6 is a left side elevational view thereof, the right side
elevational view being a mirror image; and,
FIG. 7 is a top, rear, left perspective view thereof.
The broken lines shown represent unclaimed subject matter
and form no part of the claimed design.

1 Claim, 7 Drawing Sheets



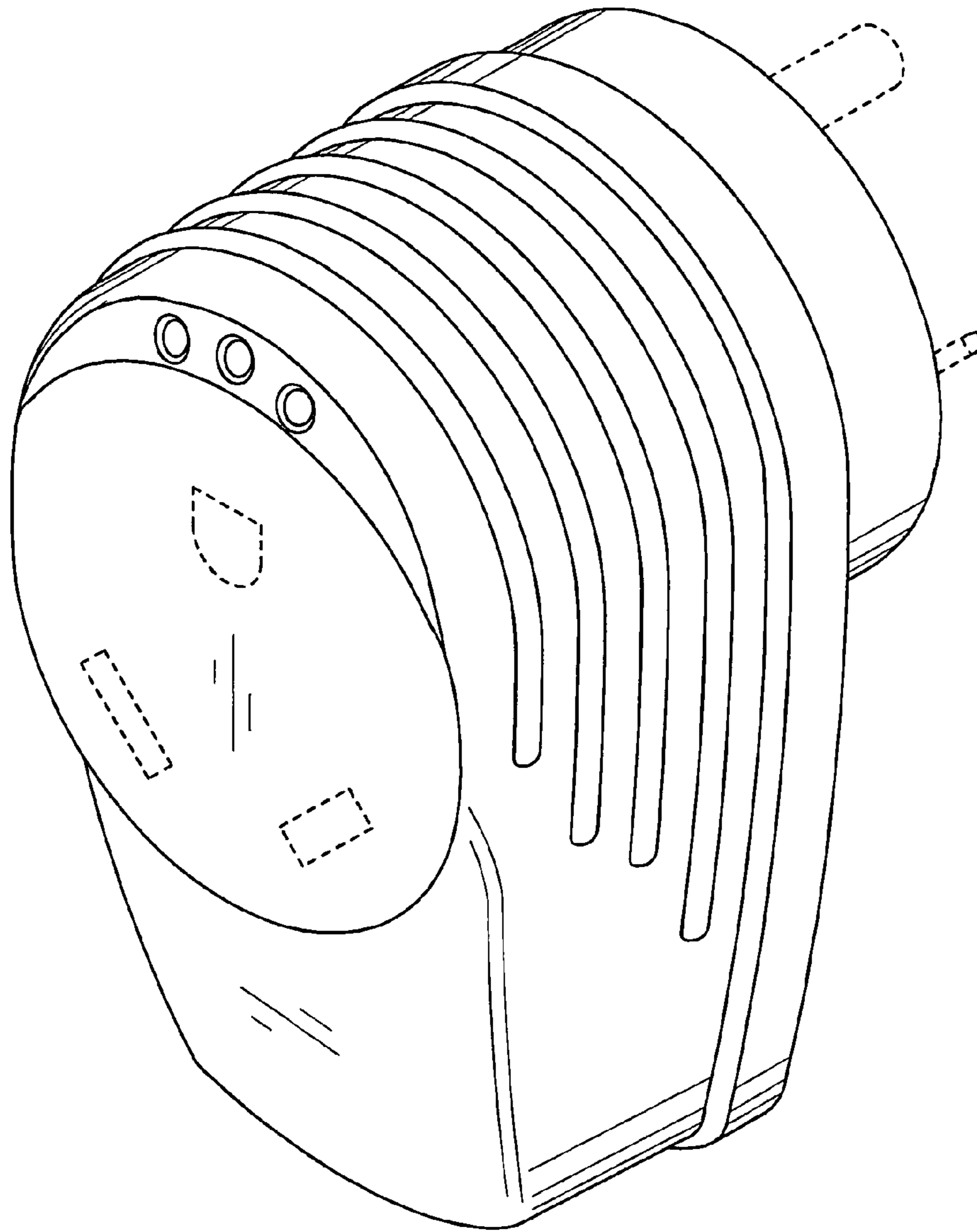


Fig. 1

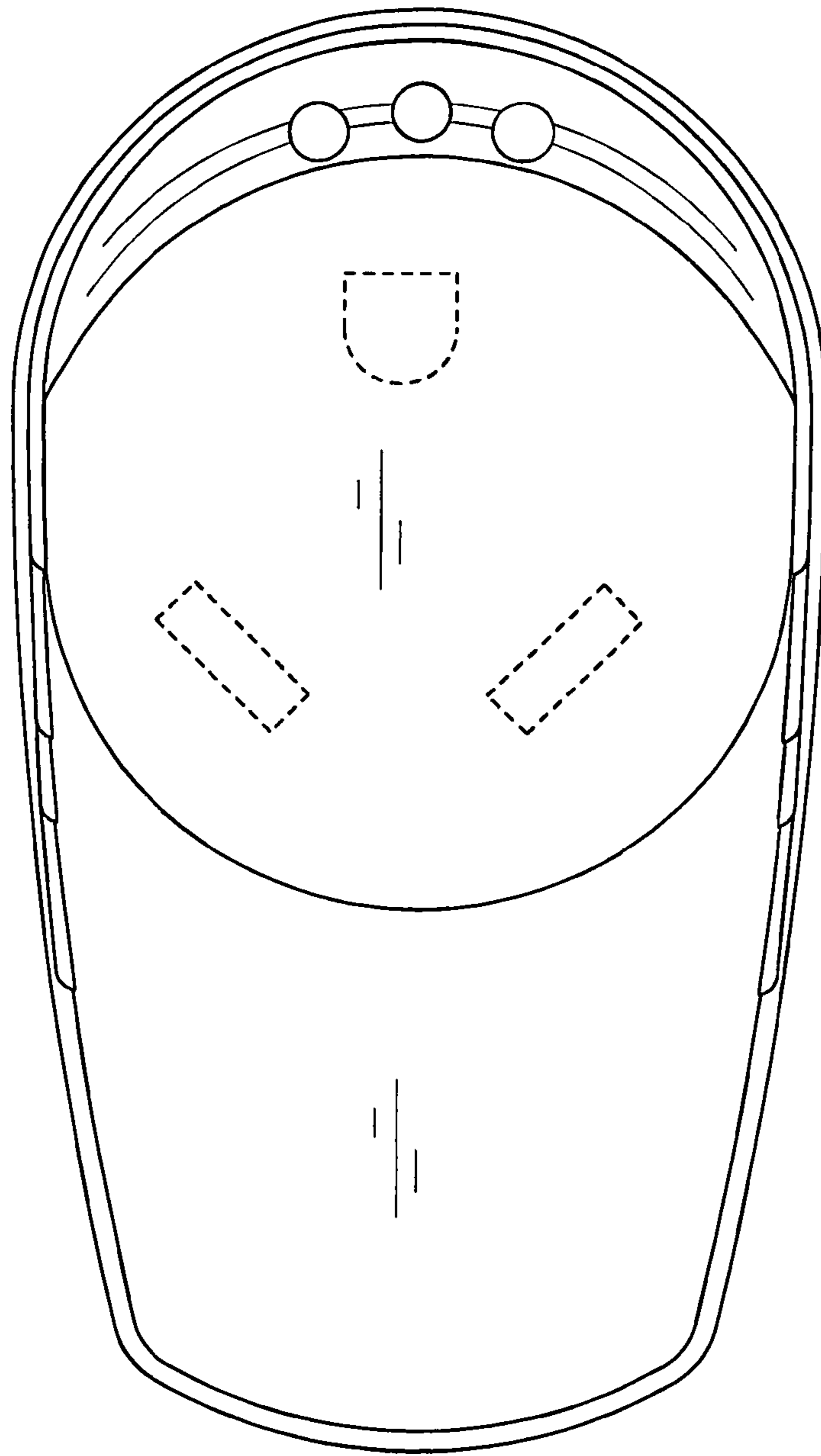


Fig. 2

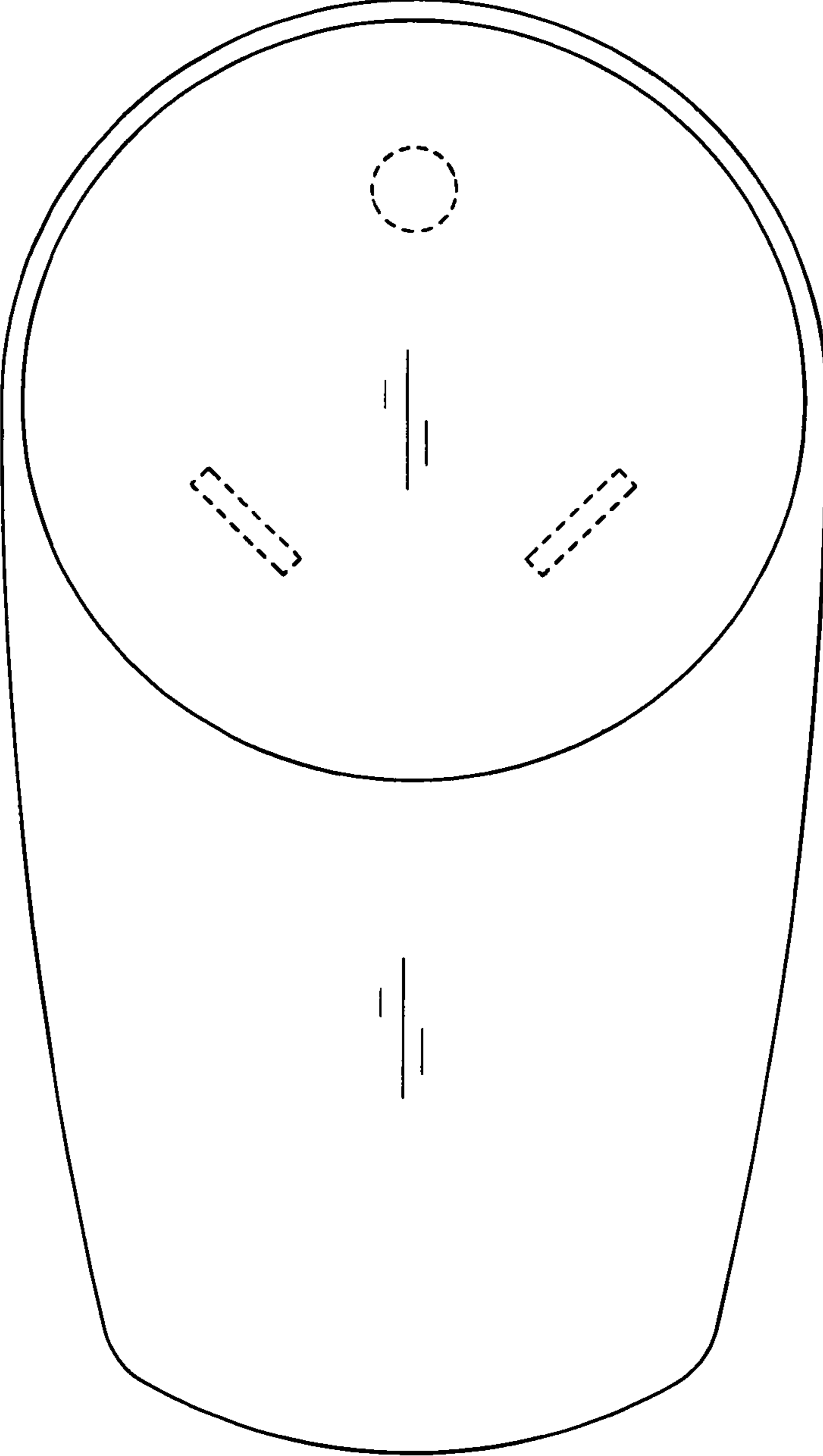


Fig. 3

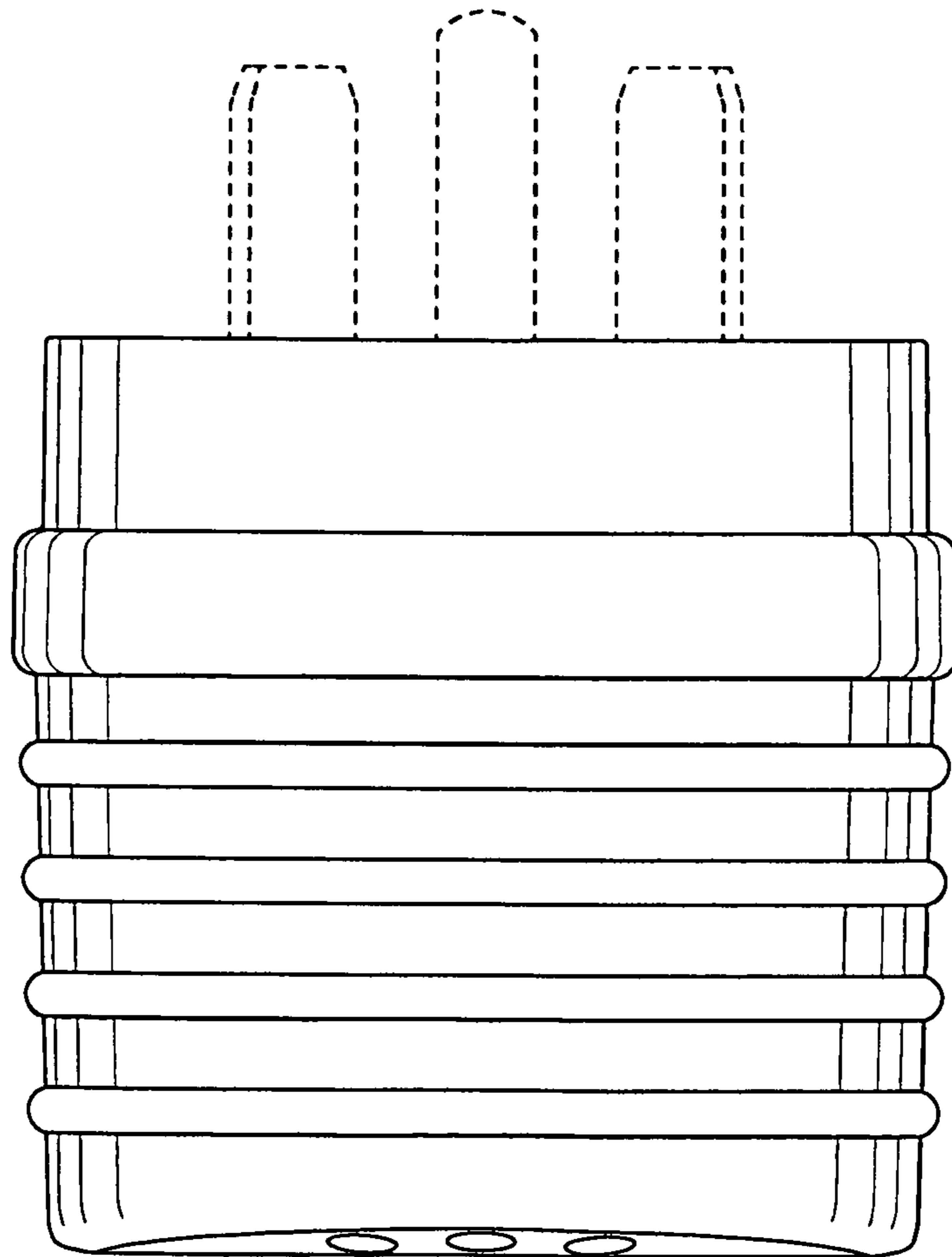


Fig. 4

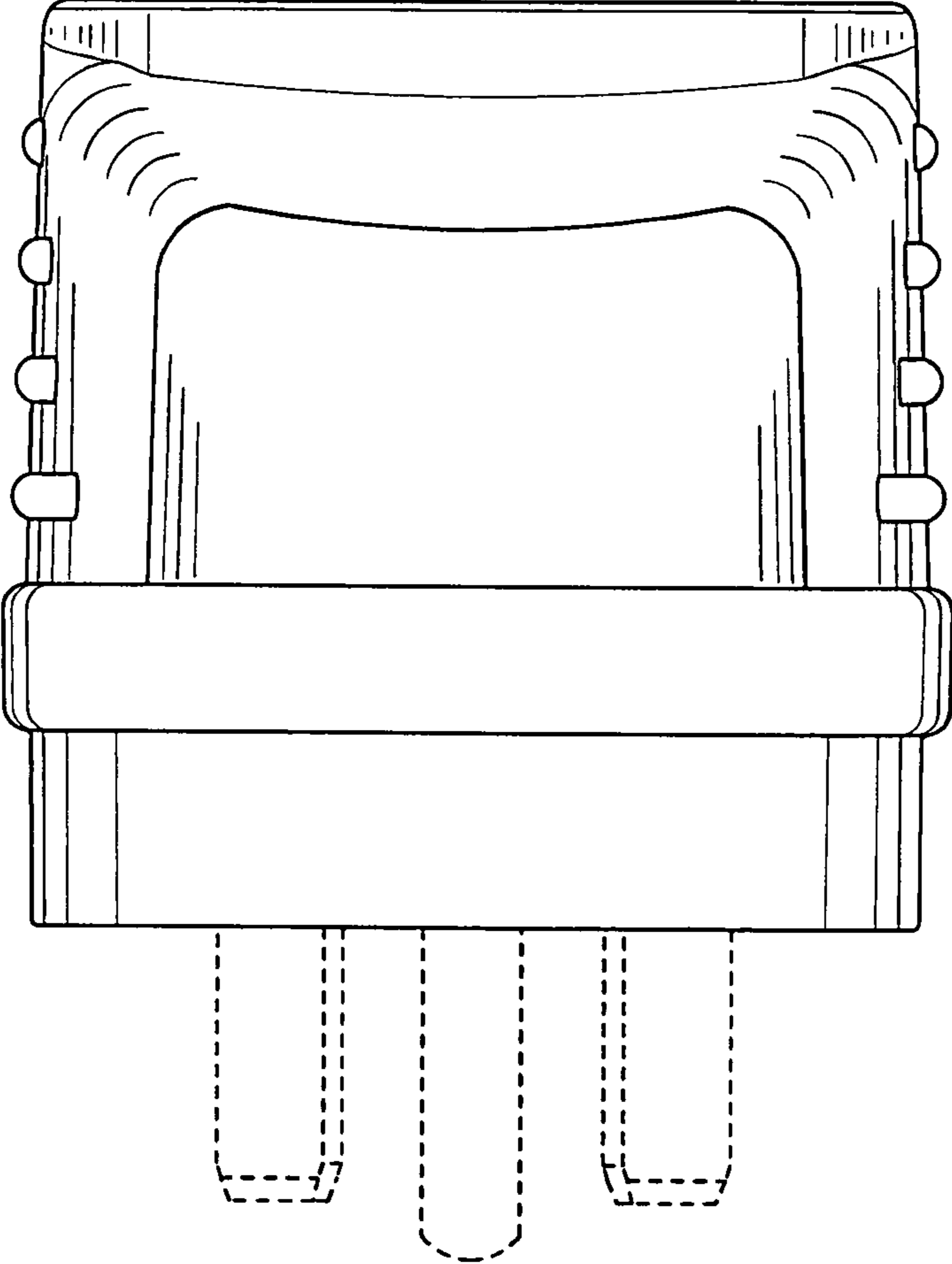


Fig. 5

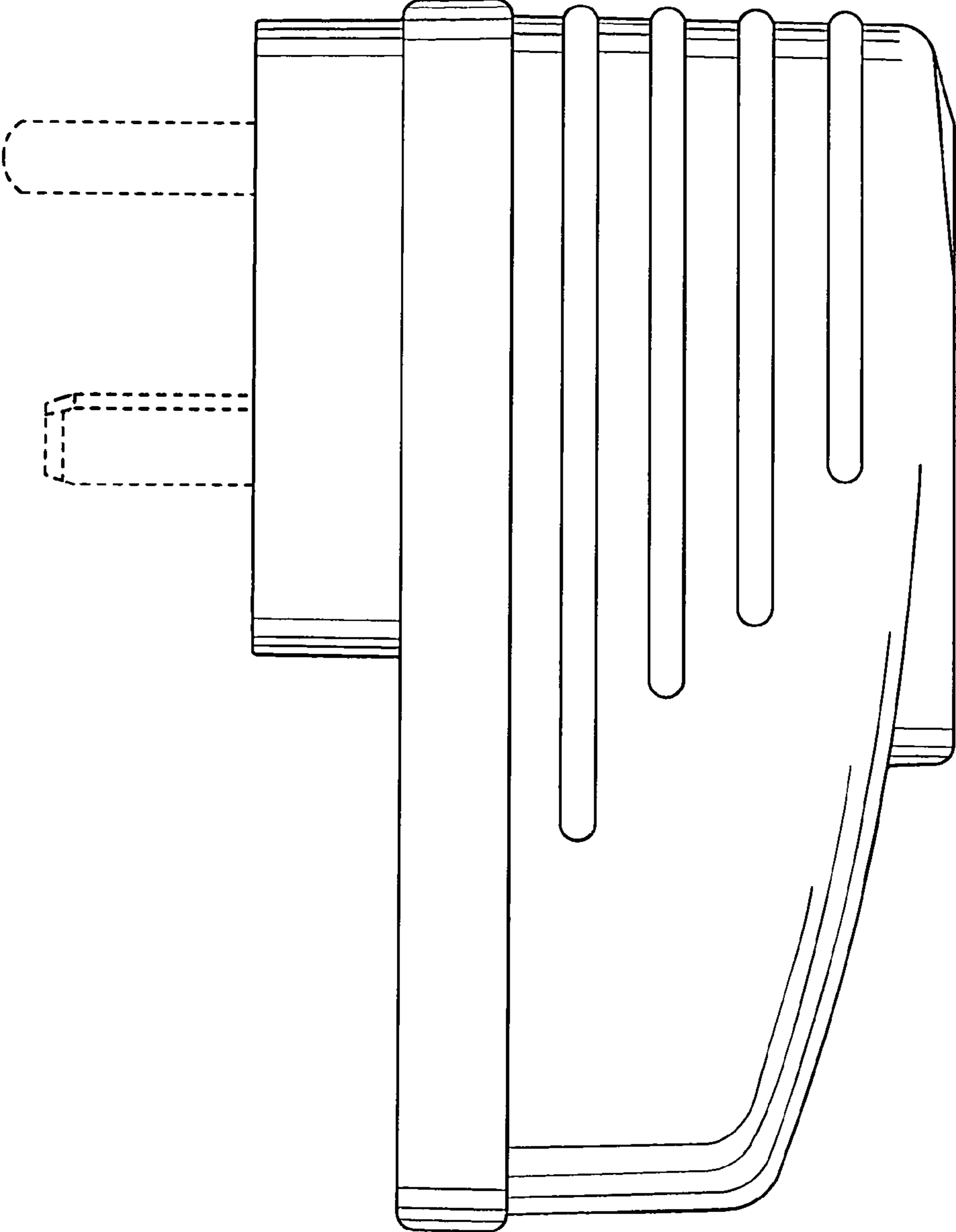


Fig. 6

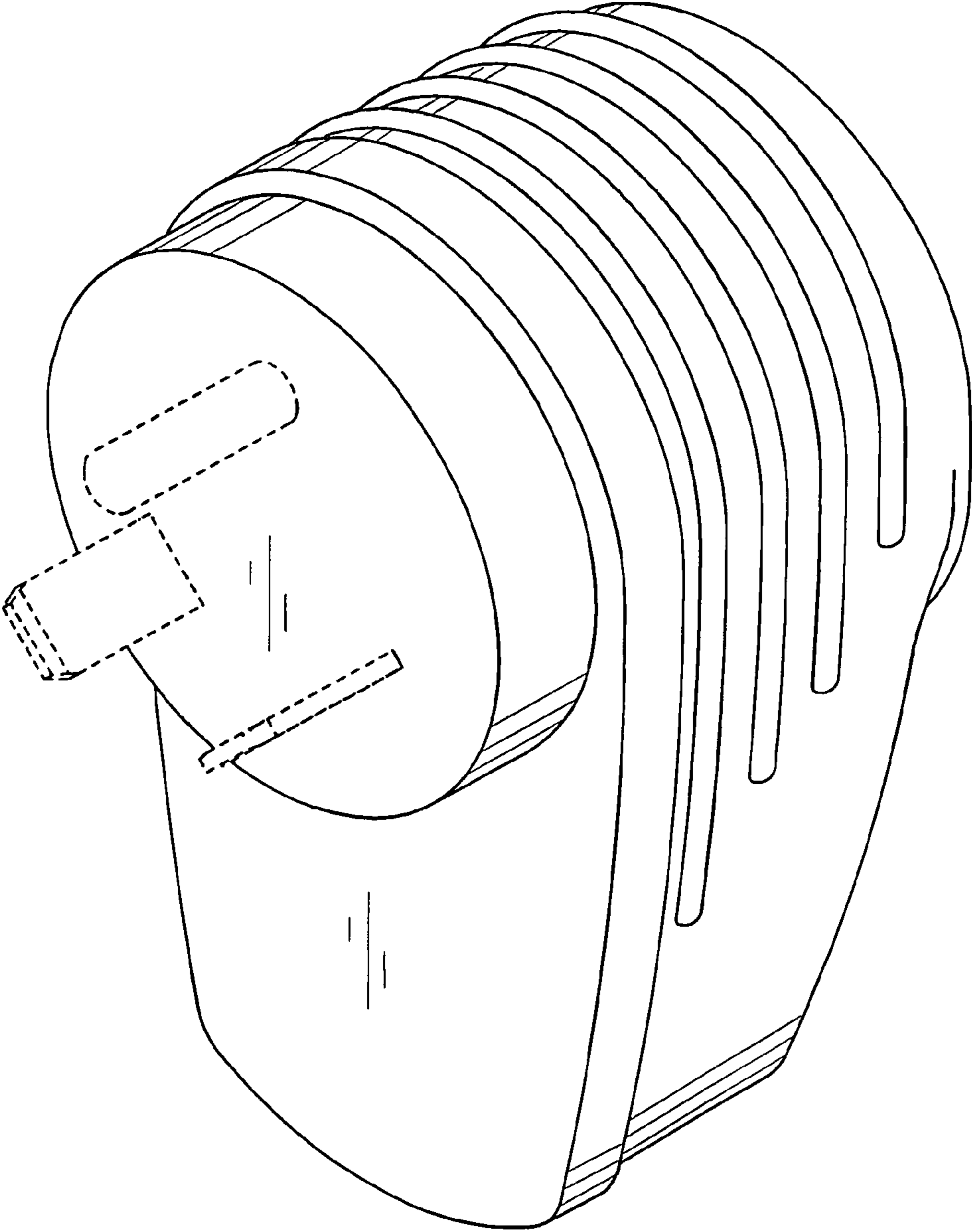


Fig. 7