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(12) **United States Design Patent**
Yamaguchi

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(54) **ELECTRICAL CONNECTOR**

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(73) Assignee: **Japan Aviation Electronics Industry, Limited, Tokyo (JP)**

(**) Term: **14 Years**

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(30) **Foreign Application Priority Data**

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(51) **LOC (10) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/147**

(58) **Field of Classification Search**
USPC D13/147, 154, 184, 199; 439/79, 108,
439/350, 353, 357, 378, 476.1, 489, 587,
439/594, 595, 598, 607.01, 607.04, 607.05,
439/607.17, 607.25, 607.34, 607.41,
439/607.53, 656, 660, 668, 680, 711, 745,
439/752

See application file for complete search history.

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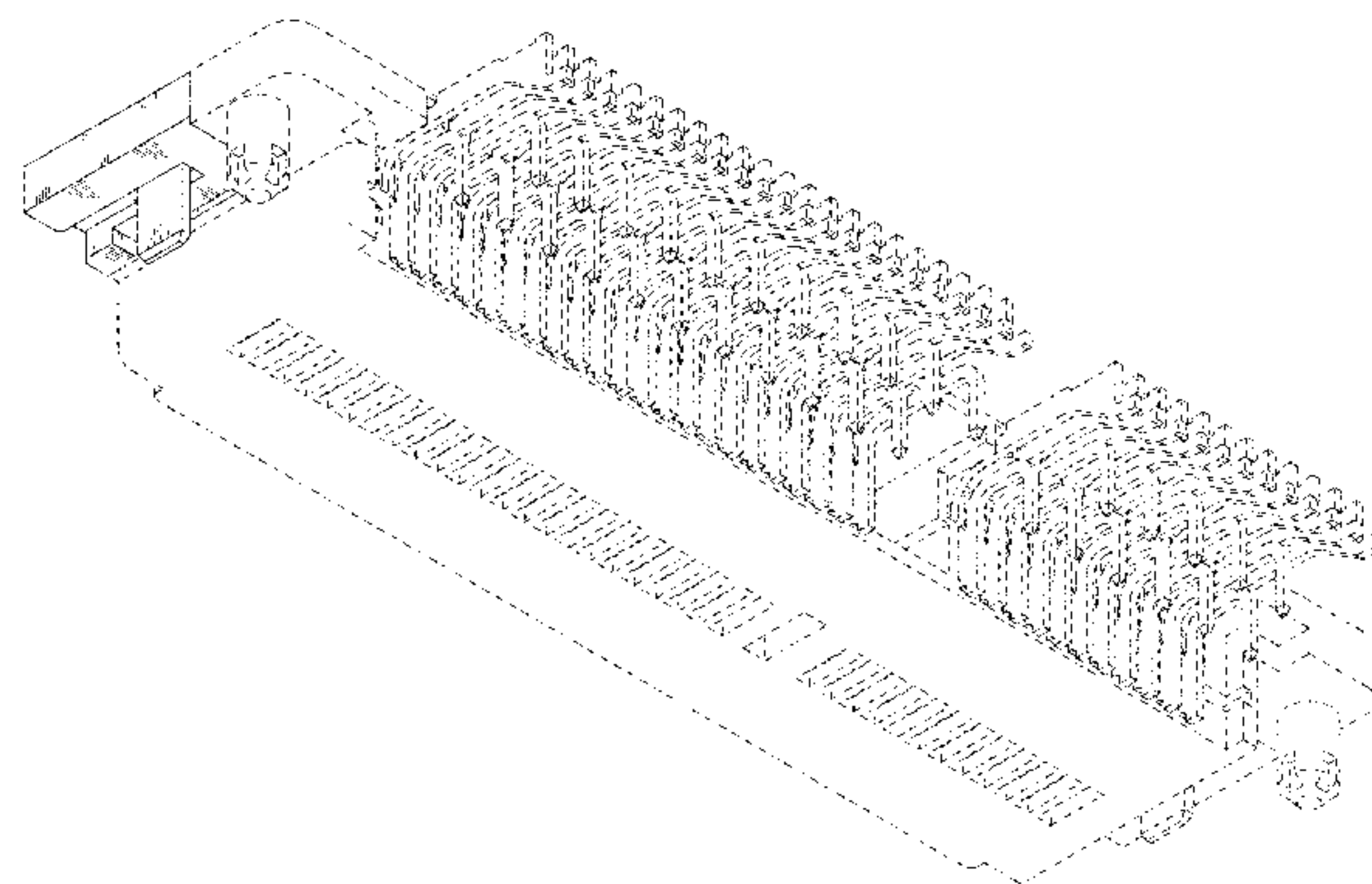
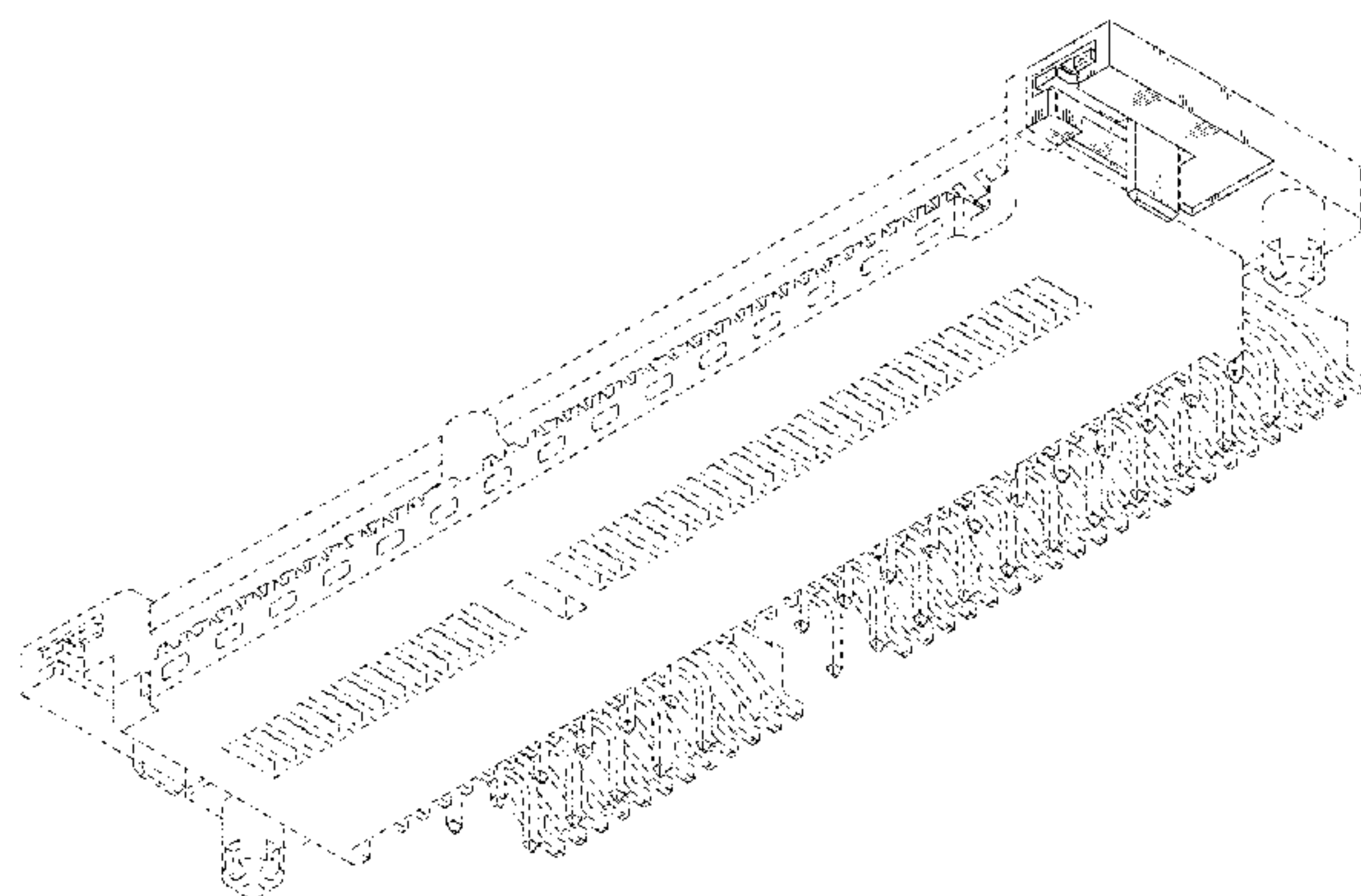
(57) **CLAIM**

The ornamental design for an electrical connector, as shown and described.

DESCRIPTION

FIG. 1 is a front elevational view of an electrical connector showing my new design;
FIG. 2 is a top plan view thereof;
FIG. 3 is a right side elevational view thereof;
FIG. 4 is a rear elevational view thereof; and
FIG. 5 is a bottom plan view thereof.
FIG. 6 is a front and top perspective view thereof;
FIG. 7 is a front and bottom perspective view thereof;
FIG. 8 is a rear and bottom perspective view thereof; and,
FIG. 9 is a left side elevational view thereof.
The broken line in the drawing views is included to show unclaimed environment only and forms no part of the claimed design.
The dash-dot line represents the boundaries of the claimed design.

1 Claim, 9 Drawing Sheets



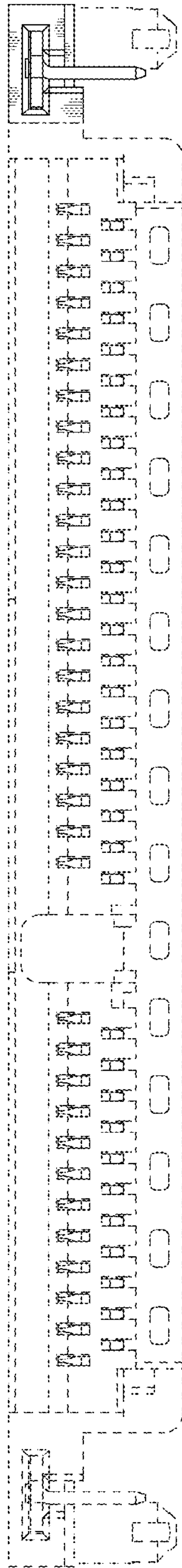


FIG.1

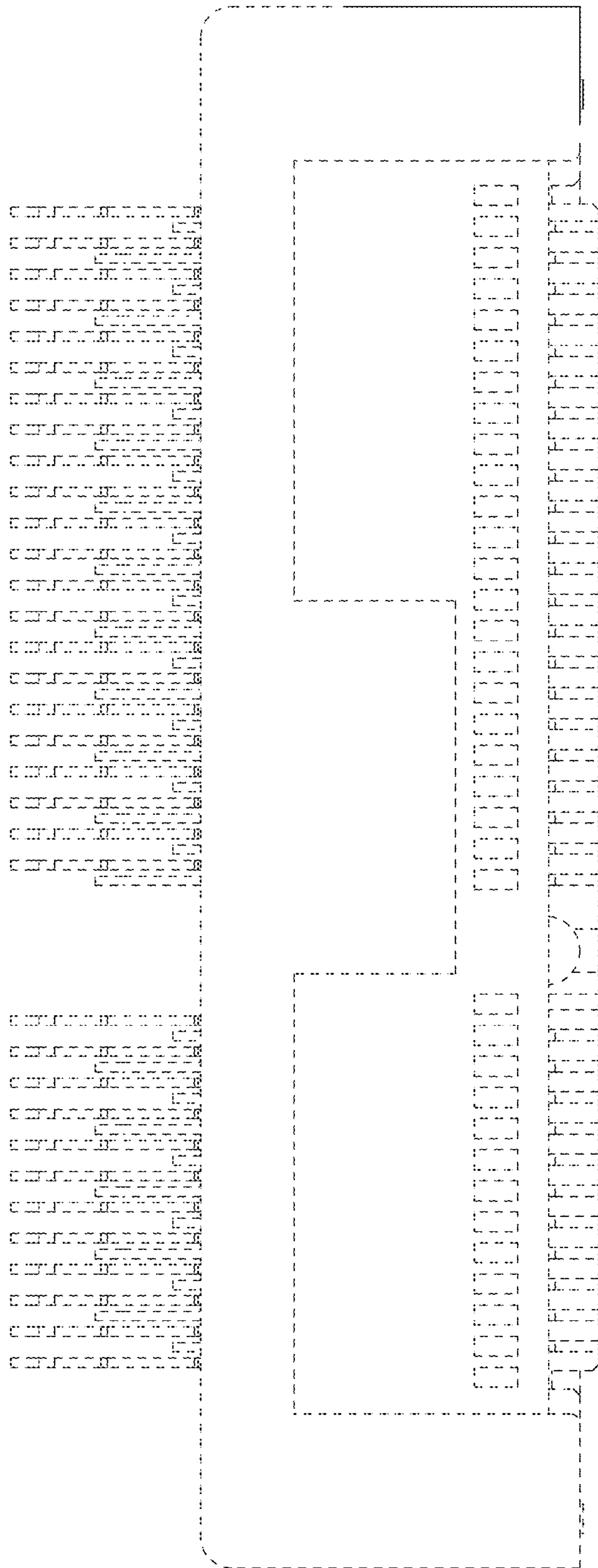


FIG.2

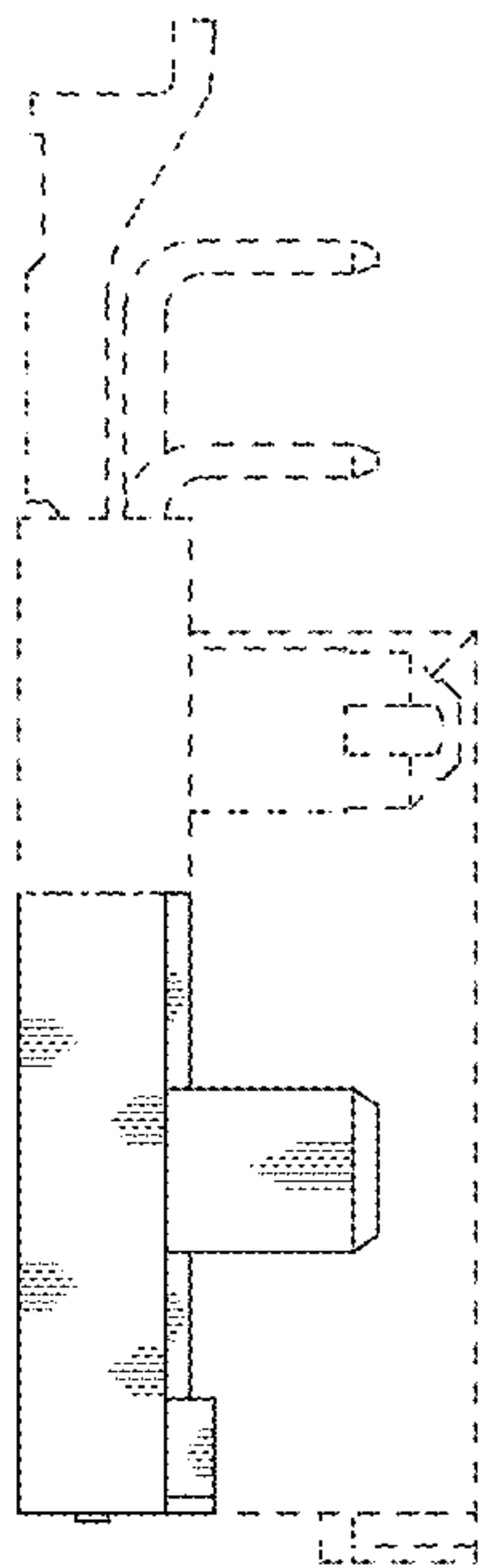


FIG.3

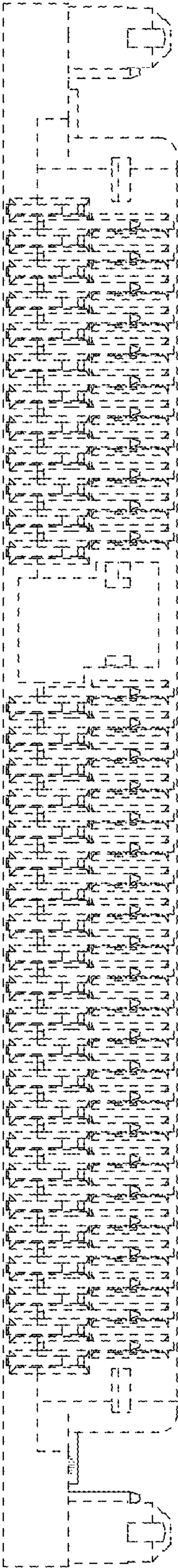


FIG.4

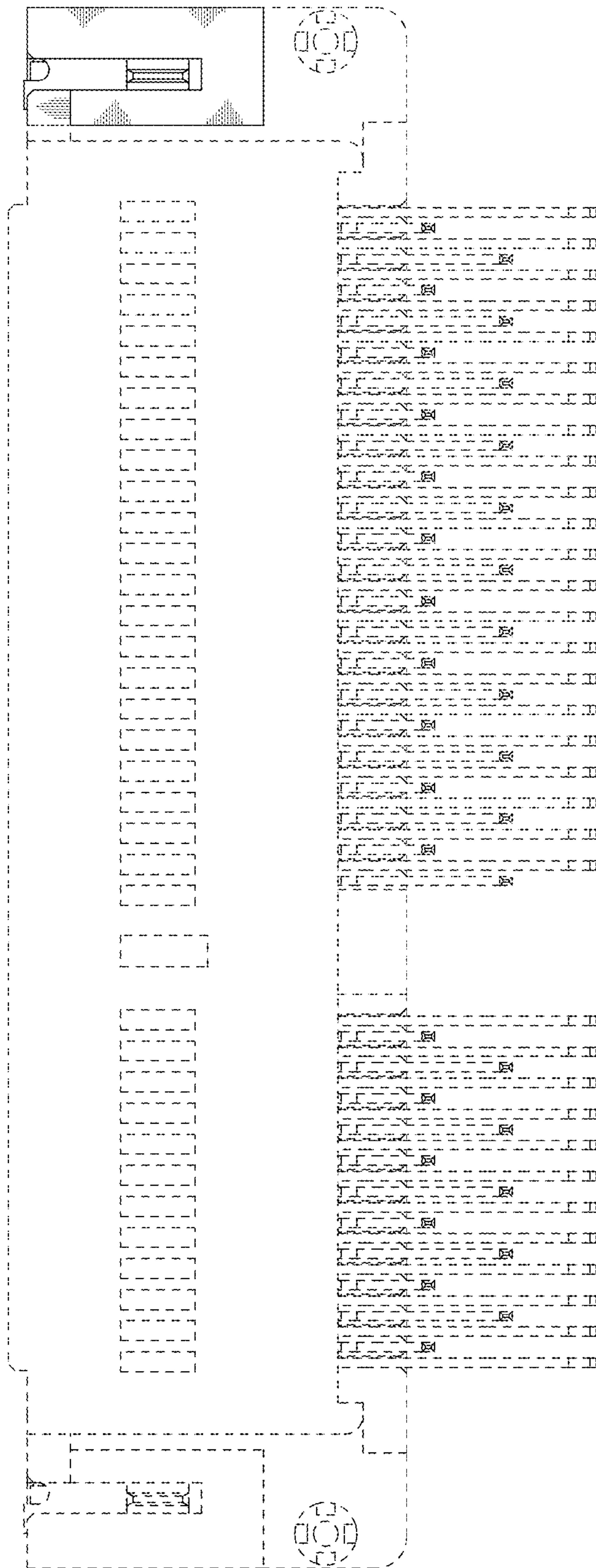


FIG.5

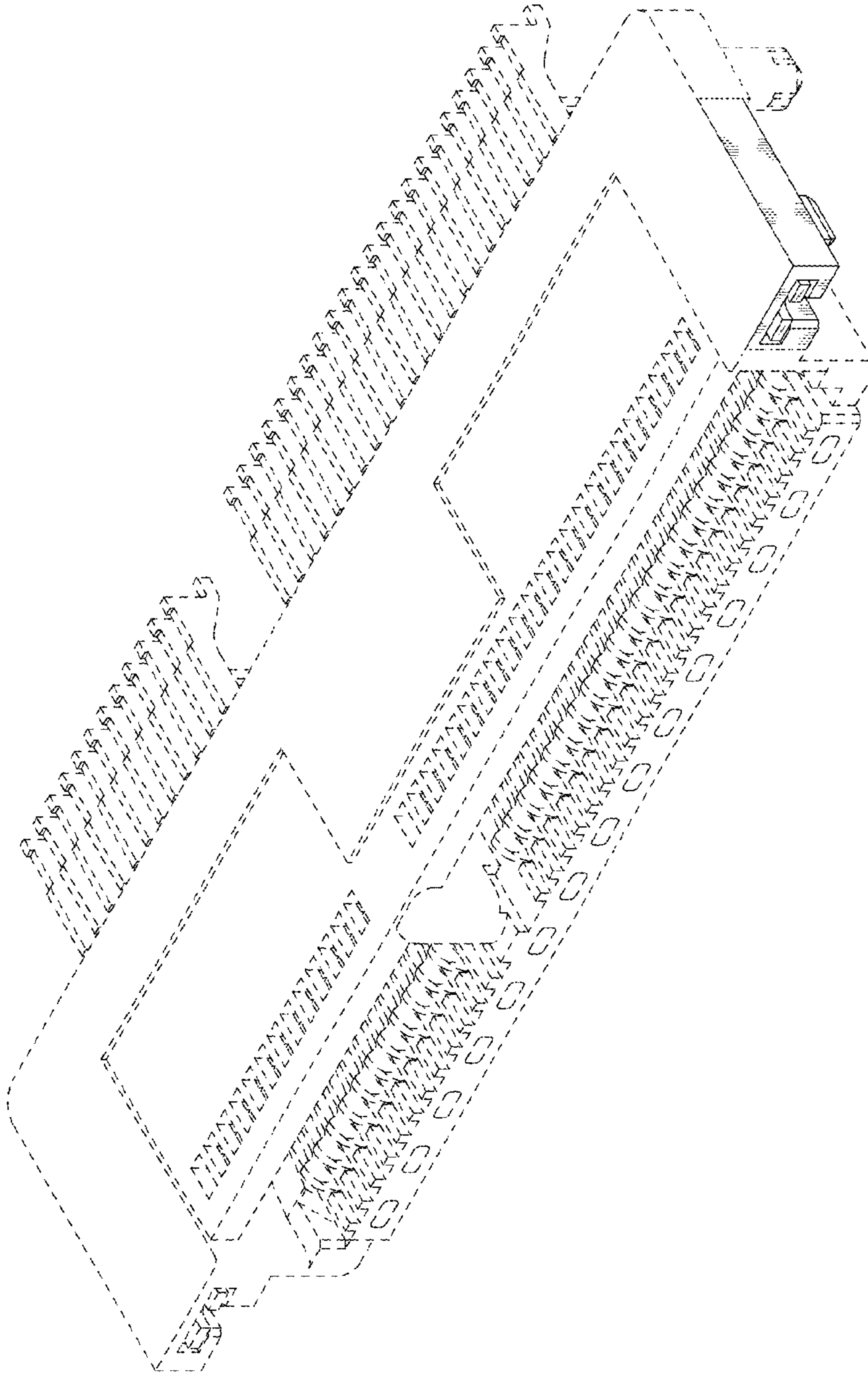


FIG.6

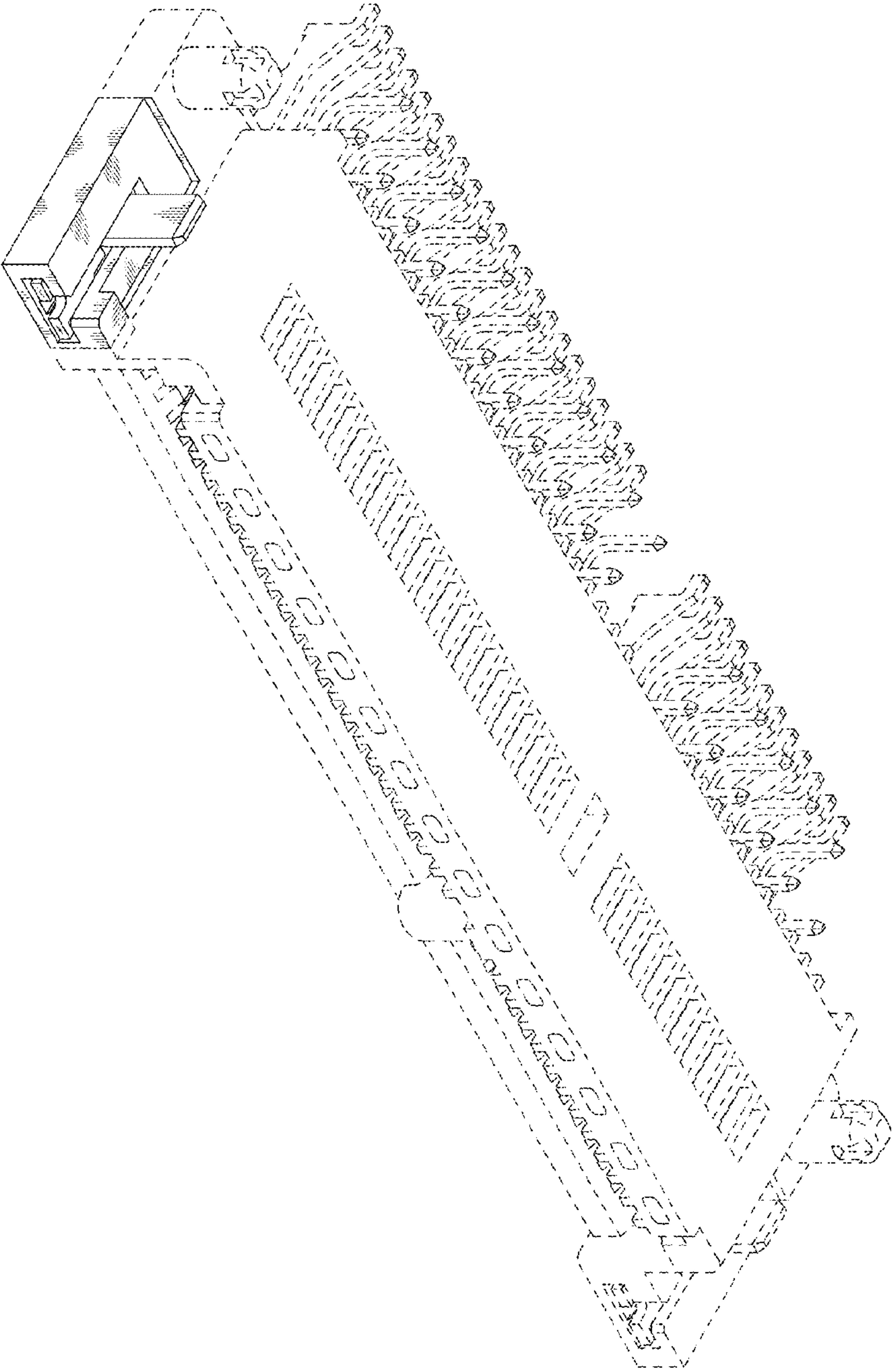


FIG.7

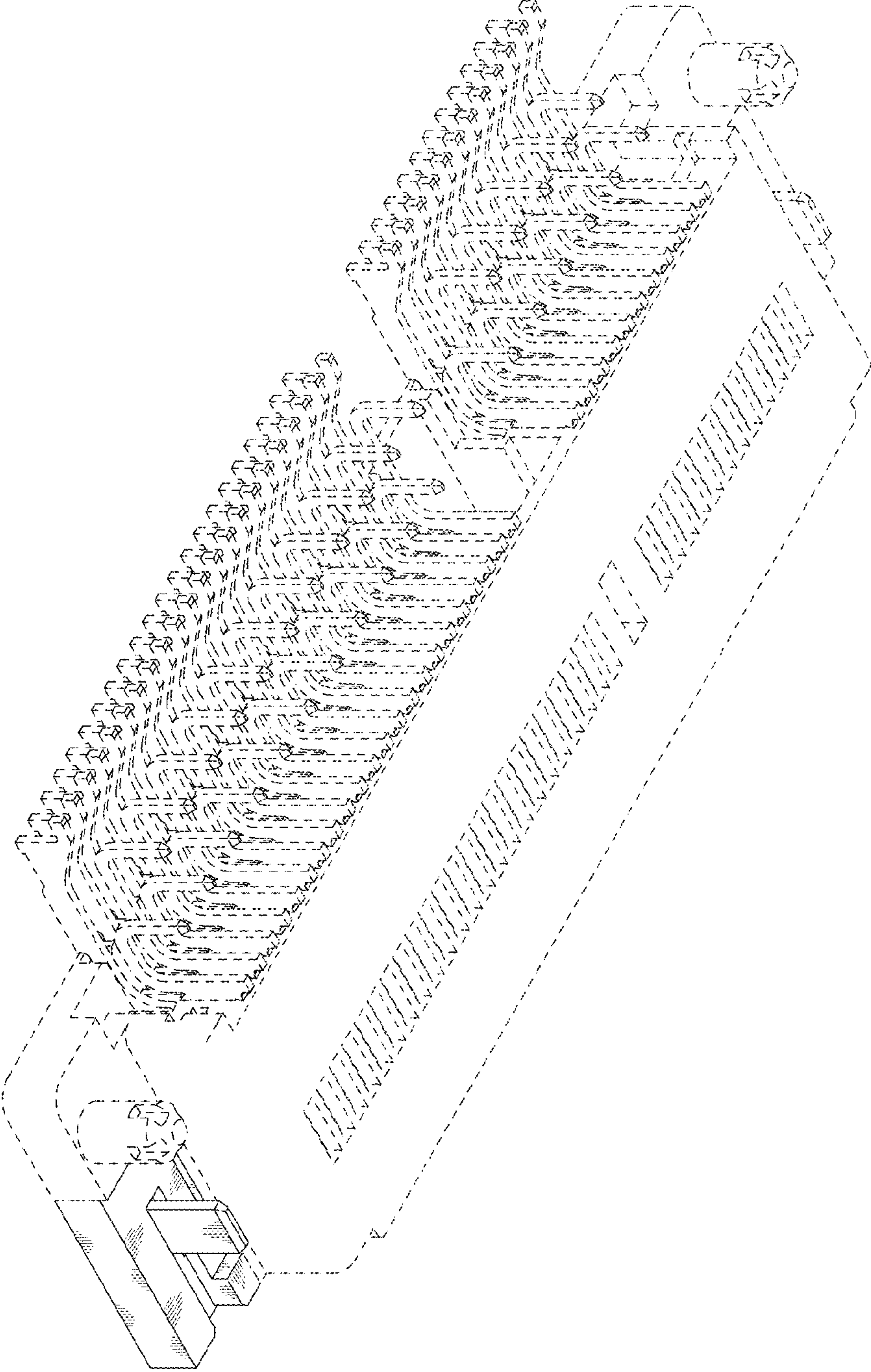


FIG.8

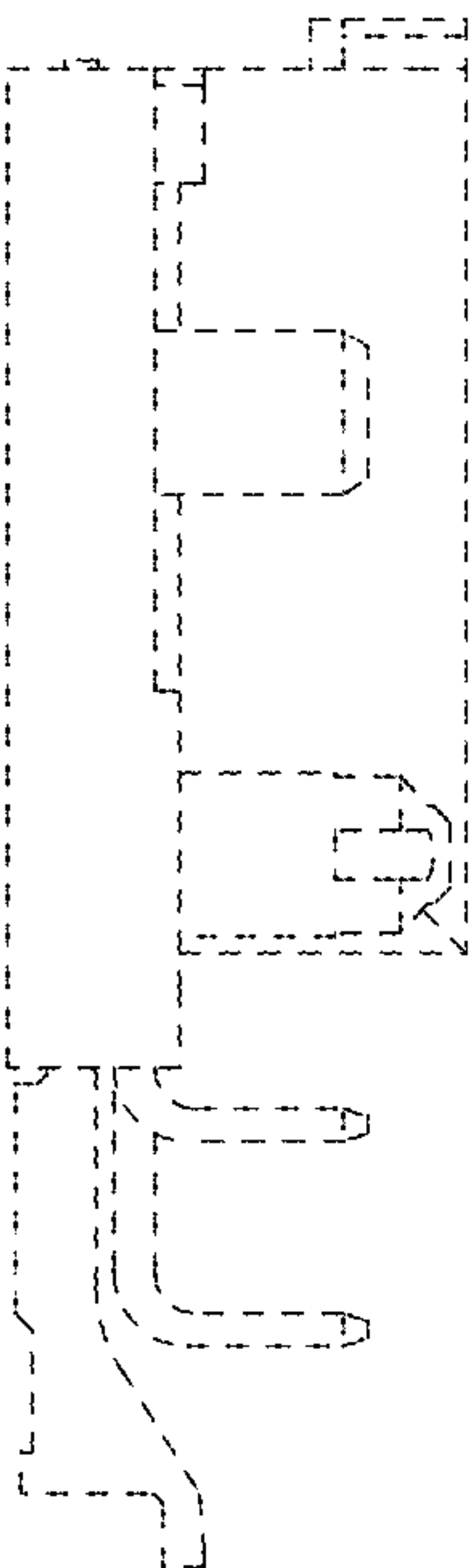


FIG.9