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(12) **United States Design Patent**
Mayr

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(54) **TEST DEVICE**

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(AT)

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(73) Assignee: **Omicron Electronics GmbH** (AT)

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Dec. 21, 2012	(EM)	002157974-0004
Dec. 21, 2012	(EM)	002157974-0005
Dec. 21, 2012	(EM)	002157974-0006
Dec. 21, 2012	(EM)	002157974-0007
Dec. 21, 2012	(EM)	002157974-0008
Dec. 21, 2012	(EM)	002157974-0009

(51) **LOC (10) Cl.** **10-04**

(52) **U.S. Cl.**
USPC **D10/103; D10/80**

(58) **Field of Classification Search**
USPC D10/103, 80; D13/158, 173-178;
200/5 A, 5 R; 307/11; 361/627, 622;
700/17, 22, 292

See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

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D679,665 S *	4/2013	Saari	D13/177

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Primary Examiner — Antoine D Davis

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(57) **CLAIM**

The ornamental design for a test device, as shown and described.

DESCRIPTION

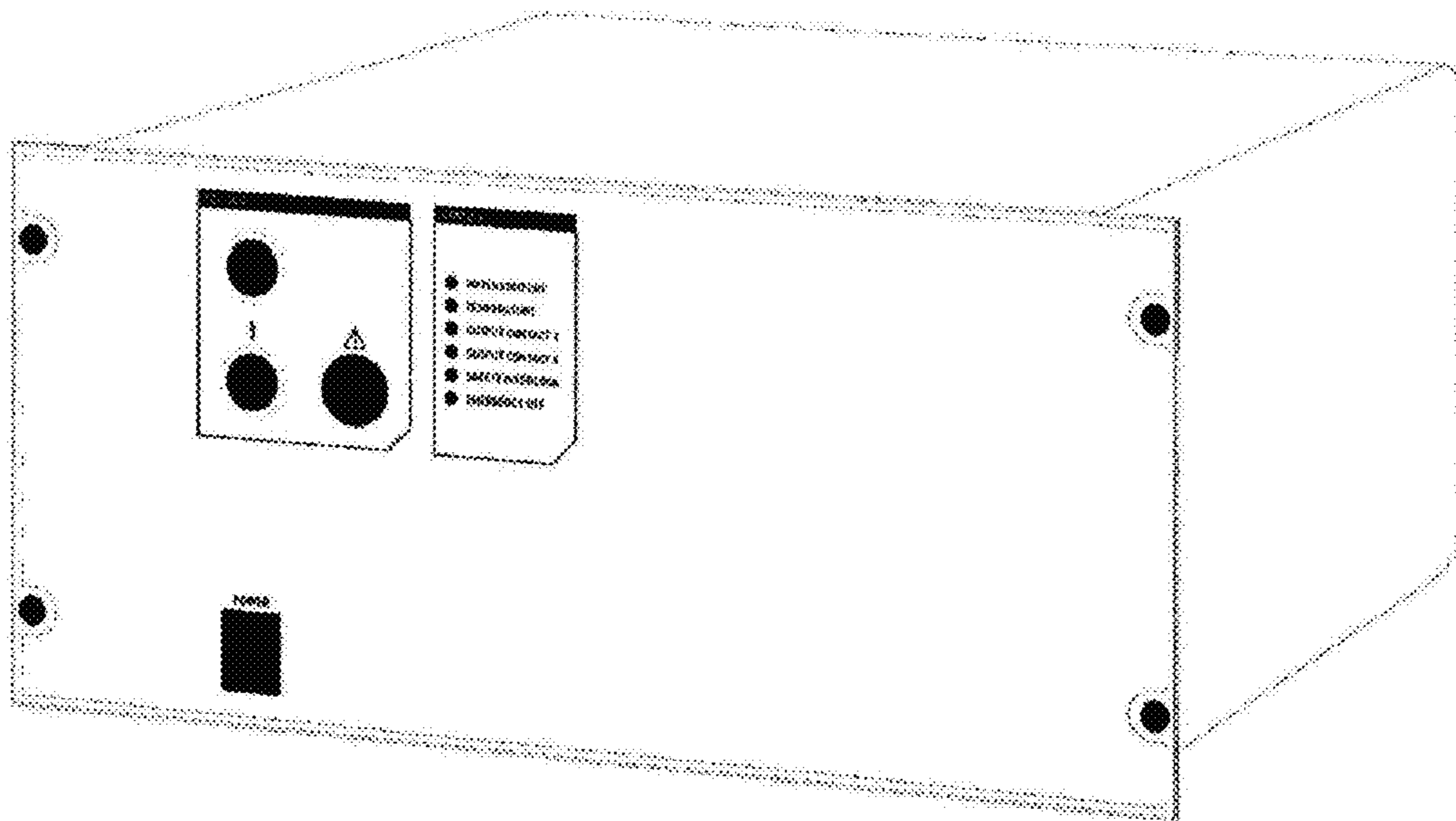
FIG. 1 shows a perspective view of a first design with elements being grouped in a plurality of groups, each of the groups being surrounded by a rectangular frame having a lower right corner that has a 45° edge.

FIG. 2 shows a second embodiment of a front view of the design of FIG. 1; and,

FIG. 3 shows a third embodiment of a front view of the design of FIG. 1.

The design relates to test devices for power engineering systems and power engineering components in which an arbitrary number of elements, such as input ports, output ports or status displays, are grouped in a group being surrounded by a polygonal frame. The corners of the polygonal frame are rectangular except for the lower right corner. The lower right corner of the polygonal frame has a 45° edge. Each group may have a group label in a frame which is arranged directly above the group and has the same width.

1 Claim, 3 Drawing Sheets



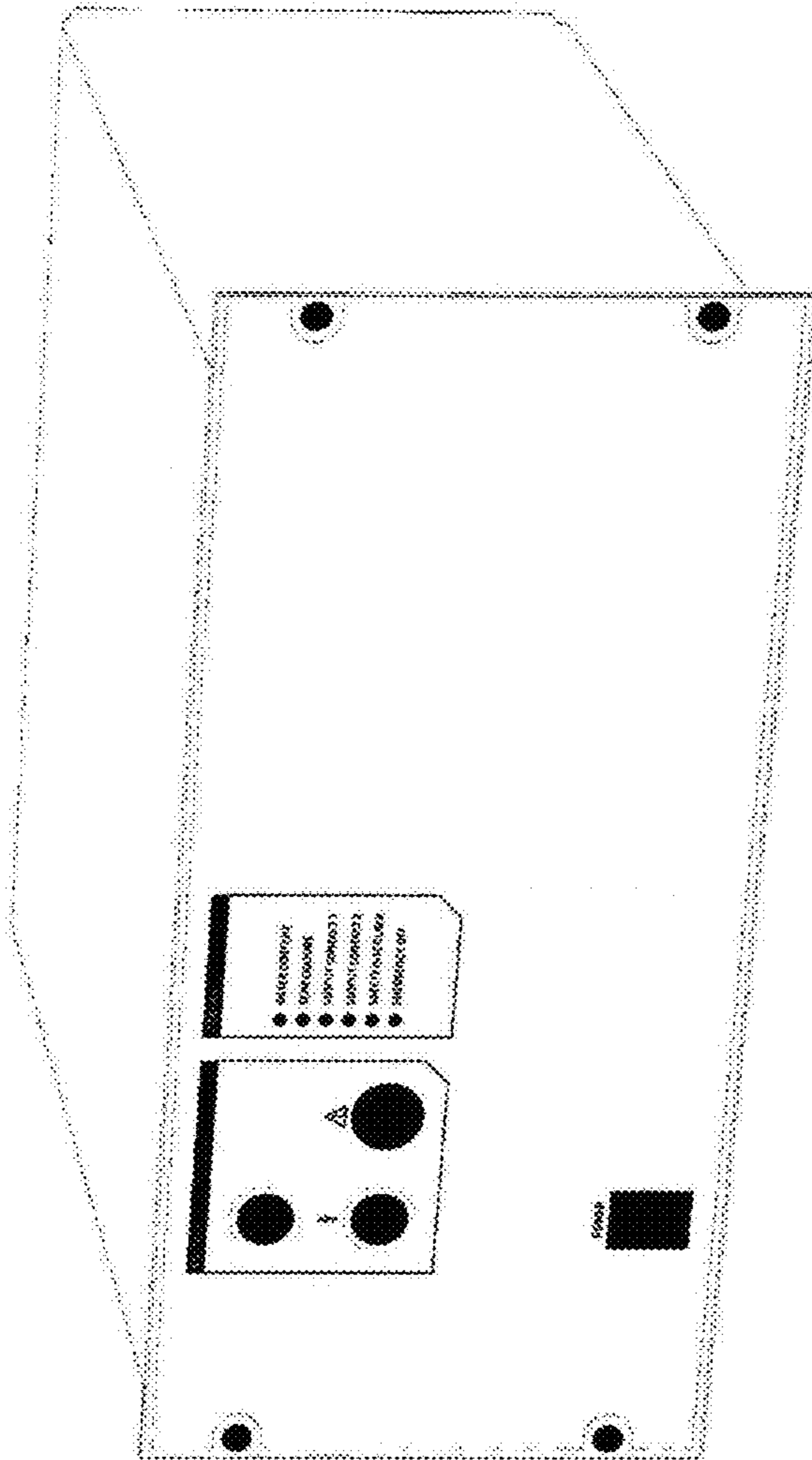


FIG. 1

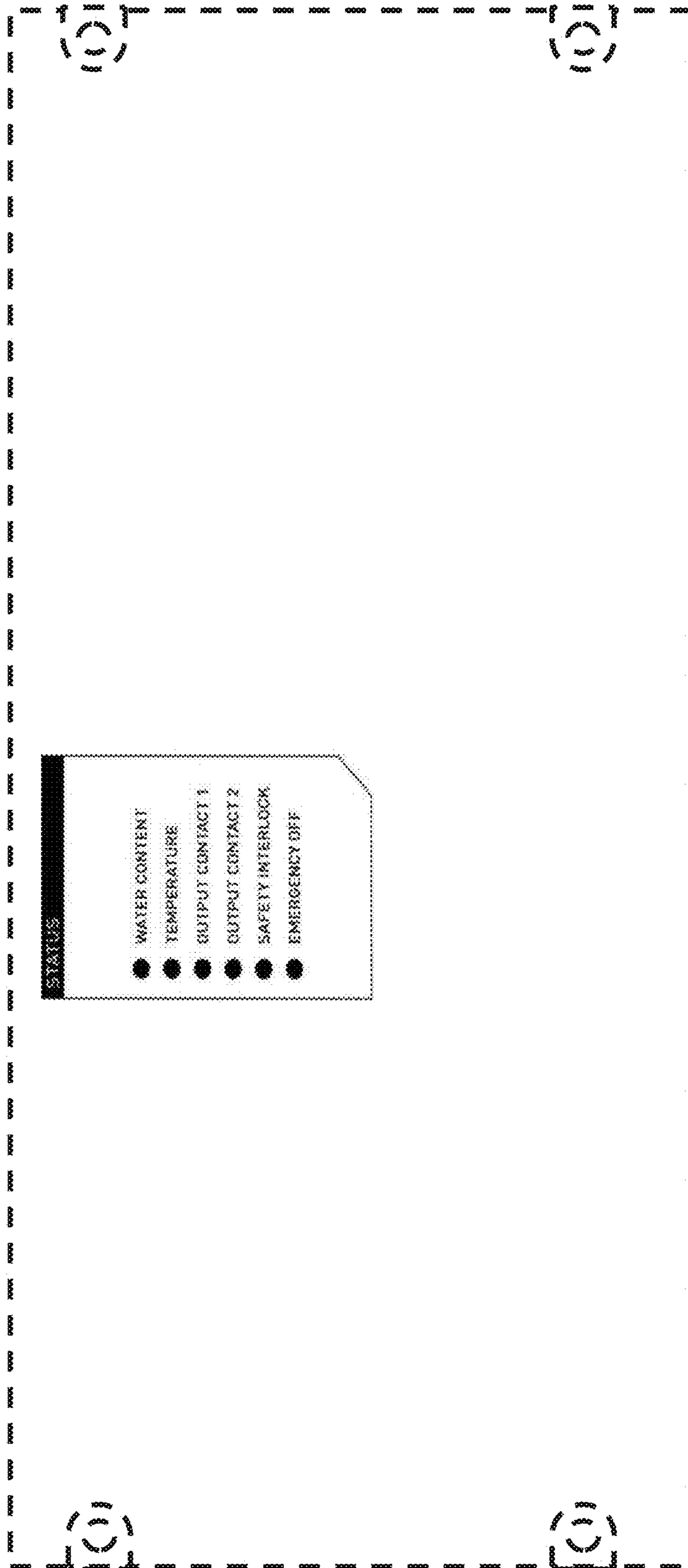


FIG. 2

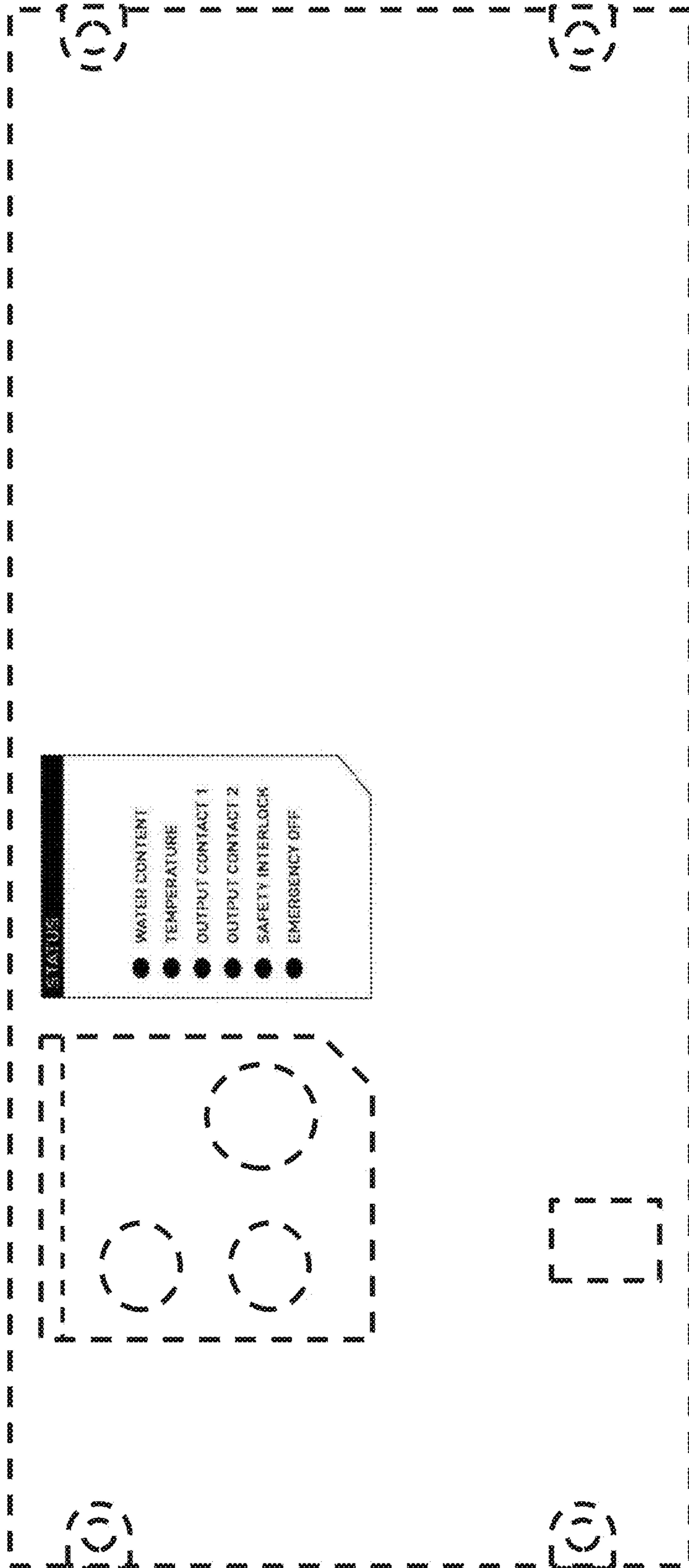


FIG. 3