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(12) **United States Design Patent**  
**Vosch et al.**

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(45) **Date of Patent:** **\*\* Dec. 16, 2014**

- (54) **ELECTRODE PATCH ARRAY**
- (71) Applicant: **Tesseract Sensors, LLC**, Sanford, FL (US)
- (72) Inventors: **Michael J. Vosch**, Titusville, FL (US);  
**Benjamin M. Downey**, Sanford, FL (US)
- (73) Assignee: **Tesseract Sensors, LLC**, Sanford, FL (US)
- (\*\*) Term: **14 Years**
- (21) Appl. No.: **29/493,061**
- (22) Filed: **Jun. 5, 2014**

**Related U.S. Application Data**

- (63) Continuation-in-part of application No. 29/449,876, filed on Mar. 15, 2013.
- (51) **LOC (10) Cl.** ..... **24-01**
- (52) **U.S. Cl.**  
USPC ..... **D24/187**
- (58) **Field of Classification Search**  
USPC ..... D24/168, 186–187, 200, 232;  
600/372–386, 390–393, 509; 607/62,  
607/121, 148  
See application file for complete search history.

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*Primary Examiner* — T. Chase Nelson  
*Assistant Examiner* — Mark Cavanna

(74) *Attorney, Agent, or Firm* — Olive Law Group, PLLC

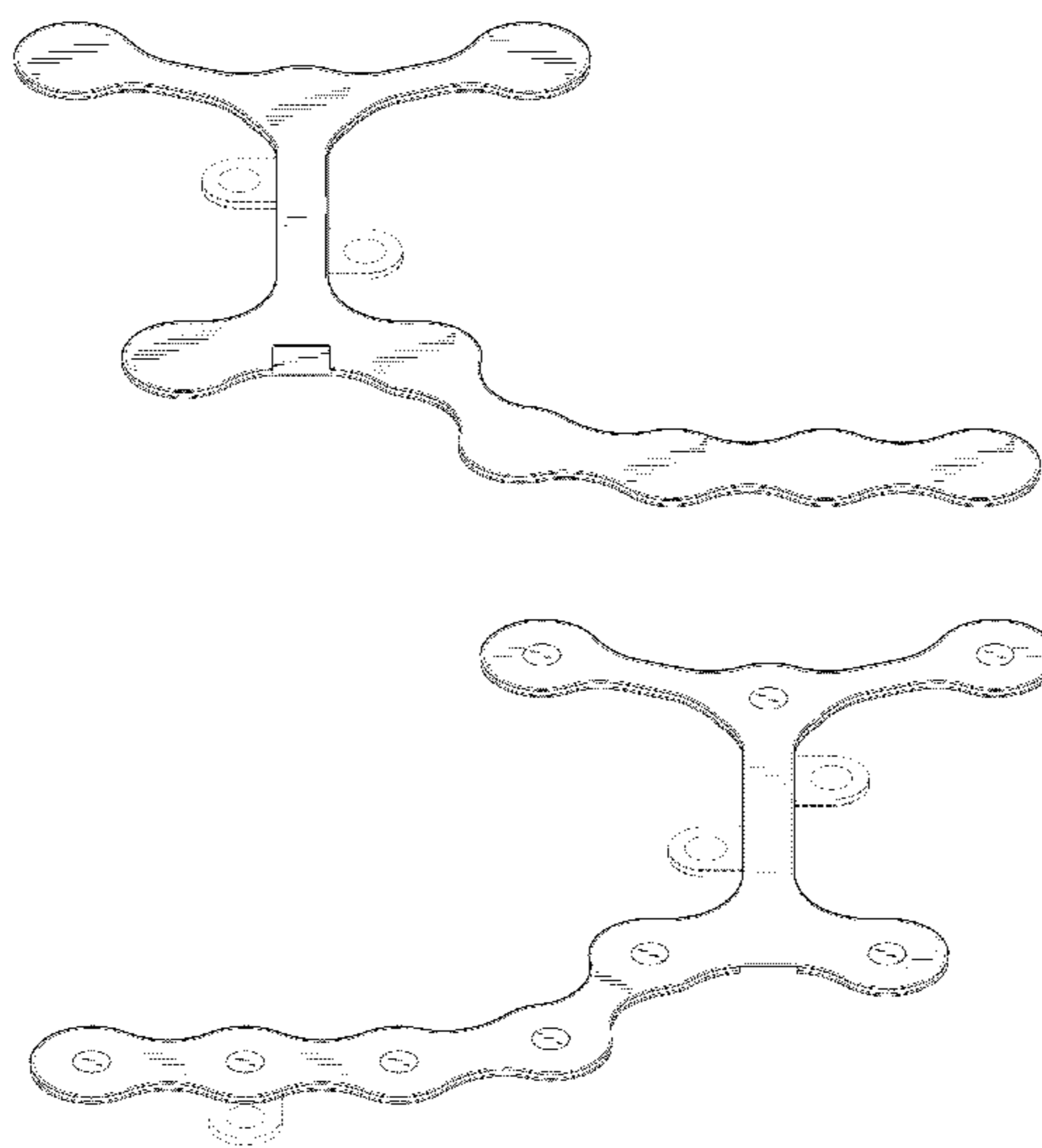
(57) **CLAIM**

We claim the ornamental design for the electrode patch array, as shown and described.

**DESCRIPTION**

FIG. 1 is a top view of an electrode patch array showing our new design;  
 FIG. 2 is a bottom view thereof;  
 FIG. 3 is a side view thereof;  
 FIG. 4 is a side view that opposed the side view of FIG. 3 thereof;  
 FIG. 5 is a front view thereof;  
 FIG. 6 is a back view thereof;  
 FIG. 7 is a top perspective view thereof; and,  
 FIG. 8 is a bottom perspective view thereof.  
 The broken lines are included for the purpose of illustrating portions of the electrode patch array that form no part of the claimed design.

**1 Claim, 3 Drawing Sheets**



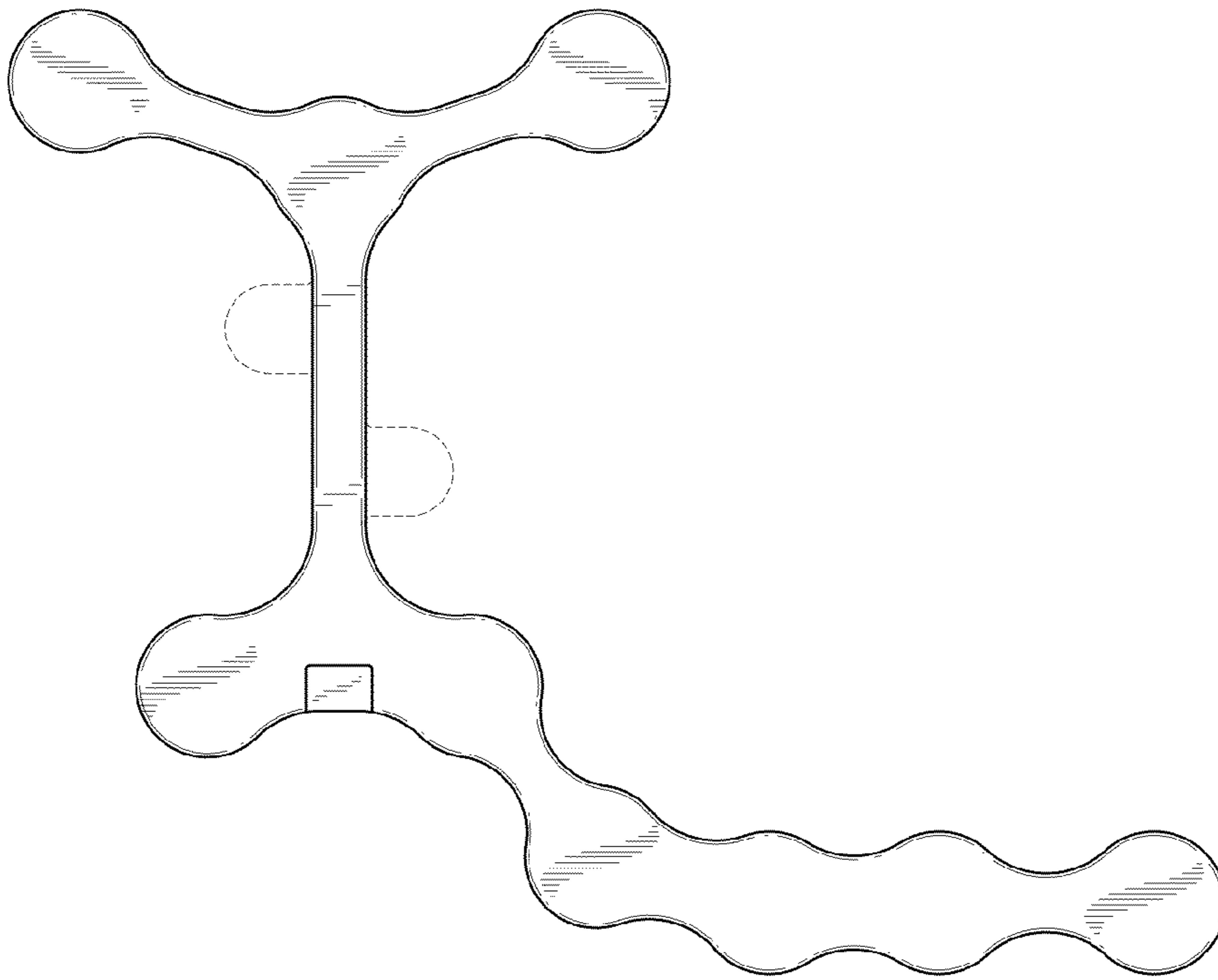


FIG. 1

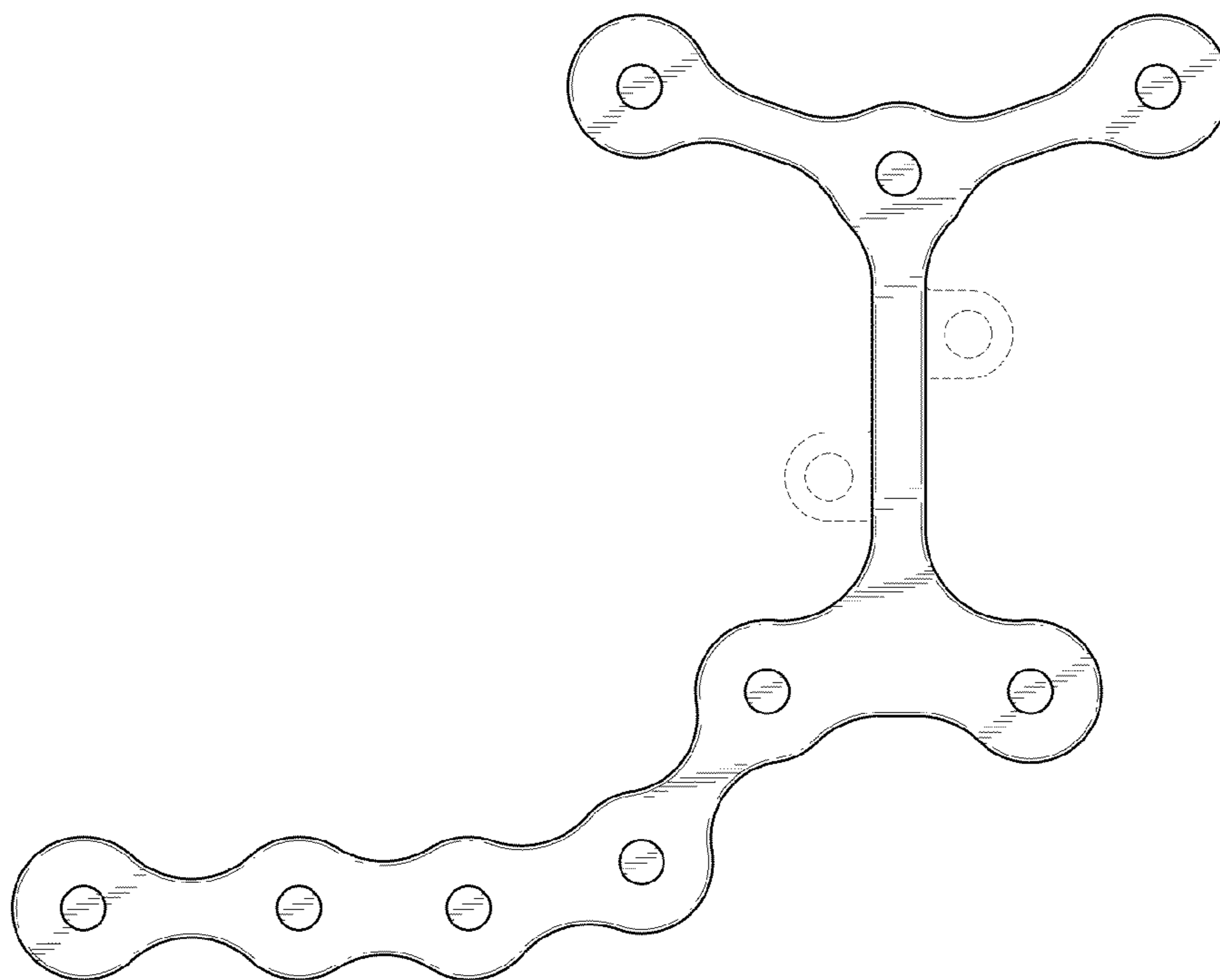


FIG. 2

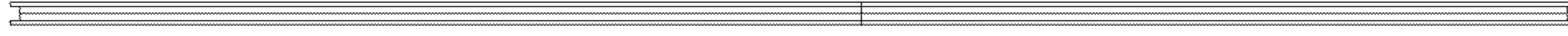


FIG. 3

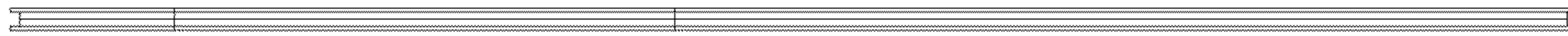


FIG. 4

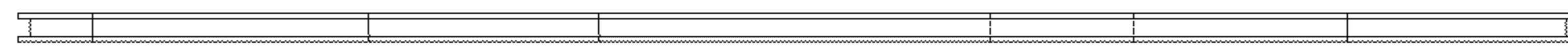


FIG. 5

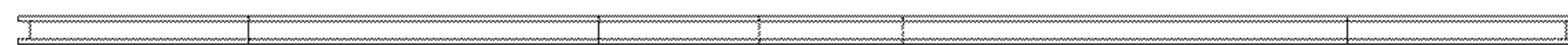


FIG. 6

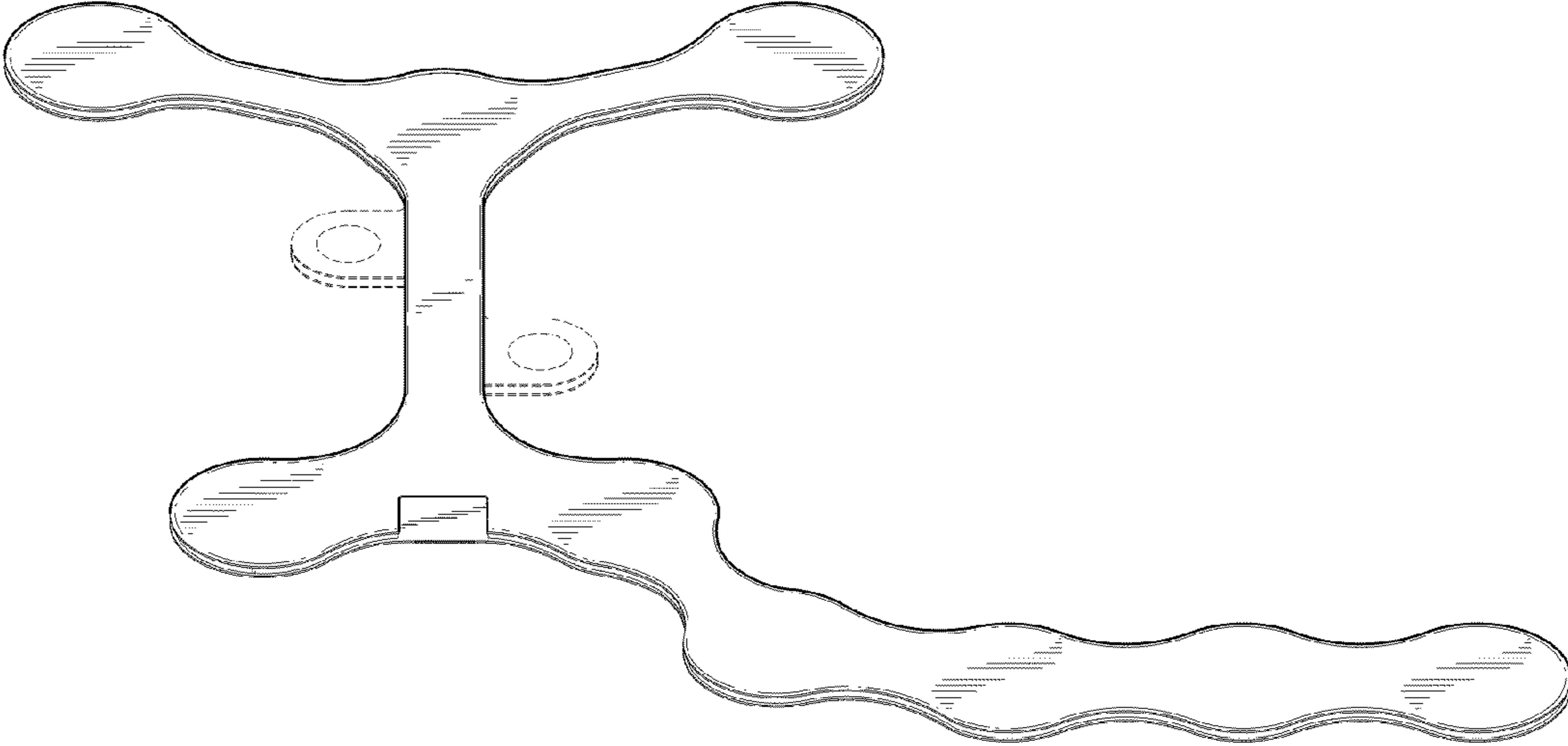


FIG. 7

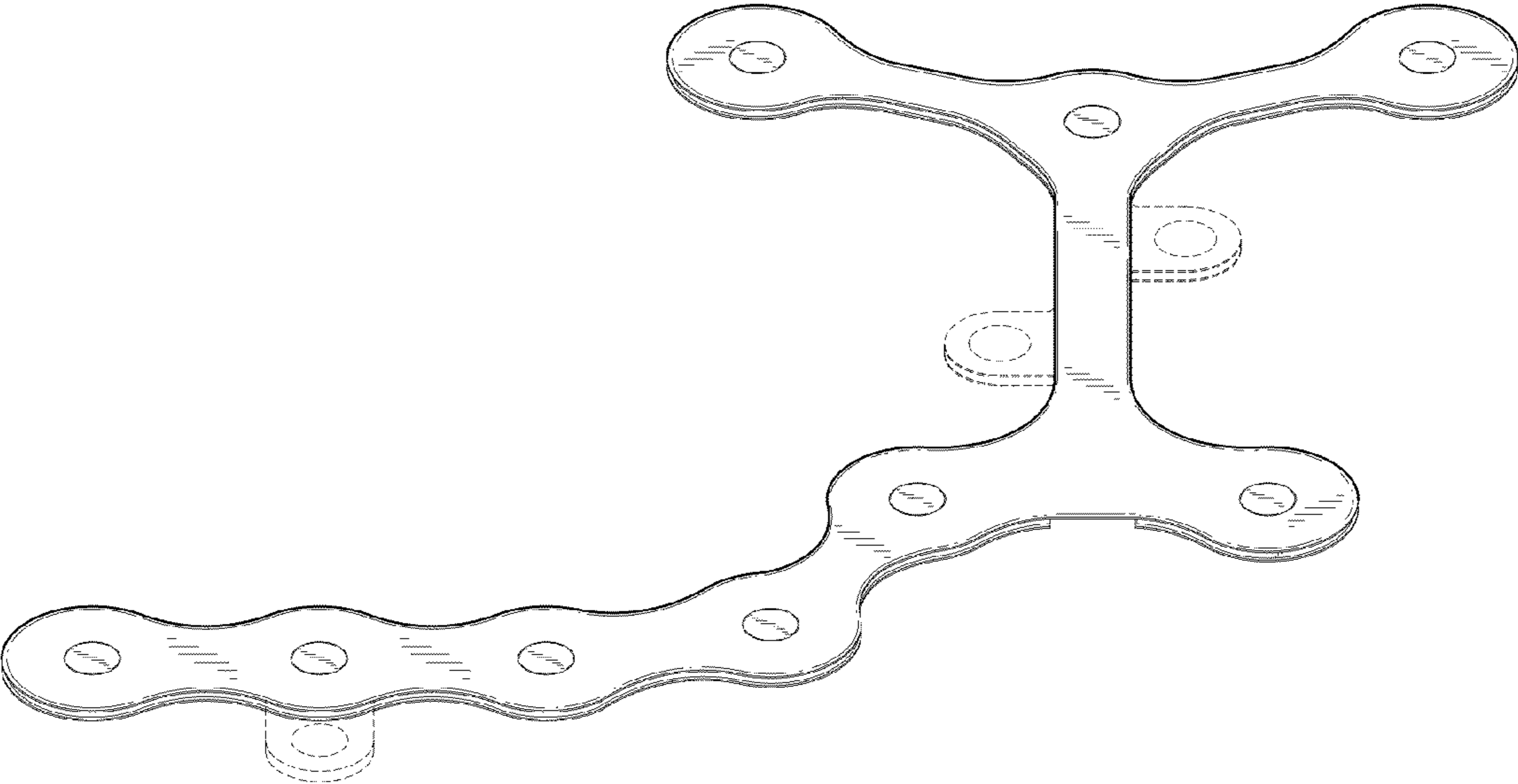


FIG. 8