



US00D719511S

(12) **United States Design Patent**
Dodal et al.

(10) **Patent No.:** **US D719,511 S**
(45) **Date of Patent:** **** Dec. 16, 2014**

- (54) **ELECTRICAL RECEPTACLE**
- (71) Applicant: **Cooper Technologies Company**,
Houston, TX (US)
- (72) Inventors: **Rohit Sumerchand Dodal**, Peachtree
City, GA (US); **Carlos Eduardo**
Restrepo, Atlanta, GA (US); **Alex**
Zhuang, Shanghai (CN); **Edward Gu**,
Shanghai (CN)
- (73) Assignee: **Cooper Technologies Company**,
Houston, TX (US)

- D531,121 S 10/2006 Fort et al.
- D535,256 S 1/2007 Fort et al.
- D536,305 S 2/2007 Fort et al.
- D542,223 S 5/2007 Bazayev et al.
- D545,272 S 6/2007 Zhang et al.
- D558,676 S 1/2008 Fort et al.
- D568,817 S * 5/2008 Yu D13/139.7
- D573,952 S * 7/2008 Schwartz D13/139.1
- D598,859 S 8/2009 Vaccaro et al.
- D601,962 S 10/2009 Song
- D604,873 S 11/2009 Richter et al.
- D674,753 S 1/2013 Jansen et al.
- D679,256 S * 4/2013 Ericksen et al. D13/156

(Continued)

- (**) Term: **14 Years**
- (21) Appl. No.: **29/468,297**
- (22) Filed: **Sep. 27, 2013**

OTHER PUBLICATIONS

<http://www.pilotmedia.us/wordpress/admin/features/rapid-usb-charger-ready-to-come-aboard>; Feb. 18, 2013.

(Continued)

Related U.S. Application Data

- (62) Division of application No. 29/399,151, filed on Aug. 10, 2011, now Pat. No. Des. 703,139.
- (51) **LOC (10) Cl.** **13-03**
- (52) **U.S. Cl.**
USPC **D13/139.1**
- (58) **Field of Classification Search**
USPC D13/139.1, 139.3, 108, 152, 156, 154,
D13/137.1, 137.2, 137.3, 138.1, 138.2,
D13/139.7, 130, 160, 199; 174/53, 50, 58,
174/55, 66-68; 439/107, 108, 135, 136,
439/620.15; 307/11; 361/692, 752
See application file for complete search history.

Primary Examiner — Ian Simmons
Assistant Examiner — Rhea Shields
(74) *Attorney, Agent, or Firm* — King & Spalding LLP

(57) **CLAIM**
The ornamental design for an electrical receptacle, as shown and described.

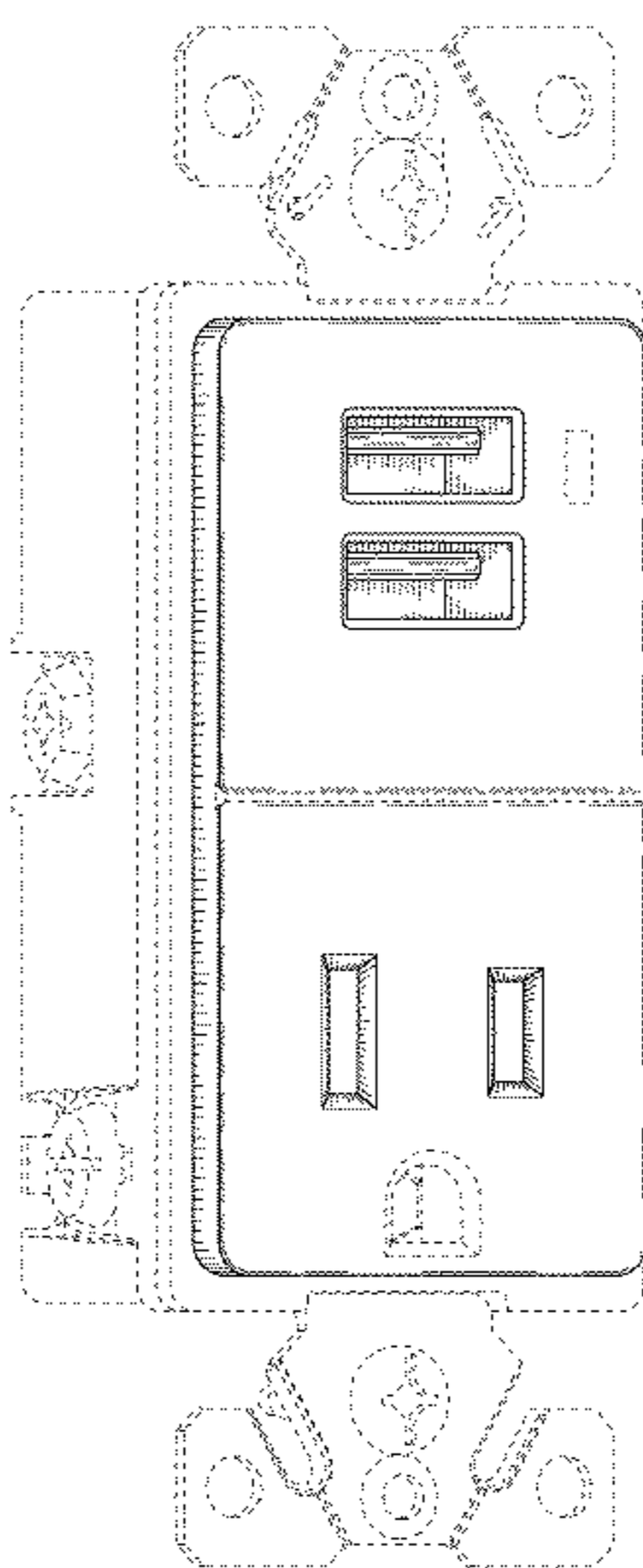
DESCRIPTION

FIG. 1 is a perspective view of an electrical receptacle, showing our new design;
FIG. 2 is a front elevation view thereof;
FIG. 3 is a rear elevation view thereof;
FIG. 4 is a right-side elevation view thereof;
FIG. 5 is a left-side elevation view thereof; and,
FIG. 6 is a top plan view thereof, with the bottom plan view being a mirror image of FIG. 6.
The broken lines shown represent unclaimed subject matter and form no part of the claimed design.

- (56) **References Cited**
U.S. PATENT DOCUMENTS

- D429,694 S * 8/2000 Schlessinger et al. D13/154
- D430,539 S 9/2000 Leopold et al.
- D432,990 S * 10/2000 Stekelenburg D13/137.3
- D462,660 S 9/2002 Huang et al.

1 Claim, 4 Drawing Sheets



(56)

References Cited

U.S. PATENT DOCUMENTS

D680,953 S * 4/2013 Kuo D13/137.2
D693,305 S * 11/2013 Kuo et al. D13/137.2
D694,706 S * 12/2013 Kuo D13/108
D694,713 S * 12/2013 Yeo et al. D13/138.1
D695,693 S * 12/2013 Lee et al. D13/152
D700,892 S * 3/2014 Segnit D13/137.2
D702,640 S * 4/2014 Restrepo et al. D13/139.1
D703,139 S * 4/2014 Dodal et al. D13/139.1

8,696,368 B2 * 4/2014 Quezada 439/108
D705,170 S * 5/2014 Diehl et al. D13/137.1
8,758,031 B2 * 6/2014 Cheng et al. 439/107

OTHER PUBLICATIONS

<http://www.slashgear.com/truepower-outlet-packs-usb-ports-and-ac-for-just-9-95-0465164>; Dec. 4, 2009.
<http://www.virginiasweetpea.com/2013/01/how-to-add-a-usb-port-to-a-wall-outlet.html>; Jan. 16, 2013.

* cited by examiner

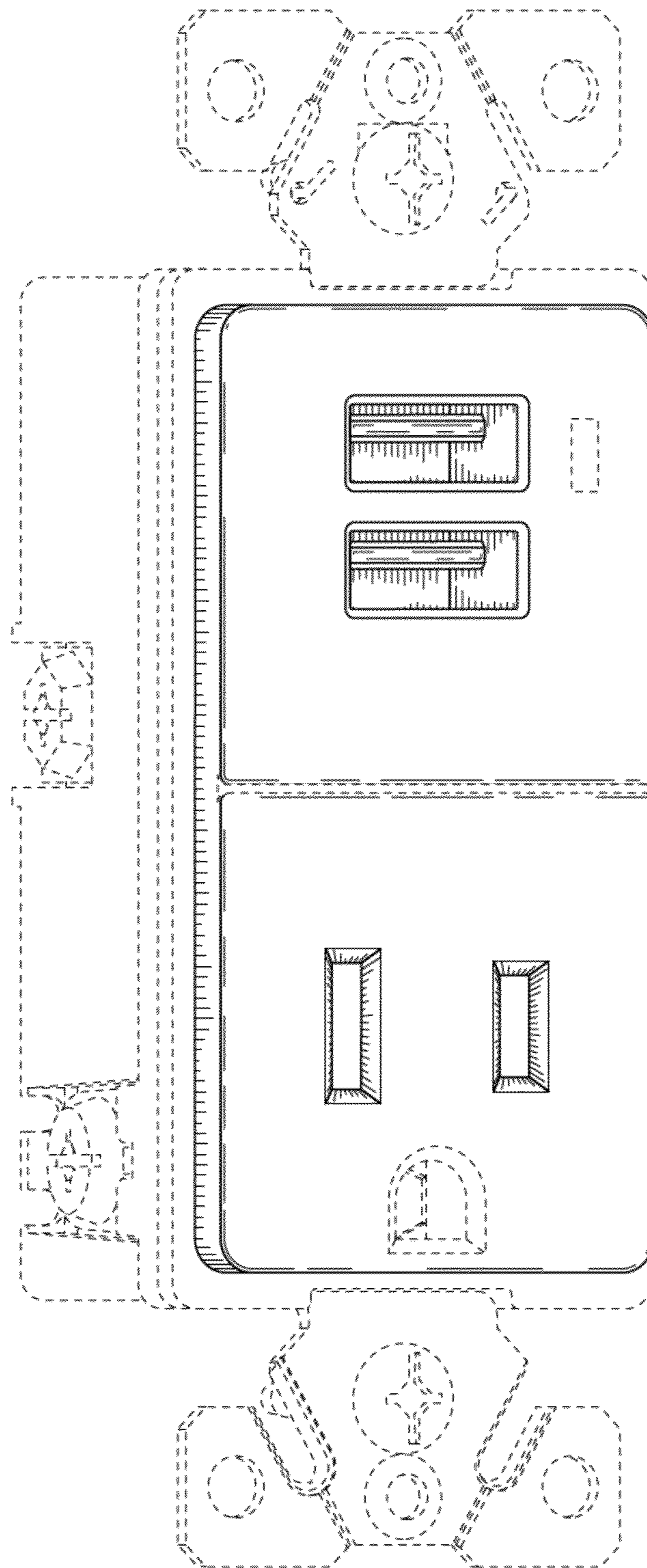


FIG. 1

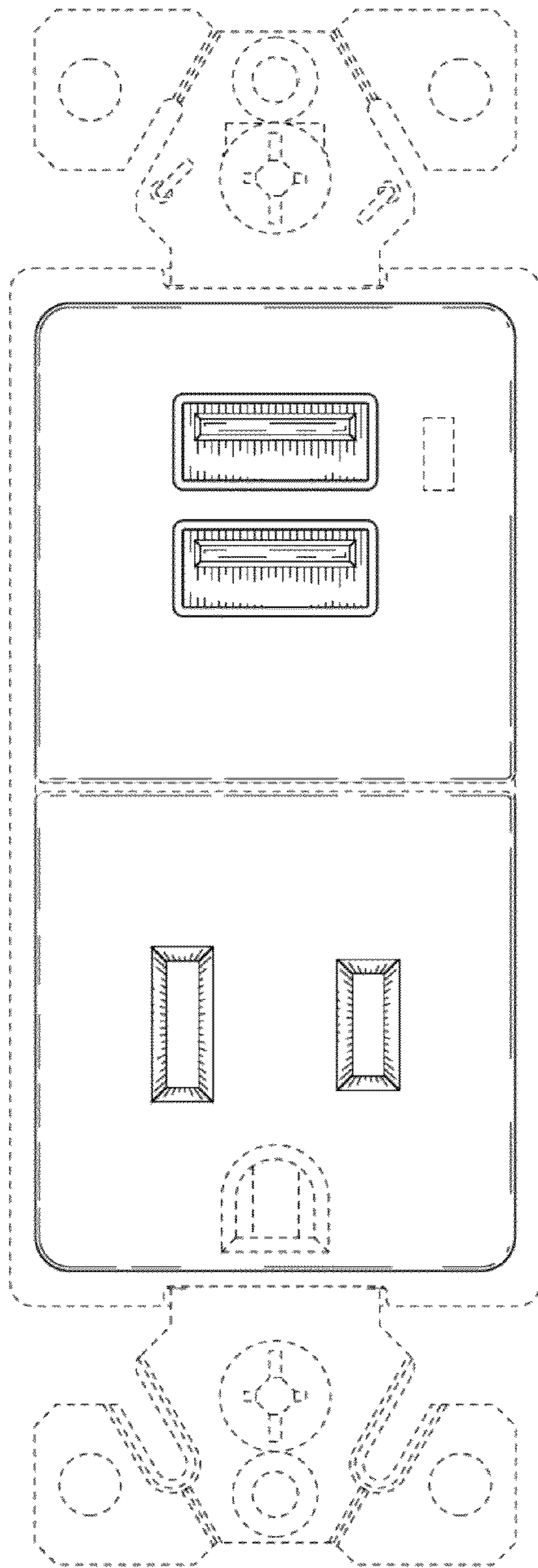


FIG. 2

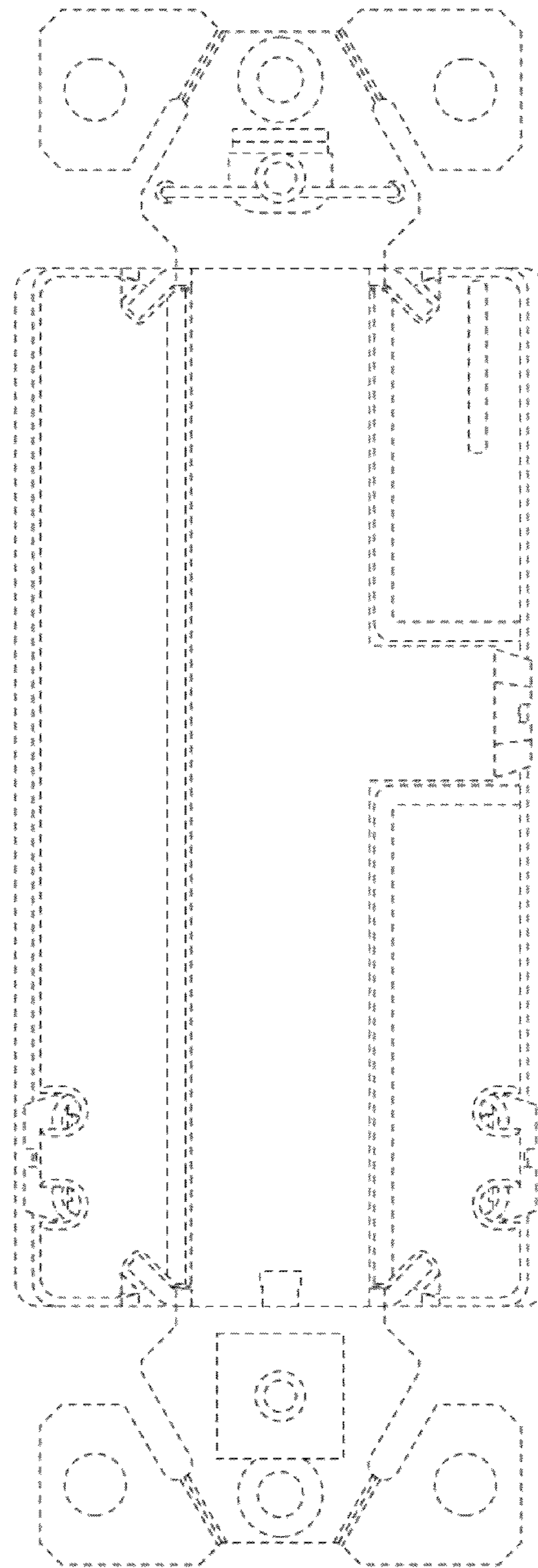


FIG. 3

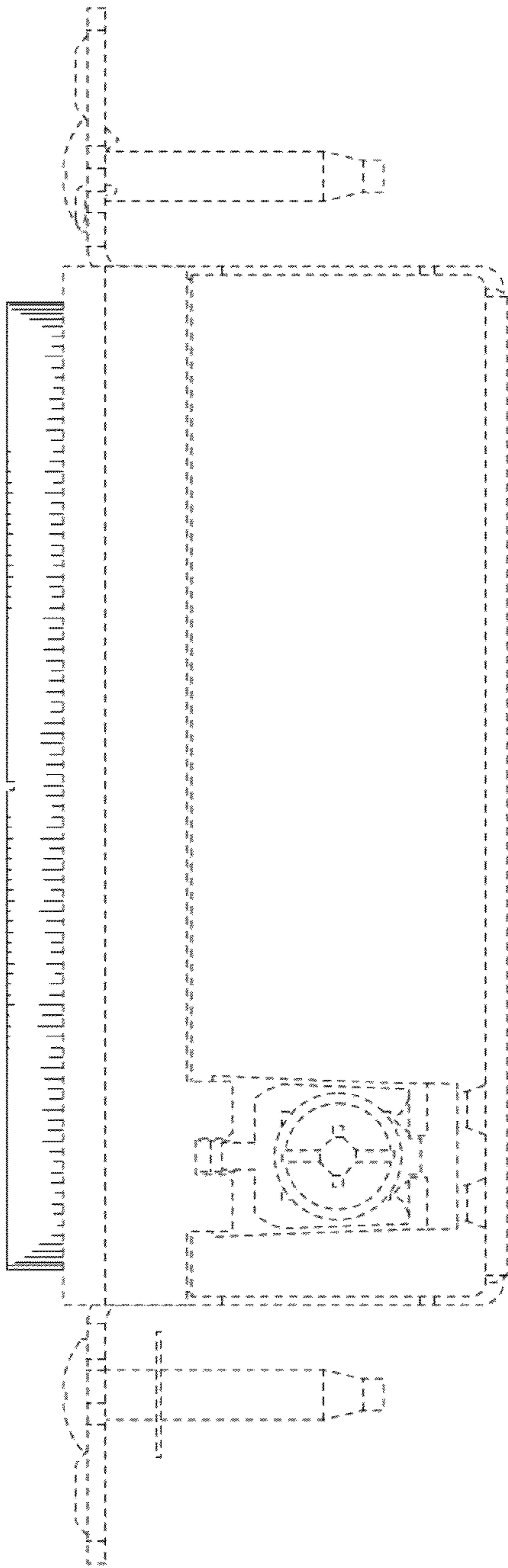


FIG. 4

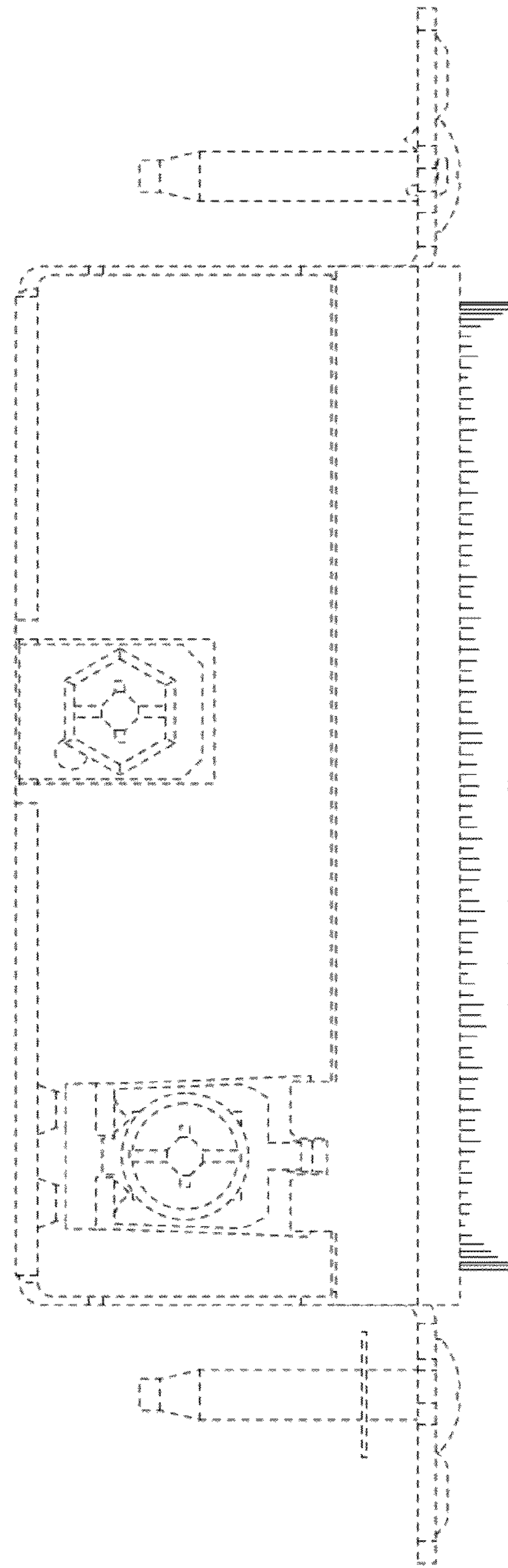


FIG. 5

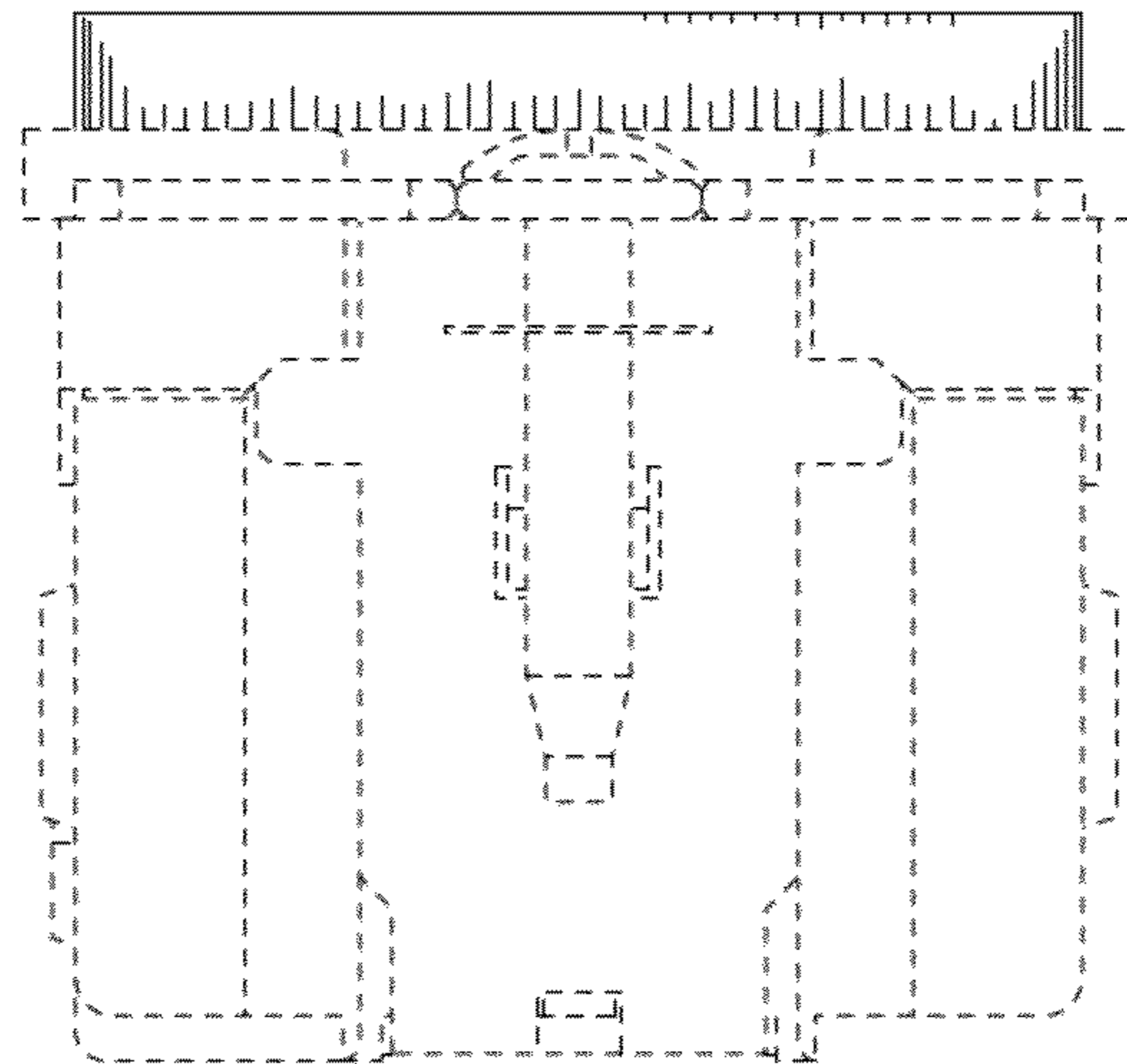


FIG. 6