



US00D719091S

(12) **United States Design Patent**
Leddusire

(10) **Patent No.:** **US D719,091 S**

(45) **Date of Patent:** **** Dec. 9, 2014**

(54) **ELECTRICAL OUTLET ASSEMBLY**

(56) **References Cited**

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U.S. PATENT DOCUMENTS

(72) Inventor: **Ronald R. Leddusire**, Seattle, WA (US)

D436,919	S	*	1/2001	Wakefield et al.	D13/110
D492,253	S	*	6/2004	Lee	D13/137.2
D558,143	S	*	12/2007	Hsu	D13/110
D561,694	S	*	2/2008	Lam	D13/137.1
D575,730	S	*	8/2008	Hung	D13/110
7,488,203	B2		2/2009	Leddusire		
D624,495	S	*	9/2010	Shum et al.	D13/110

(**) Term: **14 Years**

* cited by examiner

(21) Appl. No.: **29/463,388**

(22) Filed: **Nov. 6, 2013**

Primary Examiner — Derrick Holland

(51) **LOC (10) Cl.** **13-02**

(57) **CLAIM**

(52) **U.S. Cl.**

The ornamental design for an electrical outlet assembly, as shown and described.

USPC **D13/110**

DESCRIPTION

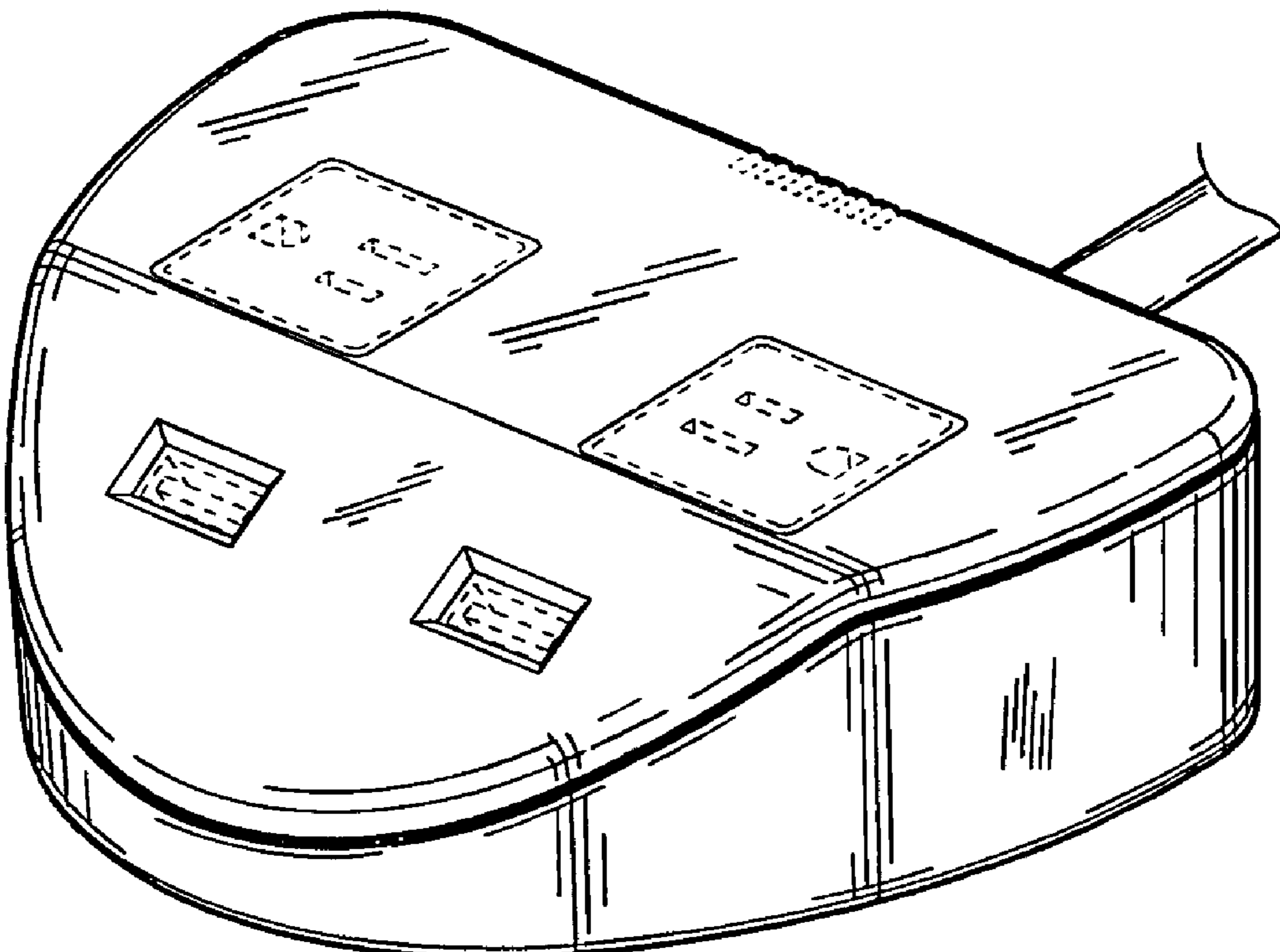
(58) **Field of Classification Search**

USPC D13/110, 103, 107, 108, 118, 119, 123,
D13/133, 137.1, 139.4, 184, 199; D14/240,
D14/251, 356, 432, 433, 434; 174/53, 54,
174/59, 61; 307/11, 150, 151; 320/107,
320/111, 112, 113, 114, 115; 361/679.01;
363/142, 146; 439/131, 172, 626, 628,
439/638

FIG. 1 is a perspective view of an electrical outlet assembly showing my new design;
FIG. 2 is a front elevational view thereof;
FIG. 3 is a right side view;
FIG. 4 is a left side view;
FIG. 5 is a rear elevation view thereof;
FIG. 6 is a top plan view thereof; and,
FIG. 7 is a bottom plan view thereof.
The broken lines shown represent unclaimed subject matter and form no part of the claimed design.

See application file for complete search history.

1 Claim, 7 Drawing Sheets



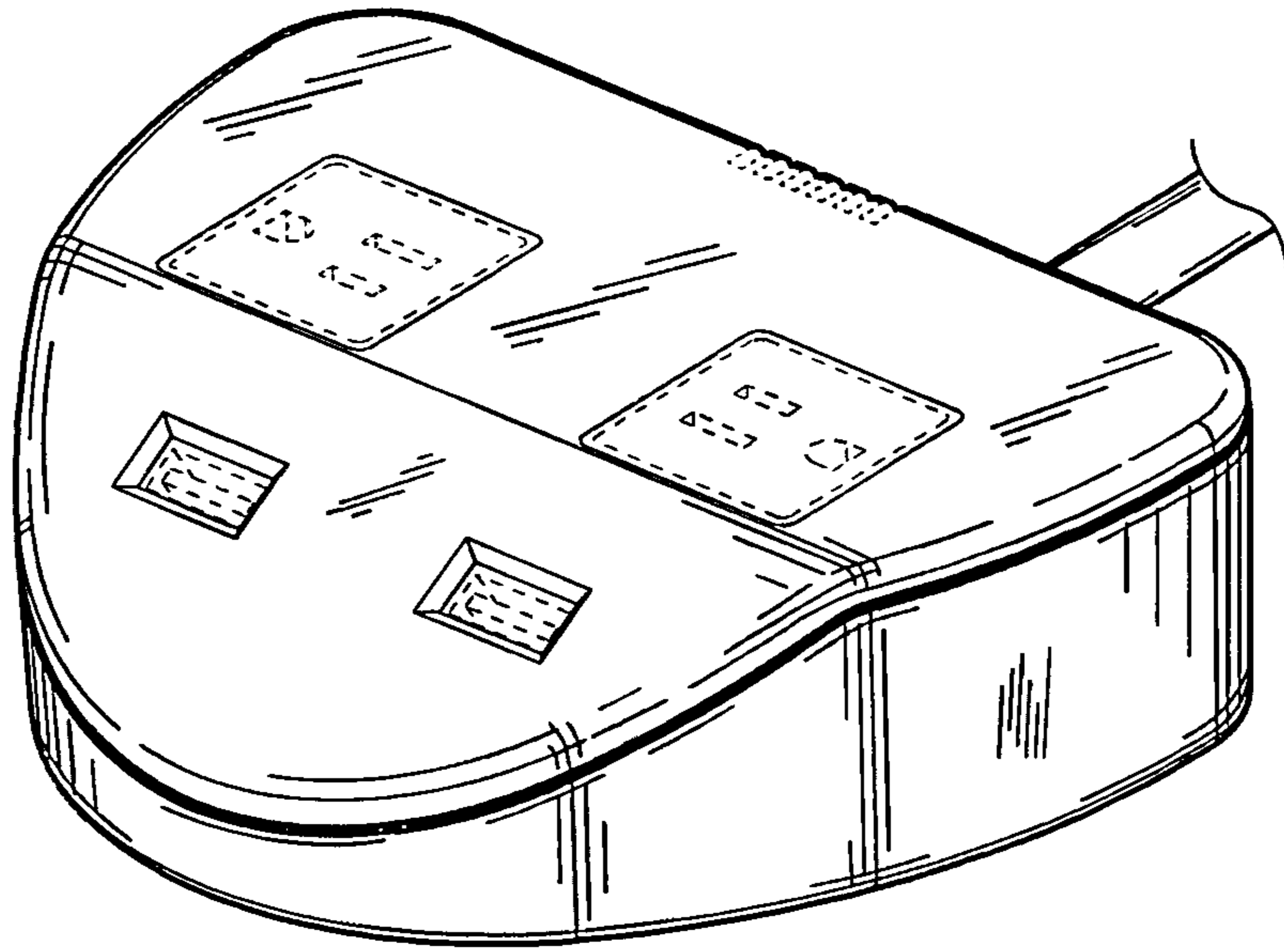


FIG. 1

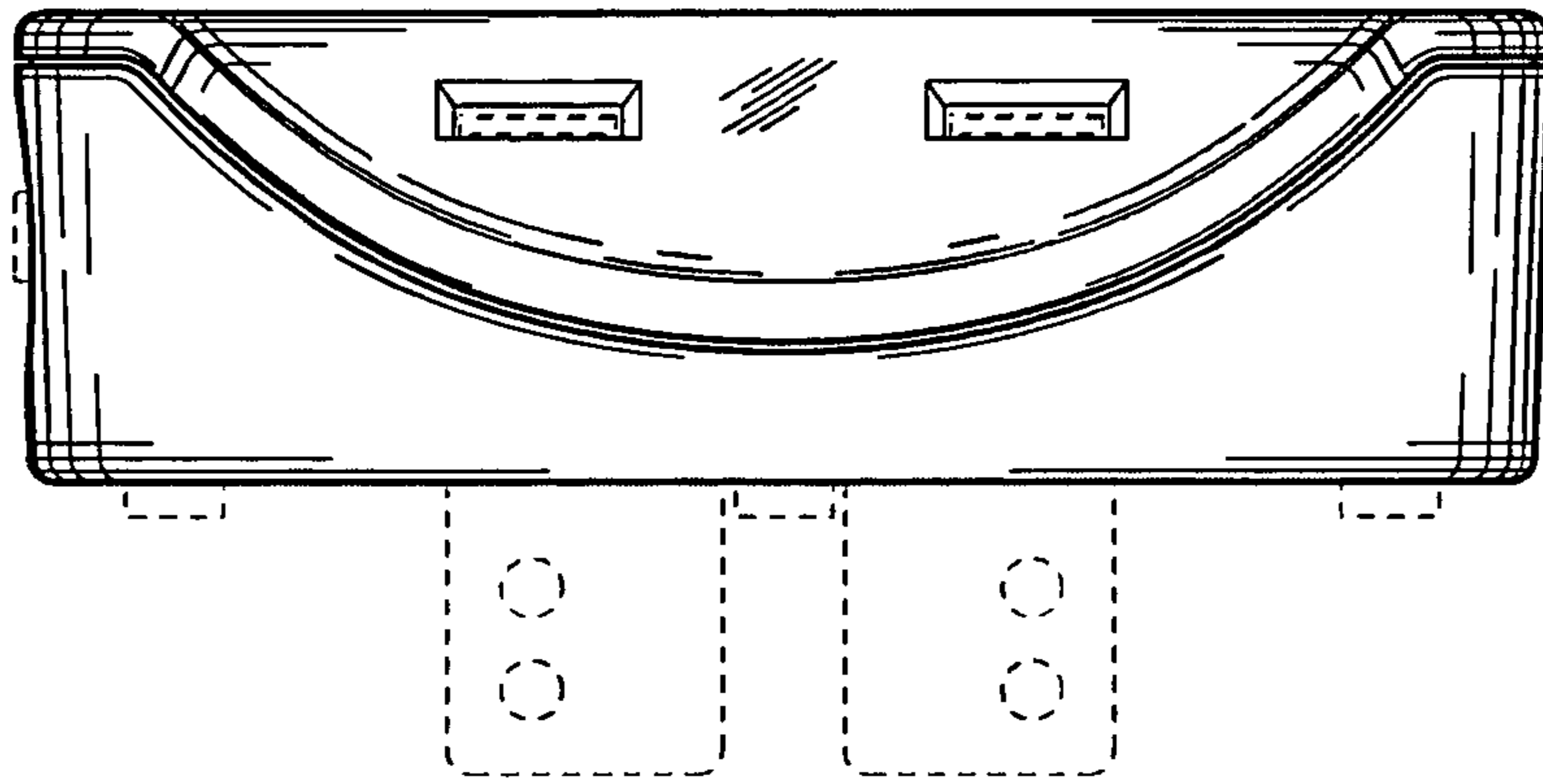


FIG. 2

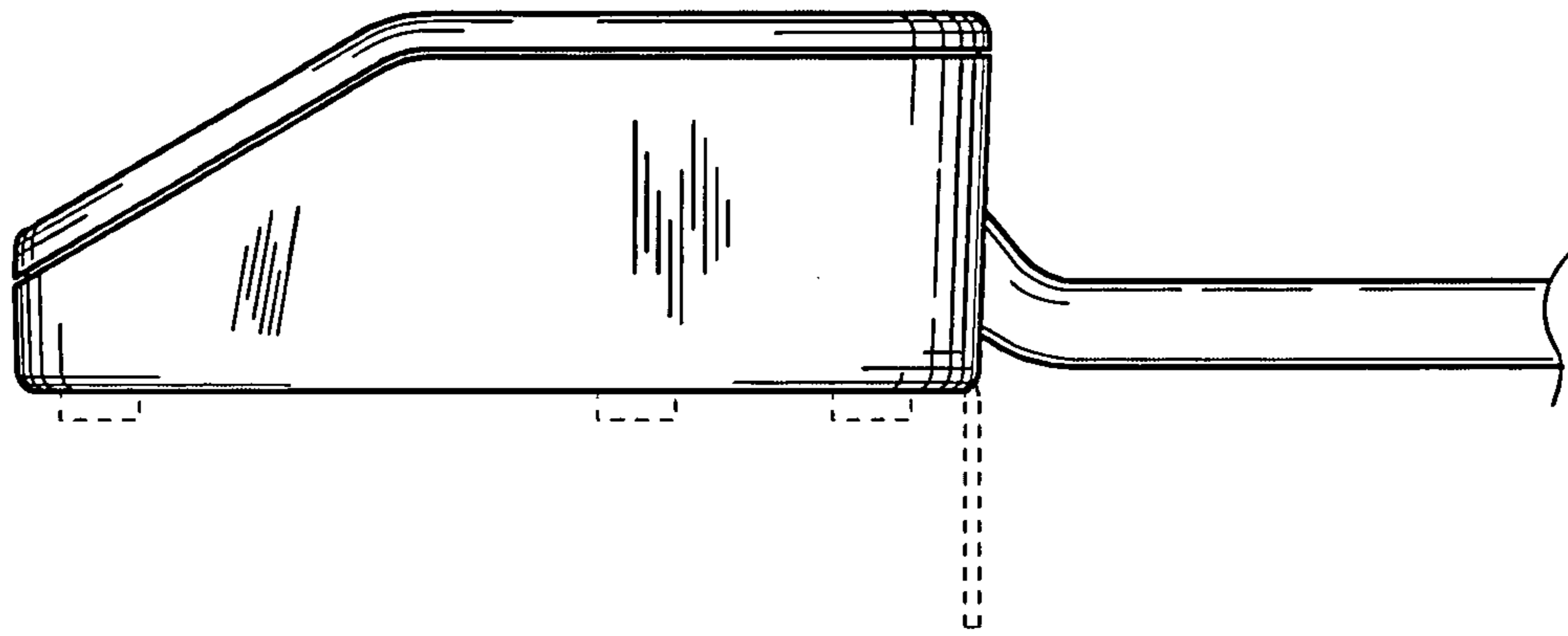


FIG. 3

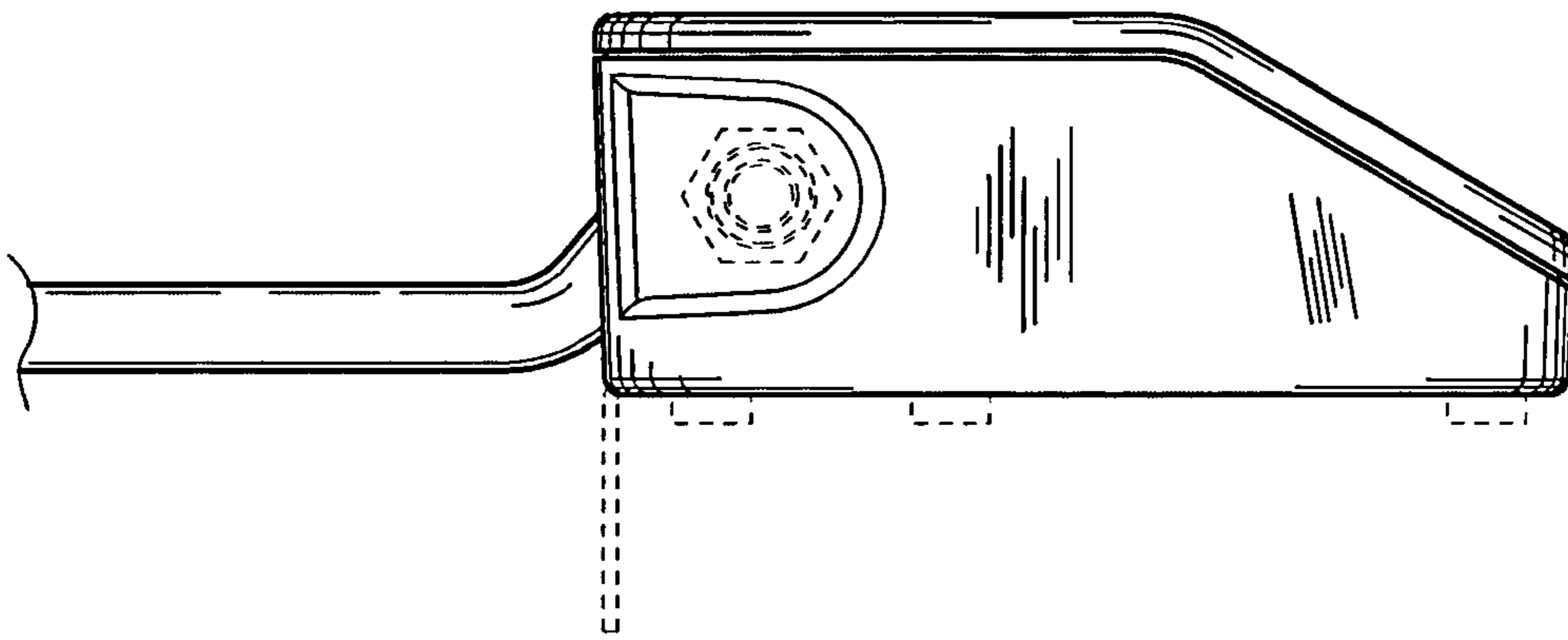


FIG. 4

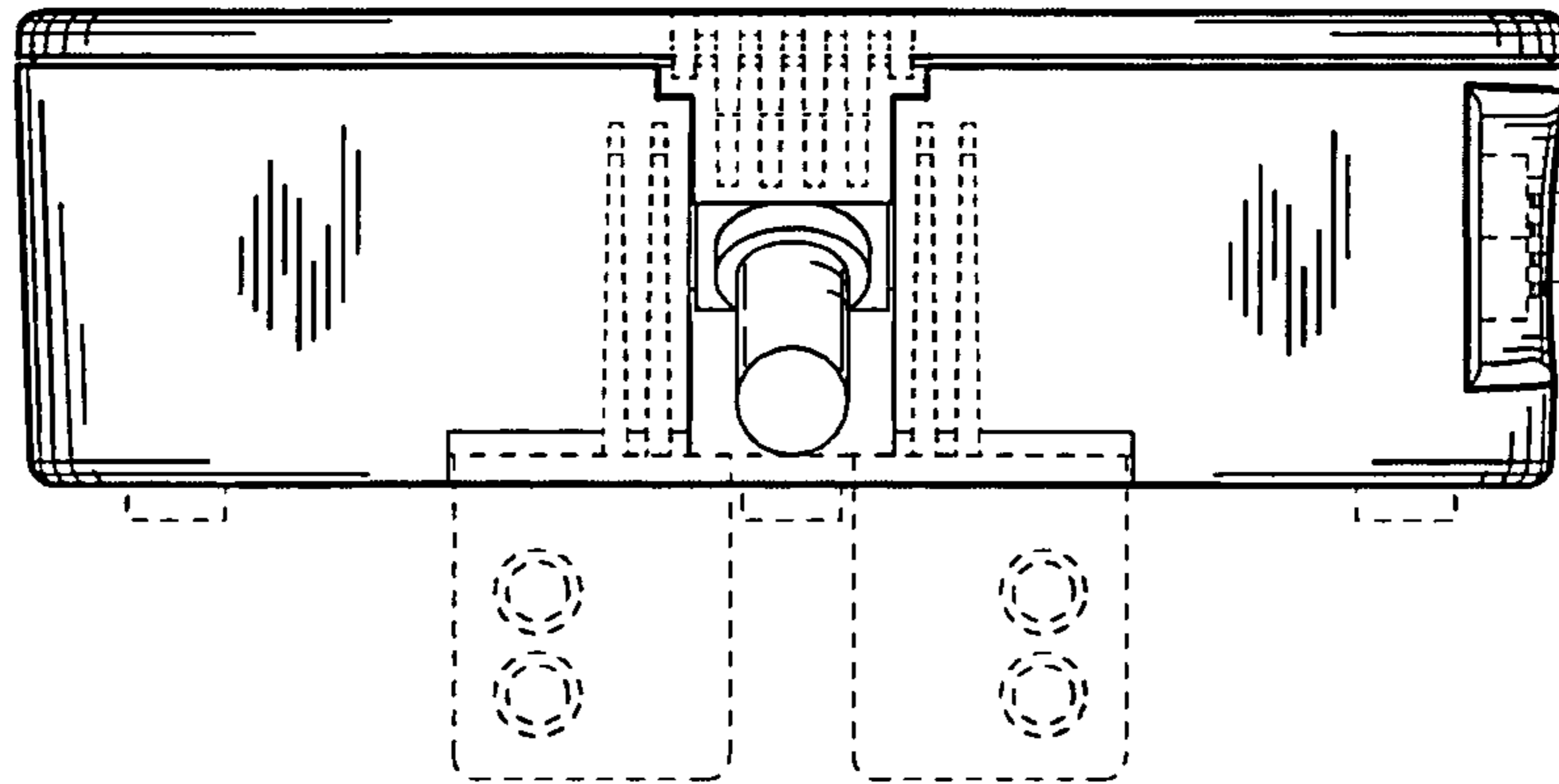


FIG. 5

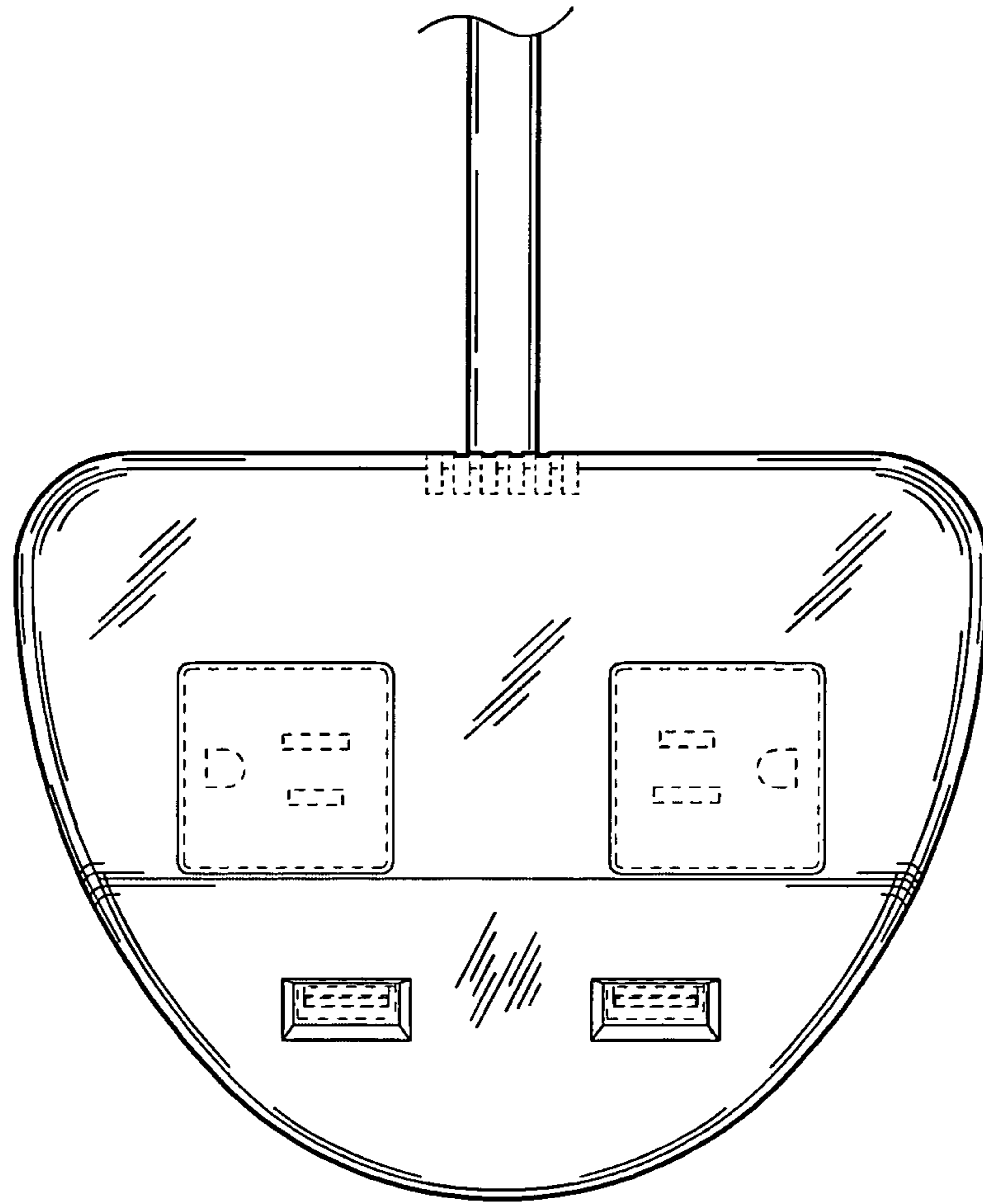


FIG. 6

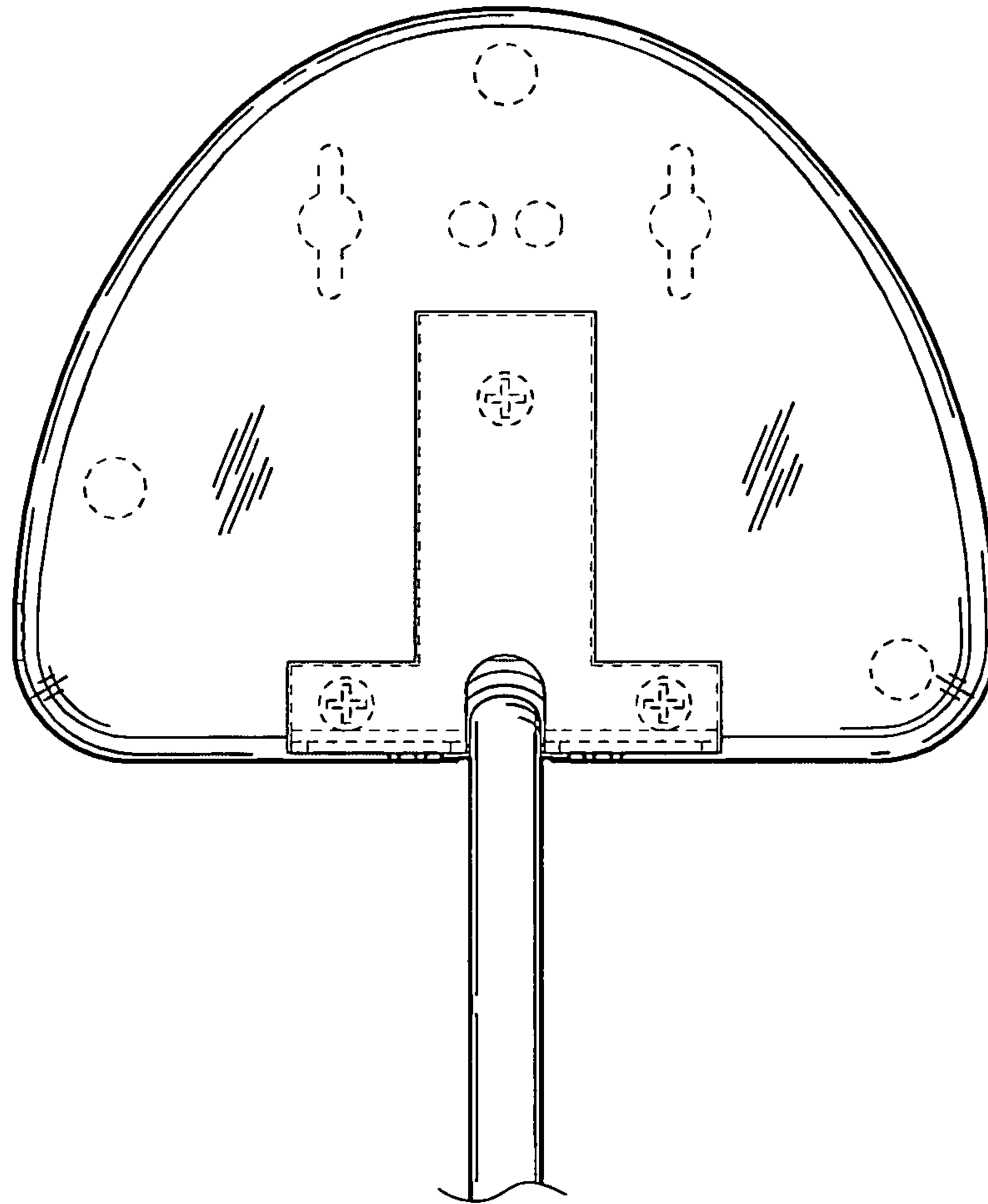


FIG. 7