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(12) **United States Design Patent**  
**Oakley**

(10) **Patent No.:** **US D718,300 S**  
(45) **Date of Patent:** **\*\* Nov. 25, 2014**

- (54) **MULTIPLE MODE DISPLAY DEVICE**
- (71) Applicant: **Intel Corporation**, Santa Clara, CA (US)
- (72) Inventor: **Nicholas W. Oakley**, Portland, OR (US)
- (73) Assignee: **Intel Corporation**, Santa Clara, CA (US)

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- (\*\*) Term: **14 Years**
- (21) Appl. No.: **29/441,011**
- (22) Filed: **Dec. 29, 2012**

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*Primary Examiner* — Freda S Nunn  
(74) *Attorney, Agent, or Firm* — Patent Capital Group

**Related U.S. Application Data**

- (63) Continuation of application No. 13/094,068, filed on Apr. 26, 2011, which is a continuation of application No. 11/588,070, filed on Oct. 24, 2006, now Pat. No. 7,932,894, which is a continuation of application No. 10/185,154, filed on Jun. 27, 2002, now Pat. No. 7,126,588.
- (51) **LOC (10) Cl.** ..... **14-02**
- (52) **U.S. Cl.**  
USPC ..... **D14/315**
- (58) **Field of Classification Search**  
USPC ..... D14/315-327, 336, 341, 371; D18/1, 2, D18/7, 11; 235/145 A, 145 R; 341/22, 23; 345/104, 156, 168, 169, 173; 361/679.06, 679.08, 679.09, 679.11, 361/679.26, 679.27; 312/223.2  
See application file for complete search history.

(57) **CLAIM**

The ornamental design for a multiple mode display device, as shown and described.

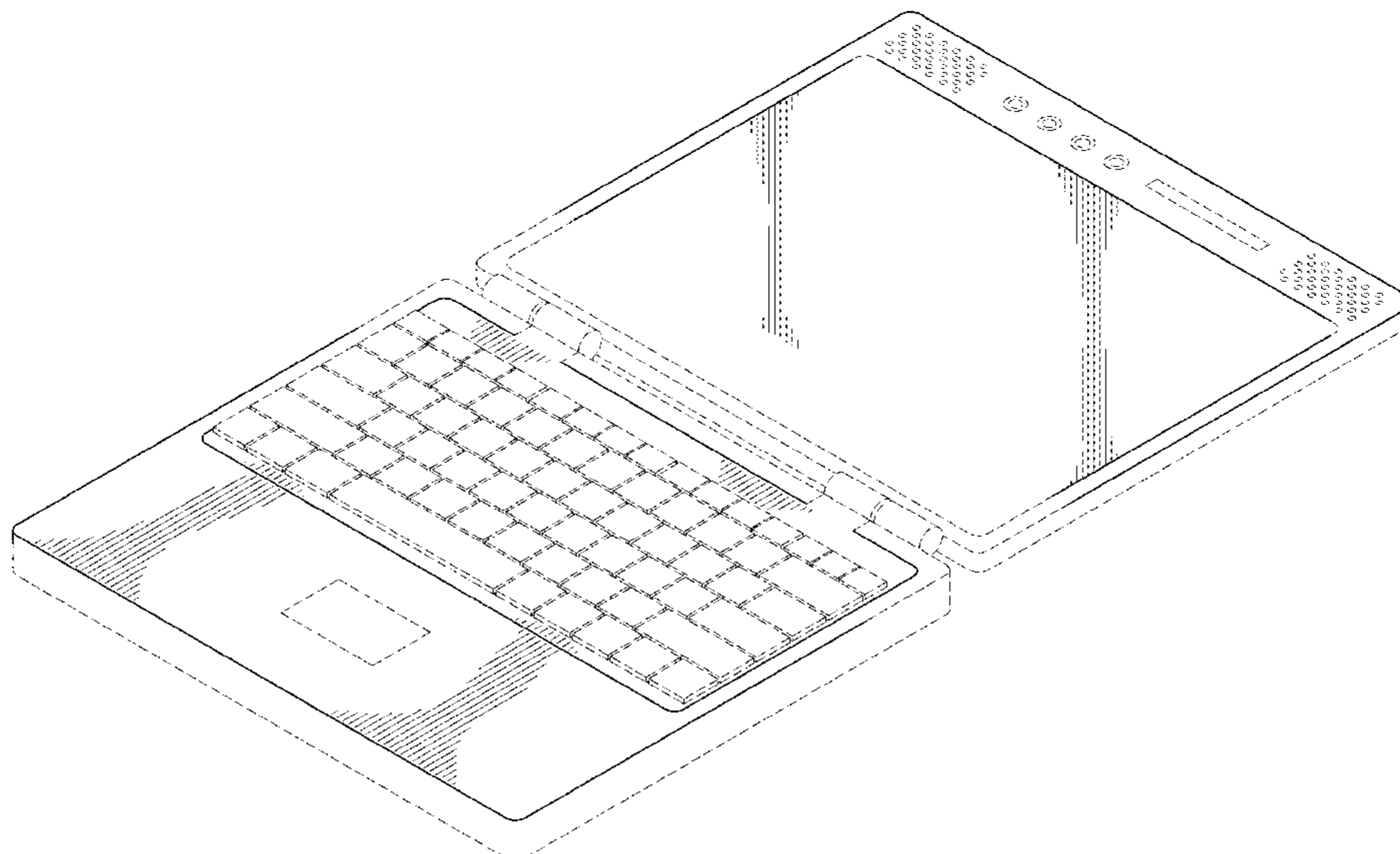
**DESCRIPTION**

FIG. 1 is a perspective view illustrating a design for a multiple mode display device;  
FIG. 2 is a front view thereof;  
FIG. 3 is a back view thereof;  
FIG. 4 is a side view thereof;  
FIG. 5 is another side view thereof;  
FIG. 6 is a top view thereof; and,  
FIG. 7 is a bottom view thereof.  
The broken lines shown in the drawings illustrate portions of the multiple mode display device and form no part of the claimed design.

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**1 Claim, 5 Drawing Sheets**



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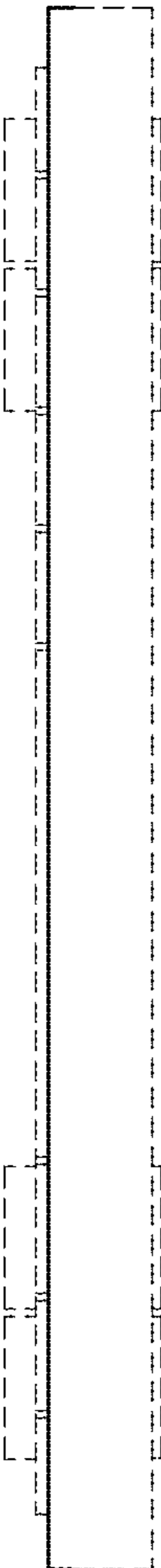


FIG. 2

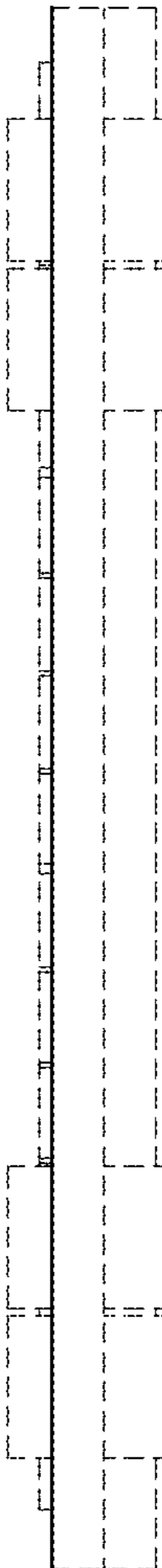


FIG. 3

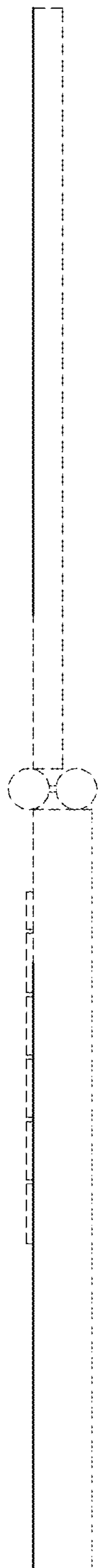


FIG. 4

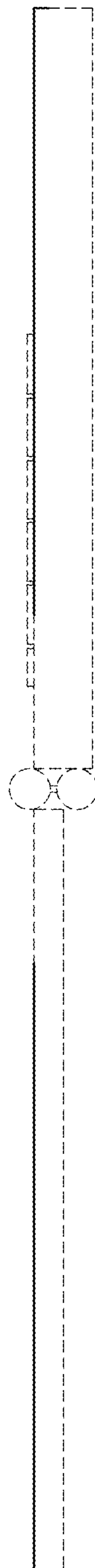


FIG. 5

FIG. 6

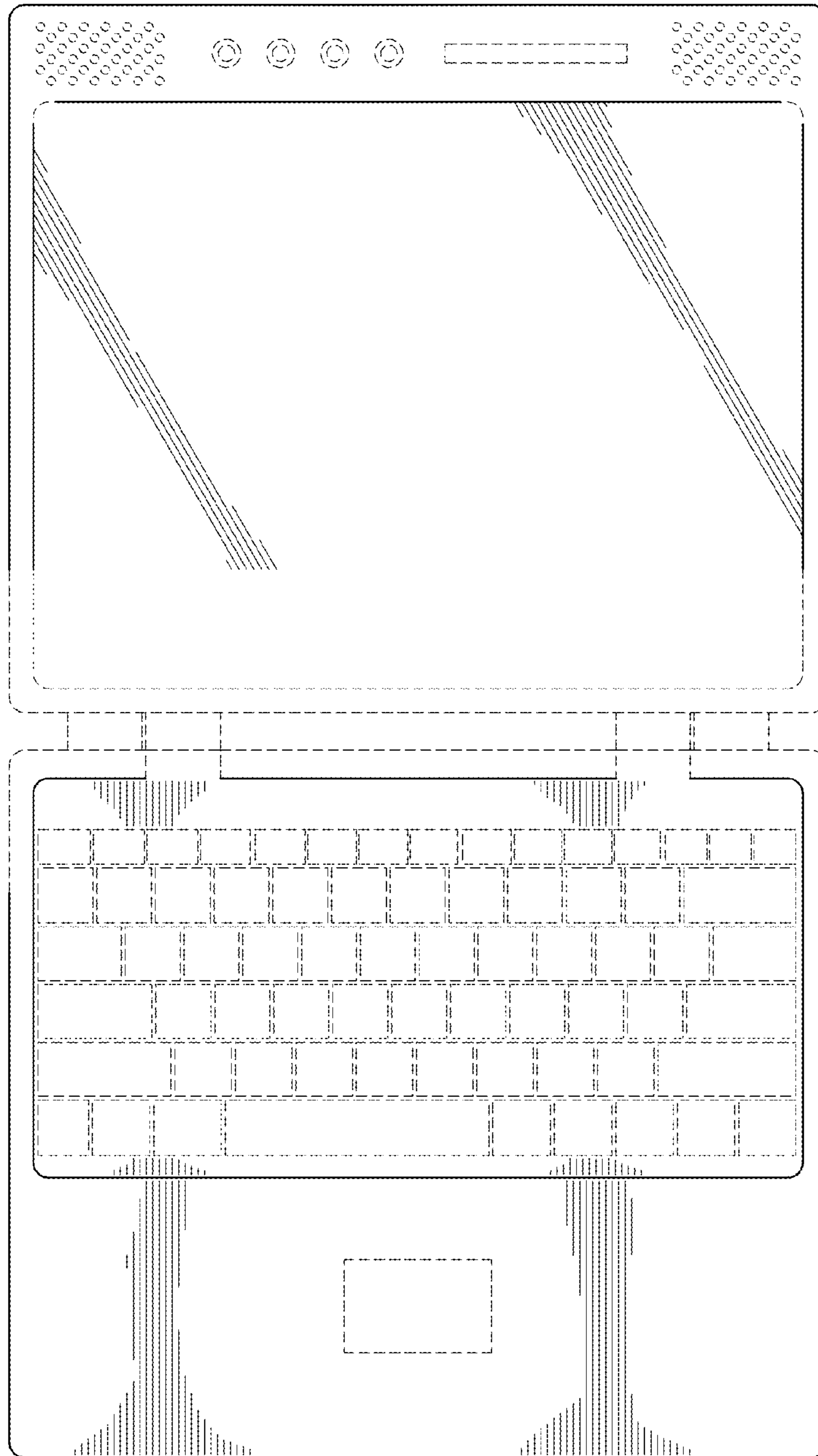


FIG. 7

