



US00D714745S

(12) **United States Design Patent**
Kanemaru et al.

(10) **Patent No.:** **US D714,745 S**
(45) **Date of Patent:** **** Oct. 7, 2014**

(54) **SEMICONDUCTOR**

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(**) Term: **14 Years**

(21) Appl. No.: **29/429,075**

(22) Filed: **Aug. 7, 2012**

(30) **Foreign Application Priority Data**

Feb. 14, 2012	(JP)	2012-003000
Feb. 14, 2012	(JP)	2012-003001
Feb. 14, 2012	(JP)	2012-003002
Feb. 14, 2012	(JP)	2012-003003
Feb. 14, 2012	(JP)	2012-003004
Feb. 14, 2012	(JP)	2012-003005
Feb. 14, 2012	(JP)	2012-003006
Feb. 14, 2012	(JP)	2012-003007
Feb. 14, 2012	(JP)	2012-003008
Feb. 14, 2012	(JP)	2012-003009

(51) **LOC (10) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/182**

(58) **Field of Classification Search**
USPC D13/110, 182; 257/678, 690; 361/713,
361/679.01, 728, 736, 775, 760, 761, 820;
324/754, 755, 756; 174/250, 253;
438/64, 65

See application file for complete search history.

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(57) **CLAIM**

We claim the ornamental design for a semiconductor, as shown and described.

DESCRIPTION

FIG. 1 is a front view of a semiconductor showing our new design;

FIG. 2 is a rear view thereof;

FIG. 3 is a first side view thereof;

FIG. 4 is a second side view thereof;

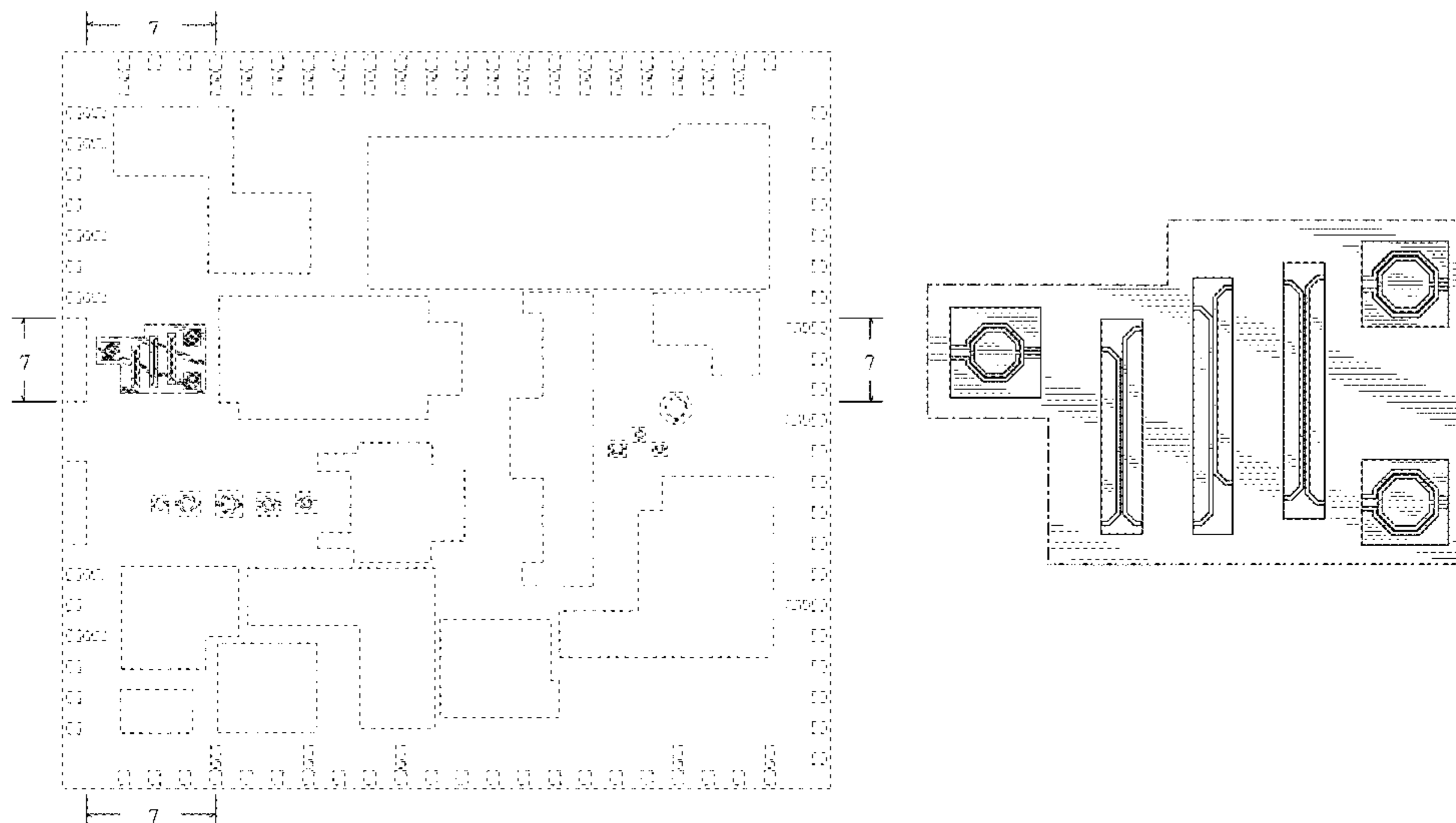
FIG. 5 is a top view thereof;

FIG. 6 is a bottom view thereof; and,

FIG. 7 is an enlarged front view taken along line 7-7 of FIG. 1

The even dashed broken lines shown represent unclaimed subject matter and form no part of the claimed design. The dashed-dot-dashed lines represent the boundary lines of the claimed design.

1 Claim, 5 Drawing Sheets



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FIG. 1

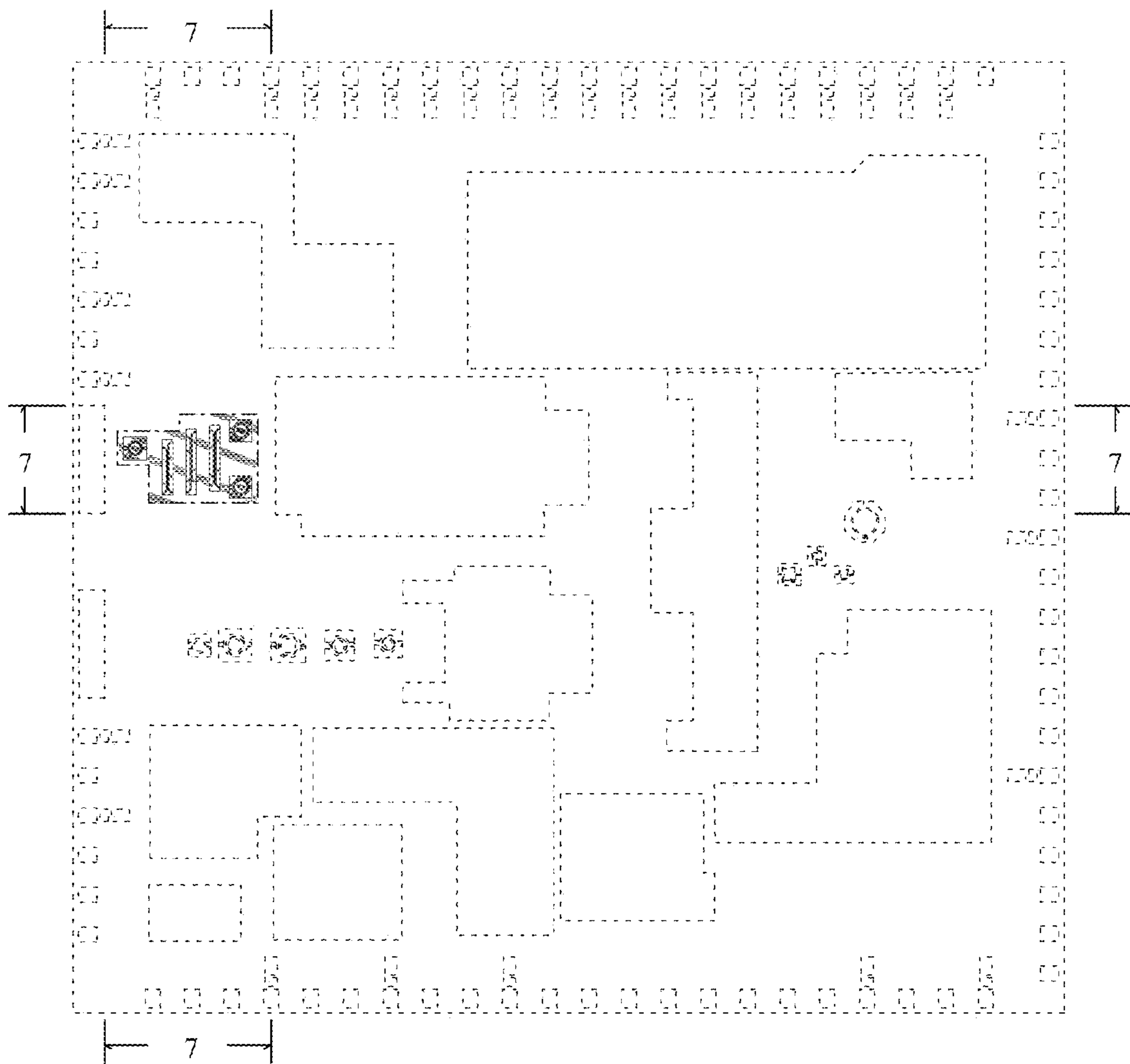


FIG.2

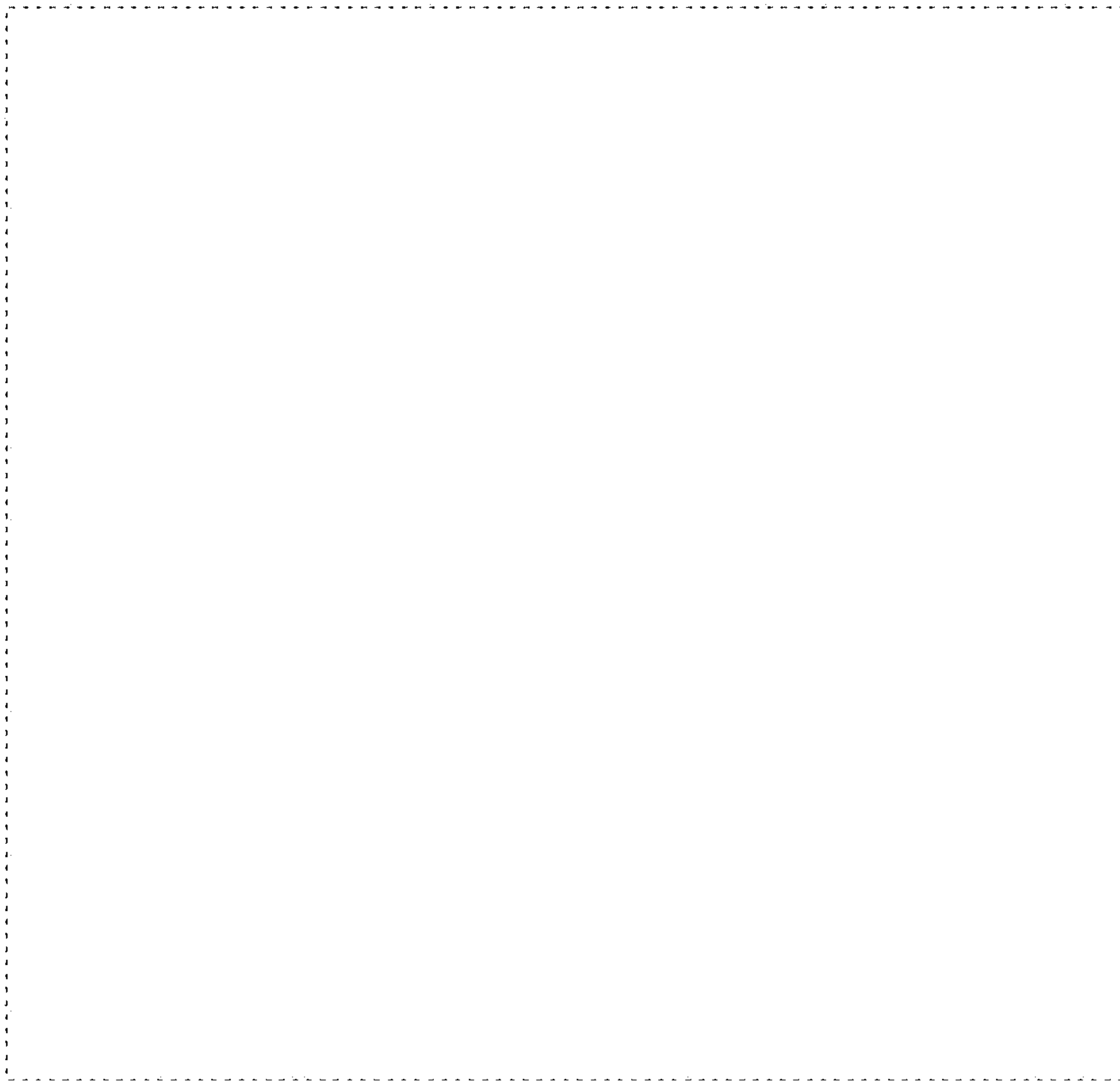


FIG.3

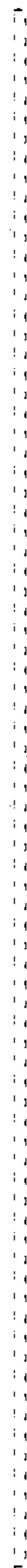


FIG.4

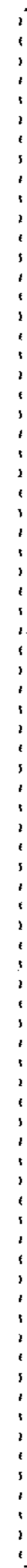


FIG.5

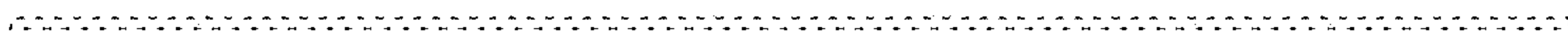


FIG.6

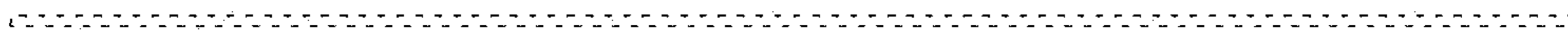


FIG. 7

