



US00D709959S

(12) **United States Design Patent**  
**Jacobs et al.**

(10) **Patent No.:** **US D709,959 S**  
(45) **Date of Patent:** **\*\* Jul. 29, 2014**

(54) **ELECTRONIC HOUSING**  
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(73) Assignee: **Microsoft Corporation**, Redmond, WA (US)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/447,082**

(22) Filed: **Feb. 28, 2013**

(51) **LOC (10) Cl.** ..... **21-01**

(52) **U.S. Cl.**  
USPC ..... **D21/332**

(58) **Field of Classification Search**  
USPC ..... D14/496, 401, 435, 474, 483, 217, 137,  
D14/138, 160, 168, 356, 203.1–203.8, 507;  
345/156, 169, 173–179, 905;  
715/727–729, 864; 710/1, 5, 8; 713/1,  
713/600; 455/1.1, 1.7, 73, 344–347, 93, 95,  
455/3.01–3.06, 550.1, 573.1; 370/342–344;  
369/1, 2, 6–12; 463/43–47, 29–35;  
273/148 B; D21/324–333  
See application file for complete search history.

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(57) **CLAIM**

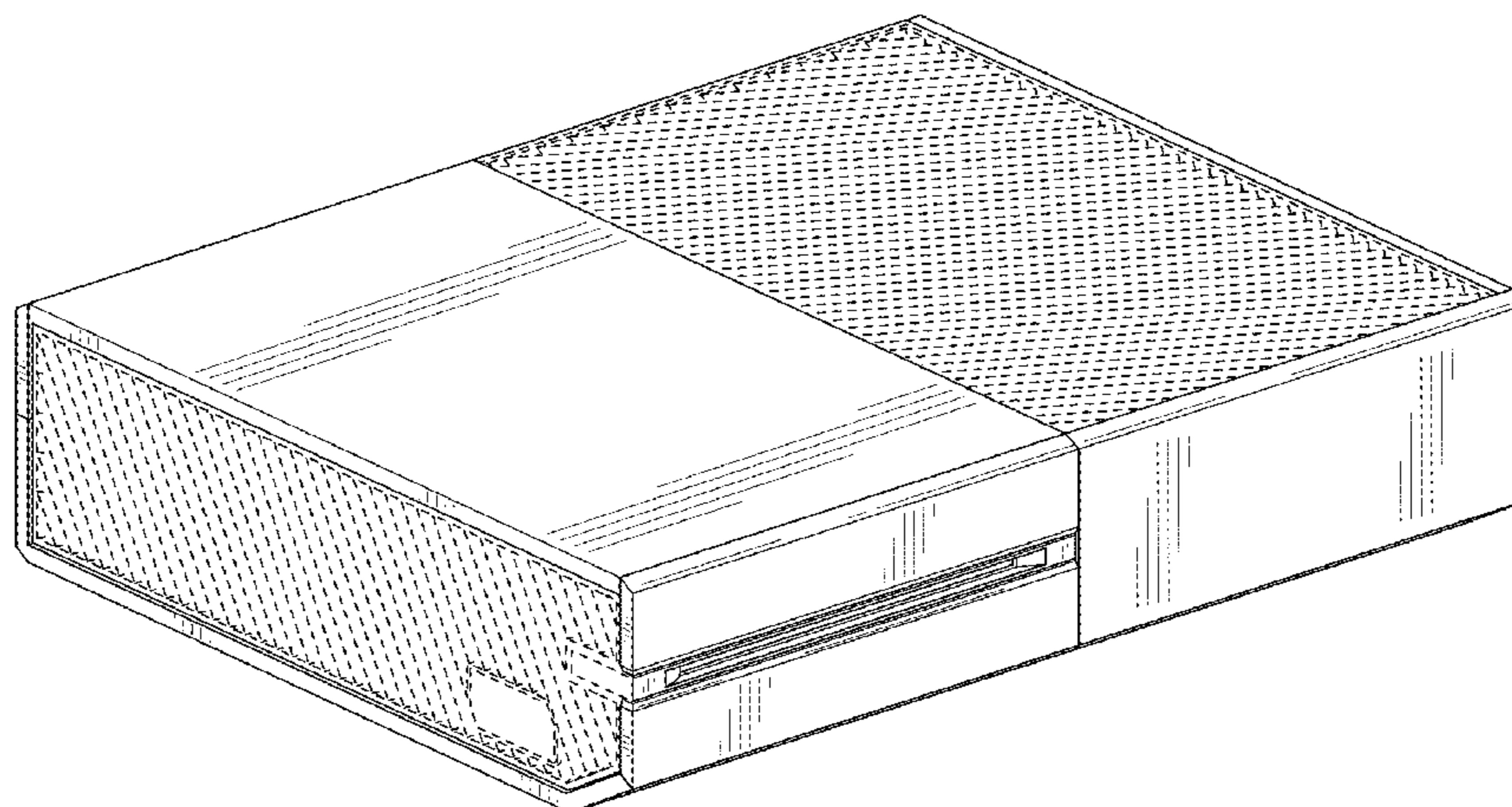
The ornamental design for an electronic housing, as shown and described.

**DESCRIPTION**

FIG. 1 is a front-left perspective view of an electronic housing showing our new design;  
FIG. 2 is a front-right perspective view thereof;  
FIG. 3 is a front view thereof;  
FIG. 4 is a rear view thereof;  
FIG. 5 is a left side view thereof;  
FIG. 6 is a right side view thereof;  
FIG. 7 is a top view thereof; and,  
FIG. 8 is a bottom view thereof.

The broken line showing of the remainder of the electronic housing and the unshaded regions therein in FIGS. 1-8 is for environmental purposes only and forms no part of the claimed design.

**1 Claim, 6 Drawing Sheets**



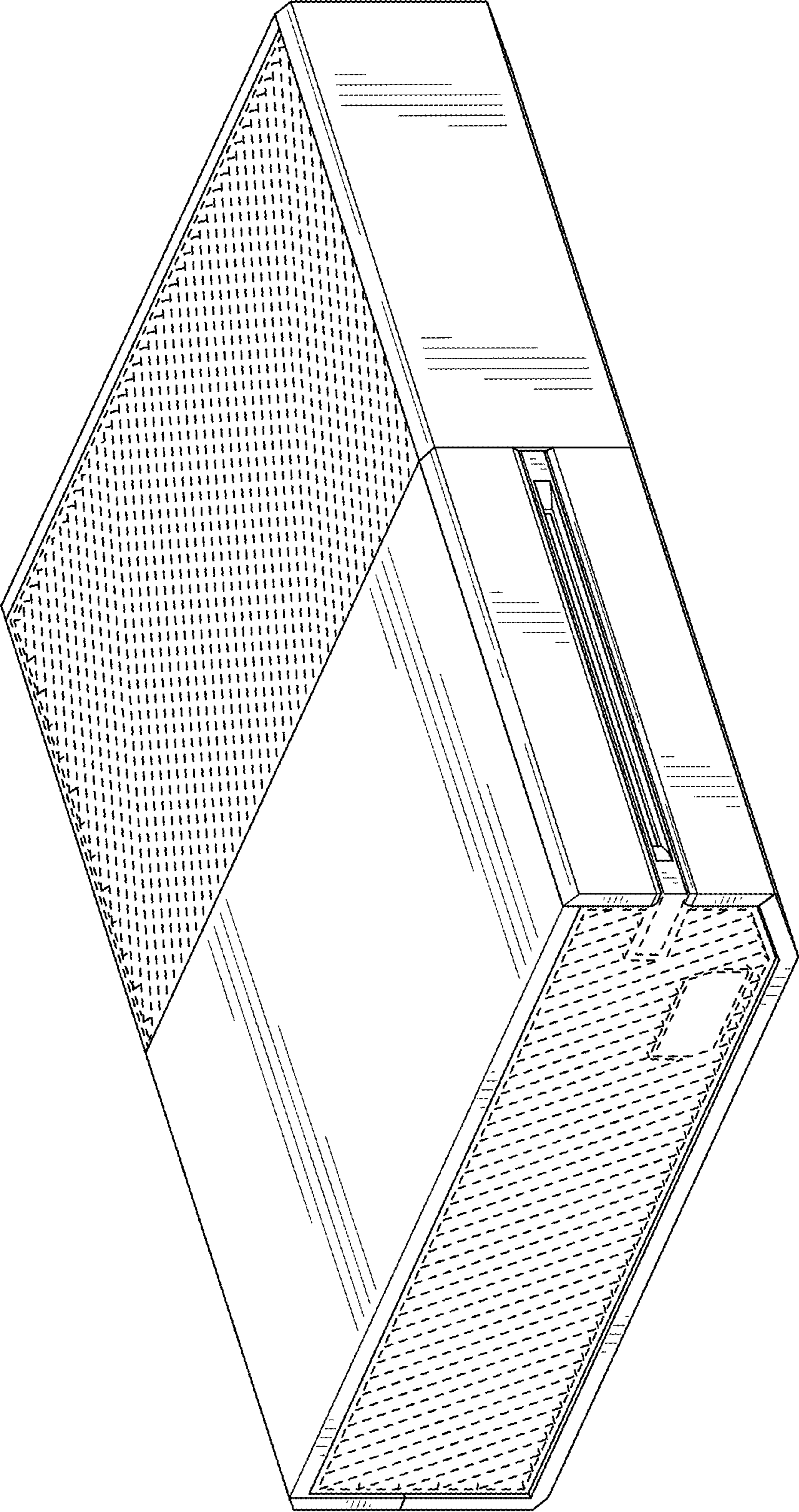


FIG. 1

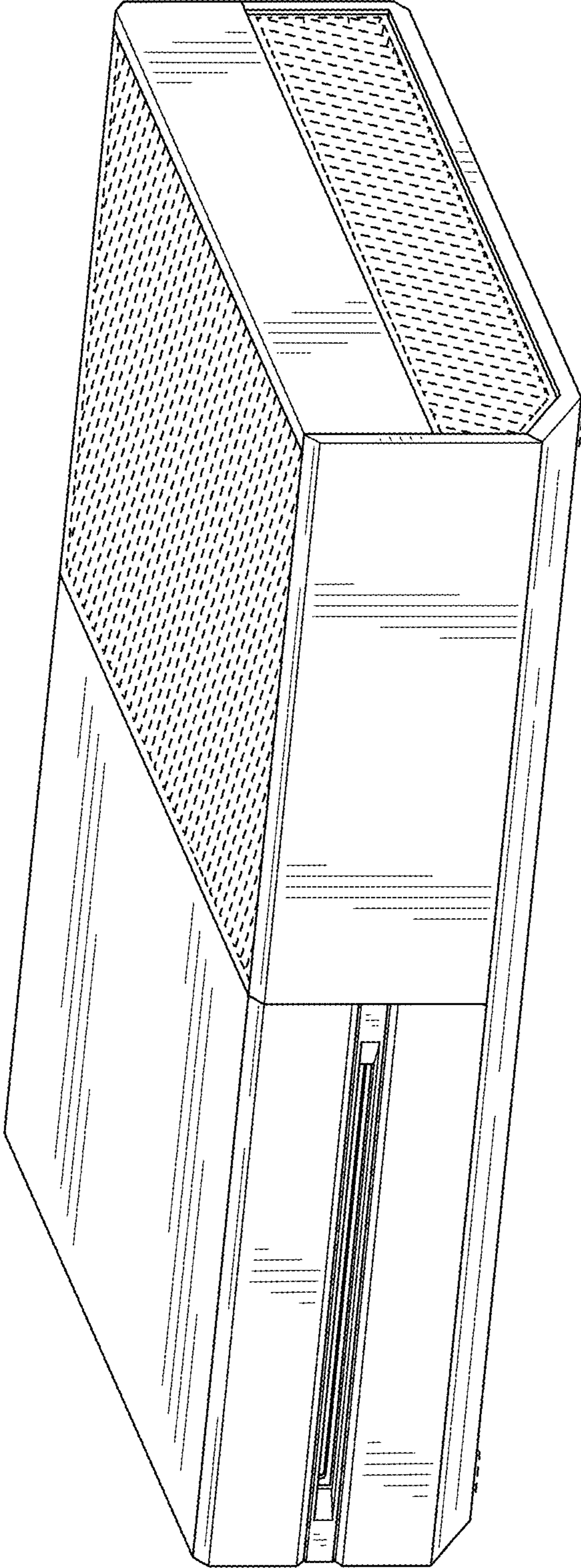


FIG. 2

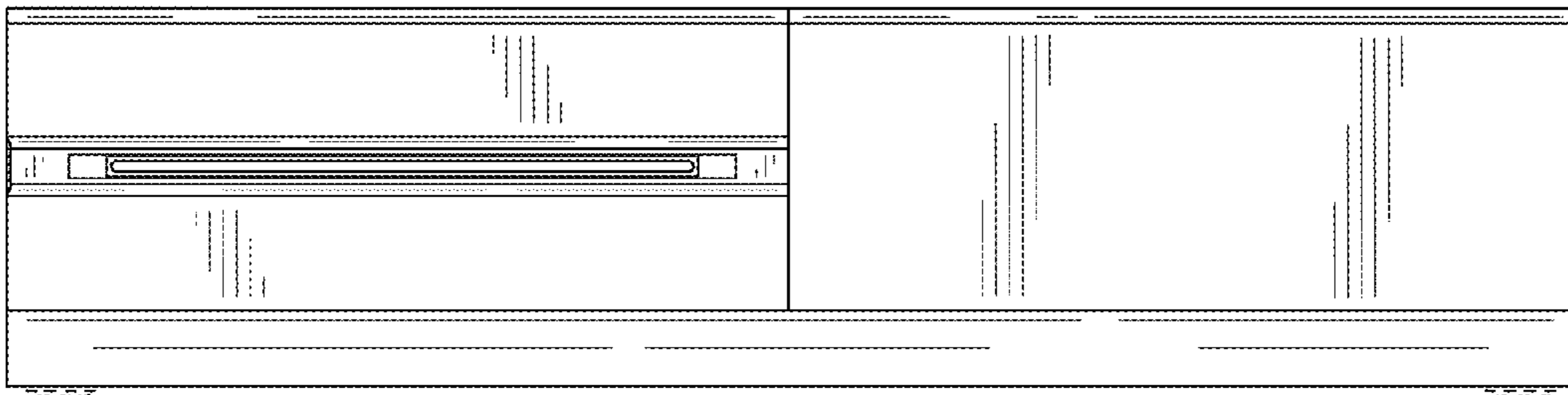


FIG. 3

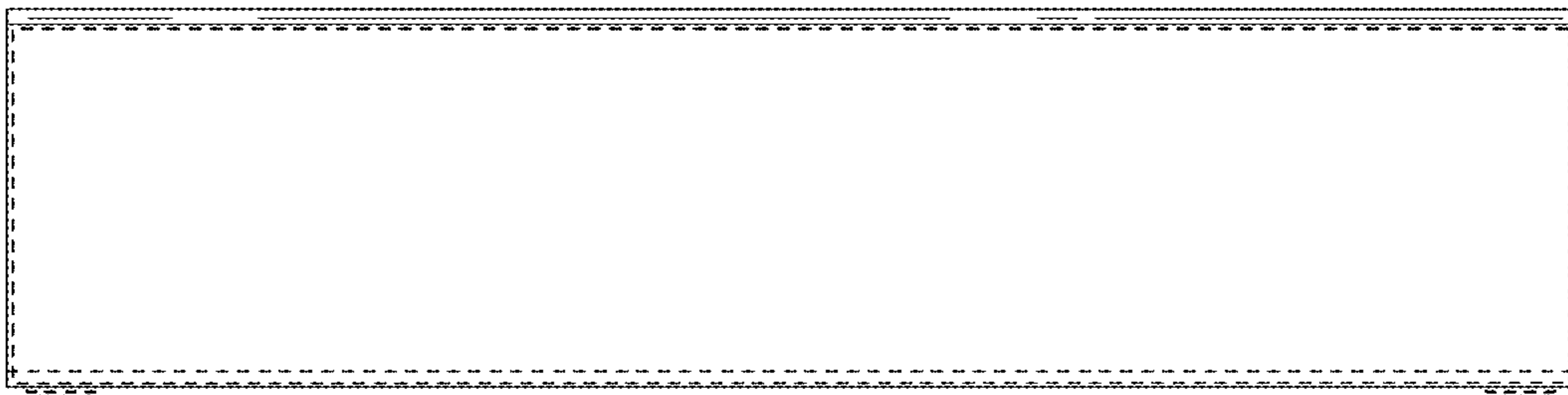


FIG. 4

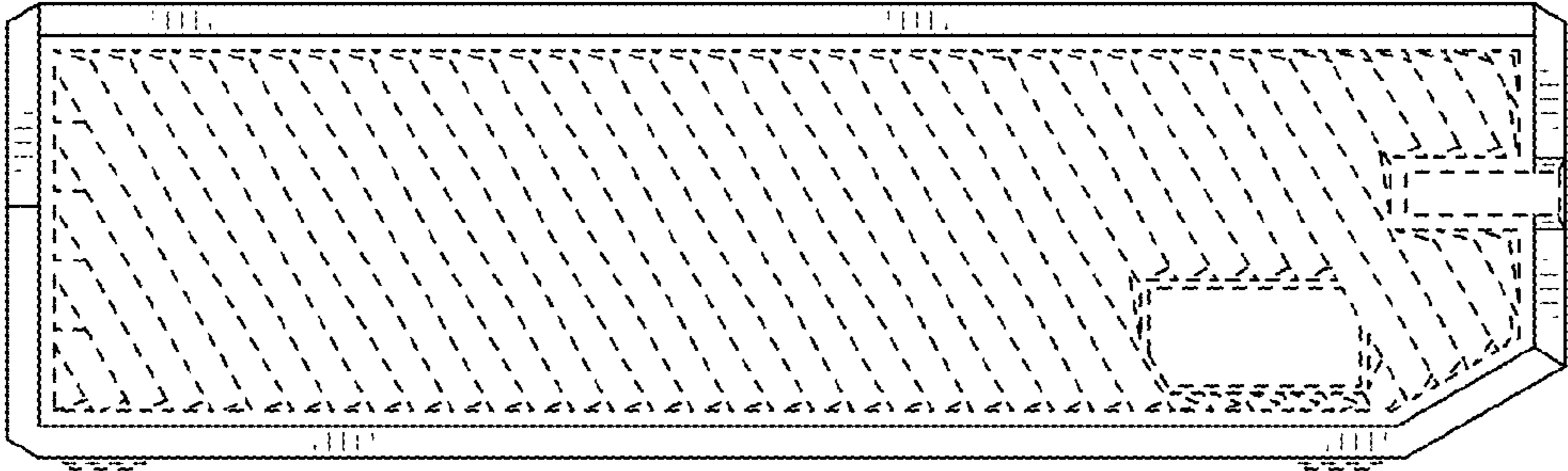


FIG. 5

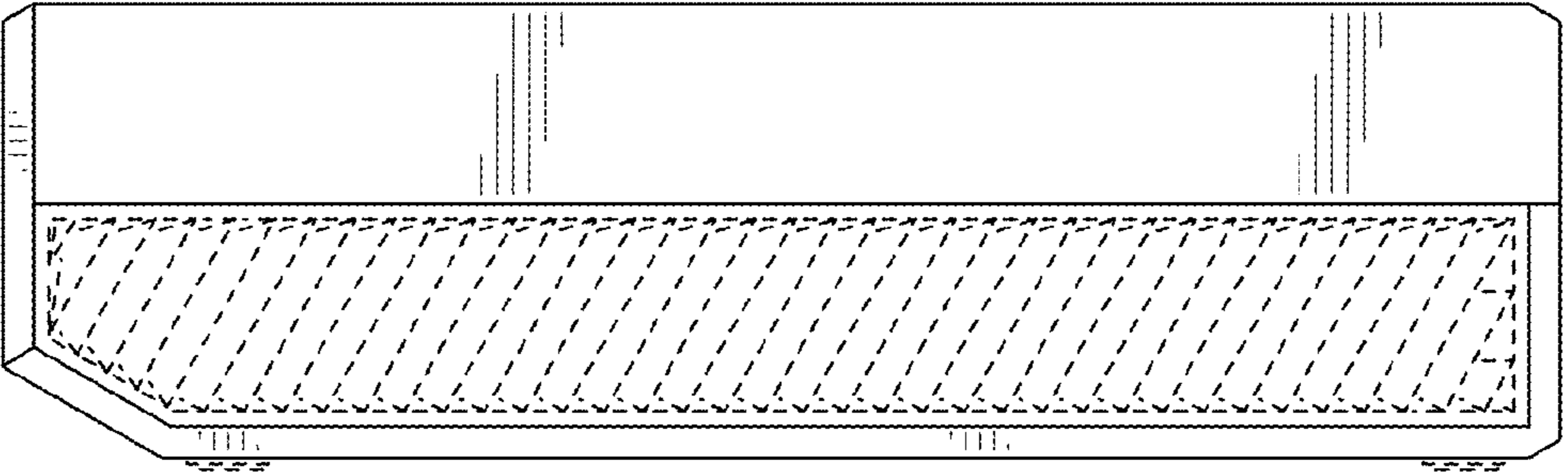


FIG. 6

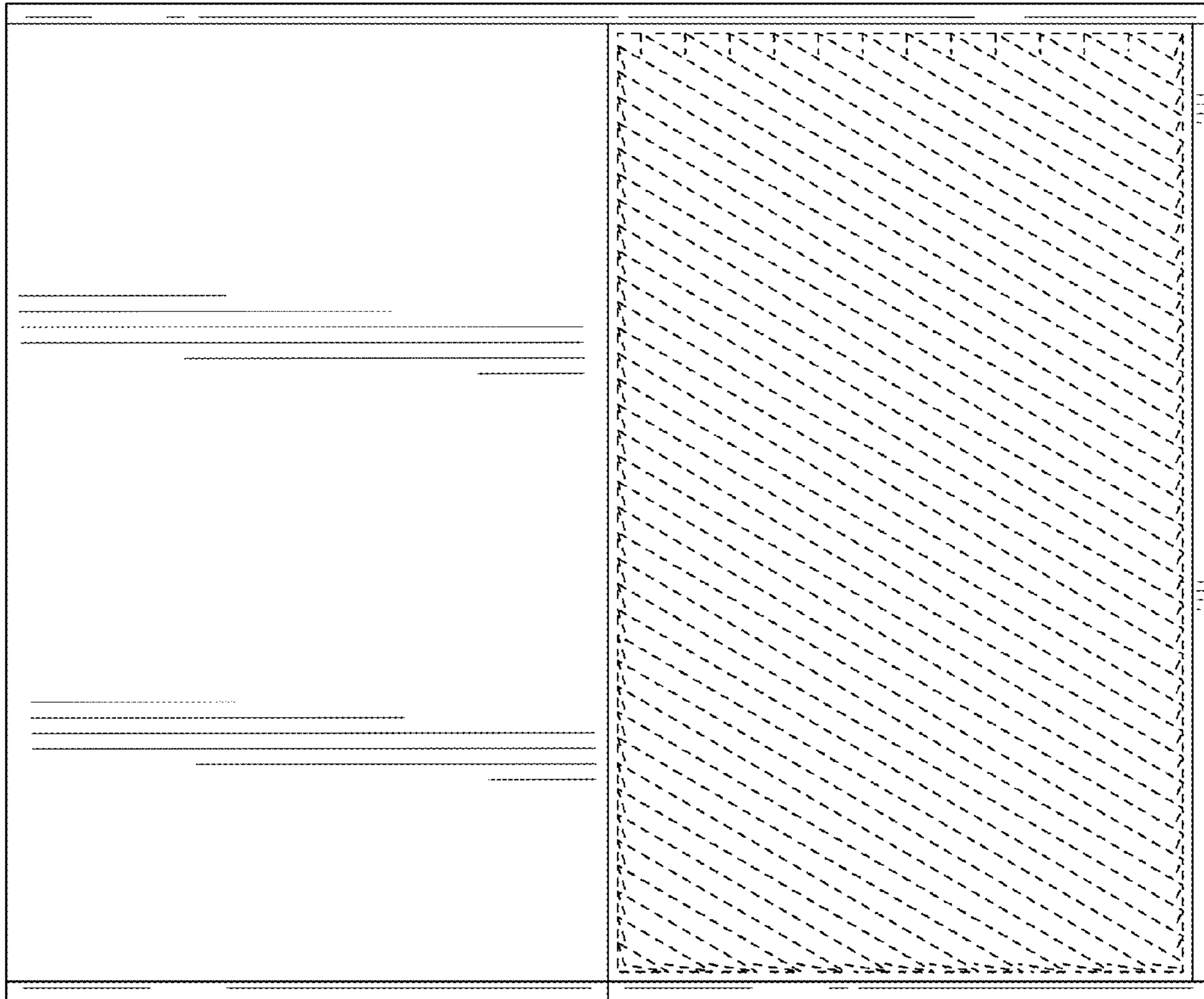


FIG. 7

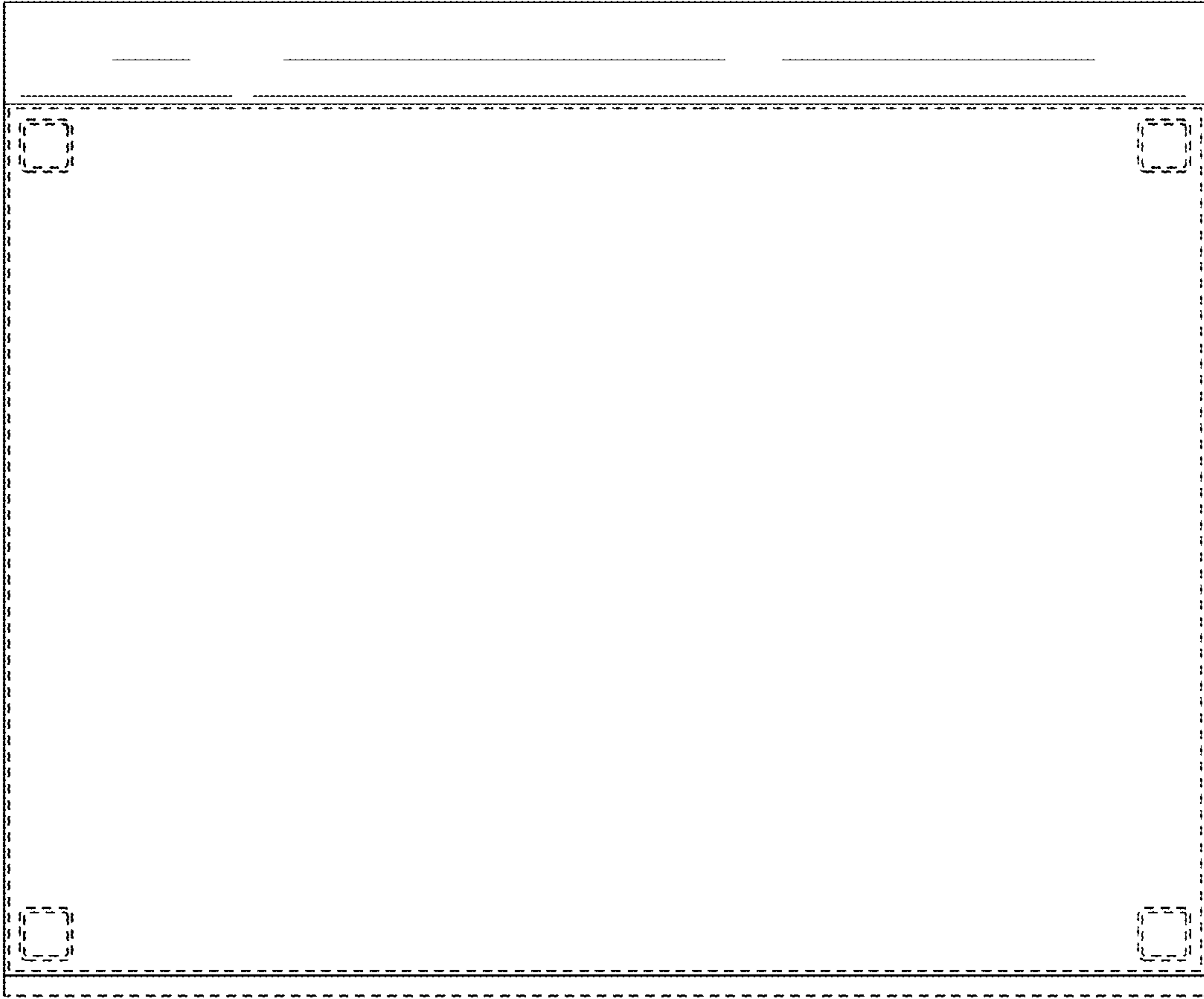


FIG. 8