



US00D709465S

(12) **United States Design Patent**
Keisling et al.

(10) **Patent No.:** **US D709,465 S**
(45) **Date of Patent:** **** Jul. 22, 2014**

(54) **PANEL ASSEMBLY FOR ELECTRONIC EQUIPMENT**

(71) Applicant: **Inertech IP LLC**, Danbury, CT (US)

(72) Inventors: **Earl Keisling**, Ridgefield, CT (US);
John Costakis, Hyde Park, NY (US);
Gerald McDonnell, Poughquag, NY (US)

(73) Assignee: **Inertech IP LLC**, Danbury, CT (US)

(**) Term: **14 Years**

(21) Appl. No.: **29/455,941**

(22) Filed: **May 28, 2013**

Related U.S. Application Data

(63) Continuation-in-part of application No. 29/414,697, filed on Mar. 2, 2012, now Pat. No. Des. 686,999.

(51) **LOC (10) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/184**; D14/441; D14/446

(58) **Field of Classification Search**
USPC D14/240, 300, 301, 308, 313, 349-356,
D14/364-368, 441-446; D13/162, 184,
D13/199; 312/223.1-223.3, 351.11;
360/99.01-99.12; 369/34.01-36.01;
361/679.31-679.33, 679.37, 679.39,
361/679.46, 679.53, 679.4; D23/314, 370

See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D397,997 S	9/1998	Cozzolino et al.
D454,881 S	3/2002	Daniels et al.
6,565,163 B2	5/2003	Behl et al.
D488,153 S	4/2004	Tosh et al.
D490,436 S	5/2004	Helgenberg
D568,886 S	5/2008	Wang
D617,800 S	6/2010	Kuehn et al.
D686,999 S *	7/2013	Keisling et al. D13/184

* cited by examiner

Primary Examiner — Freda S Nunn

(74) *Attorney, Agent, or Firm* — Carter, DeLuca, Farrell & Schmidt, LLP

(57) **CLAIM**

The ornamental design for an panel assembly for electronic equipment, as shown and described.

DESCRIPTION

FIG. 1 is a fragmented front side perspective view of a panel assembly for electronic equipment in accordance with the present design wherein the assembly is shown on a reduced scale;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a left side elevational view thereof, the right side elevational view being a mirror image thereof; and,

FIG. 4 is a top plan view thereof.

The broken lines shown in FIG. 4 immediately adjacent the shaded areas represent the bounds of the claimed design, while all other broken lines in the drawings illustrate portions of the panel assembly for electronic equipment and form no part of the claimed design, and the stippling in the drawings is for shading purposes only.

1 Claim, 4 Drawing Sheets

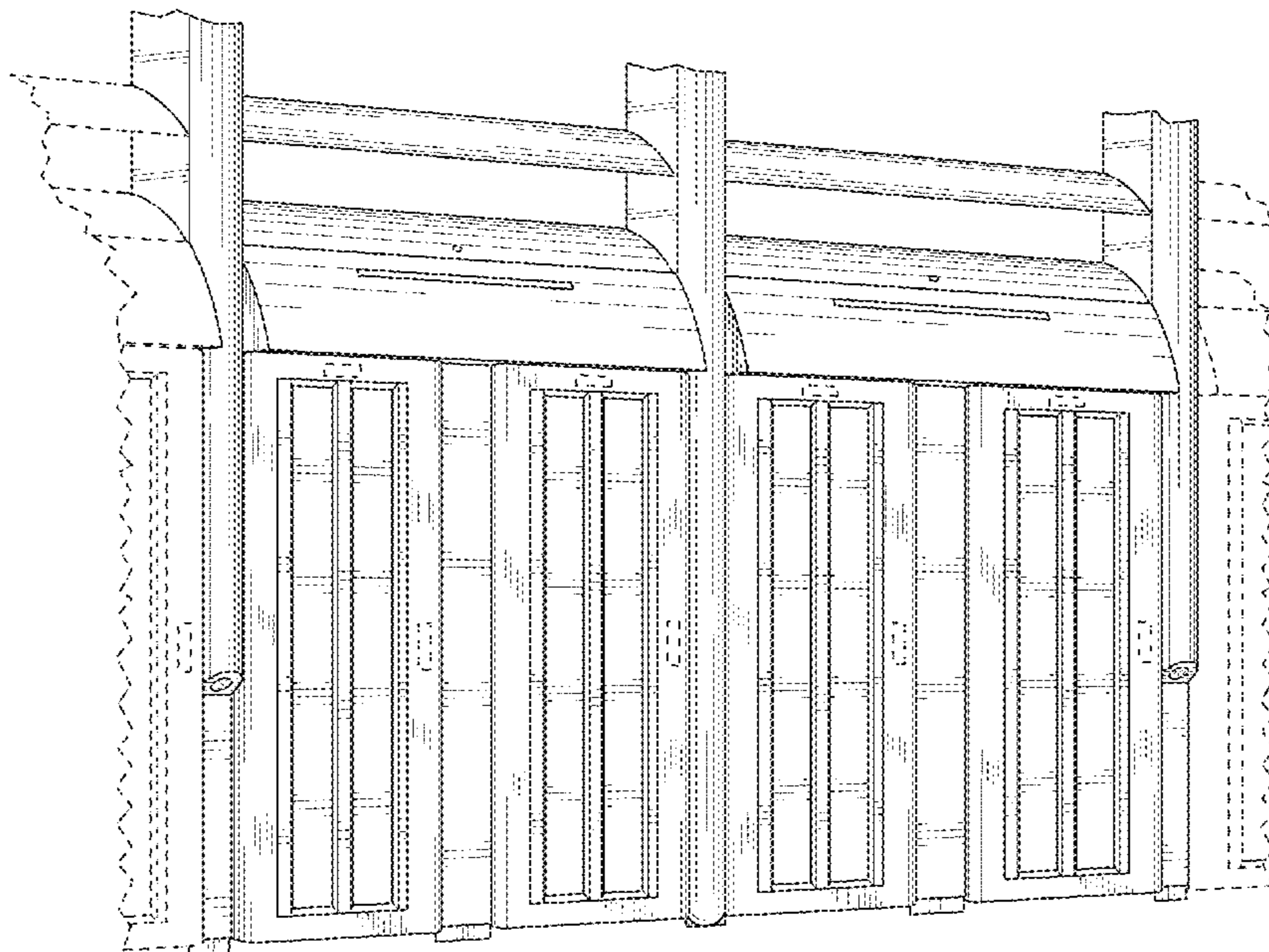




FIG. 1

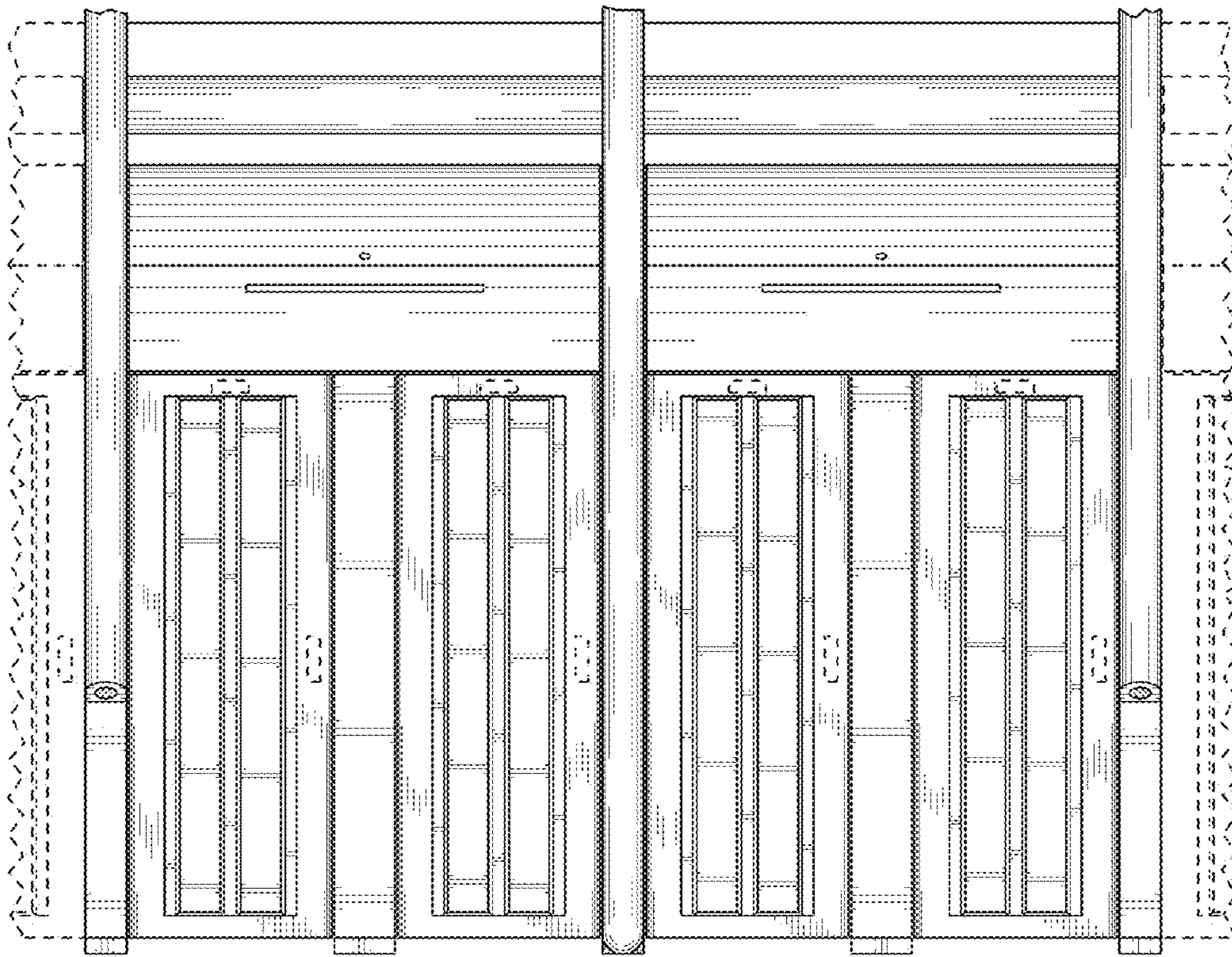


FIG. 2

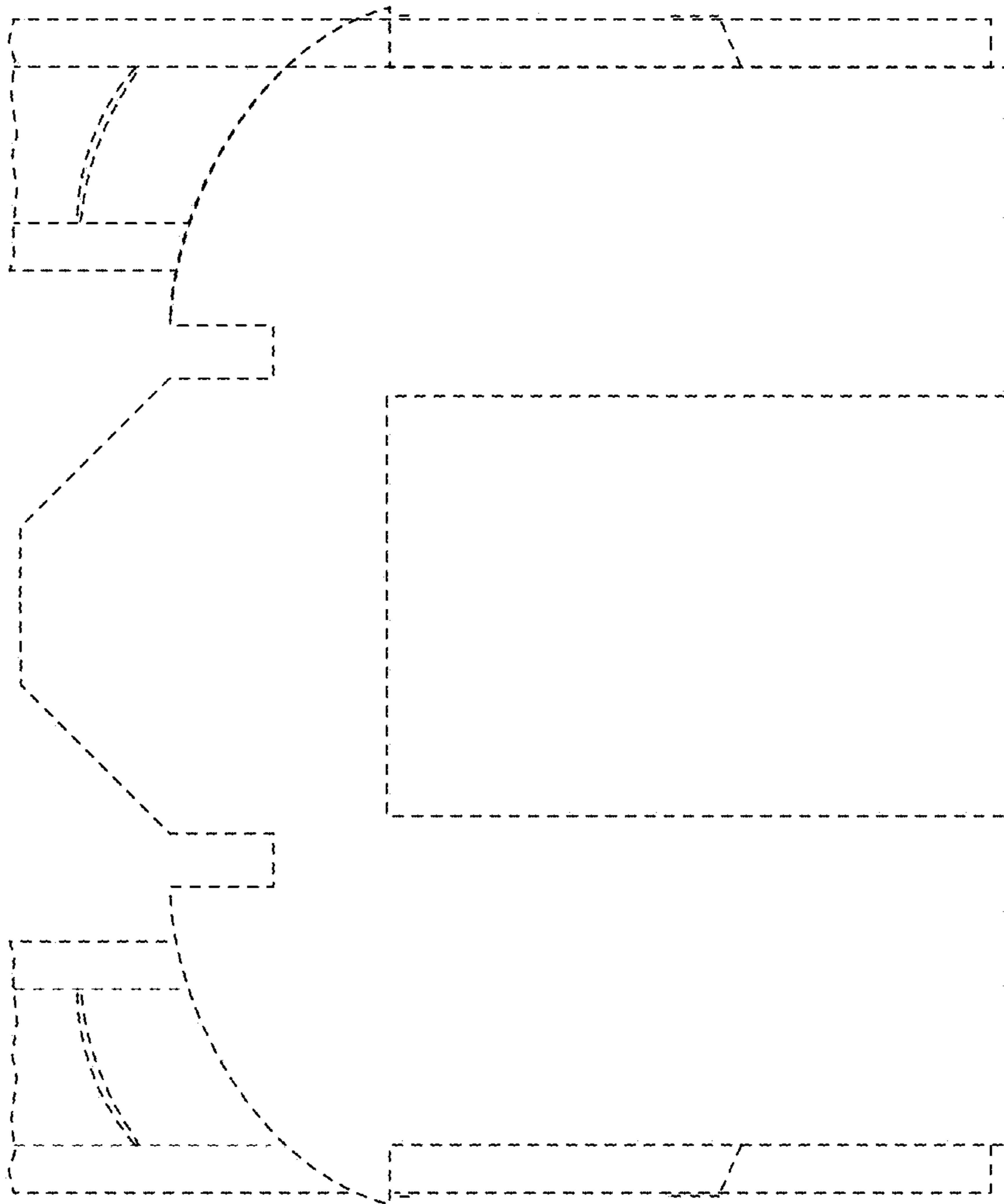


FIG. 3

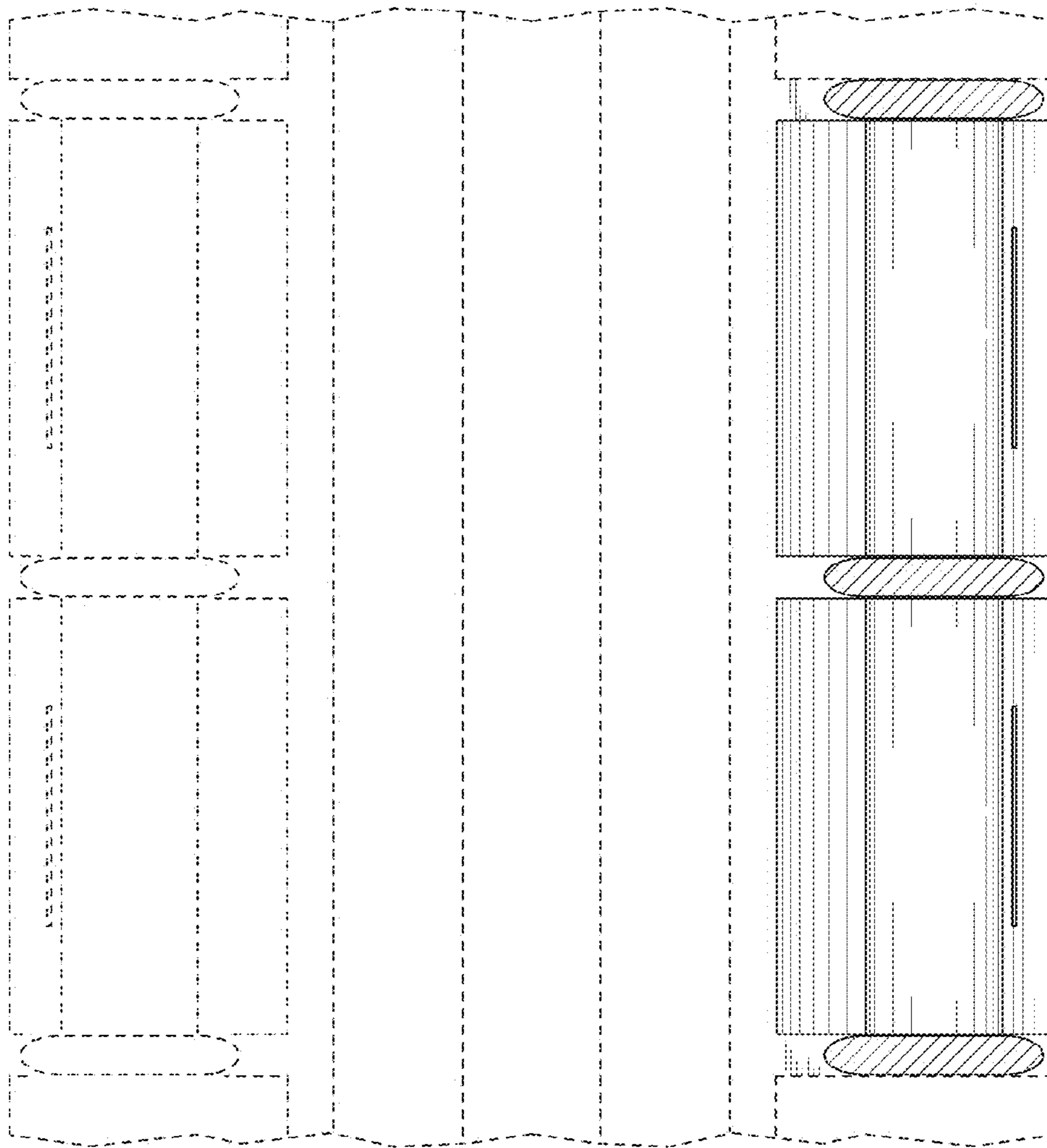


FIG. 4