



US00D707574S

(12) **United States Design Patent**
Ku et al.

(10) **Patent No.:** **US D707,574 S**
(45) **Date of Patent:** **** Jun. 24, 2014**

- (54) **FLIP SWITCH TIMER**
- (75) Inventors: **Shiao-Tsun Ku**, Waukegan, IL (US);
Matthew Haws, Indianapolis, IN (US)
- (73) Assignee: **Coleman Cable, LLC**, Waukegan, IL (US)
- (**) Term: **14 Years**
- (21) Appl. No.: **29/414,746**
- (22) Filed: **Mar. 2, 2012**
- (51) **LOC (10) Cl.** **10-03**
- (52) **U.S. Cl.**
USPC **D10/40**
- (58) **Field of Classification Search**
USPC D10/40, 41, 56, 75; D13/158, 162,
D13/162.1, 164, 165, 168, 170, 125;
D24/186; 368/10, 89, 96-100,
368/107-121; 200/33 R, 315, 329, 330;
307/139, 140, 141.4
See application file for complete search history.

- D618,808 S * 6/2010 Fisher et al. D24/186
- D628,702 S * 12/2010 Fisher et al. D24/186
- 8,084,700 B1 12/2011 Massaro et al.
- D692,325 S * 10/2013 Meyer D10/56
- 2010/0140070 A1* 6/2010 Simard et al. 200/33 R

* cited by examiner

Primary Examiner — Caron D Veynar

Assistant Examiner — Martie K Holtje

(74) *Attorney, Agent, or Firm* — Patzik, Frank & Samotny Ltd.

(57) **CLAIM**

The ornamental design for a flip switch timer, as shown and described.

DESCRIPTION

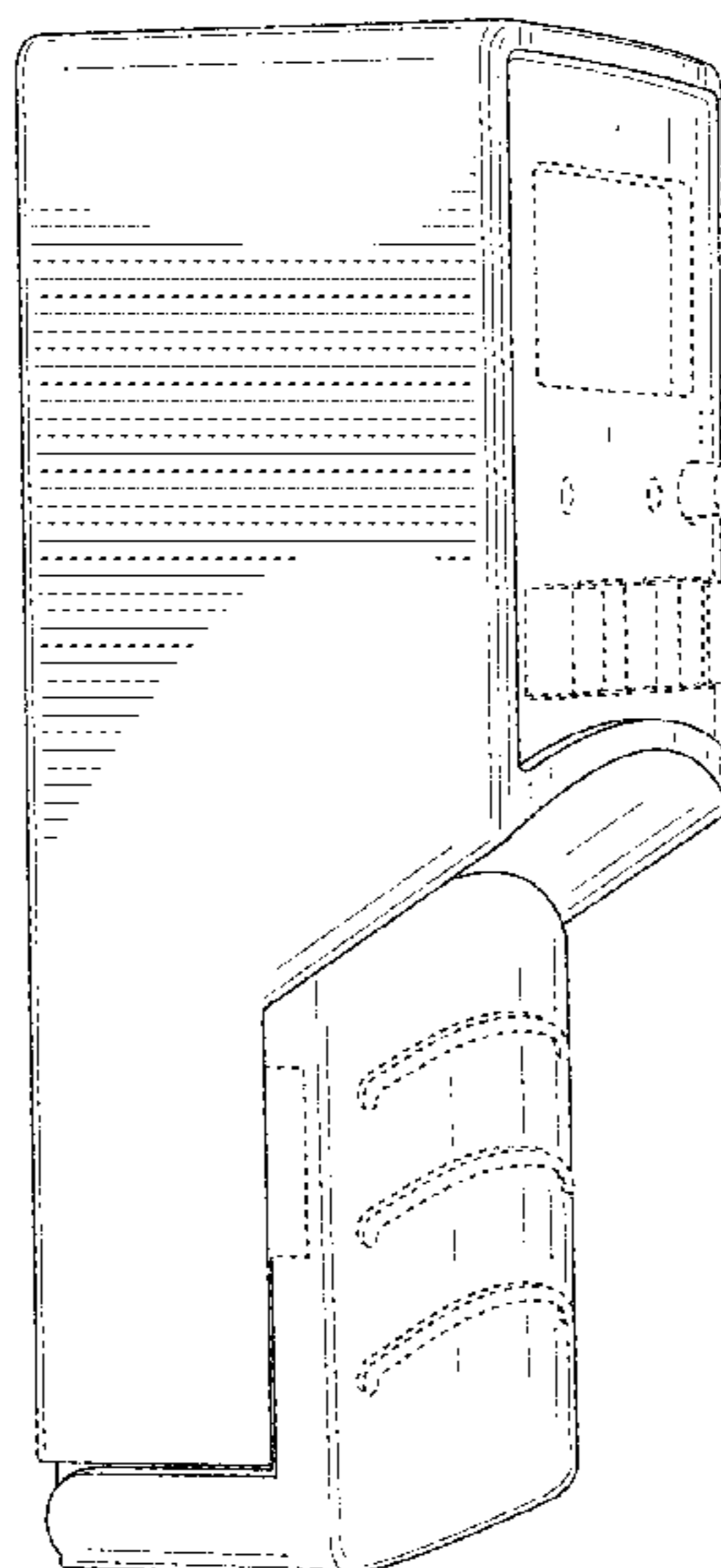
FIG. 1 is a front perspective view of a flip switch timer showing our new design;
 FIG. 2 is a front elevational view of FIG. 1;
 FIG. 3 is a rear elevational view of FIG. 1;
 FIG. 4 is a top plan view of FIG. 1;
 FIG. 5 is a bottom plan view of FIG. 1;
 FIG. 6 is a right side elevational view of FIG. 1, the left side elevational view being substantially a mirror image thereof;
 FIG. 7 is a front perspective view of a second embodiment of our design of FIG. 1;
 FIG. 8 is a front elevational view of FIG. 7;
 FIG. 9 is a rear elevational view of FIG. 7;
 FIG. 10 is a top plan view of FIG. 7;
 FIG. 11 is a bottom plan view of FIG. 7; and,
 FIG. 12 is a right side elevational view of FIG. 7, the left side elevational view being substantially a mirror image thereof.
 The portions of the flip switch timer shown in broken lines form no part of the claimed design.
 The broken lines in FIGS. 3, 5, 9, and 11 represent boundaries, the areas within which form no part of the claimed design.

(56) **References Cited**

U.S. PATENT DOCUMENTS

- D216,017 S * 11/1969 Podall D10/40
- D282,827 S * 3/1986 Hoogner D10/40
- 4,835,413 A 5/1989 Nilssen
- D315,893 S * 4/1991 Cheng D13/165
- D352,667 S * 11/1994 Chen D10/40
- 5,397,869 A 3/1995 Huen
- 5,465,031 A 11/1995 Nilssen
- D364,826 S * 12/1995 Wu D10/75
- D391,177 S * 2/1998 Stekelenburg D10/40
- 5,719,362 A 2/1998 Gray, Jr.
- 5,898,240 A 4/1999 Tsou
- 5,955,796 A 9/1999 Nilssen
- 7,189,936 B2 3/2007 Gray, Jr.
- 7,244,900 B1 7/2007 Gray, Jr.
- 7,372,355 B2 5/2008 Agronin et al.

1 Claim, 10 Drawing Sheets



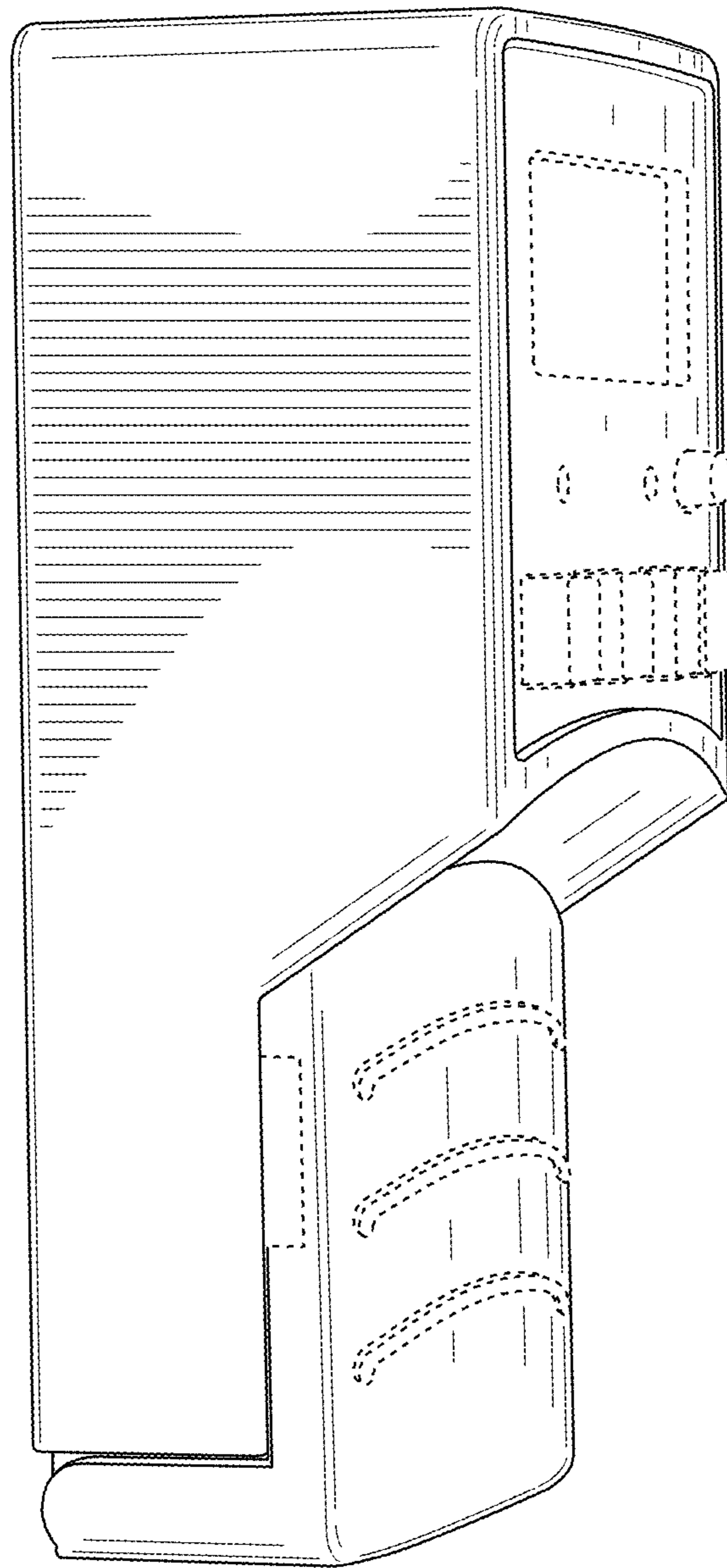


FIG. 1

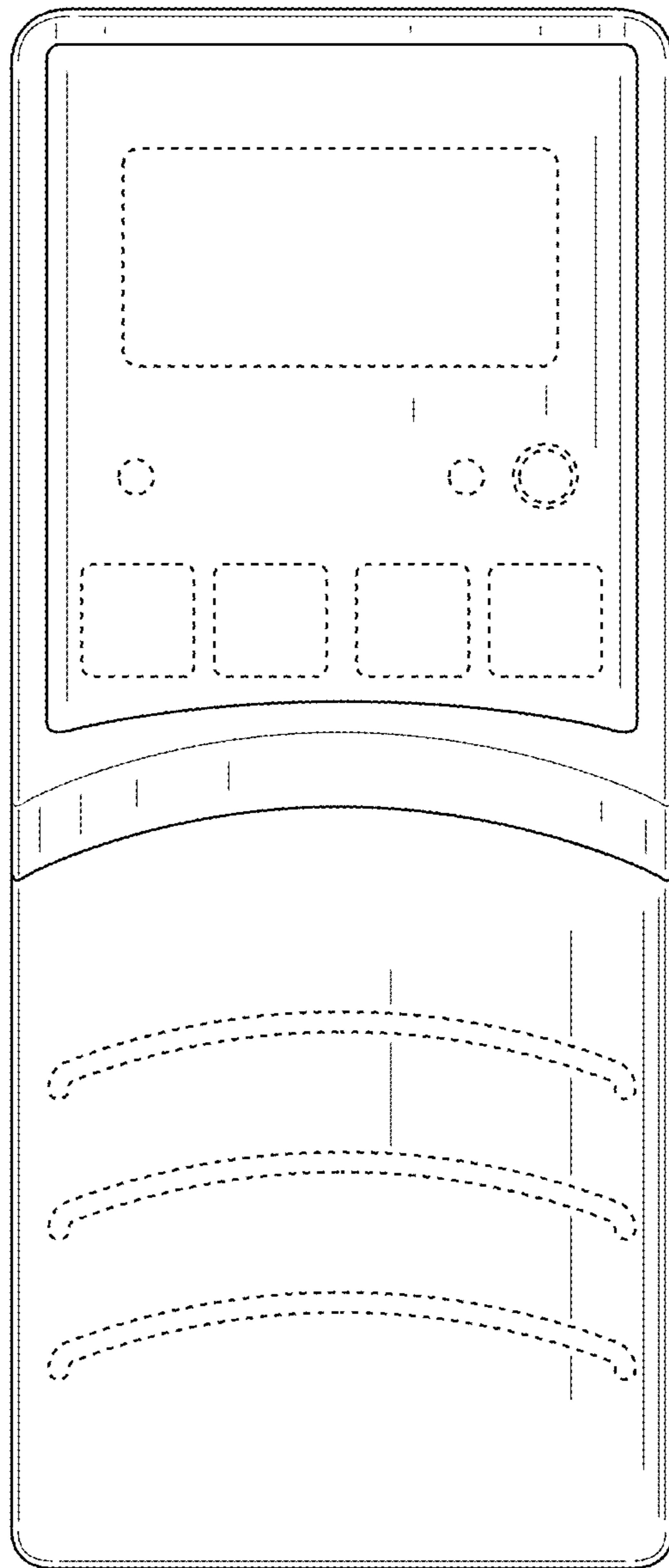


FIG. 2

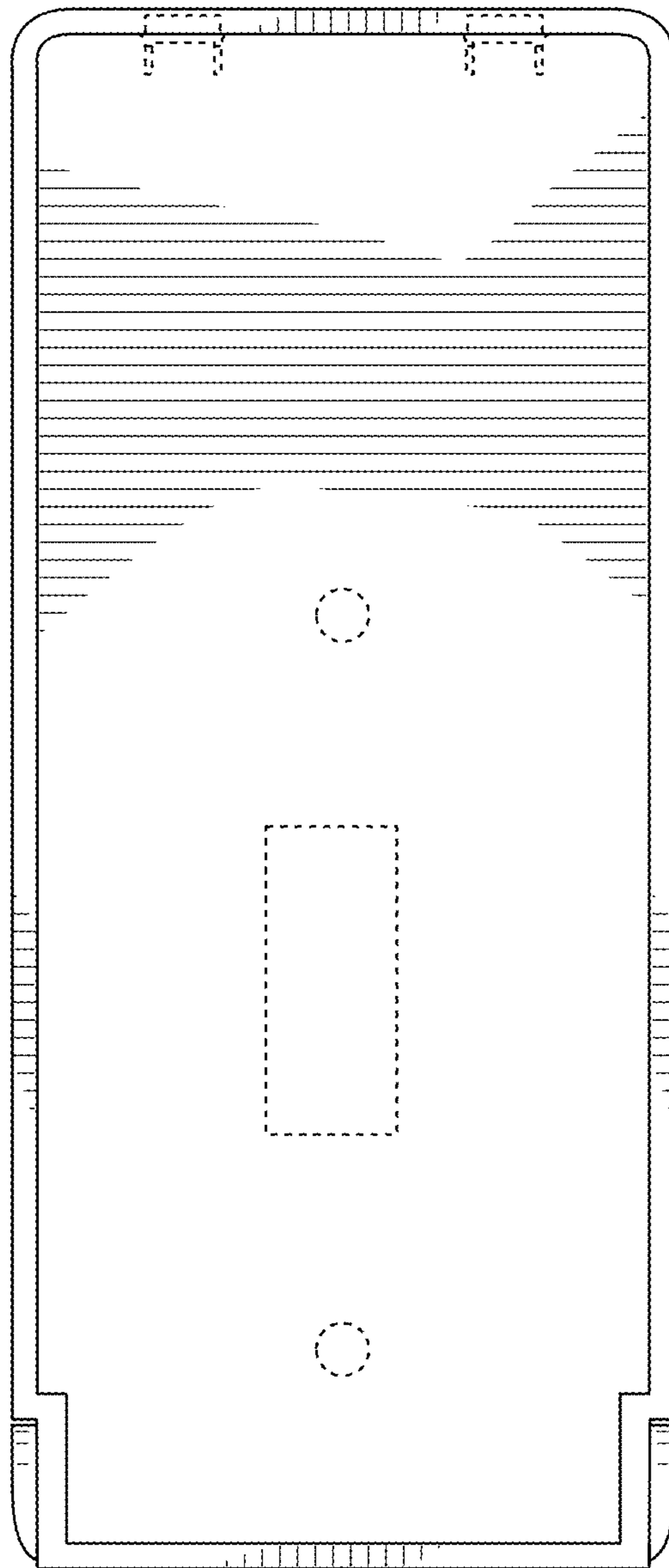


FIG. 3

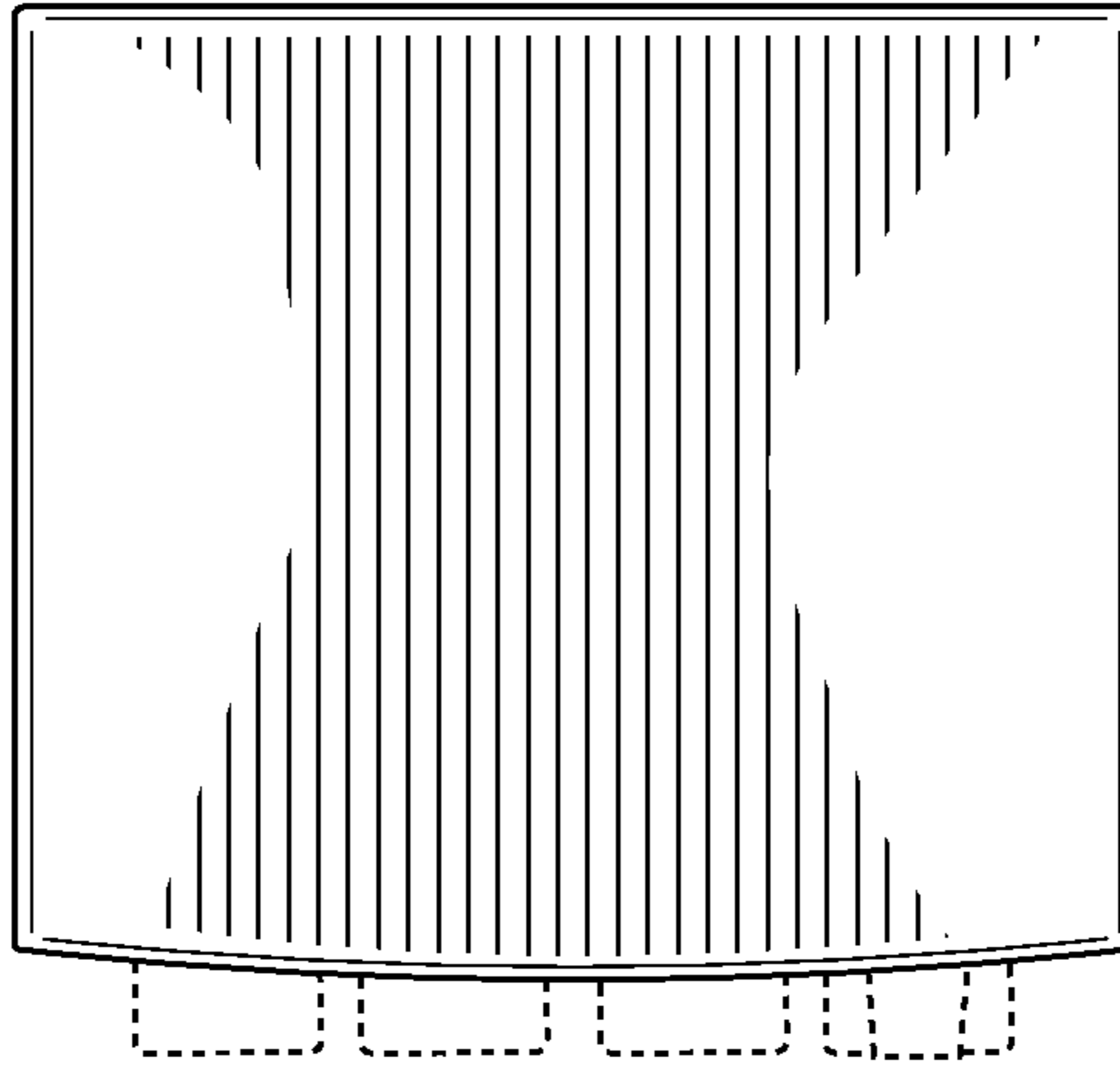


FIG. 4

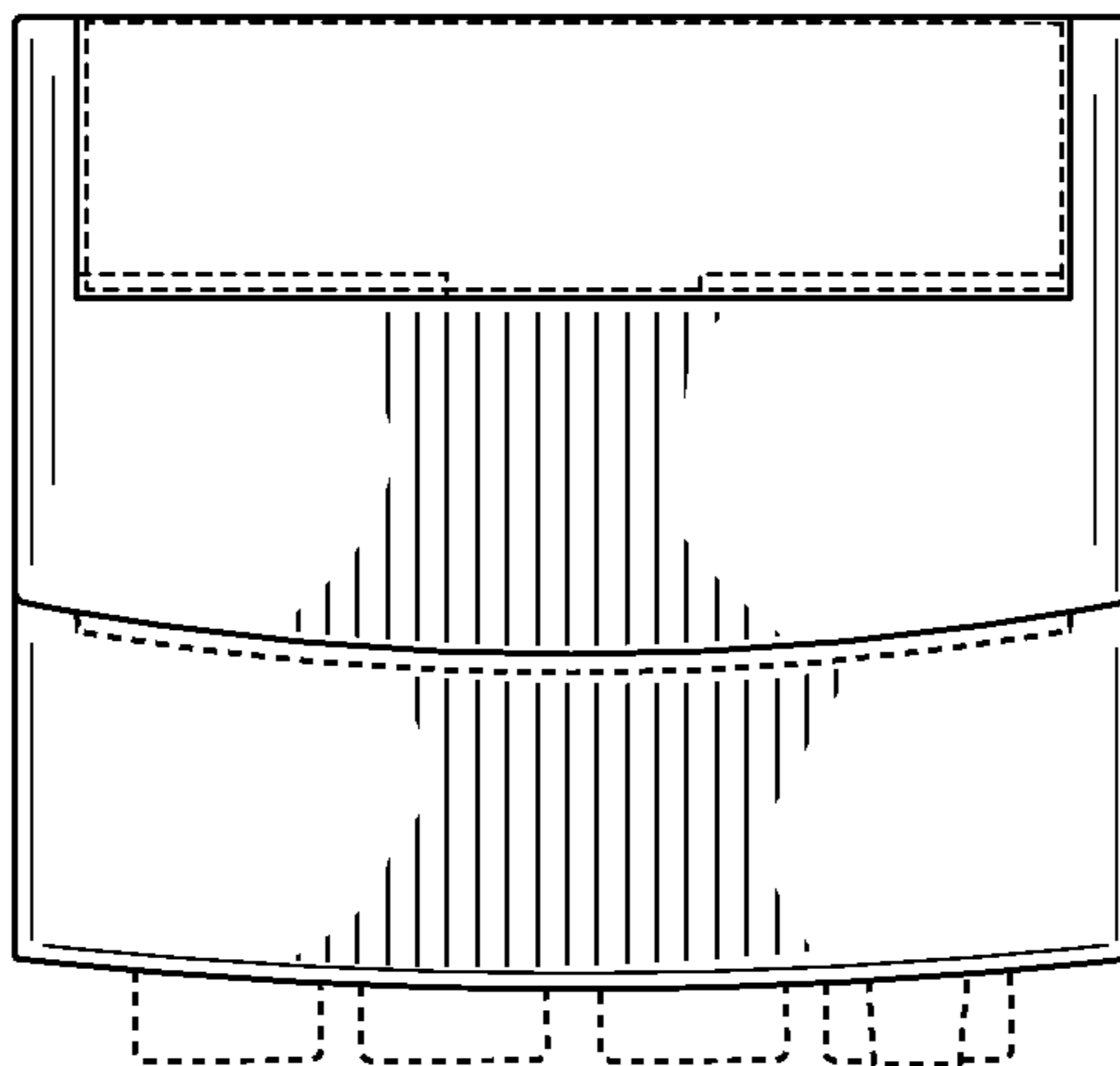


FIG. 5

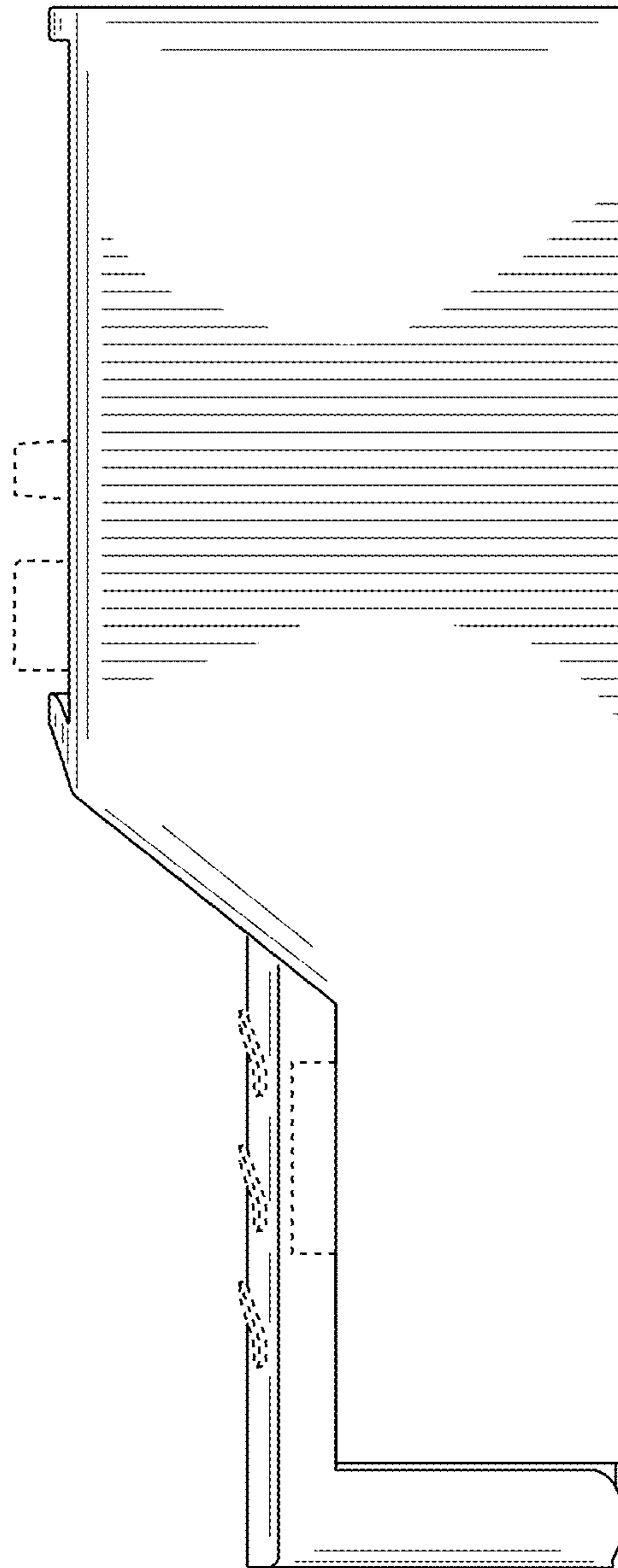


FIG. 6

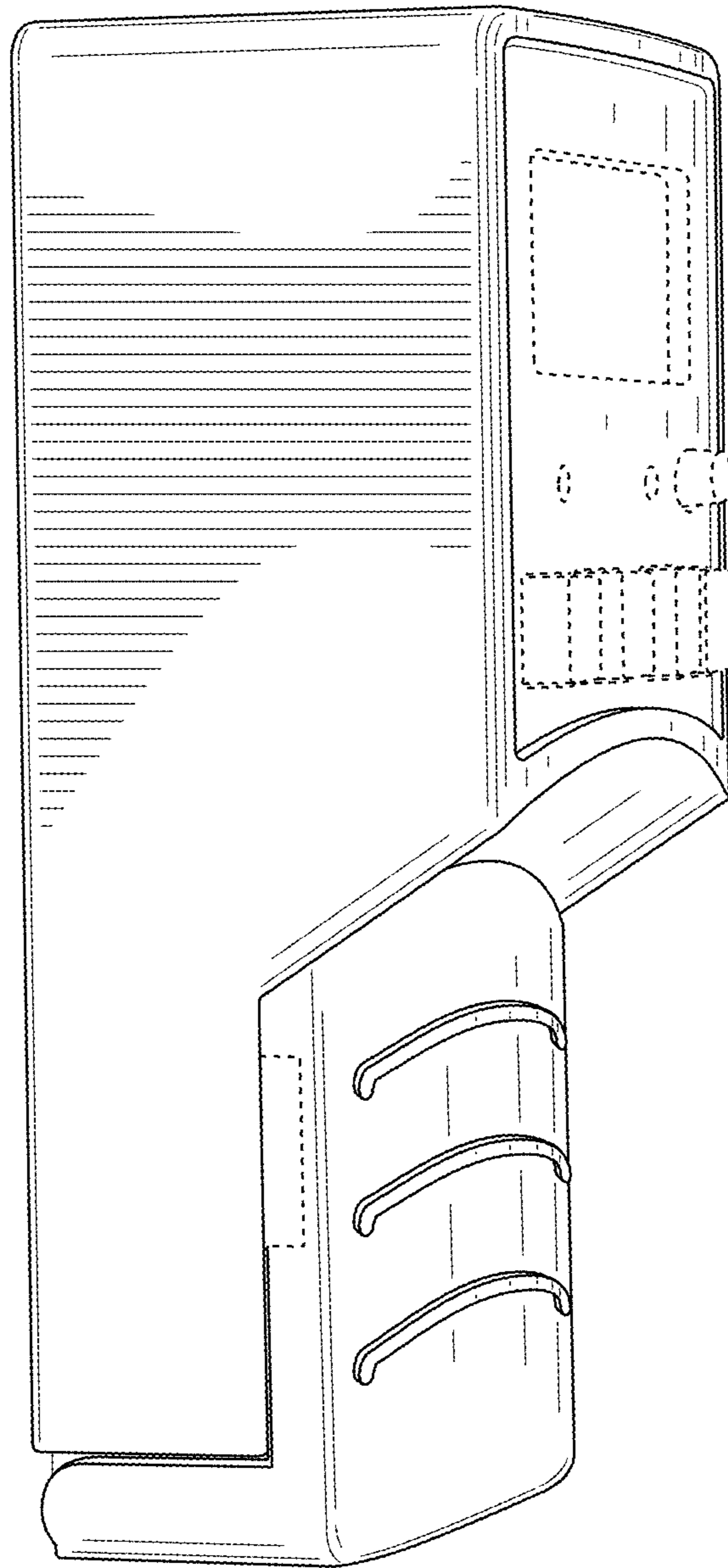


FIG. 7

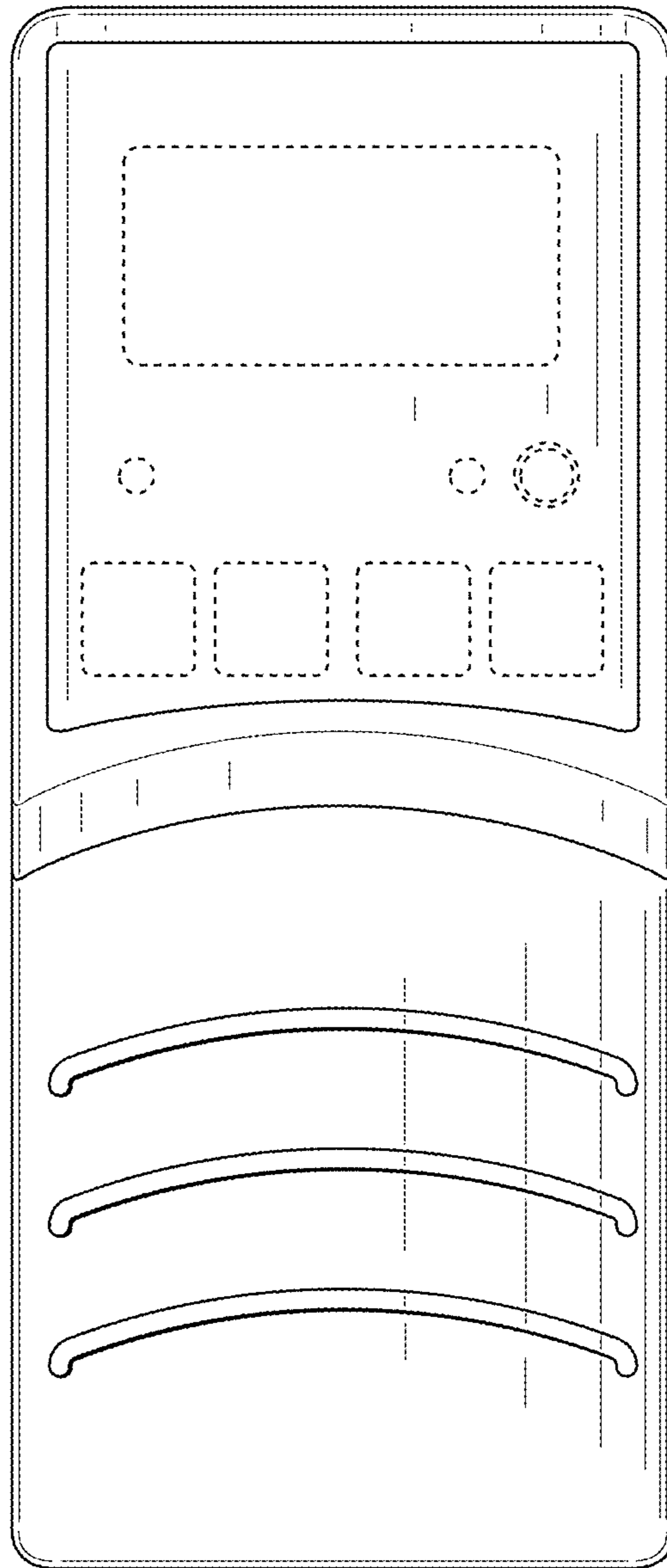


FIG. 8

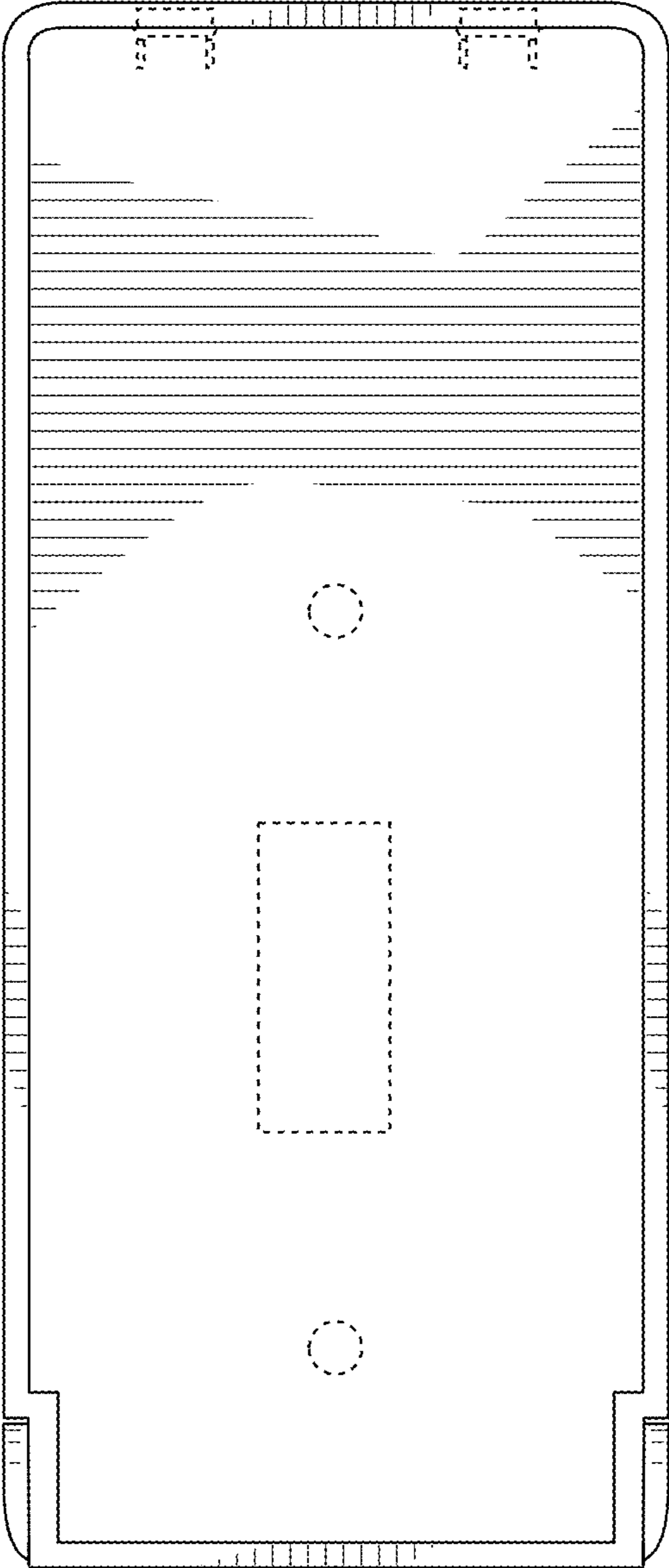


FIG. 9

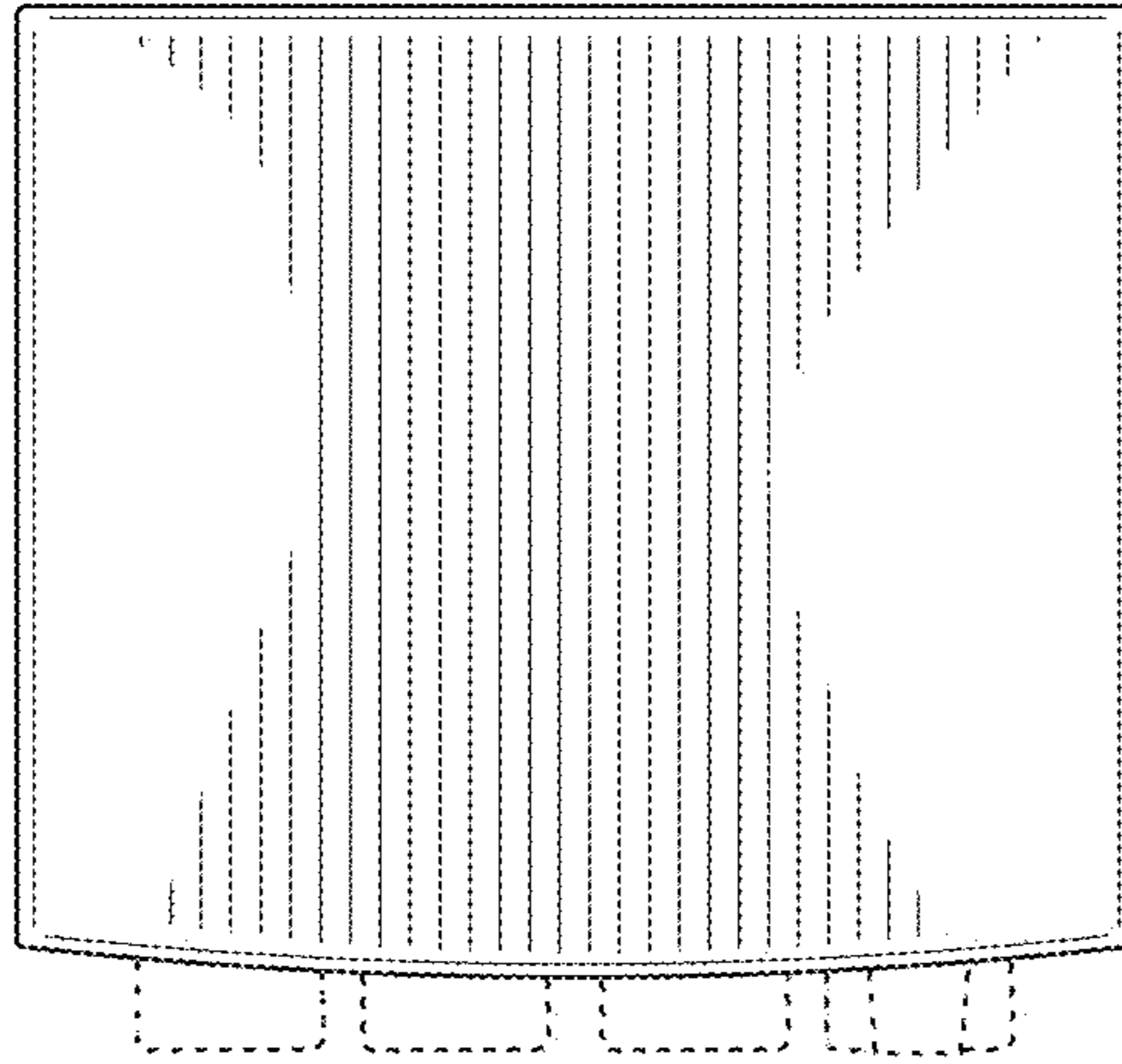


FIG. 10

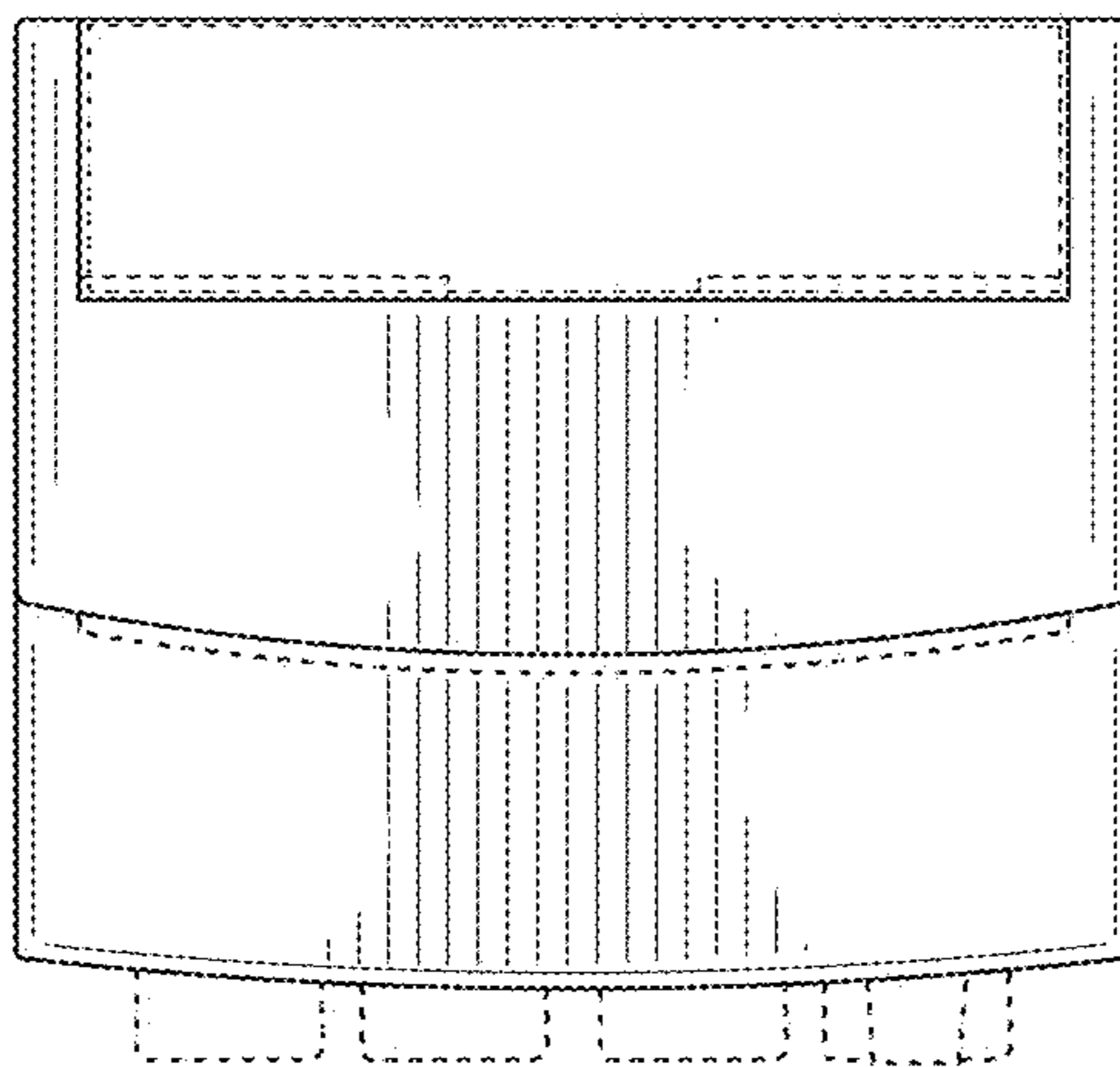


FIG. 11

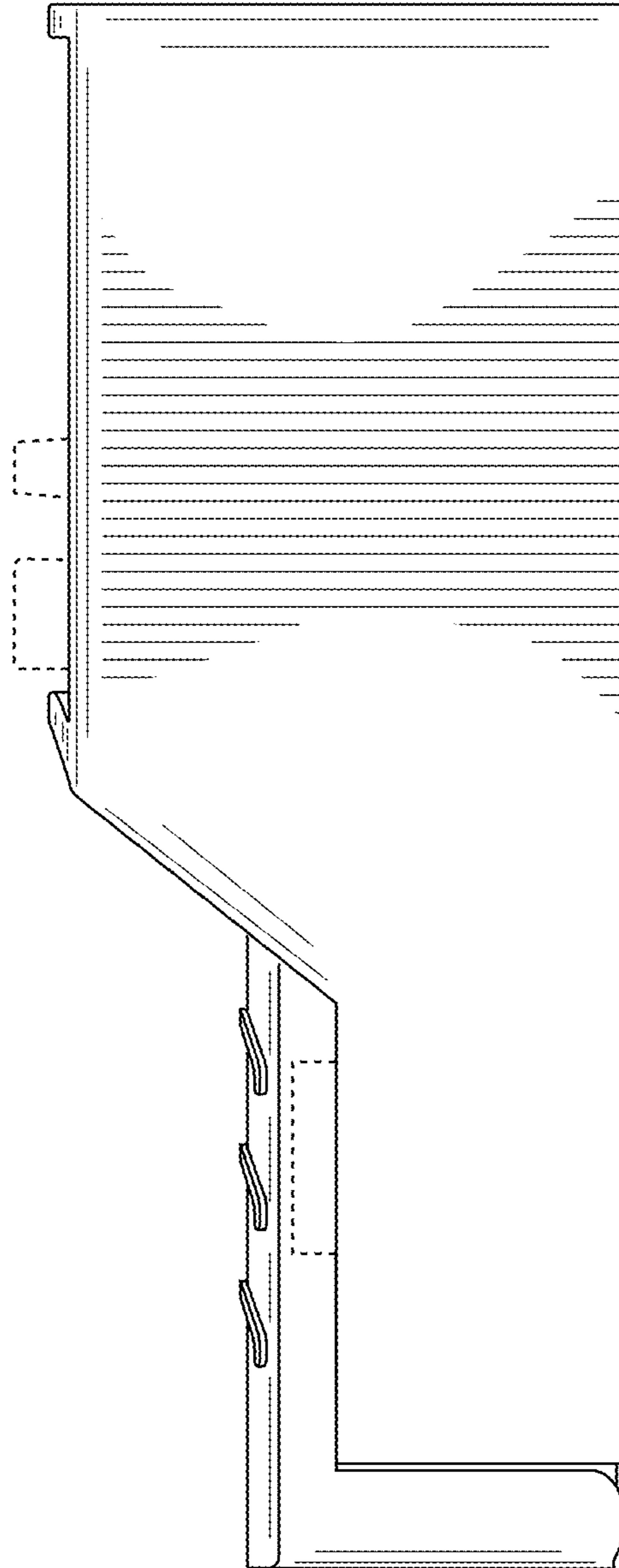


FIG. 12