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(12) **United States Design Patent**  
**DePaula**

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(54) **PROCESS CIRCUITRY INTERFACE**

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(\*\*) Term: **14 Years**

(21) Appl. No.: **29/417,017**

(22) Filed: **Mar. 29, 2012**

(51) **LOC (10) Cl.** ..... **14-02**

(52) **U.S. Cl.**  
USPC ..... **D14/357**

(58) **Field of Classification Search**

USPC ..... D14/300–302, 314, 348–370, 383–385,  
D14/435, 496, 125, 135, 155, 240–242,  
D14/299; D3/273; D9/432; D10/103, 119,  
D10/162, 184, 199; 455/550.1; 361/679.4,  
361/679.56, 679.03, 679.26; 340/7.54;  
439/717; 370/315

See application file for complete search history.

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(57) **CLAIM**

I claim the ornamental design for a process circuitry interface, as shown and described.

**DESCRIPTION**

FIG. 1 is a perspective view of a process circuitry interface according to an embodiment.

FIG. 2 is a left side elevational view thereof;

FIG. 3 is a front elevational view thereof;

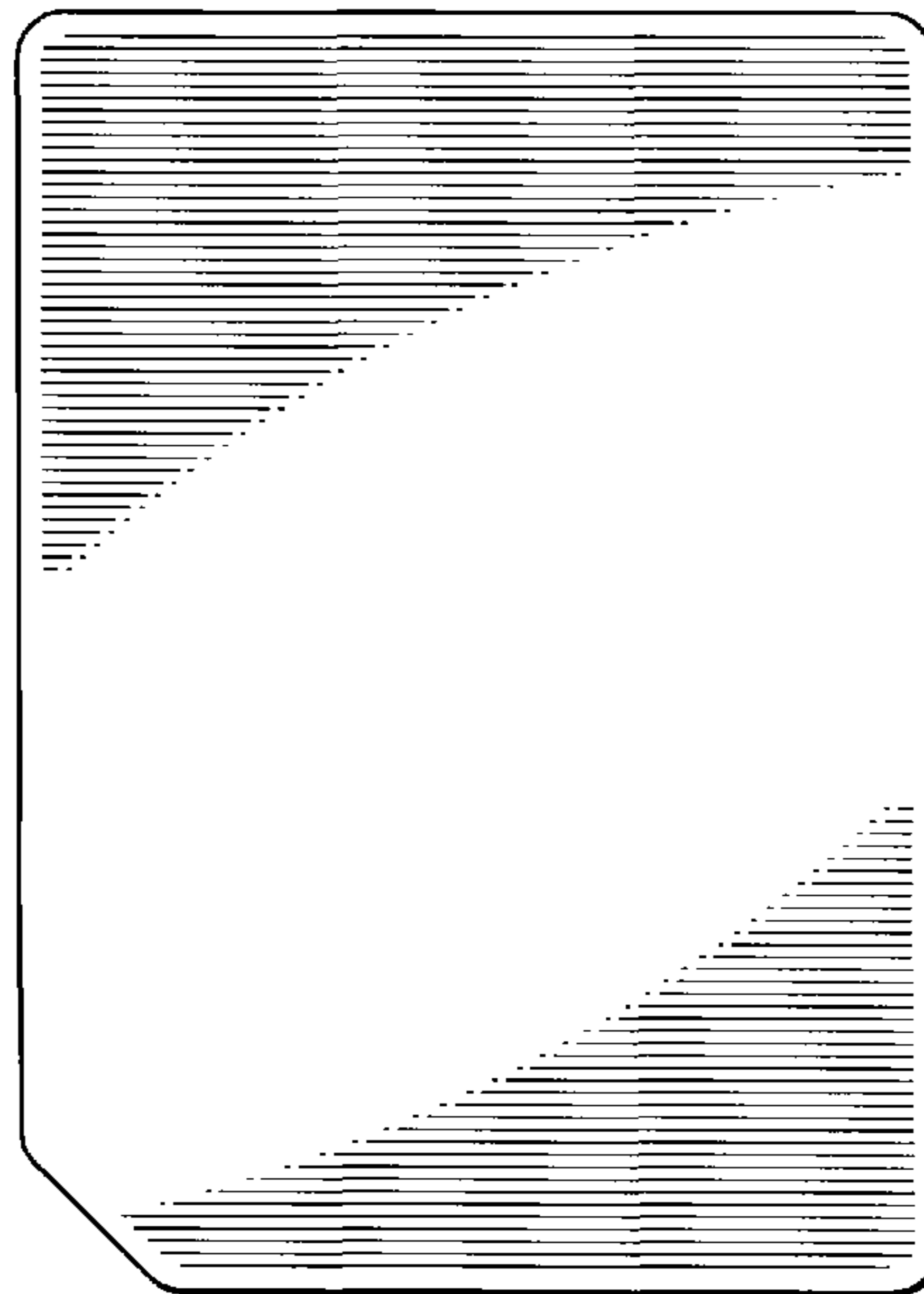
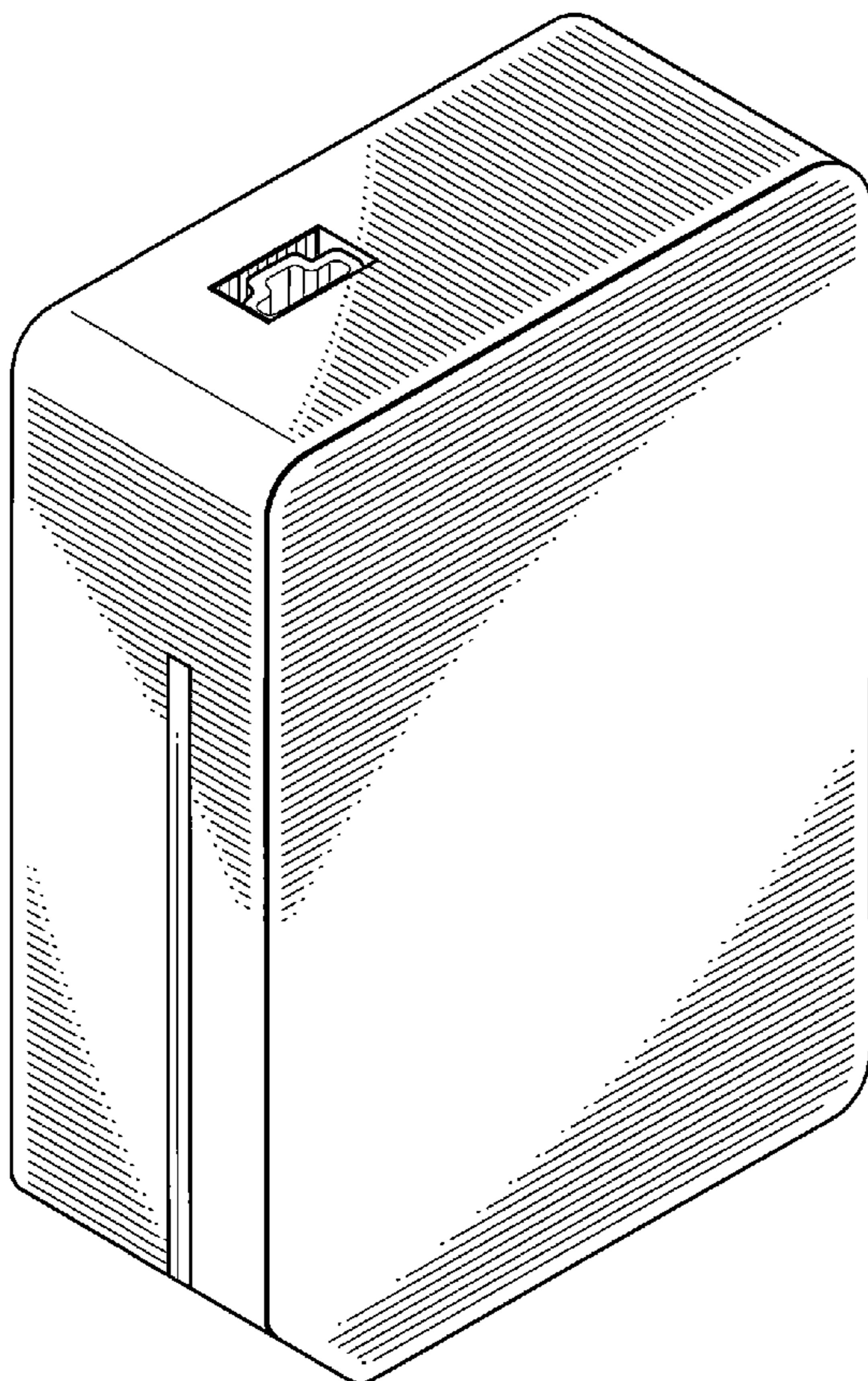
FIG. 4 is a rear elevational view thereof;

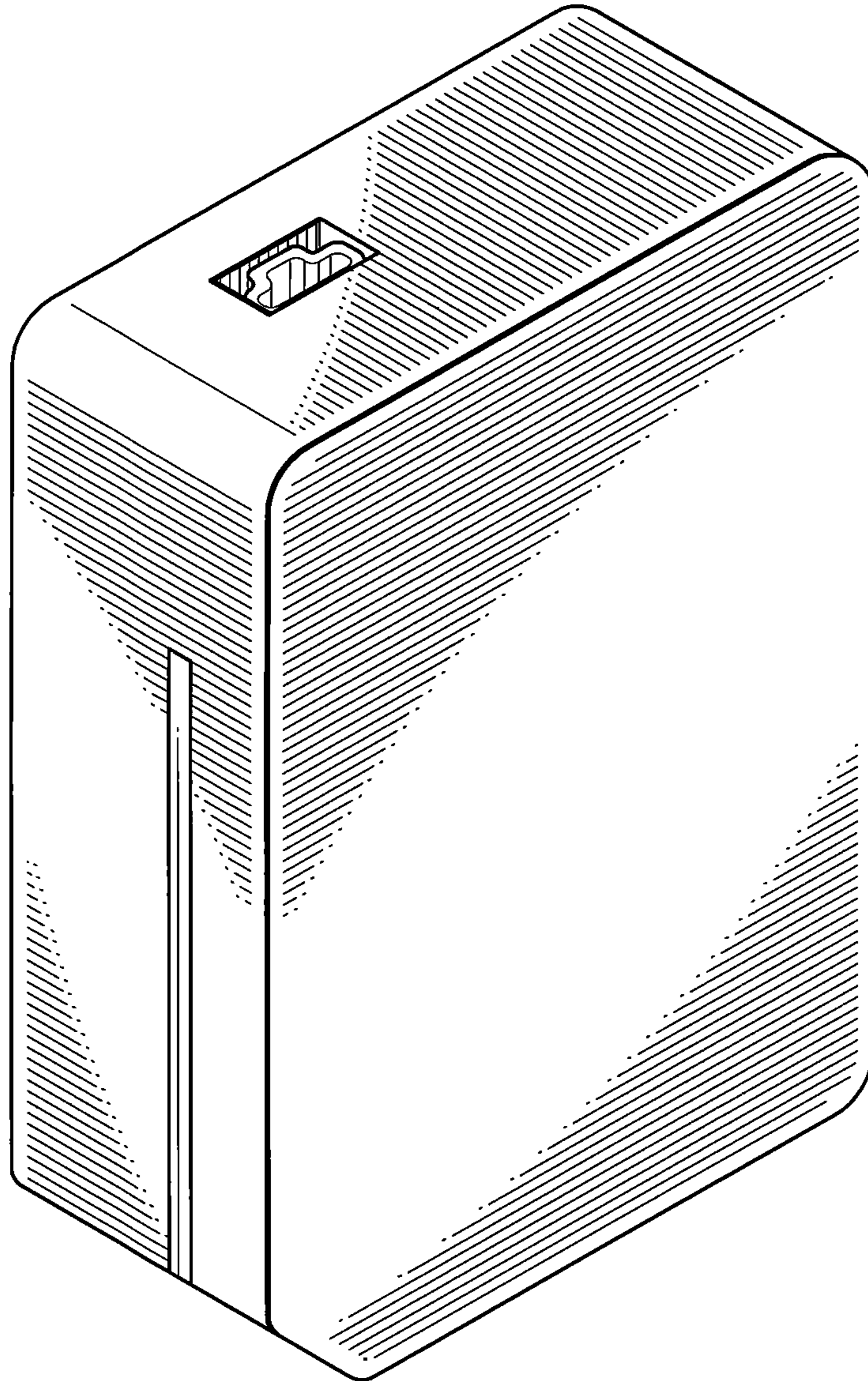
FIG. 5 is a right side elevational view thereof;

FIG. 6 is a top plan view thereof; and,

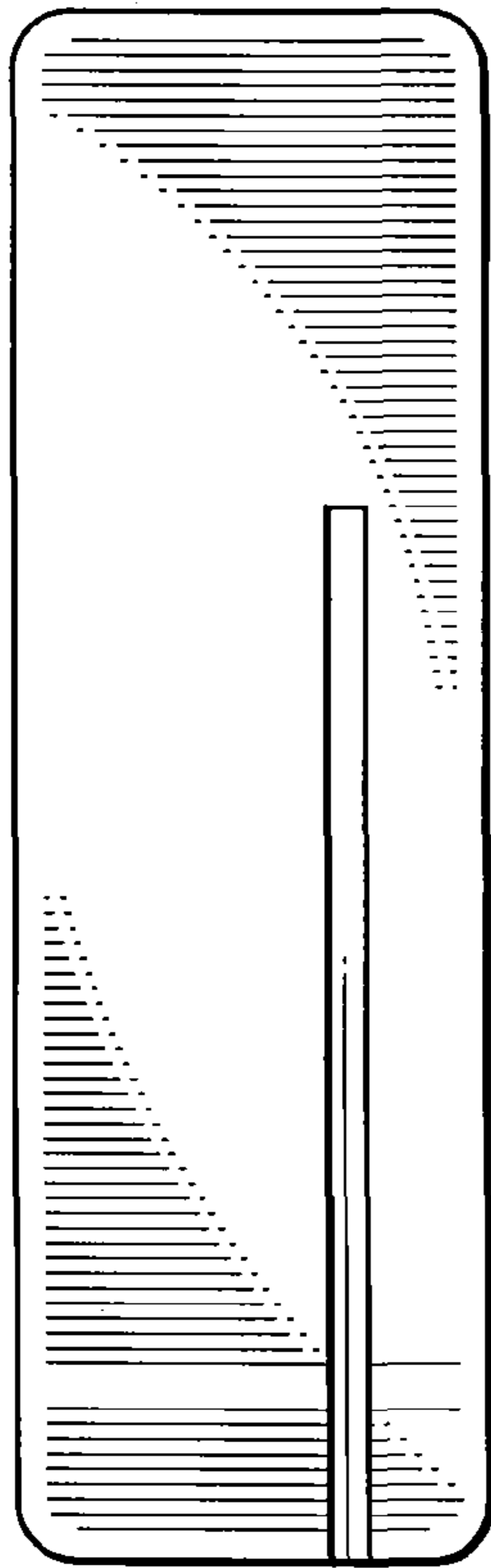
FIG. 7 is a bottom plan view thereof.

**1 Claim, 4 Drawing Sheets**

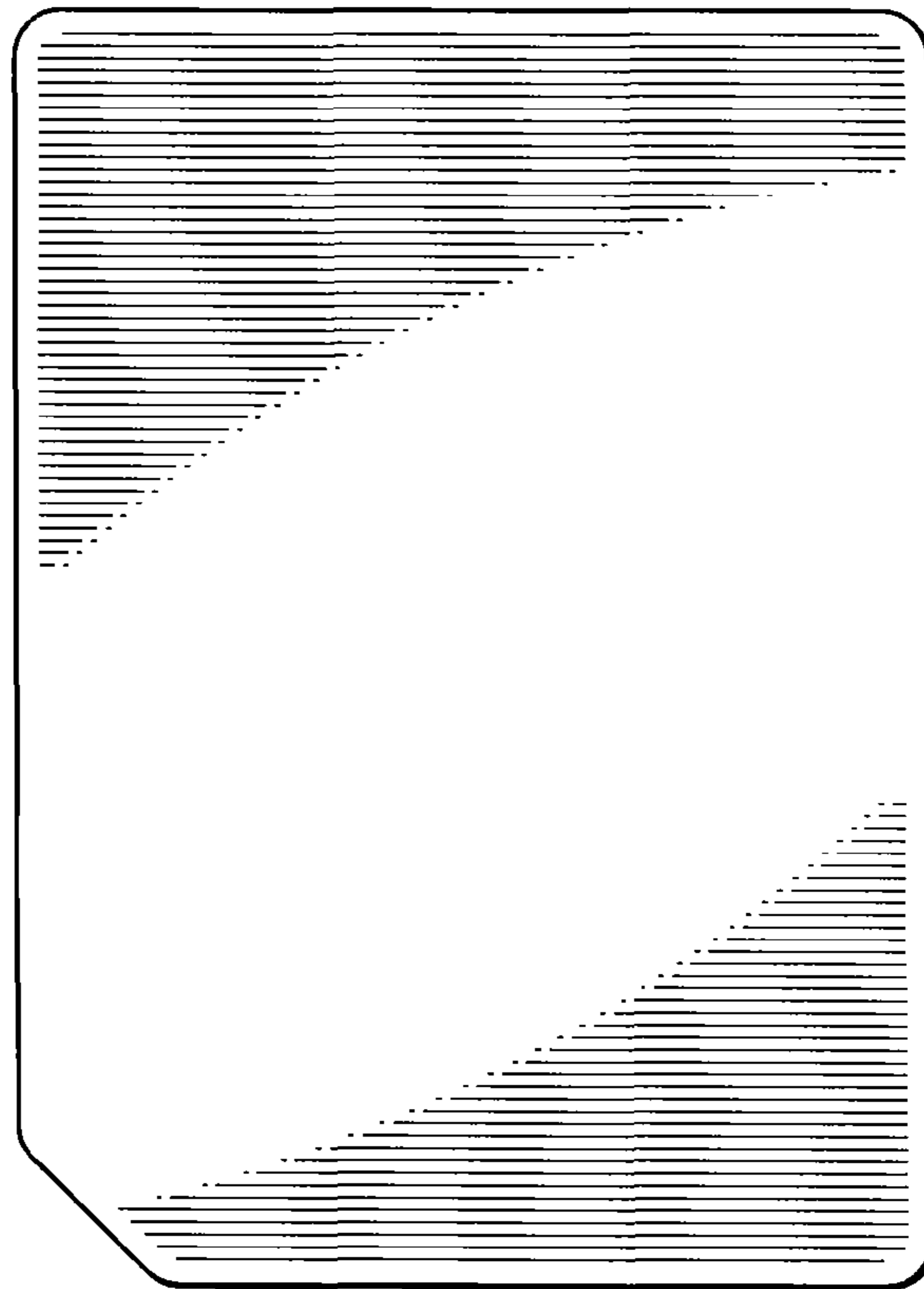




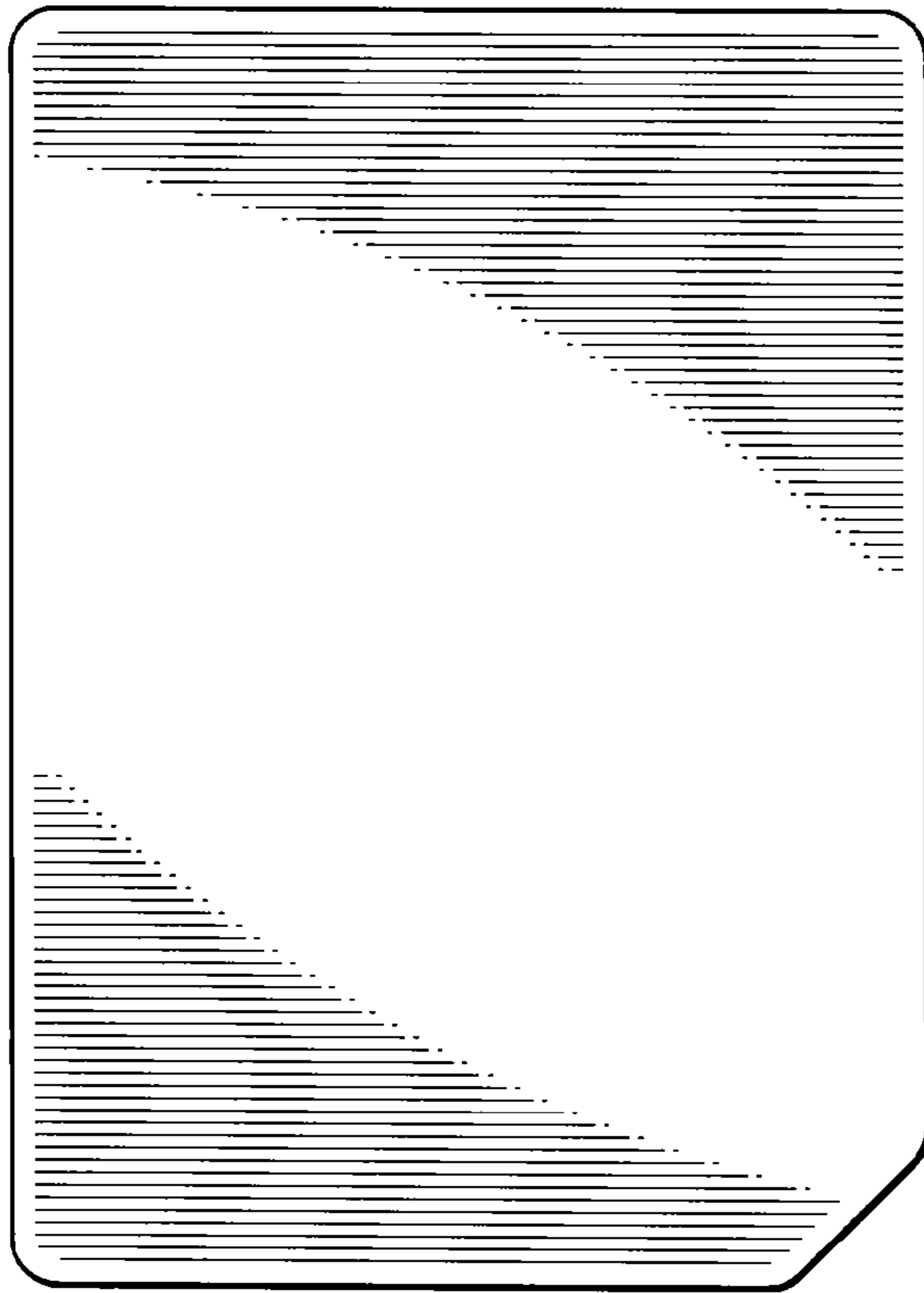
**FIG. 1**



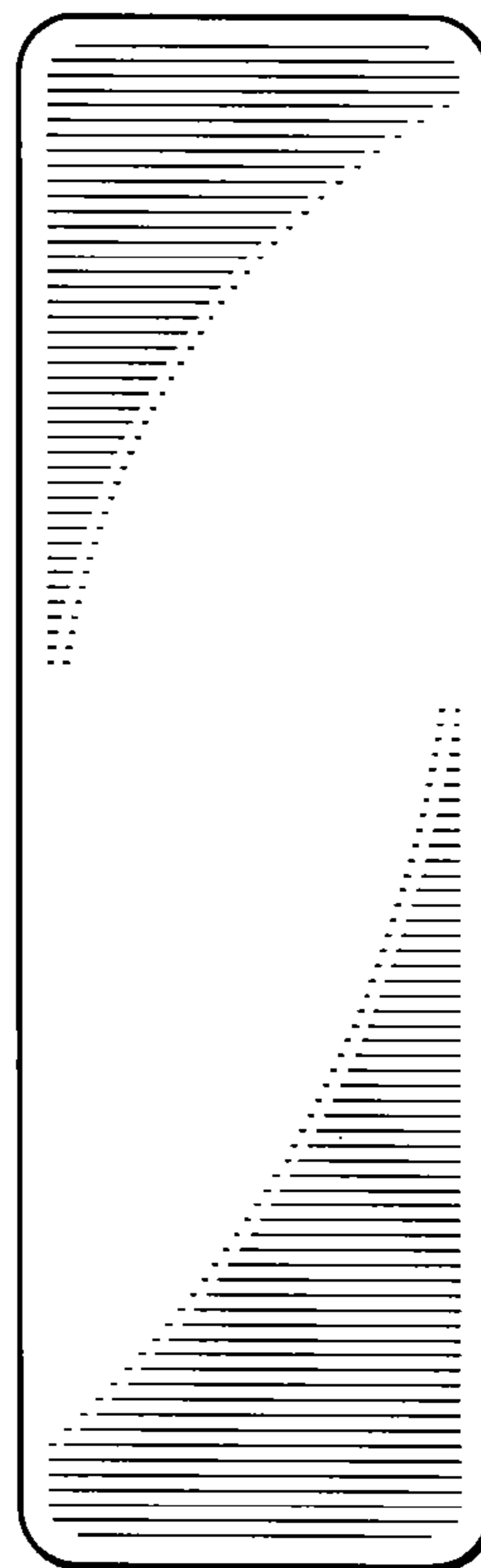
**FIG. 2**



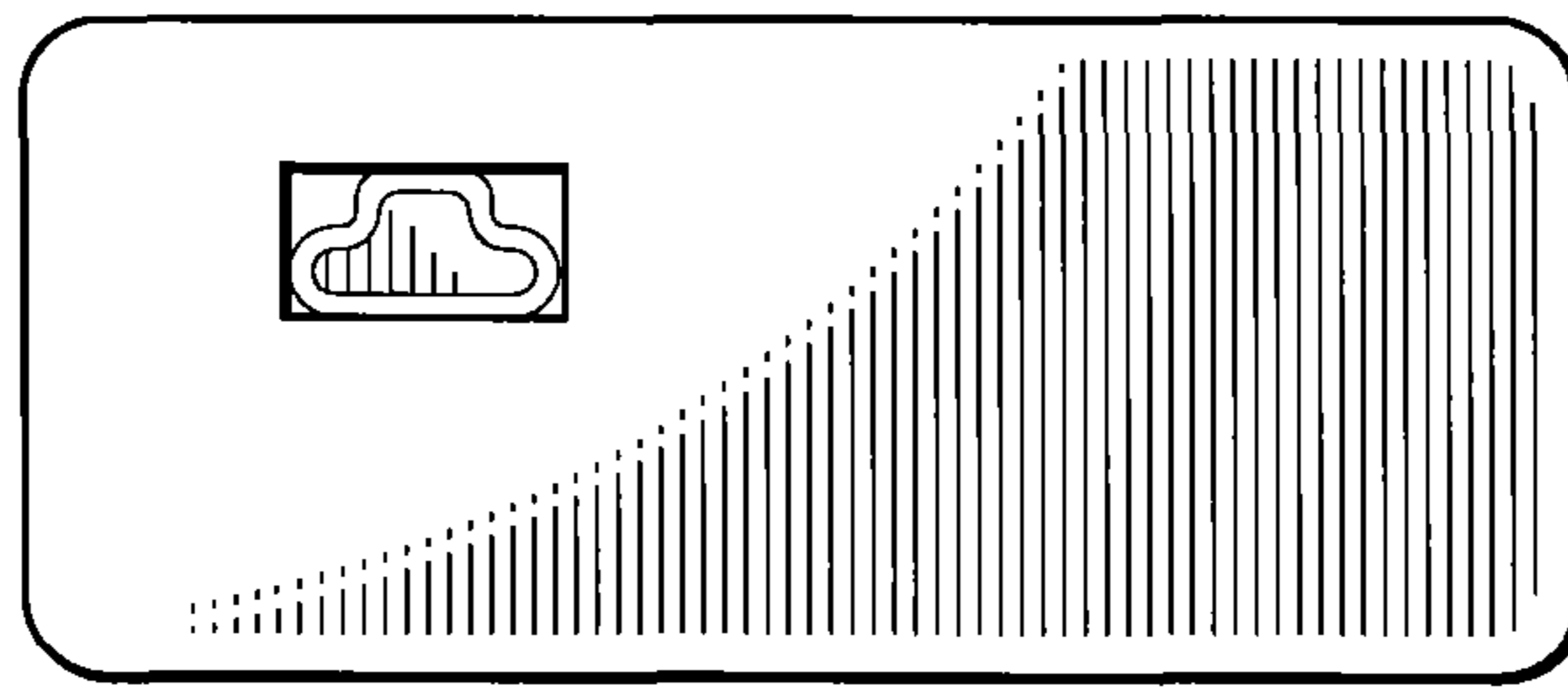
**FIG. 3**



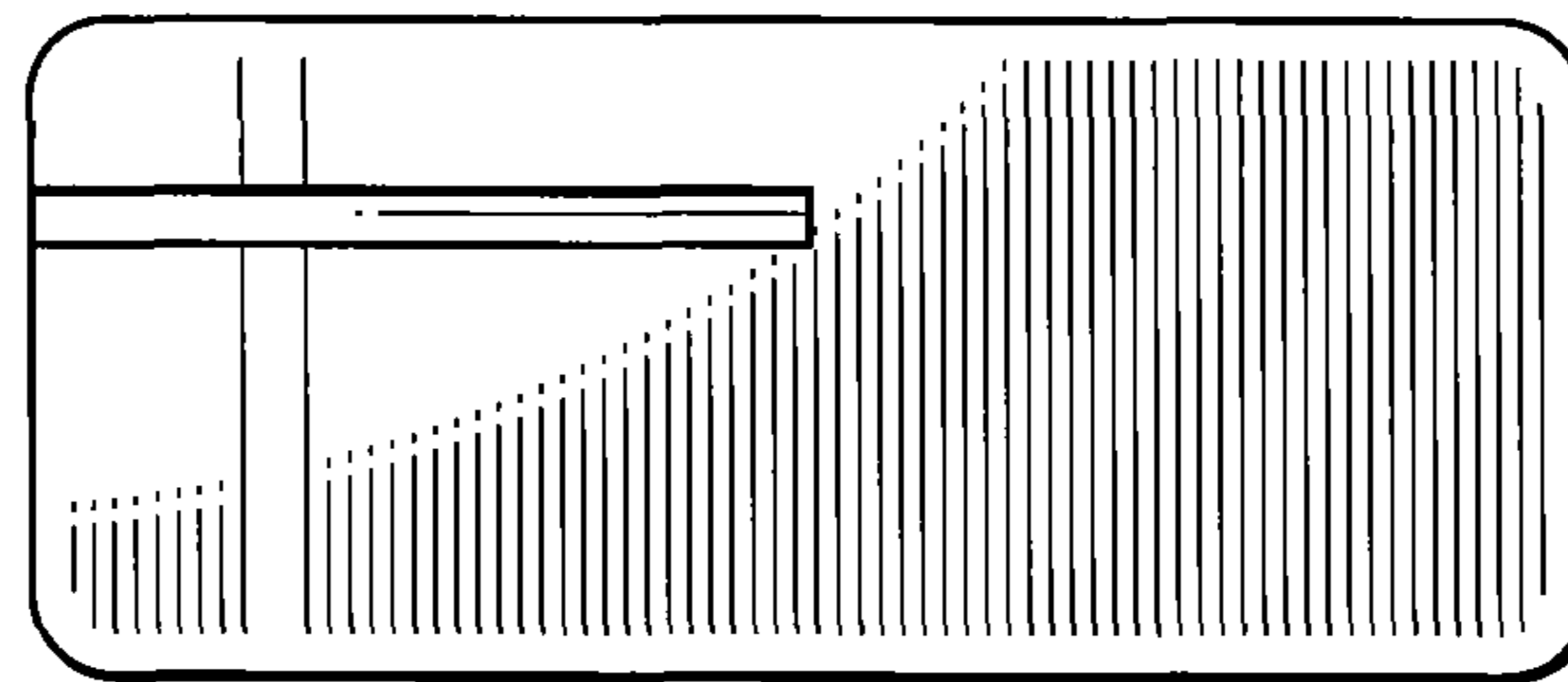
**FIG. 4**



**FIG. 5**



**FIG. 6**



**FIG. 7**