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(12) **United States Design Patent**
Naito

(10) **Patent No.:** **US D696,632 S**
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(54) **ELECTRICAL CONNECTOR**

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(73) Assignee: **Japan Aviation Electronics Industry, Limited**, Tokyo (JP)

(**) Term: **14 Years**

(21) Appl. No.: **29/411,097**

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(30) **Foreign Application Priority Data**

Jul. 28, 2011 (JP) 2011-017363

(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/147**

(58) **Field of Classification Search**
USPC D13/147, 154, 184, 199; 439/79,
439/159-160, 260, 278, 325, 328-329, 381,
439/395, 422, 492, 495, 499, 630, 862, 892,
439/894

See application file for complete search history.

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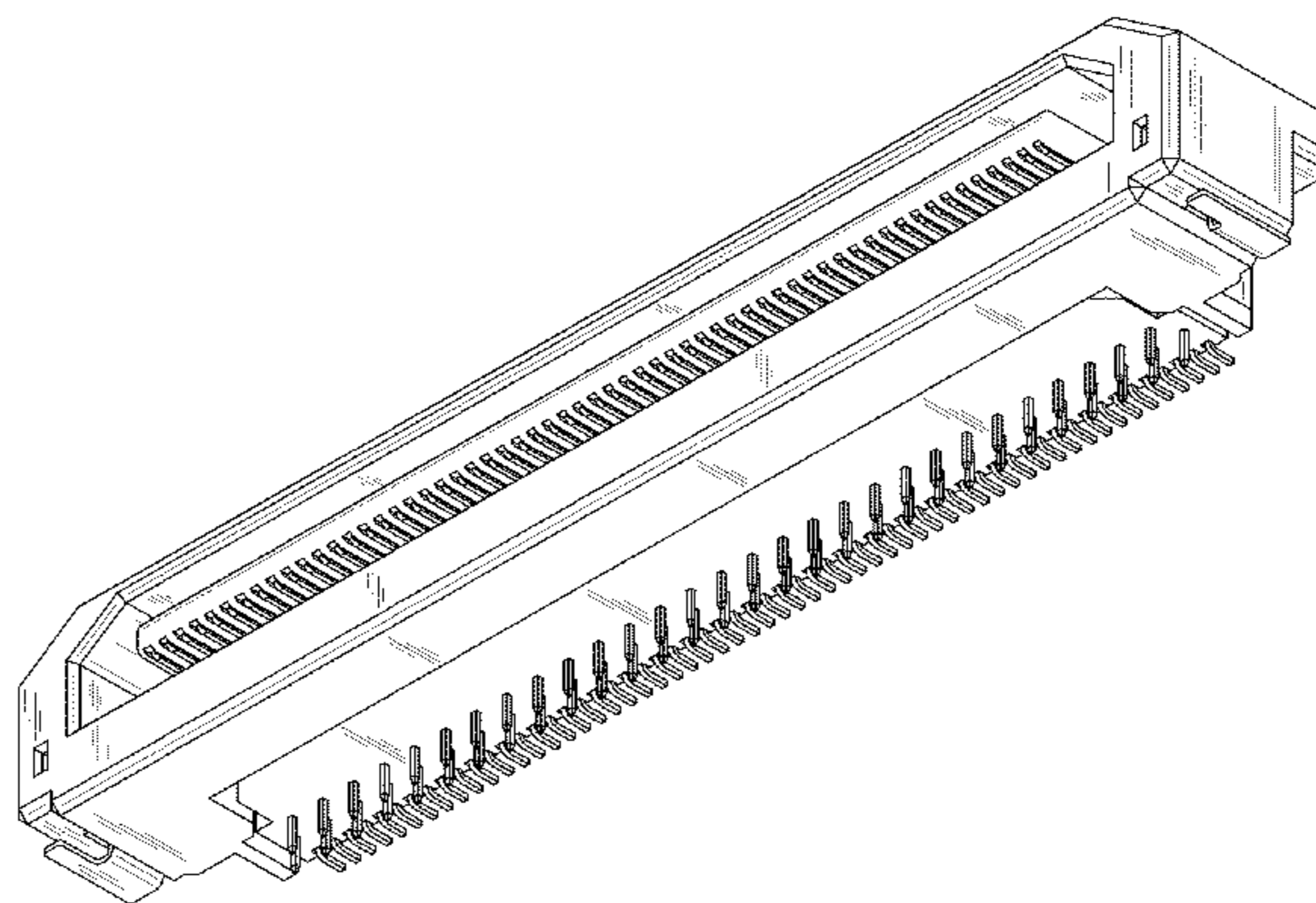
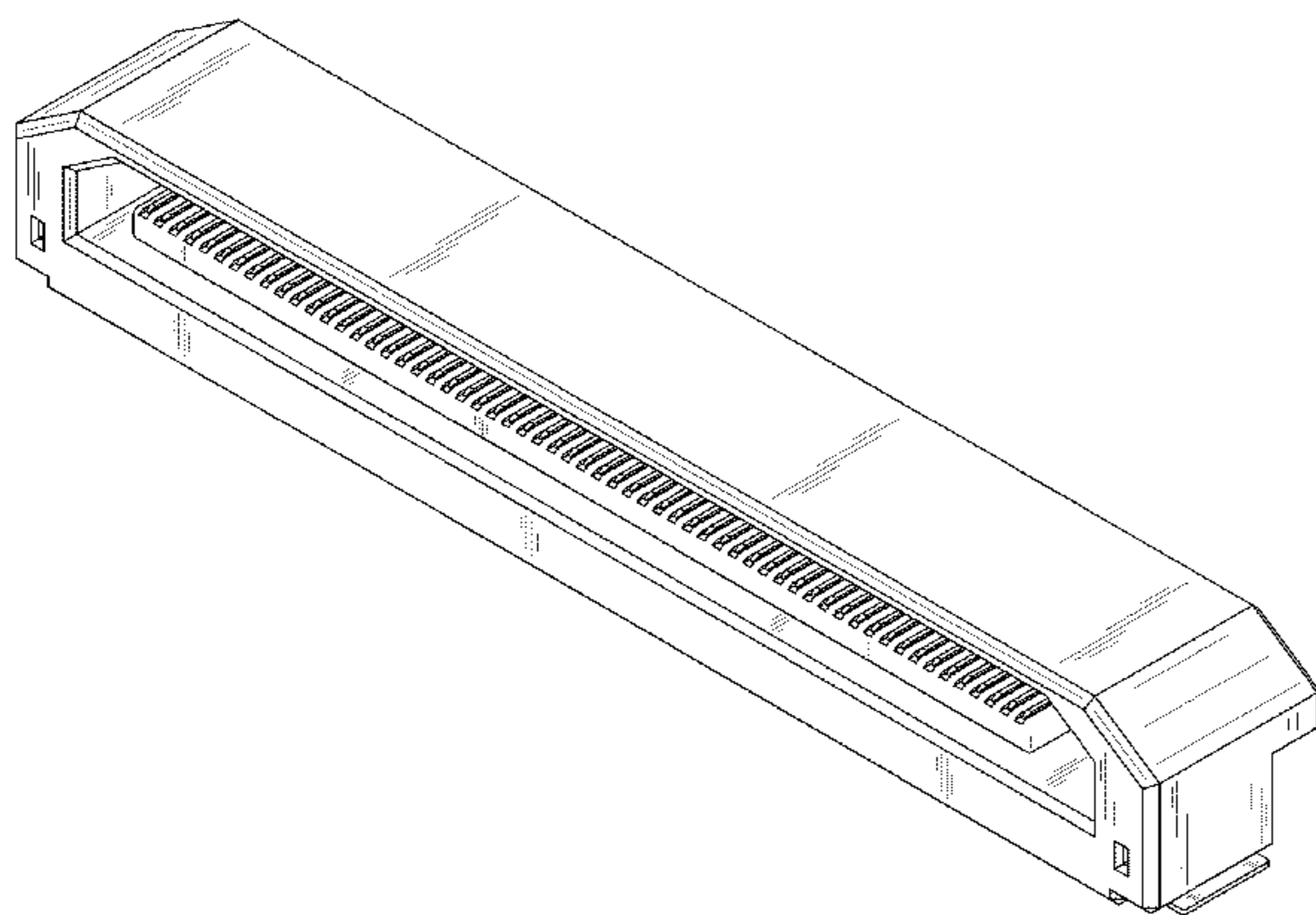
(57) **CLAIM**

The ornamental design for an electrical connector, as shown and described.

DESCRIPTION

FIG. 1 is a front elevational view of an electrical connector, showing my new design;
FIG. 2 is a rear elevational view thereof;
FIG. 3 is a right side elevational view thereof;
FIG. 4 is a left side elevational view thereof;
FIG. 5 is a top plan view thereof;
FIG. 6 is a bottom plan view thereof;
FIG. 7 is an enlarged cross-sectional view, taken along line 7-7 of FIG. 1;
FIG. 8 is a right front perspective view showing the top thereof;
FIG. 9 a left rear perspective view showing the bottom thereof;
FIG. 10 is a right front perspective view showing the bottom thereof; and,
FIG. 11 is a left rear perspective view showing the top thereof.
The broken lines illustrate environmental structure and form no part of the claimed design.
The dash-dot lines represent the boundaries of the claimed design.

1 Claim, 9 Drawing Sheets



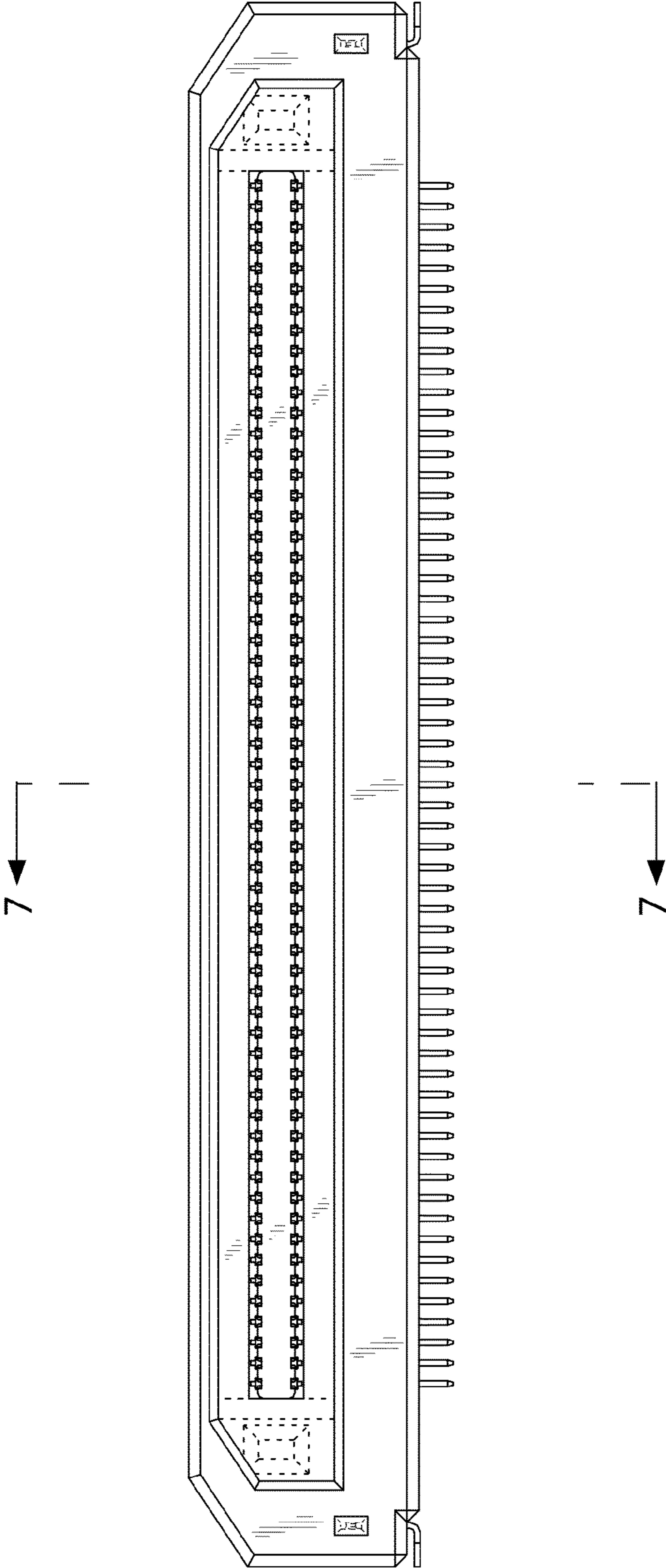


FIG.1

FIG.2

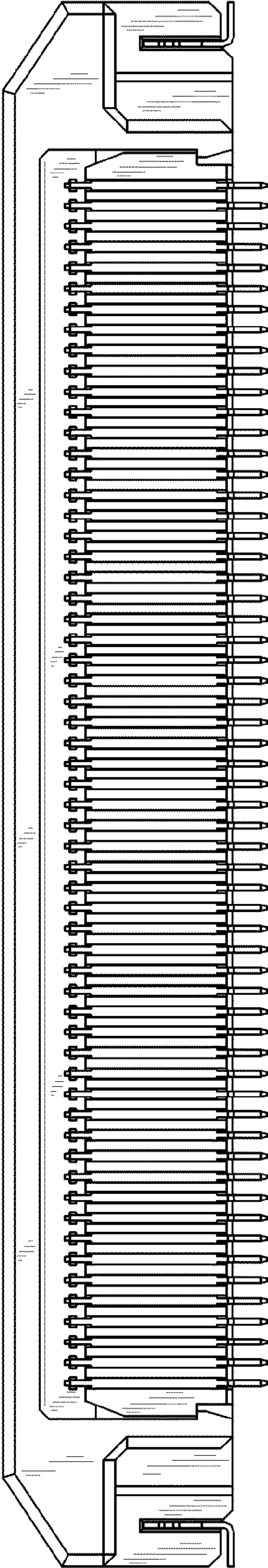


FIG.4

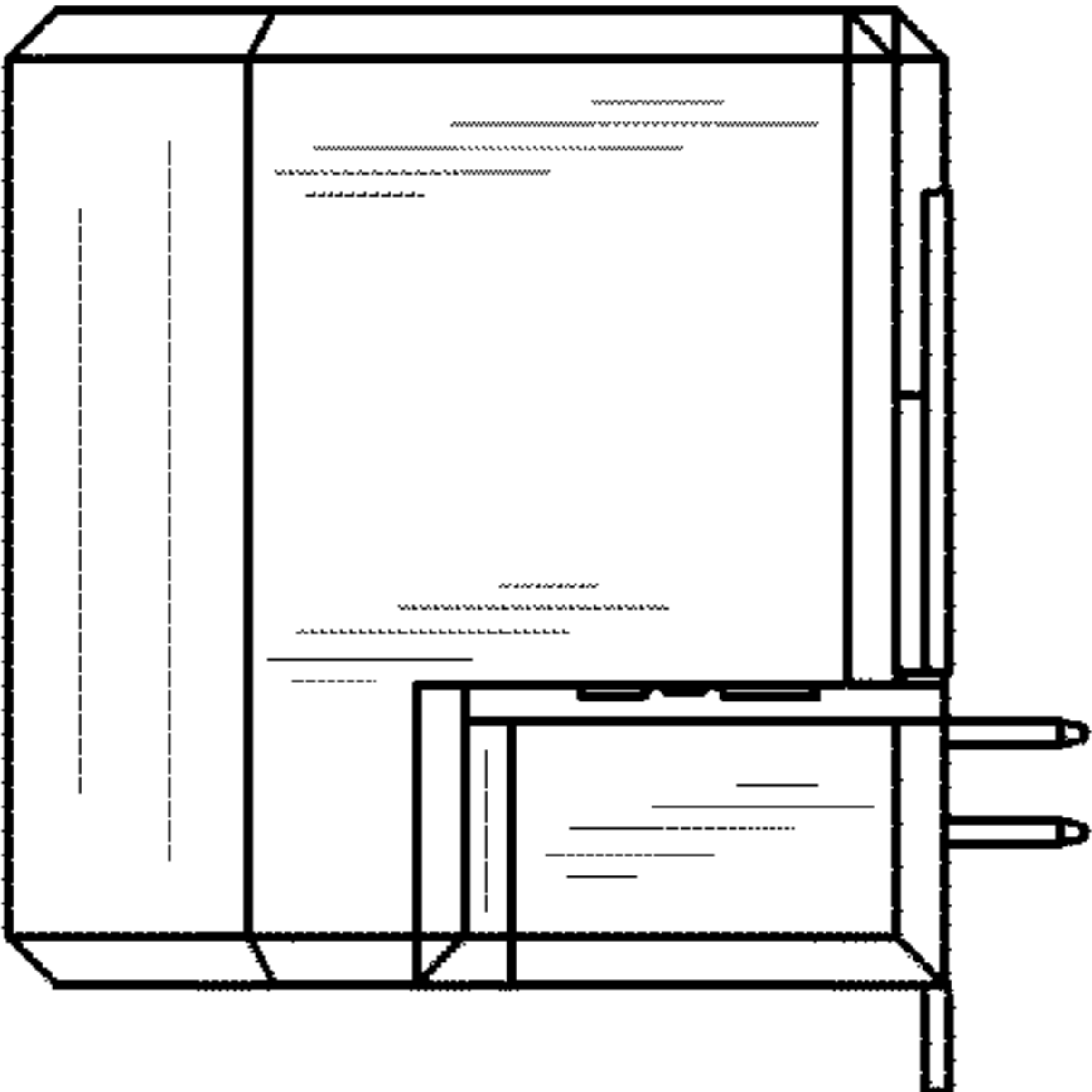


FIG.3

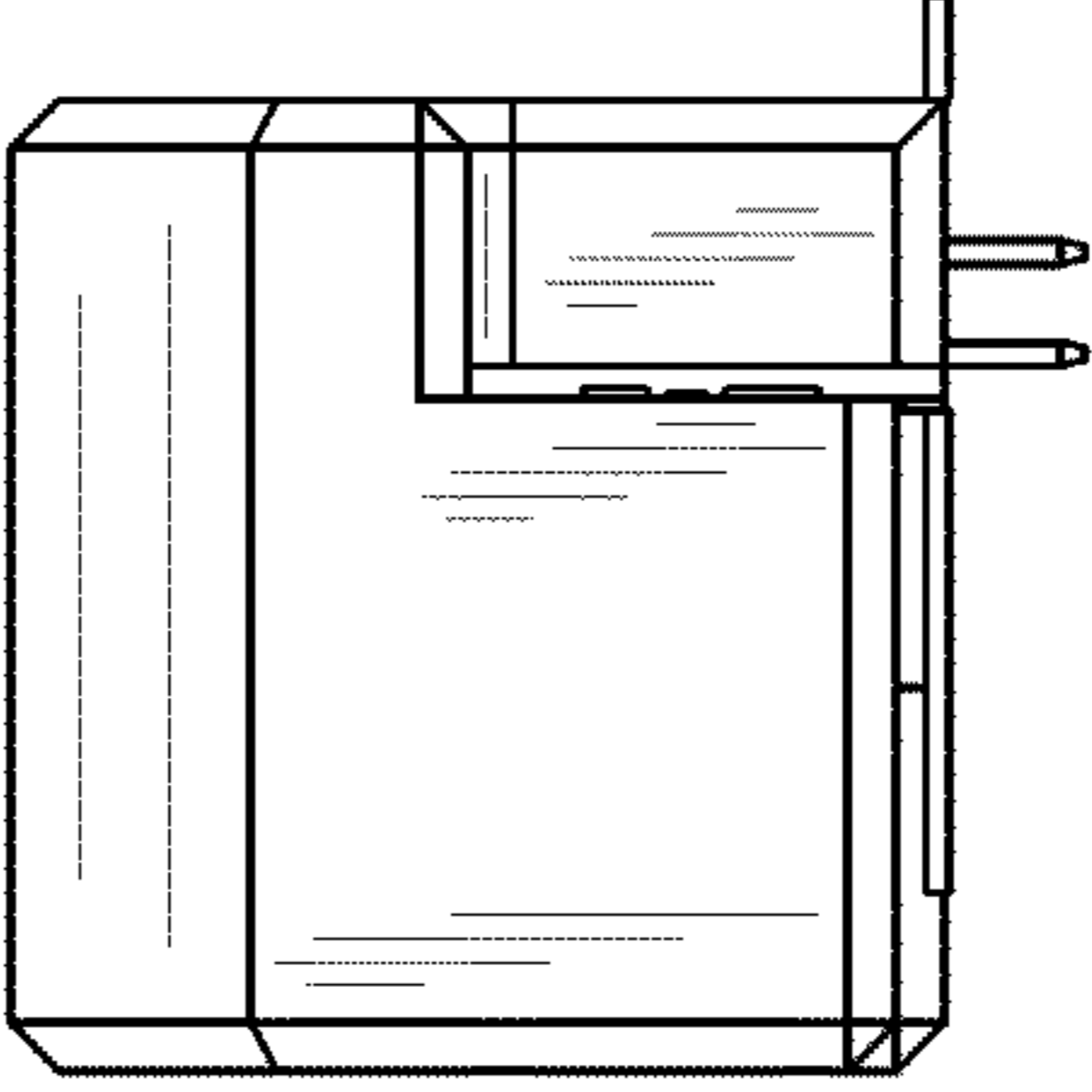


FIG.5

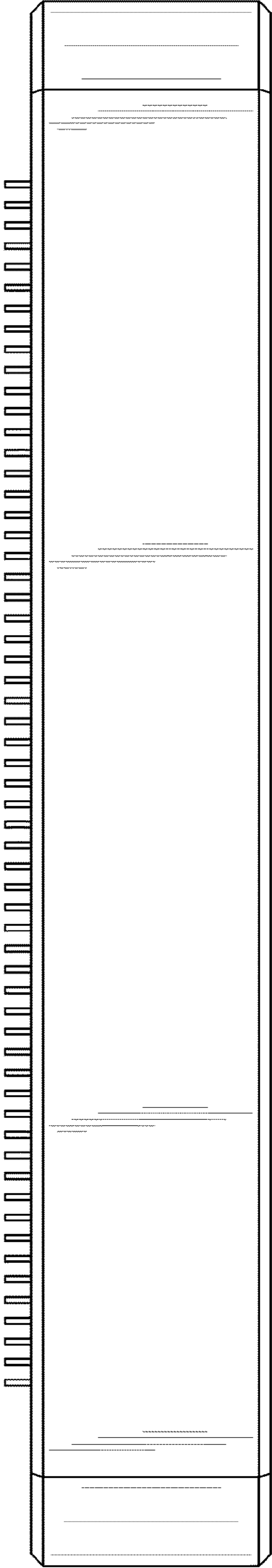


FIG.6

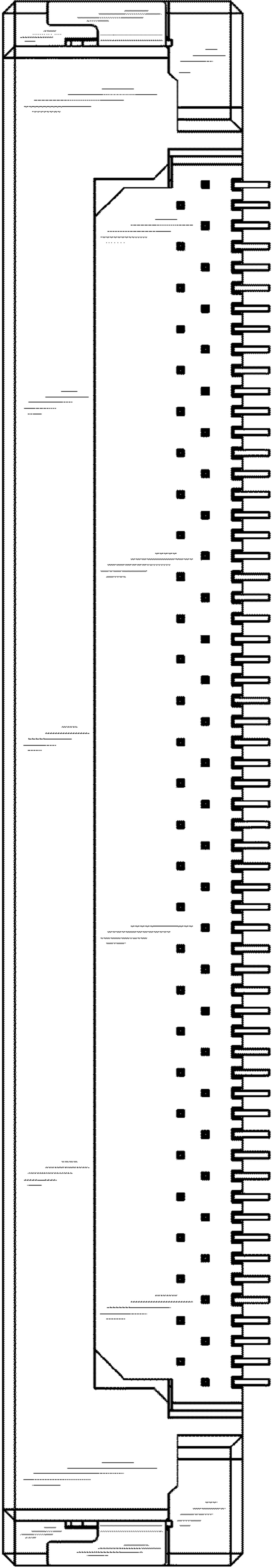


FIG.7

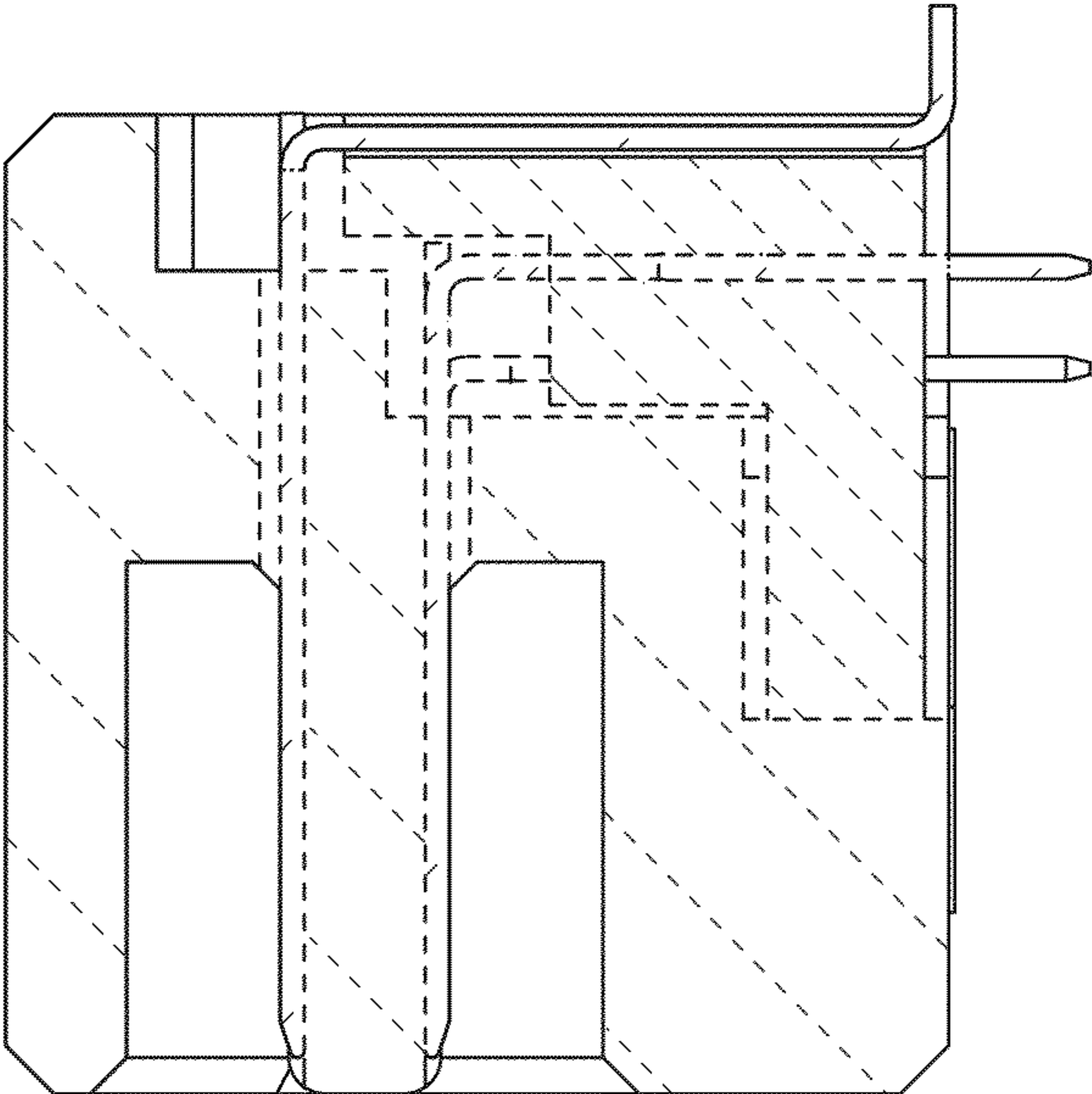
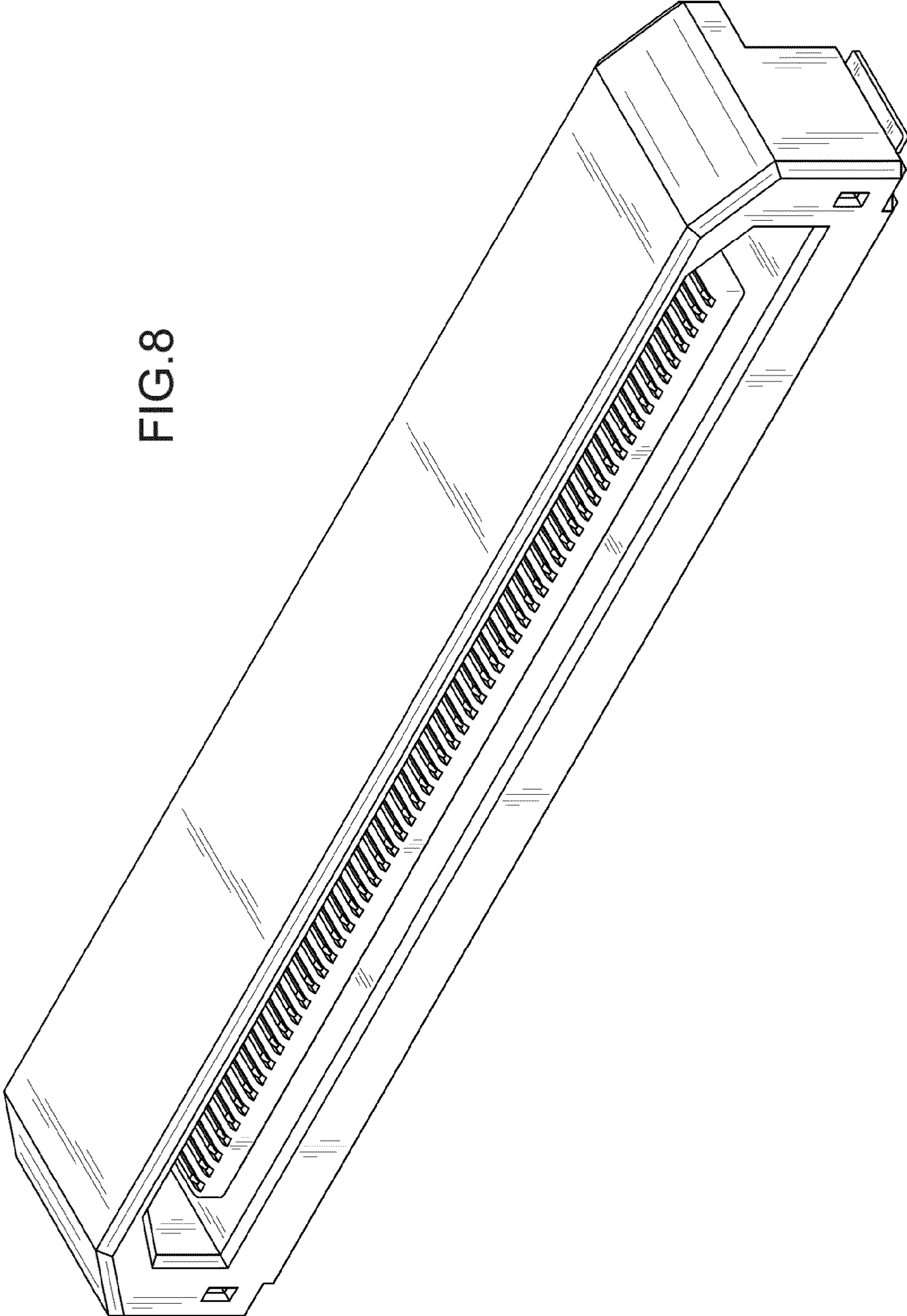


FIG.8



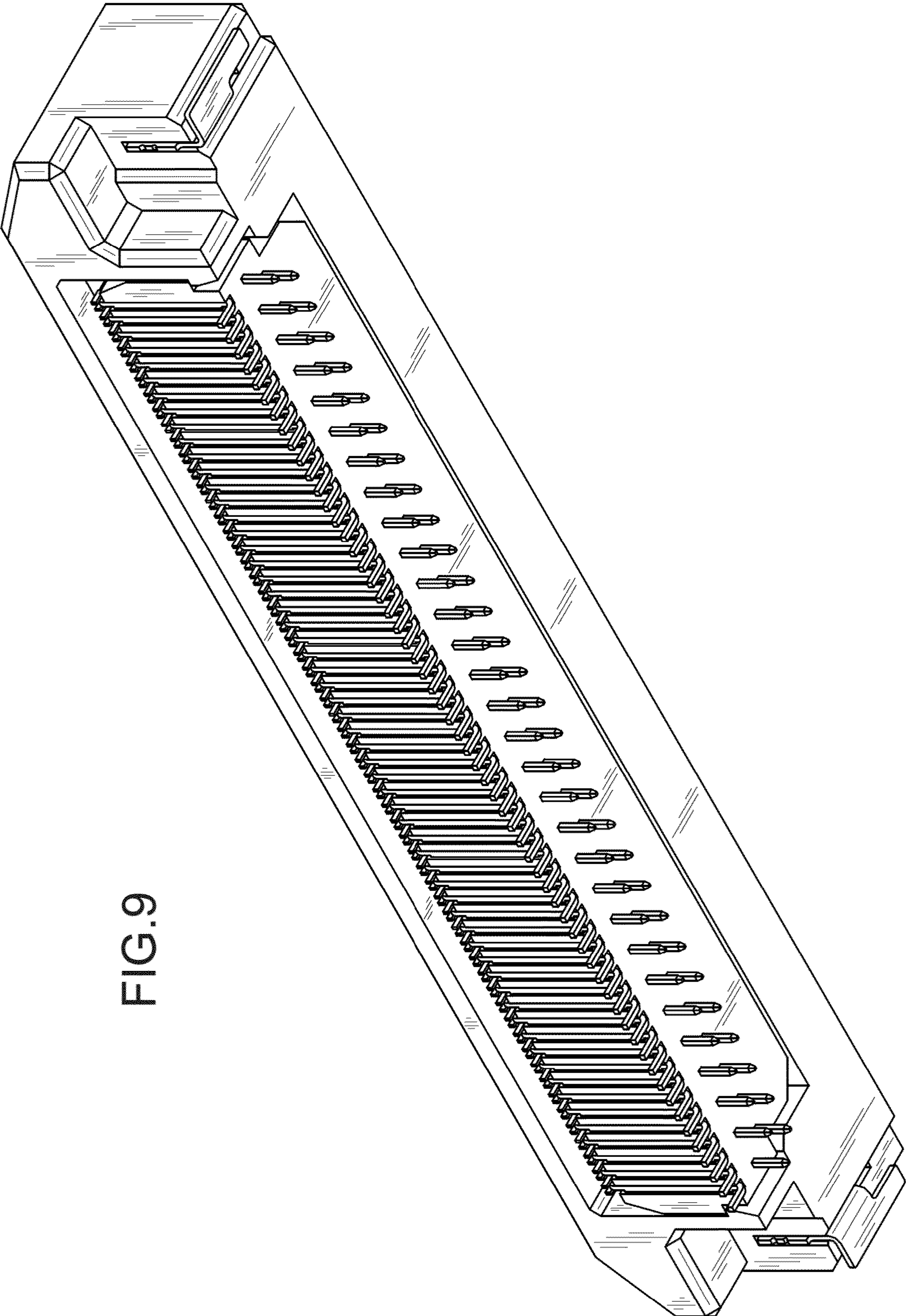


FIG.9

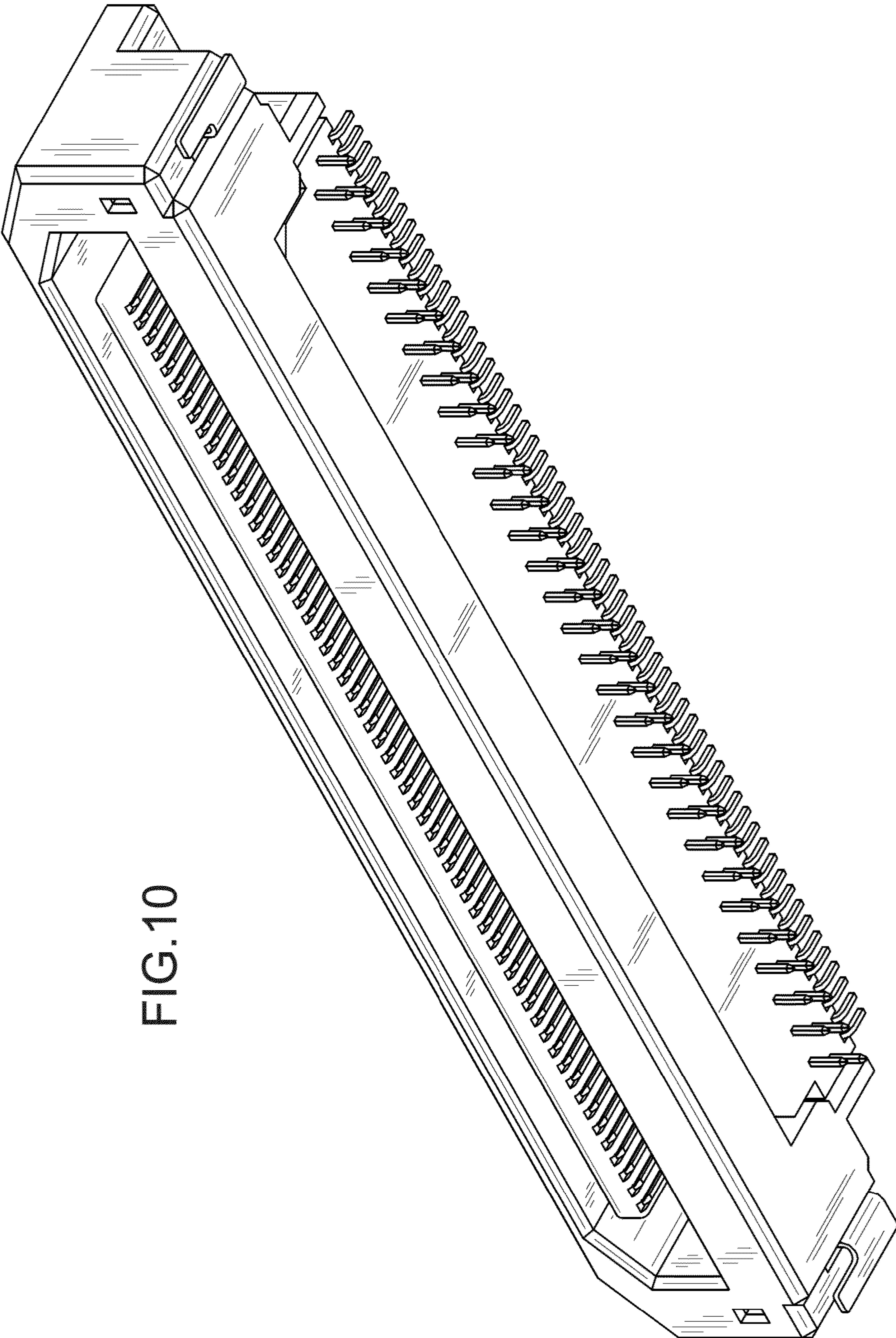


FIG.10

FIG.11

