

US00D693494S

(12) **United States Design Patent**
Hisayasu et al.

(10) **Patent No.:** **US D693,494 S**

(45) **Date of Patent:** **** Nov. 12, 2013**

(54) **LIGHT EMITTING DIODE MODULE**

(75) Inventors: **Takeshi Hisayasu**, Kanagawa (JP);
Hiroshi Takenaga, Kanagawa (JP)

(73) Assignee: **Toshiba Lighting & Technology Corporation**, Kanagawa (JP)

(**) Term: **14 Years**

(21) Appl. No.: **29/413,031**

(22) Filed: **Feb. 9, 2012**

(30) **Foreign Application Priority Data**

Aug. 12, 2011 (JP) 2011-018518

(51) **LOC (9) Cl.** **26-04**

(52) **U.S. Cl.**
USPC **D26/1; D13/180**

(58) **Field of Classification Search**
USPC D26/1; D13/180-182; 313/313, 315,
313/316, 317, 318, 493; 362/555, 800
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D528,996 S * 9/2006 Egawa D13/180
D573,553 S * 7/2008 Uemoto et al. D13/180

D577,690 S * 9/2008 Shida et al. D13/180
D598,401 S * 8/2009 Takada et al. D13/182
D622,876 S * 8/2010 Takahashi et al. D26/1
D636,899 S * 4/2011 Shibahara D26/1
D683,482 S * 5/2013 Bierhuizen et al. D26/1

FOREIGN PATENT DOCUMENTS

JP D1252006 9/2005
JP D1329744 5/2008
JP D1340857 9/2008

* cited by examiner

Primary Examiner — Marcus Jackson

(74) *Attorney, Agent, or Firm* — J.C. Patents

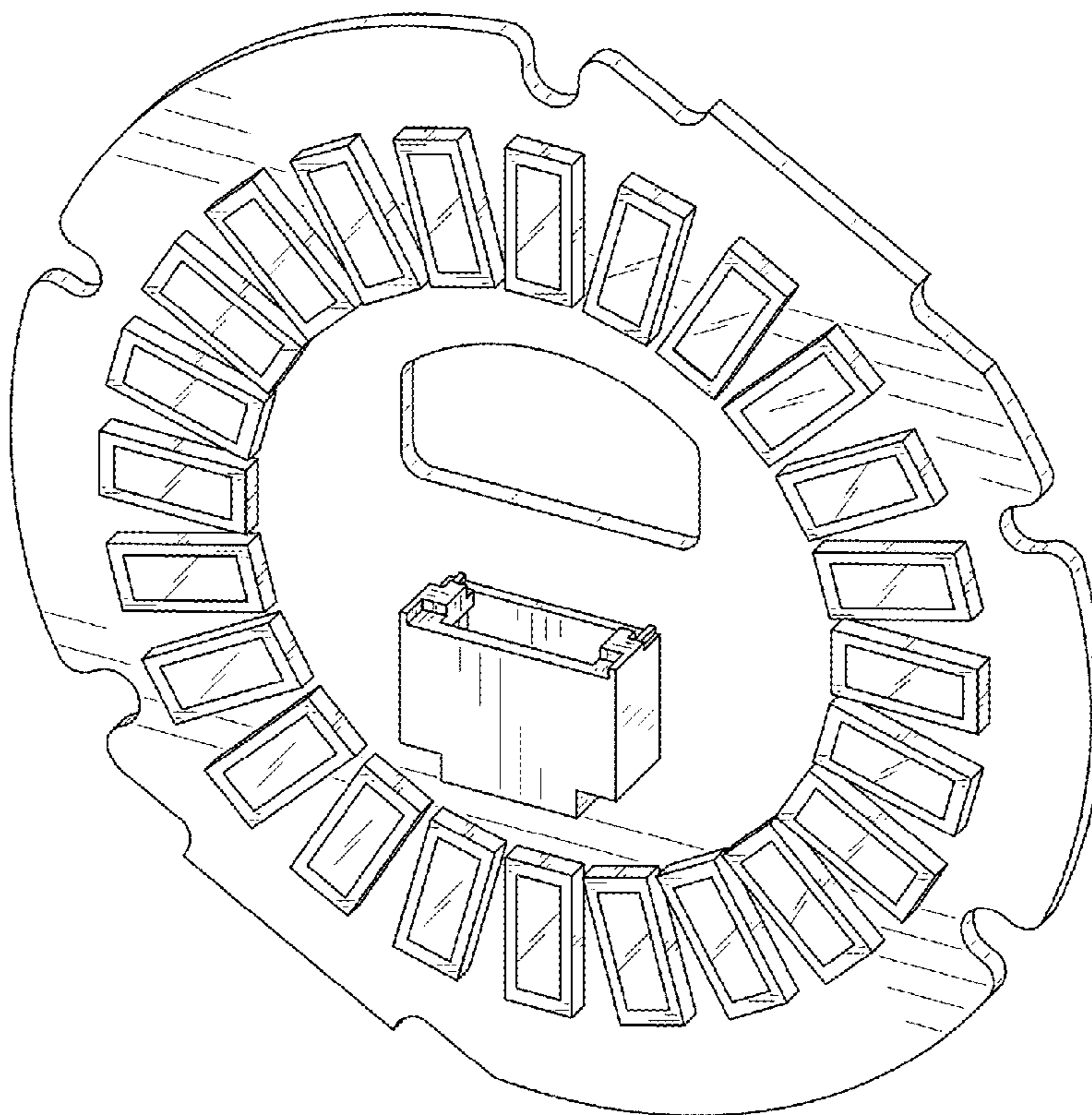
(57) **CLAIM**

The ornamental design for a light emitting diode module, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a light emitting diode module showing our new design;
FIG. 2 is a front view thereof;
FIG. 3 is a rear view thereof;
FIG. 4 is a left side view thereof;
FIG. 5 is a right side view thereof;
FIG. 6 is a top view thereof; and,
FIG. 7 is a bottom view thereof.

1 Claim, 5 Drawing Sheets



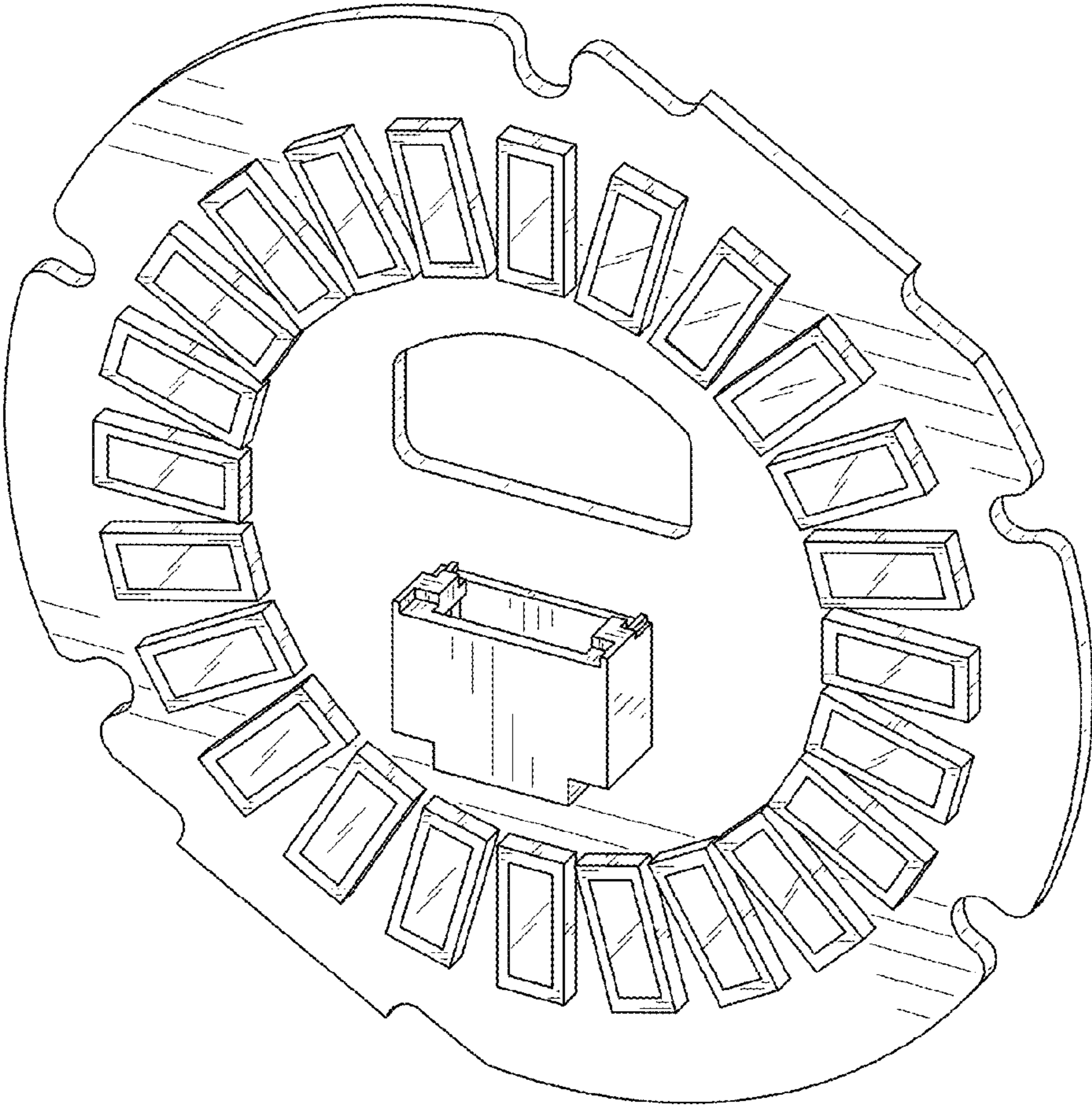


FIG. 1

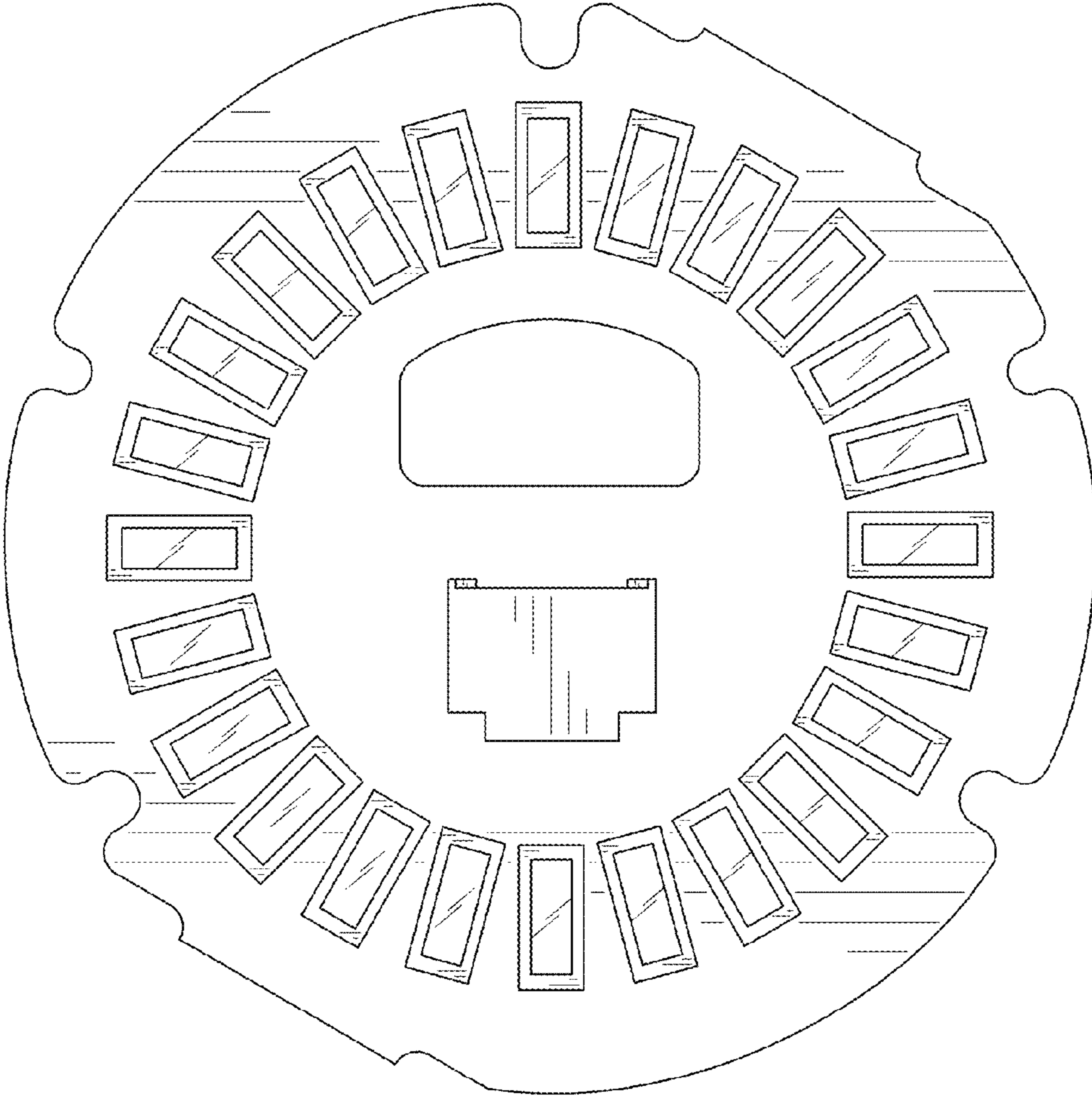


FIG. 2

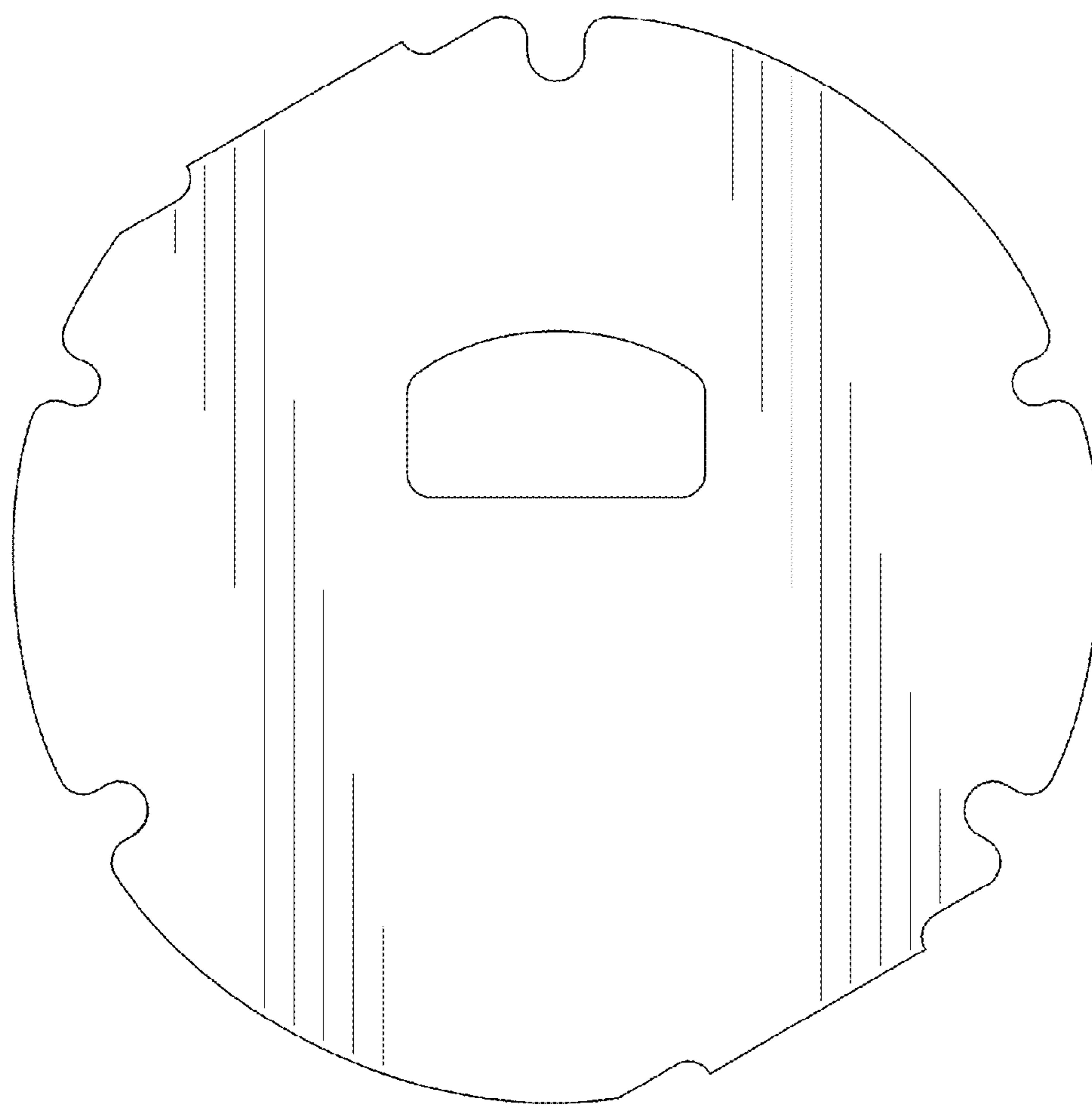


FIG. 3

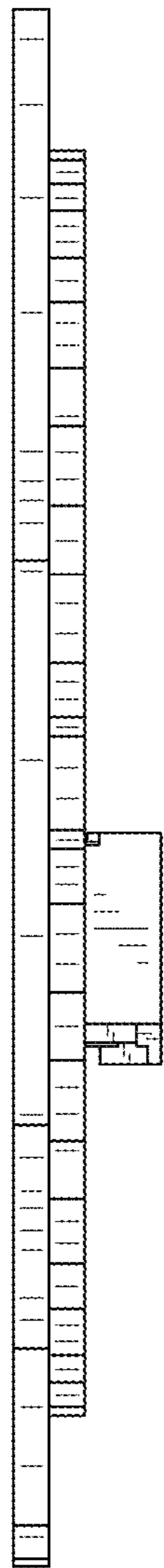


FIG. 4

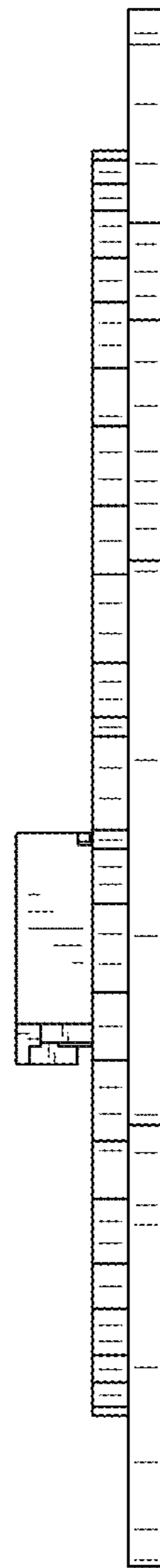


FIG. 5

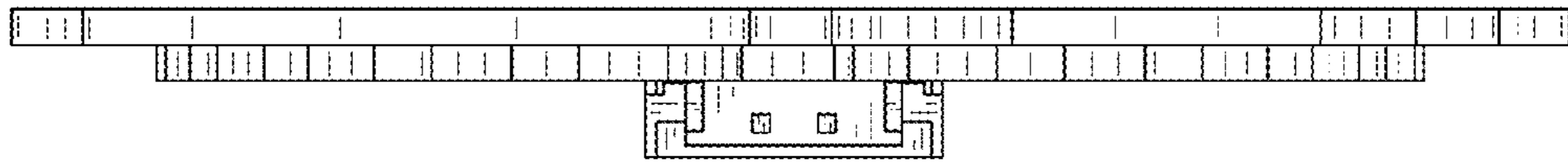


FIG. 6

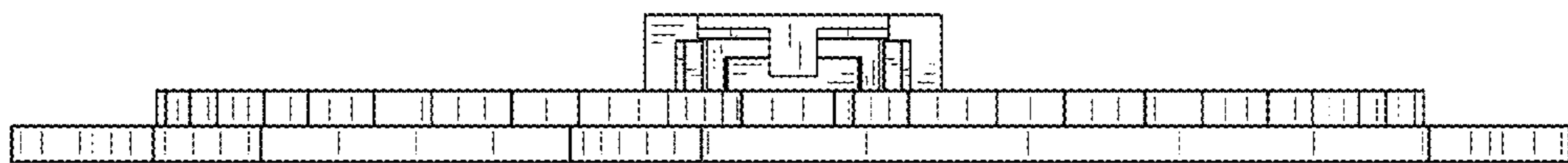


FIG. 7