



US00D693340S

(12) **United States Design Patent**
Ohshima(10) **Patent No.:** **US D693,340 S**
(45) **Date of Patent:** **** Nov. 12, 2013**(54) **ELECTRONIC COMPUTER**(75) Inventor: **Jun Ohshima**, Tokyo (JP)(73) Assignee: **Kabushiki Kaisha Toshiba** (JP)(**) Term: **14 Years**(21) Appl. No.: **29/412,825**(22) Filed: **Feb. 8, 2012**(30) **Foreign Application Priority Data**

Aug. 11, 2011 (JP) 2011-018485

(51) **LOC (9) Cl.** **14-02**(52) **U.S. Cl.**USPC **D14/341**(58) **Field of Classification Search**USPC D14/341-347, 138 C, 137, 138, 138.1,
D14/138 AA, 138 R, 138 G, 496, 203.1,
D14/203.3, 203.4, 203.5, 203.7, 432, 129,
D14/130, 439, 448, 147, 218, 248, 389, 388,
D14/315-318; D21/324, 331; D19/59-60;
D10/65, 104; D18/6, 7; 455/556.1,
455/575.1, 575.3, 575.4; 345/169,
345/156-157, 168, 173; 341/22, 23;
361/679.27

See application file for complete search history.

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Primary Examiner — Barbara Fox

(74) Attorney, Agent, or Firm — Banner & Witcoff, Ltd.

(57) **CLAIM**

The ornamental design for an electronic computer, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of an electronic computer showing my new design,

FIG. 2 is a front elevational view thereof,

FIG. 3 is a rear elevational view thereof,

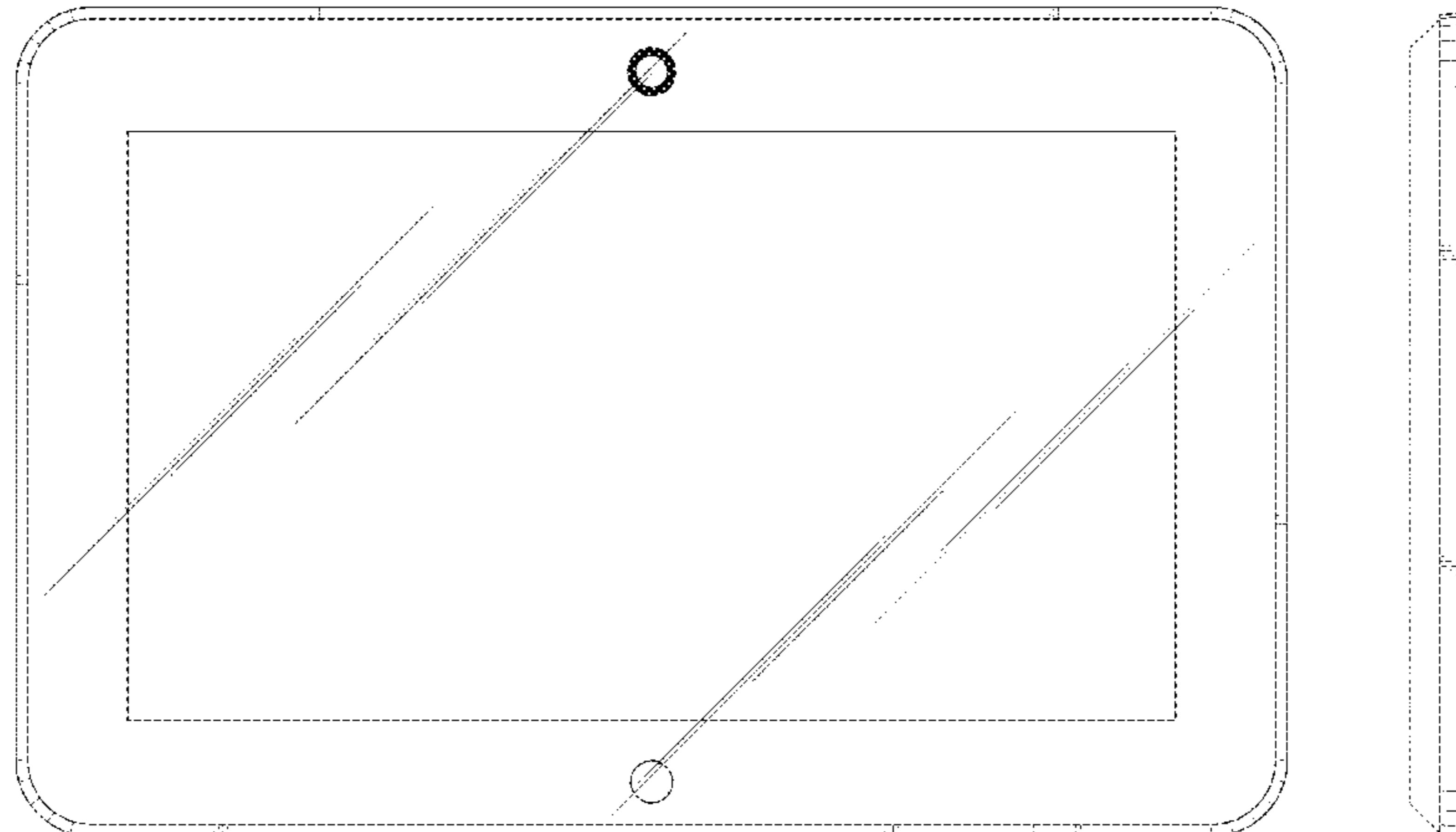
FIG. 4 is a left side elevational view thereof,

FIG. 5 is a right side elevational view thereof,

FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.

The broken lines shown in the drawings represent portions of the electronic computer which form no part of the claimed design.

1 Claim, 5 Drawing Sheets

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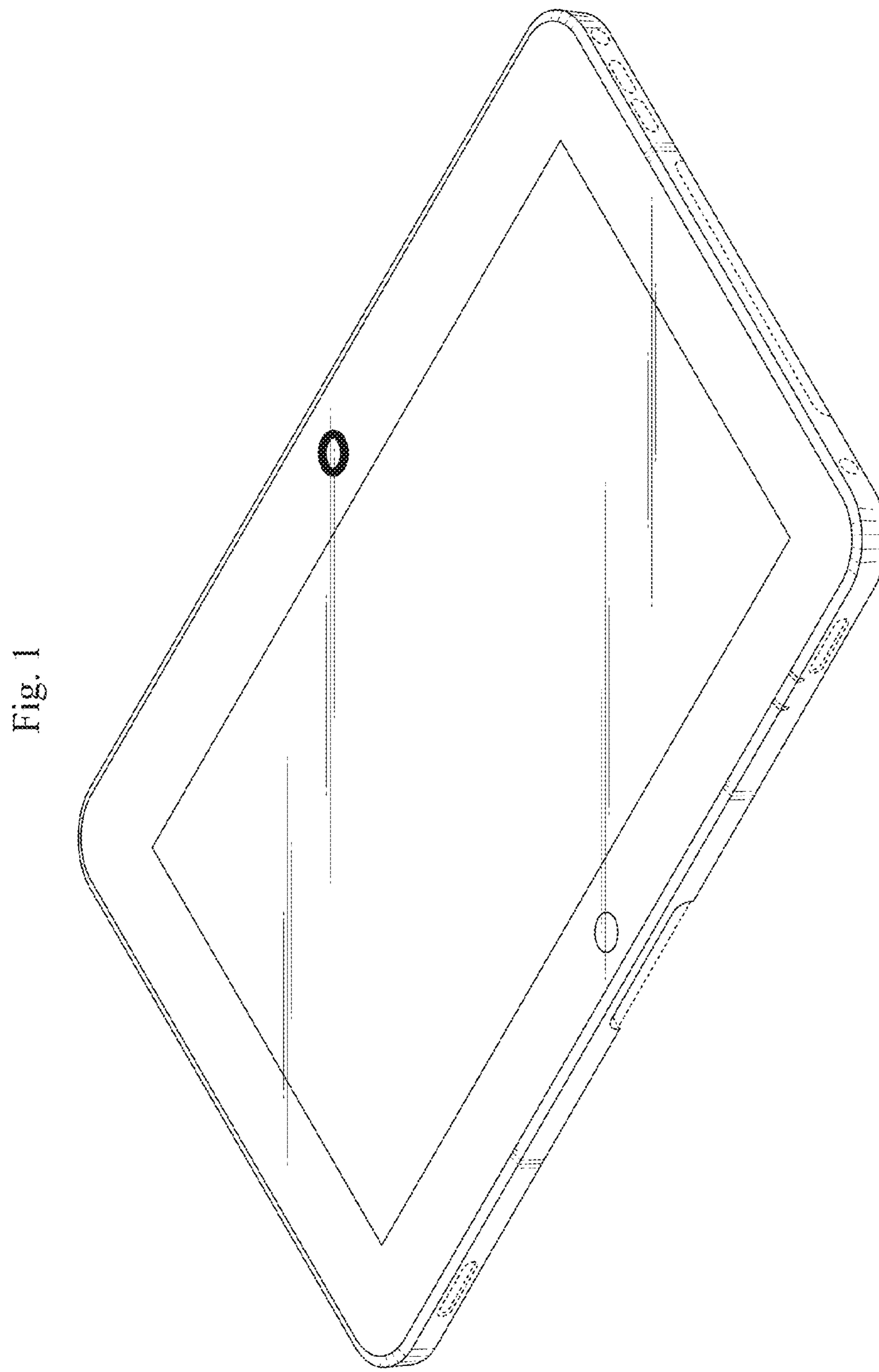


Fig. 2

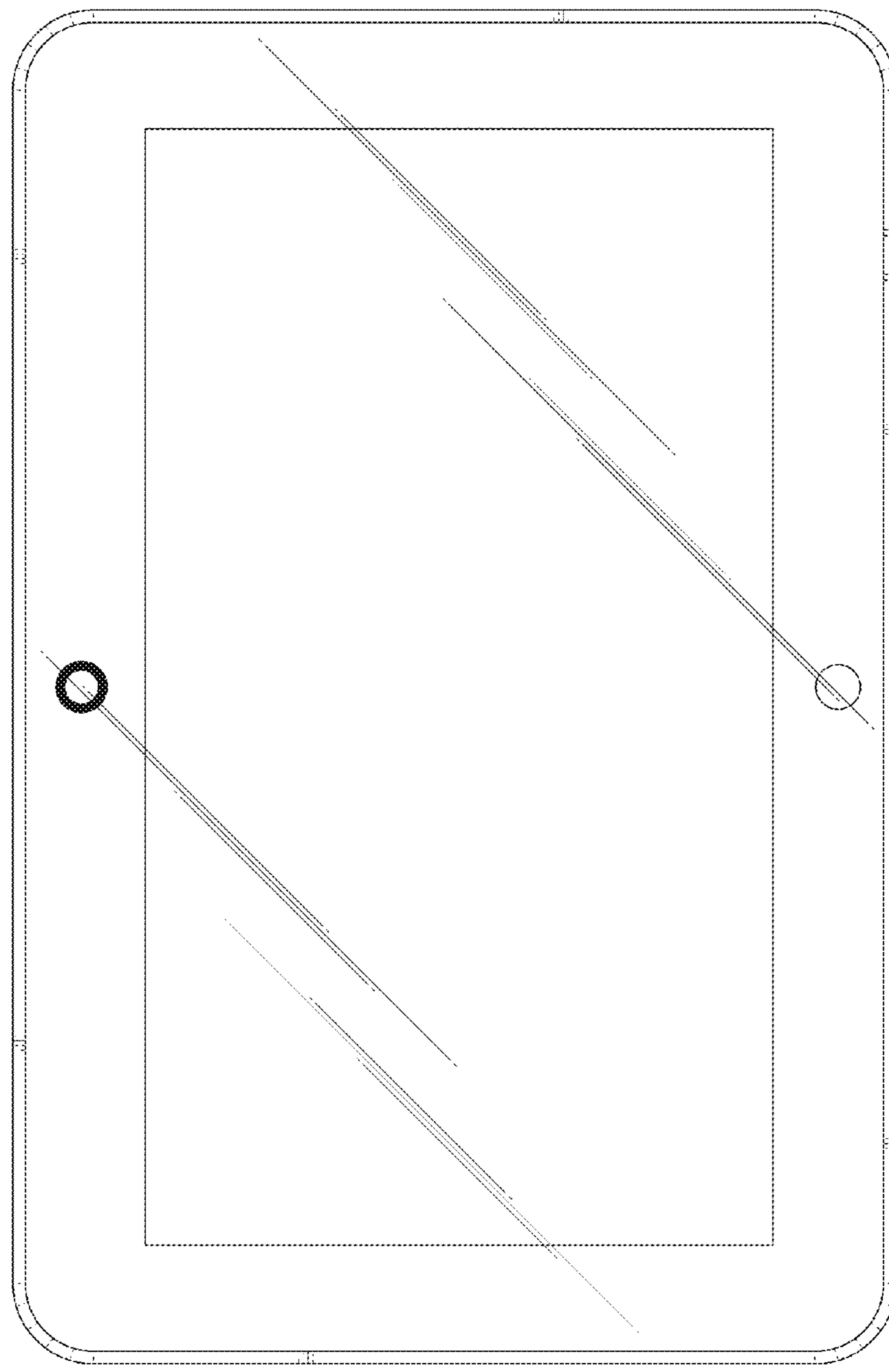


Fig. 3

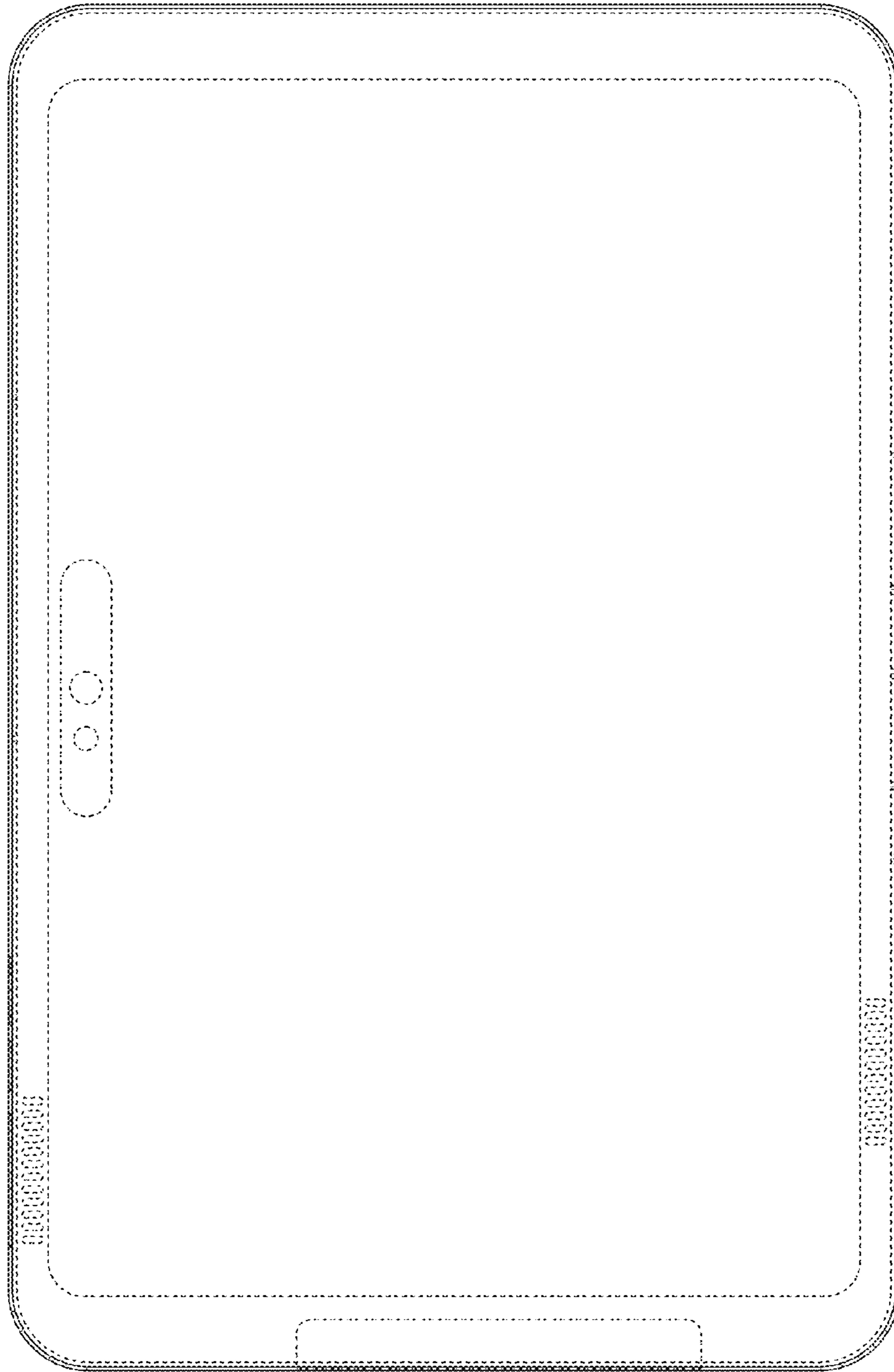


Fig. 5

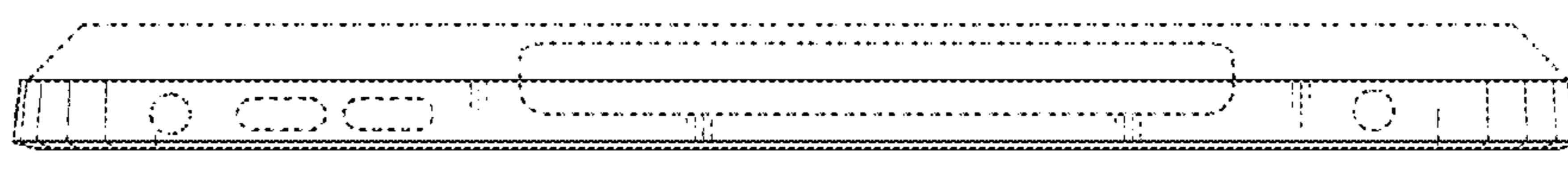


Fig. 4

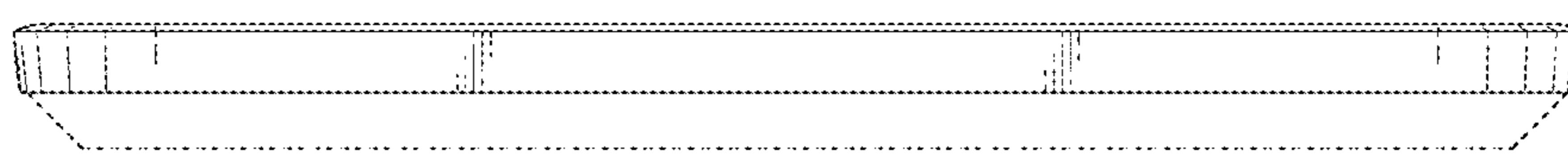


Fig. 6

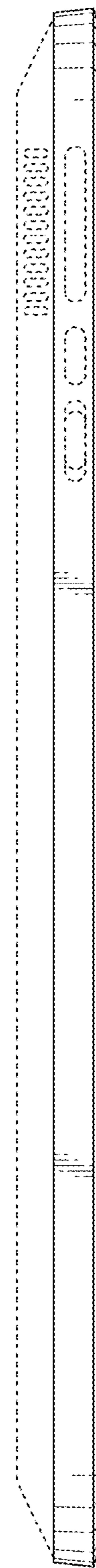


Fig. 7

