



US00D692843S

(12) **United States Design Patent**
Masuda

(10) **Patent No.:** **US D692,843 S**

(45) **Date of Patent:** **** Nov. 5, 2013**

(54) **SEMICONDUCTOR DEVICE**

(75) **Inventor:** **Takeyoshi Masuda, Osaka (JP)**

(73) **Assignee:** **Sumitomo Electric Industries, Ltd.,
Osaka-shi (JP)**

(**) **Term:** **14 Years**

(21) **Appl. No.:** **29/431,909**

(22) **Filed:** **Sep. 12, 2012**

(30) **Foreign Application Priority Data**

May 2, 2012 (JP) 2012-010399

(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/182**

(58) **Field of Classification Search**
USPC D13/182; 174/250; 257/77, 79, 83;
438/44

See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D574,339 S *	8/2008	Honjo et al.	D13/182
7,981,709 B2 *	7/2011	Masuda	438/44
8,203,151 B2 *	6/2012	Masuda	257/77
2006/0267030 A1 *	11/2006	Yamazaki et al.	257/83

2010/0000765 A1 *	1/2010	Hirakata et al.	174/250
2010/0132981 A1 *	6/2010	Muro et al.	174/250
2010/0163282 A1 *	7/2010	Tagi et al.	174/254
2010/0175912 A1 *	7/2010	Shih	174/250
2010/0200276 A1 *	8/2010	Karikalan	174/250
2012/0118614 A1 *	5/2012	Kuriki	174/250
2012/0186855 A1 *	7/2012	Lin	174/250
2012/0205141 A1 *	8/2012	Ogawa et al.	174/250

* cited by examiner

Primary Examiner — Selina Sikder

(74) *Attorney, Agent, or Firm* — Venable LLP; Michael A. Sartori; Tamatane J. Aga

(57) **CLAIM**

The ornamental design for a semiconductor device, as shown.

DESCRIPTION

FIG. 1 is a front view of a semiconductor device showing my new design;

FIG. 2 is a rear view thereof;

FIG. 3 is a top plan view thereof;

FIG. 4 is a bottom plan view thereof;

FIG. 5 is a right side view thereof;

FIG. 6 is a left side view thereof; and

FIG. 7 is an enlarged front view showing the area defined by indicia 7A and 7B of FIG. 1 thereof;

FIG. 8 is an enlarged front view showing the area defined by indicia 8C and 8D of FIG. 7 thereof; and,

FIG. 9 is an enlarged perspective view thereof.

1 Claim, 4 Drawing Sheets

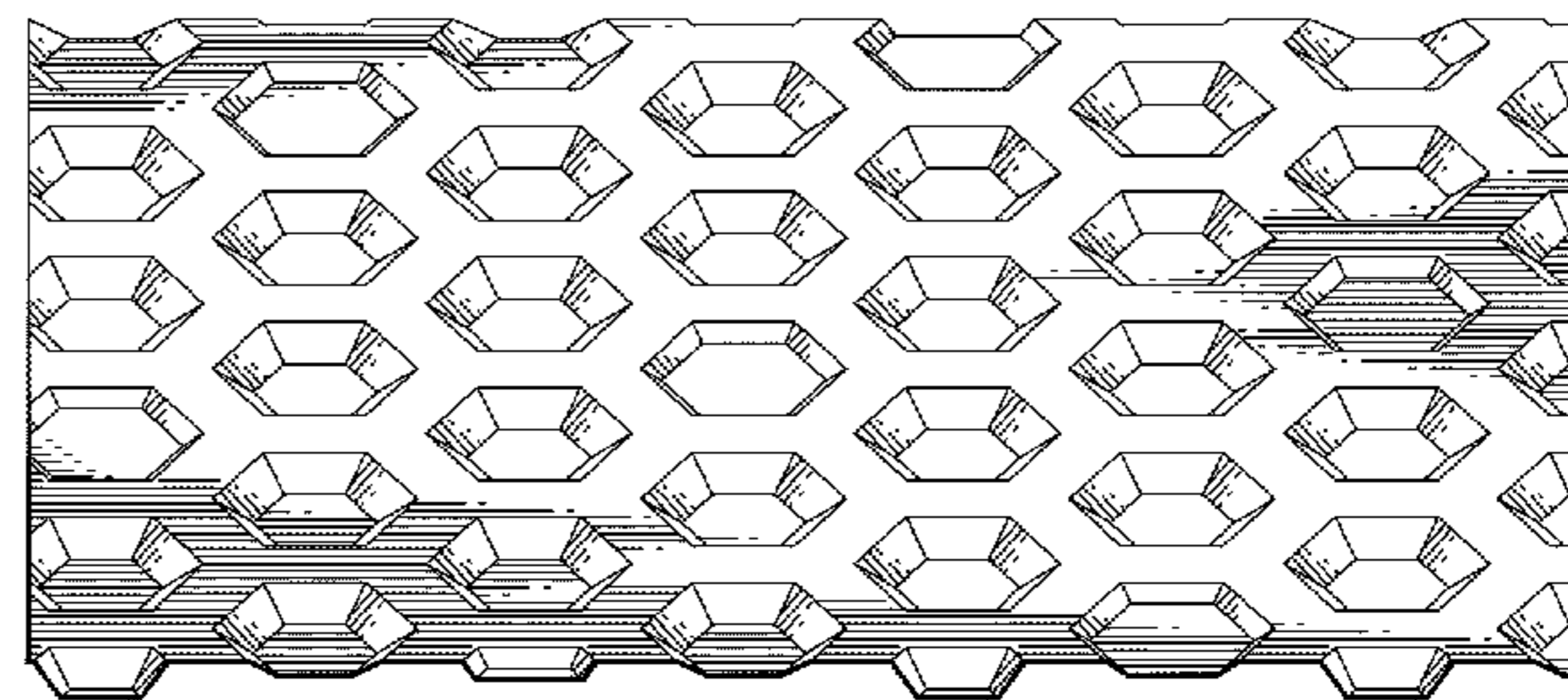
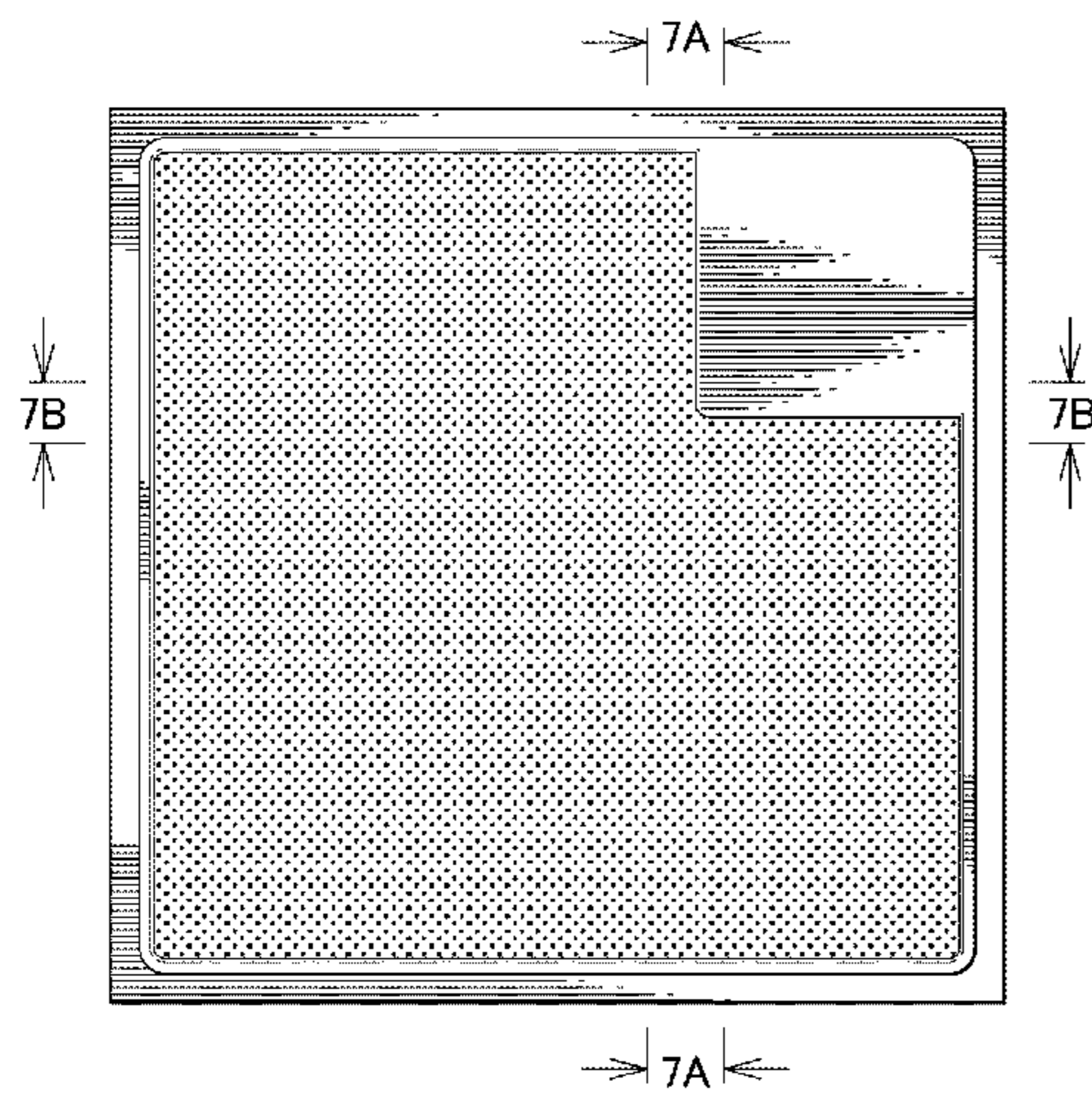


FIG. 1

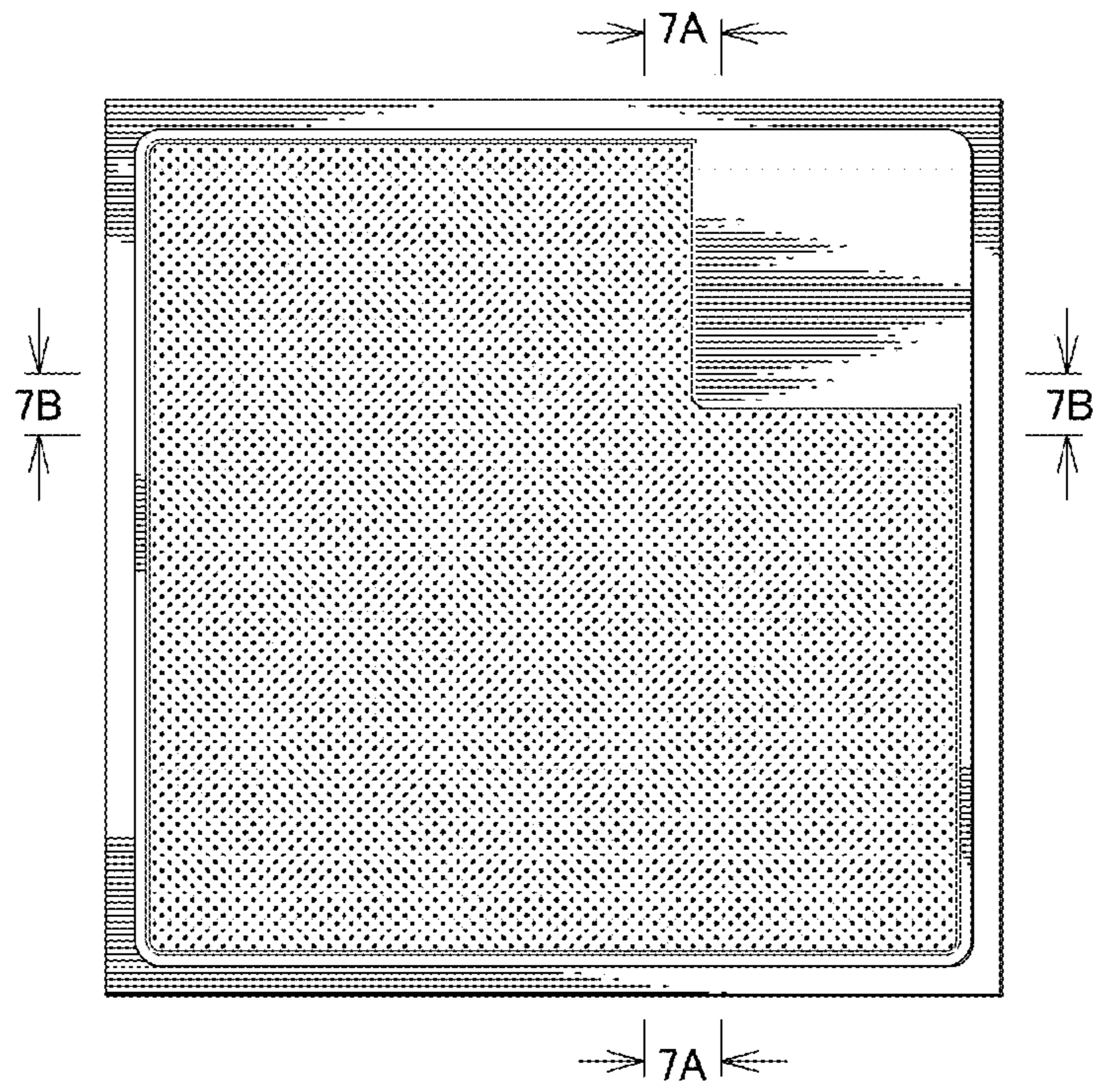


FIG. 2

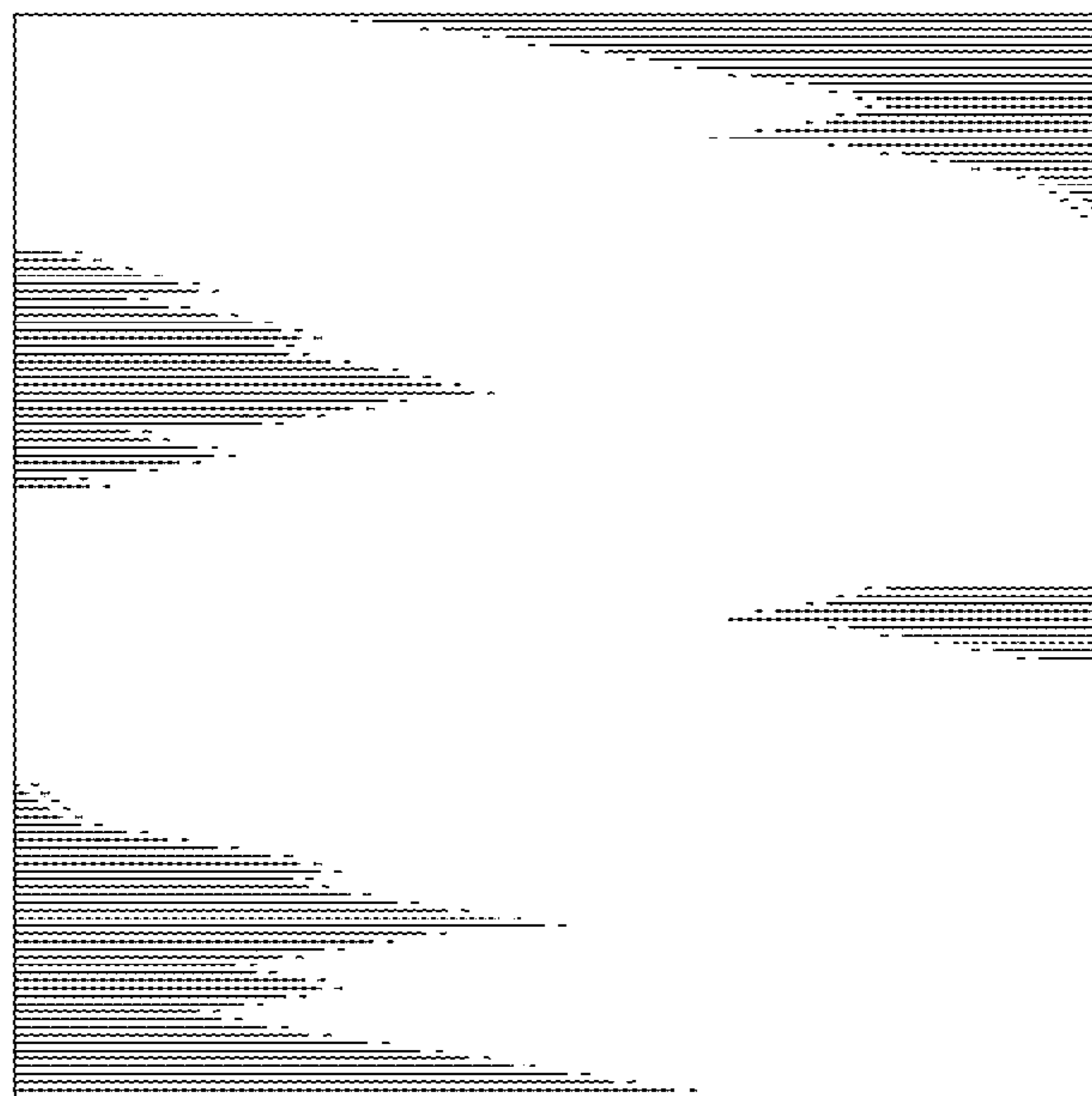


FIG.3



FIG.4

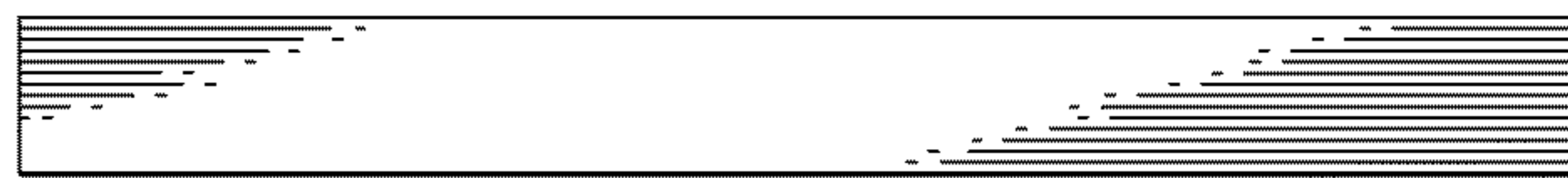


FIG.5

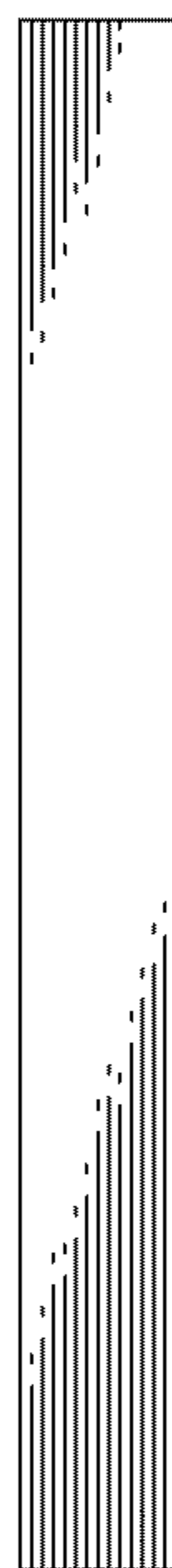


FIG.6

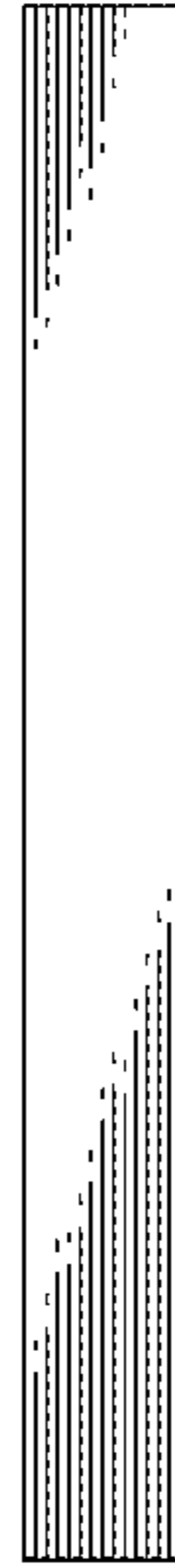


FIG.7

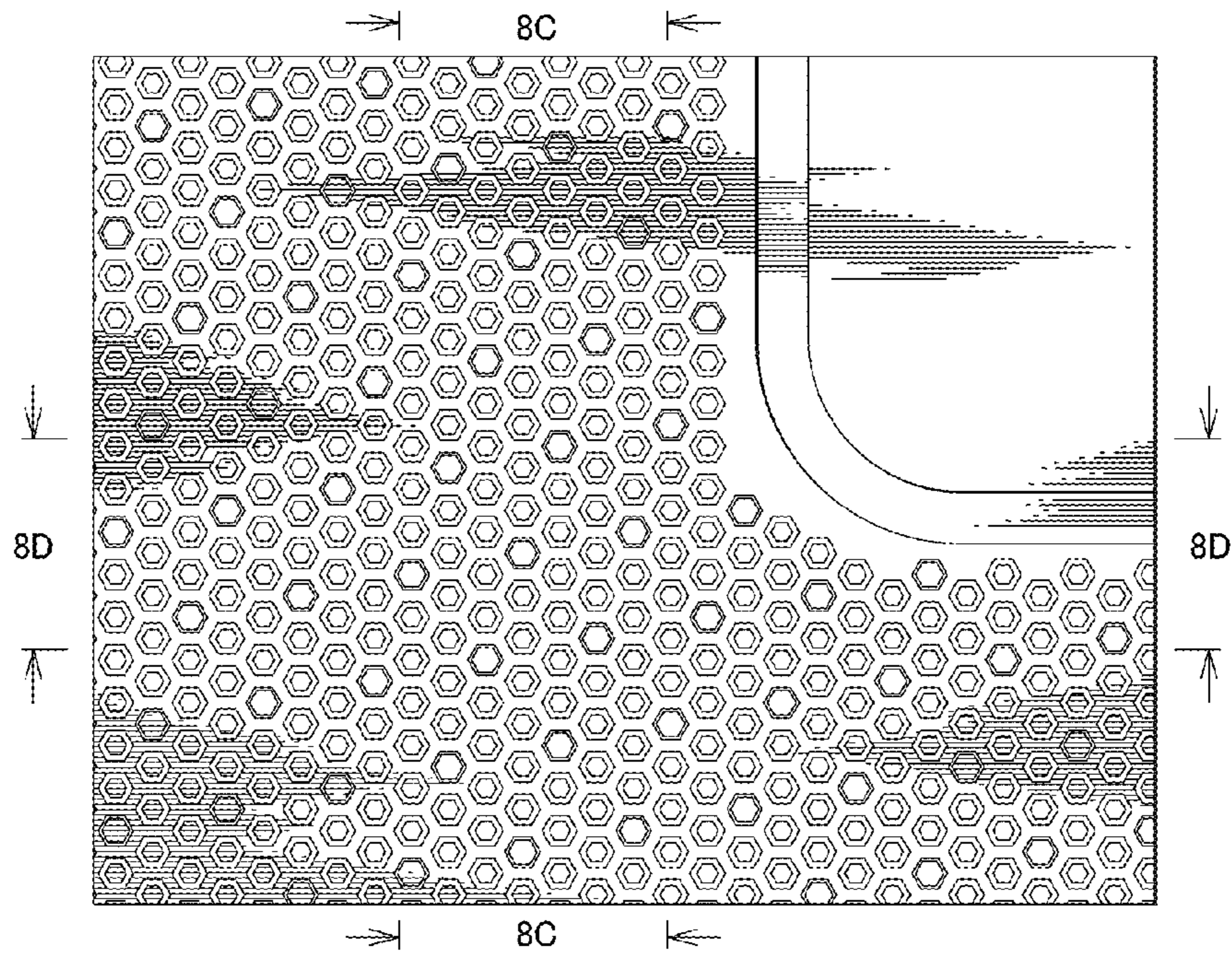


FIG.8

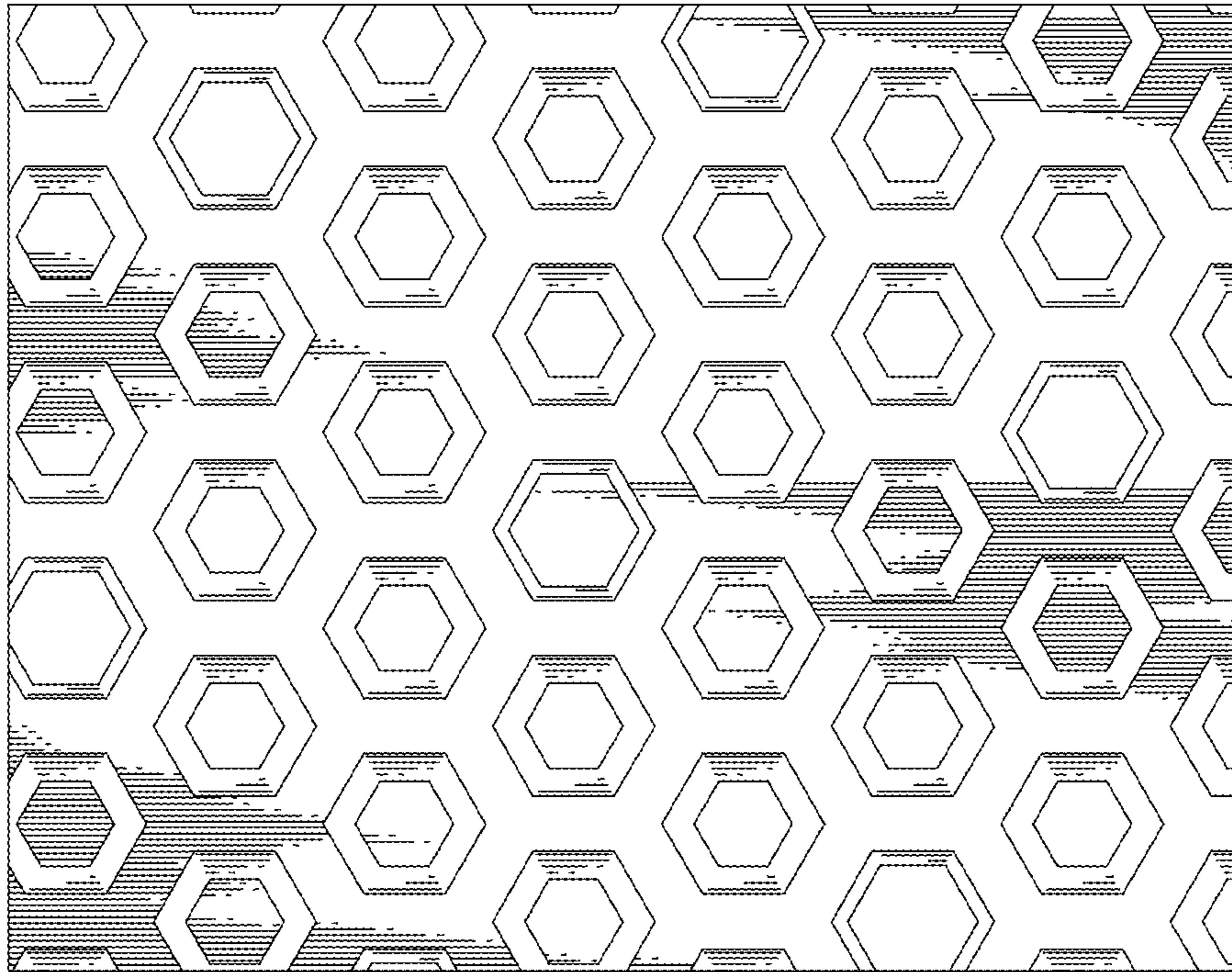


FIG.9

