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(12) **United States Design Patent**
Dodal et al.

(10) **Patent No.:** **US D692,385 S**
(45) **Date of Patent:** **** Oct. 29, 2013**

(54) **GROUND FAULT CIRCUIT INTERRUPTER
RECEPTACLE WITH NIGHTLIGHT**

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(**) Term: **14 Years**

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(21) Appl. No.: **29/435,264**

(57) **CLAIM**

(22) Filed: **Oct. 22, 2012**

The ornamental design for a GFCI receptacle with nightlight,
as shown and described.

(51) **LOC (9) Cl.** **13-03**

DESCRIPTION

(52) **U.S. Cl.**

USPC **D13/139.1**; D13/139.6

(58) **Field of Classification Search**

USPC D13/152, 164, 184, 137.1, 139.1, 153,
D13/137.2, 133, 137.4, 139.7, 130, 128,
D13/132, 138.2, 139.4, 139.8, 139.6, 123,
D13/160, 107; 361/683; D9/416, 423

See application file for complete search history.

FIG. 1 is a side perspective view of a GFCI receptacle with nightlight, showing my new design;
FIG. 2 is a front elevation view of the GFCI receptacle with nightlight of FIG. 1;
FIG. 3 is a back elevation view of the GFCI receptacle with nightlight of FIG. 1;
FIG. 4 is a left-side elevation view of the GFCI receptacle with nightlight of FIG. 1;
FIG. 5 is a right-side elevation view of the GFCI receptacle with nightlight of FIG. 1;
FIG. 6 is a top elevation view of the GFCI receptacle with nightlight of FIG. 1; and,
FIG. 7 is a bottom elevation view of the GFCI receptacle with nightlight of FIG. 1.

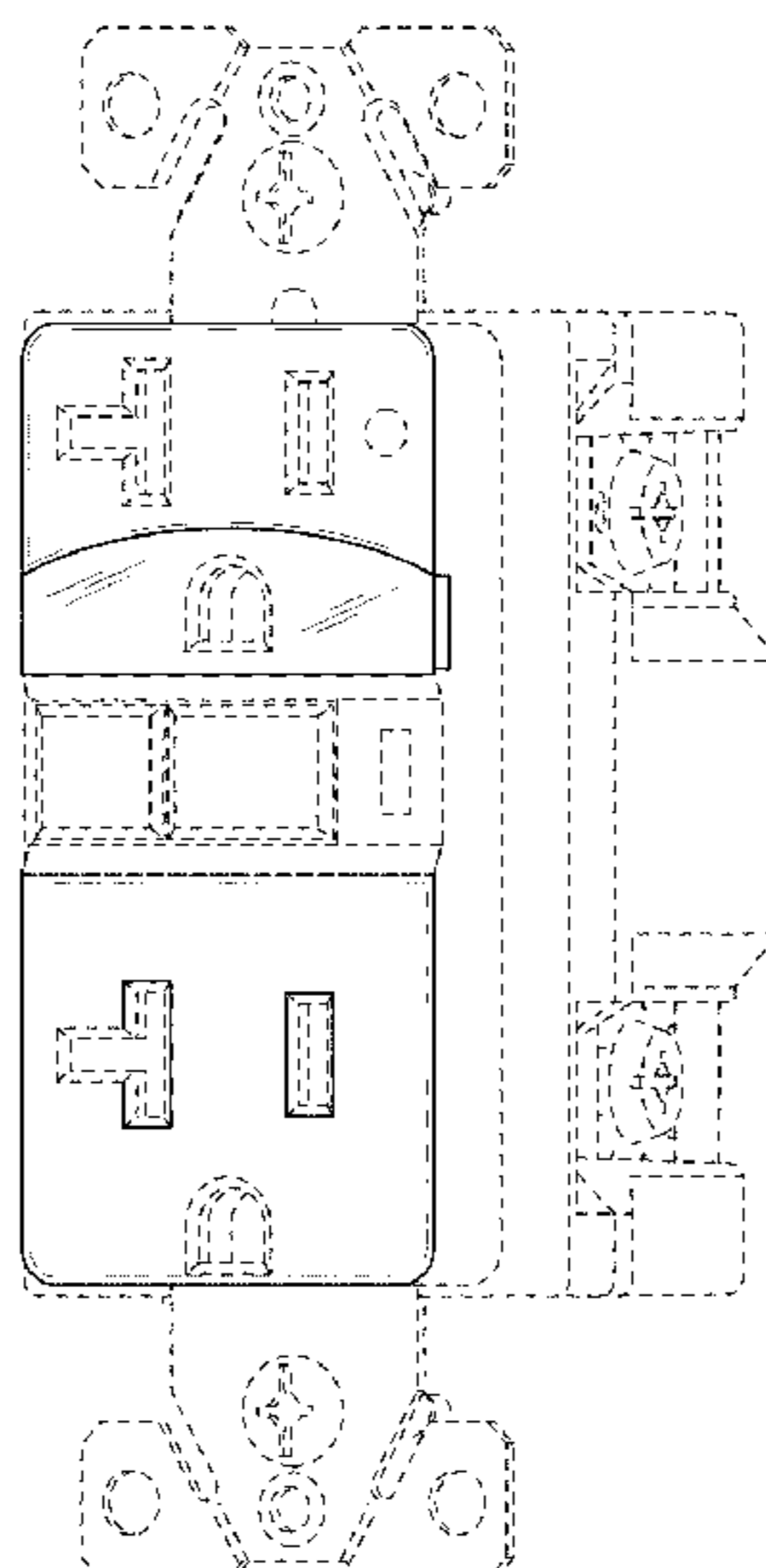
The broken lines shown represent unclaimed subject matter and form no part of the claimed design.

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1 Claim, 2 Drawing Sheets



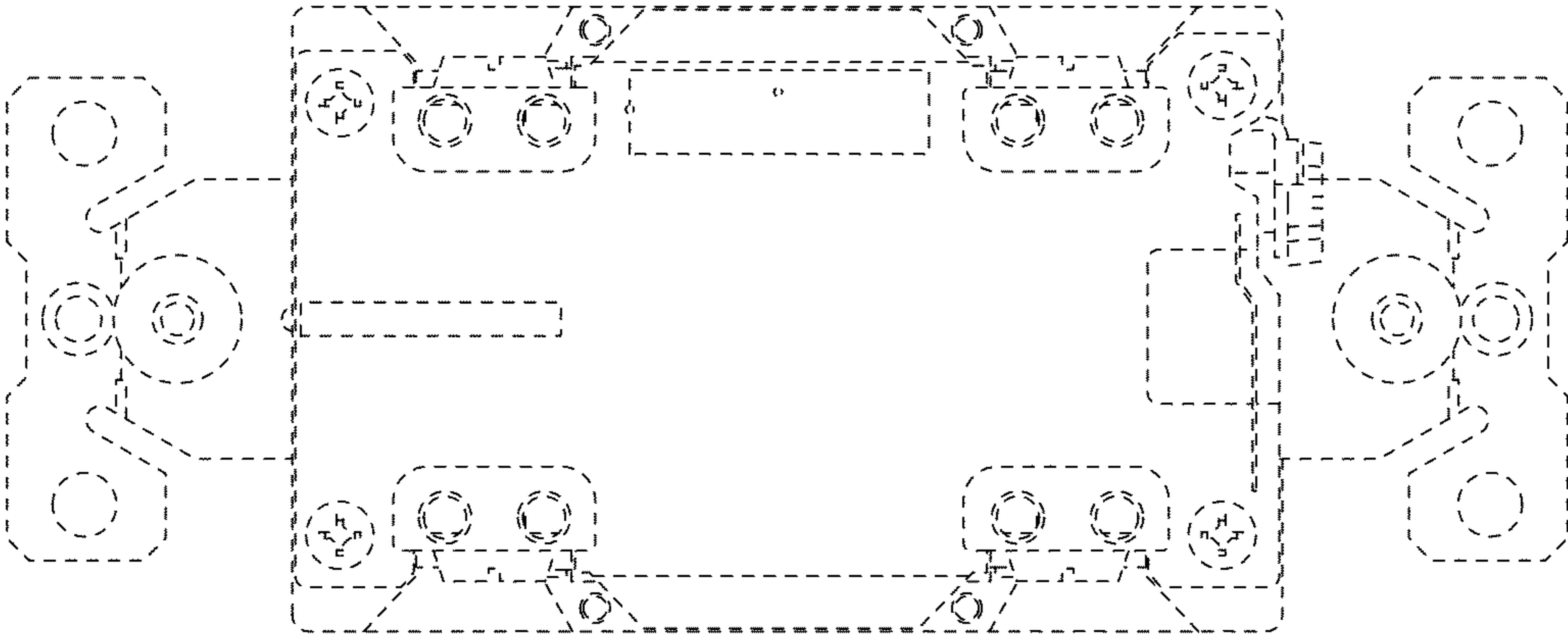


FIG. 3

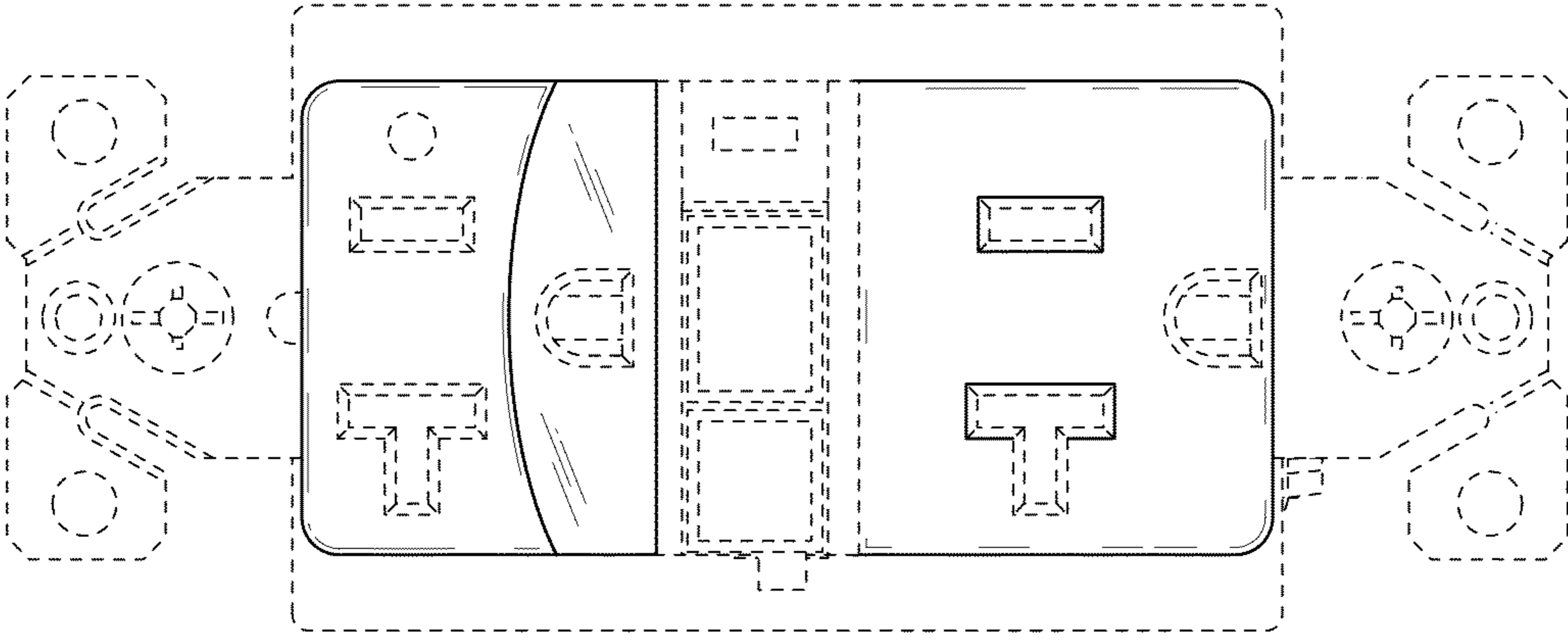


FIG. 2

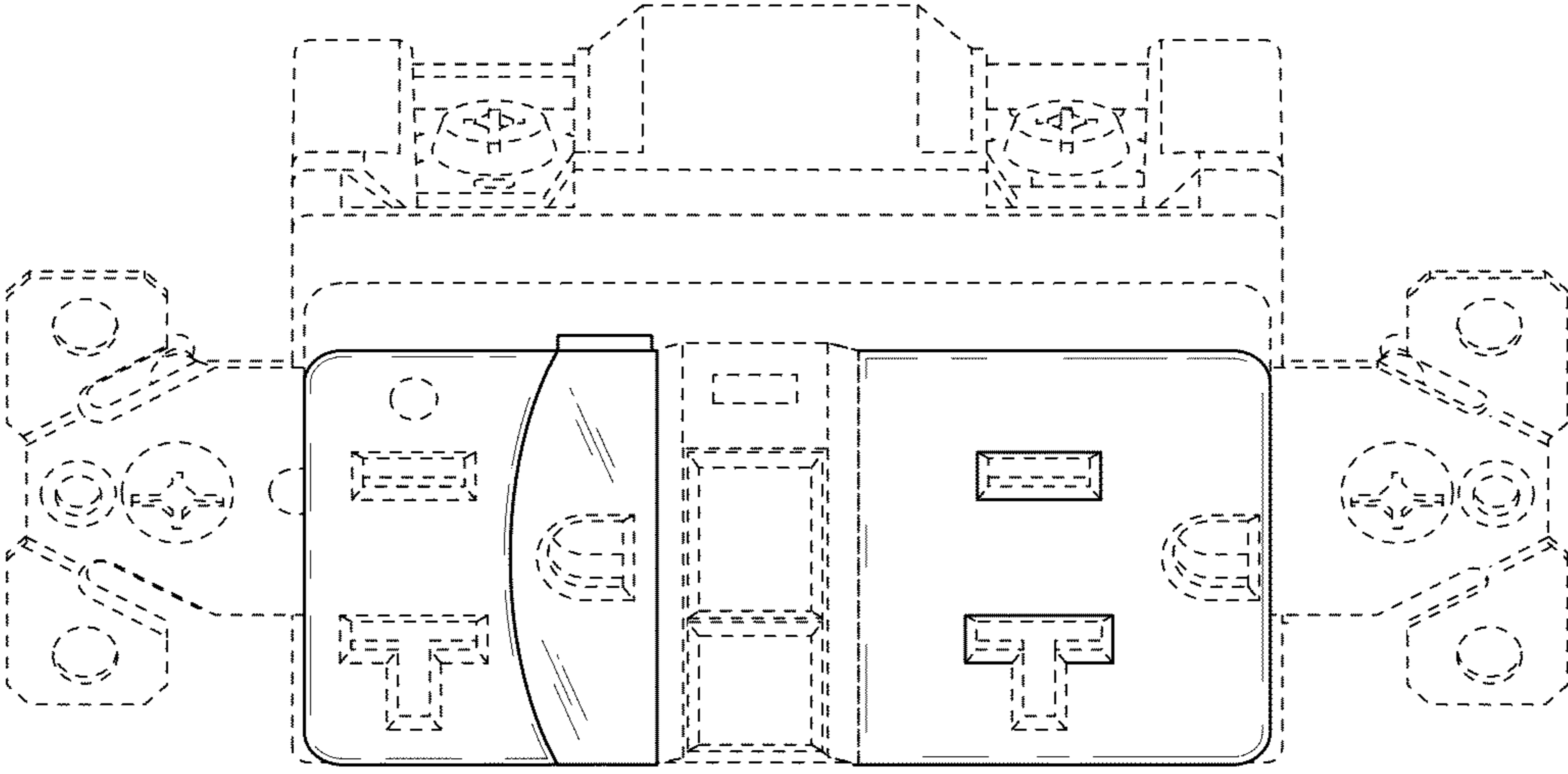


FIG. 1

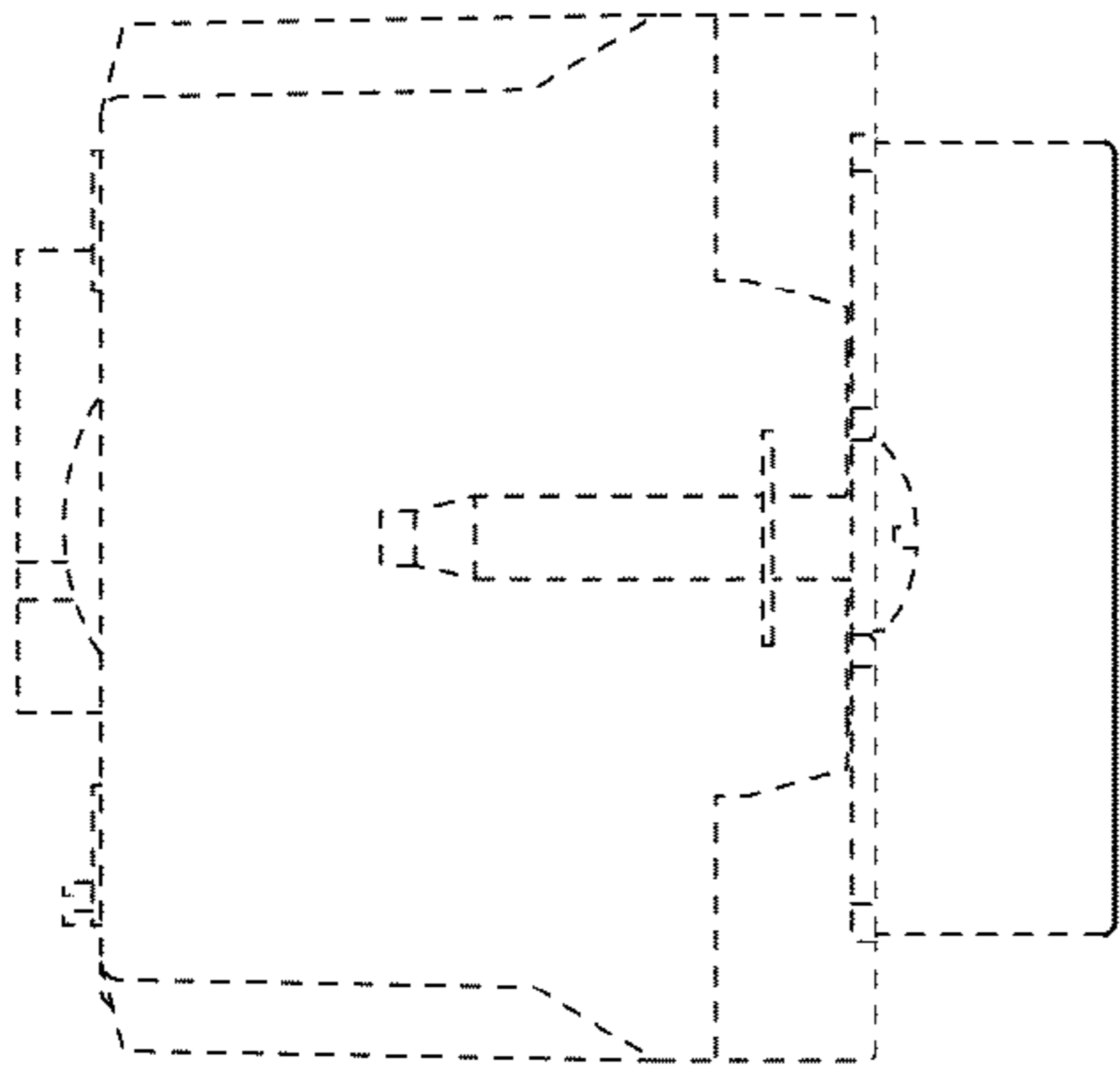


FIG. 6

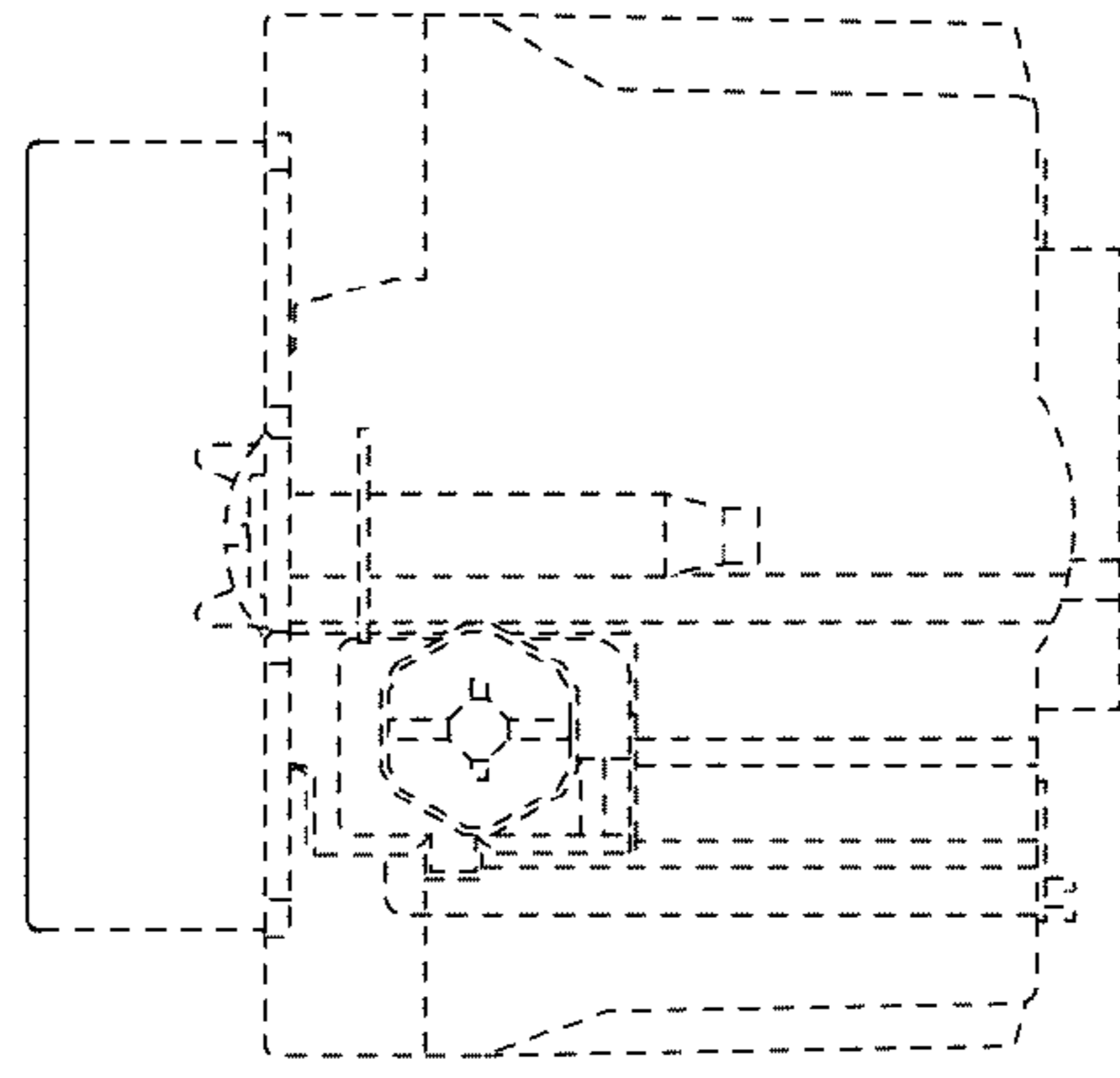


FIG. 7

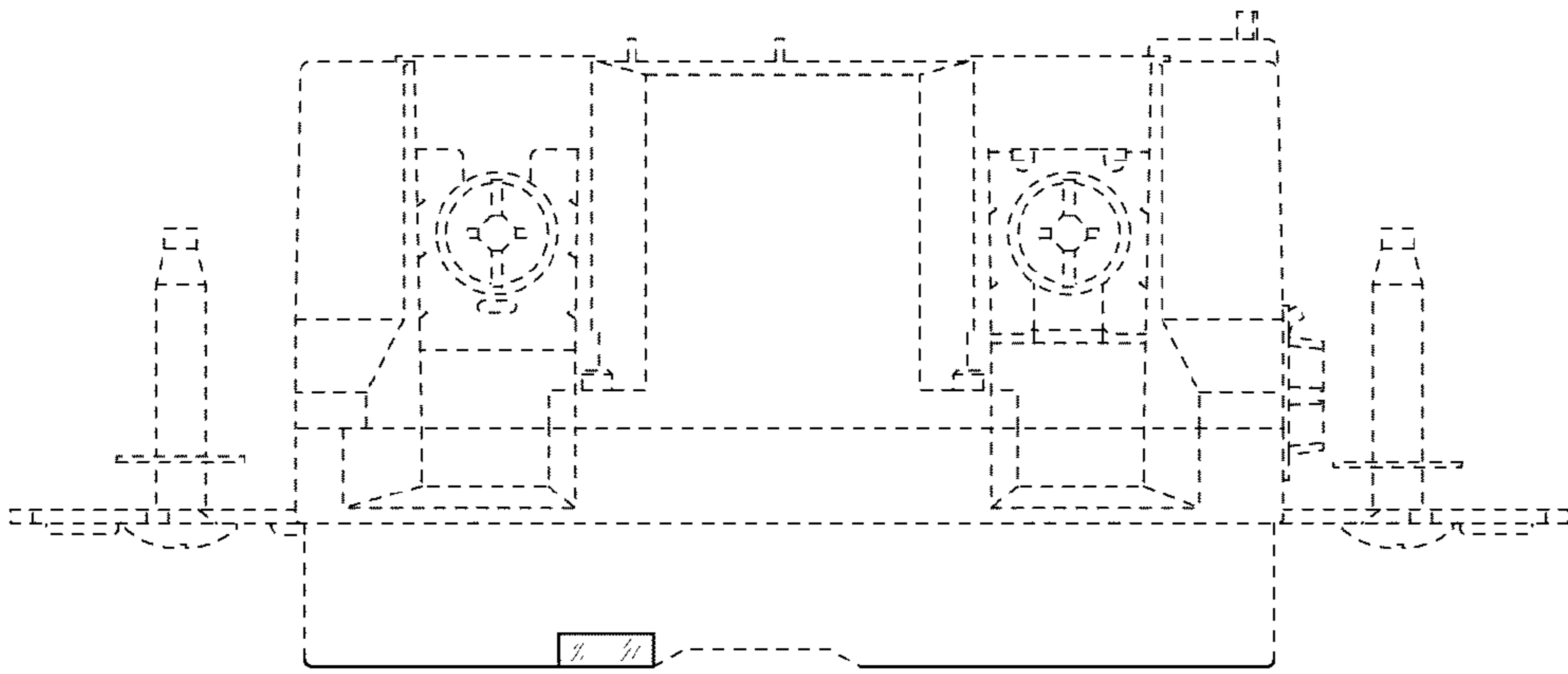


FIG. 5

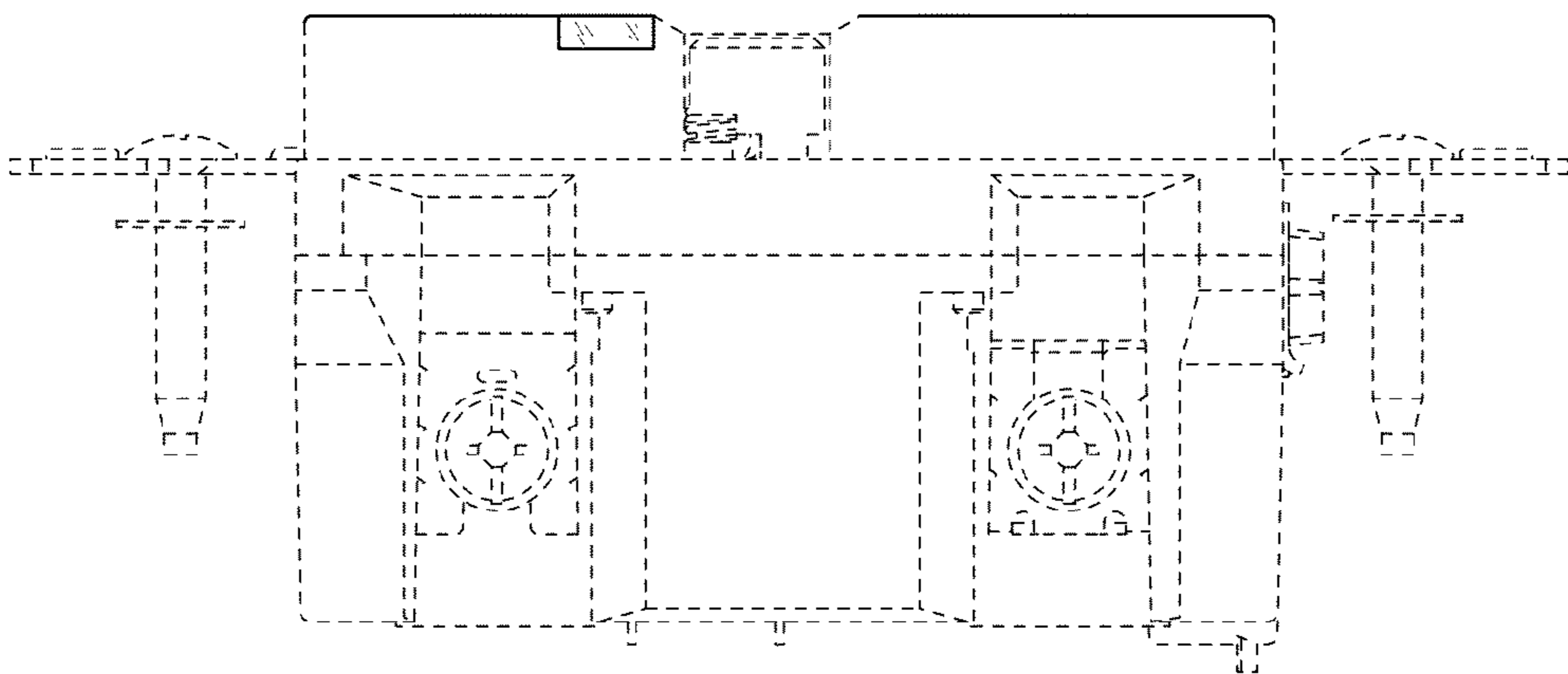


FIG. 4