

US00D691995S

(12) **United States Design Patent**
Adamson et al.

(10) **Patent No.:** **US D691,995 S**
(45) **Date of Patent:** **** Oct. 22, 2013**

(54) **ELECTRONIC COMPUTER WITH
PARTIALLY TRANSPARENT INPUT DEVICE**

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(73) Assignee: **Intel Corporation**, Santa Clara, CA
(US)

(**) Term: **14 Years**

(21) Appl. No.: **29/423,470**

(22) Filed: **May 31, 2012**

(51) **LOC (9) Cl.** **14-02**

(52) **U.S. Cl.**
USPC **D14/318**

(58) **Field of Classification Search**
USPC D14/315-327; D18/1, 2, 7, 11;
235/145 A, 145 R; 341/22, 23; 345/104,
345/156, 168, 169, 173; 361/679.06,
361/679.08, 679.09, 679.11, 679.26, 679.27
See application file for complete search history.

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Primary Examiner — Freda S Nunn

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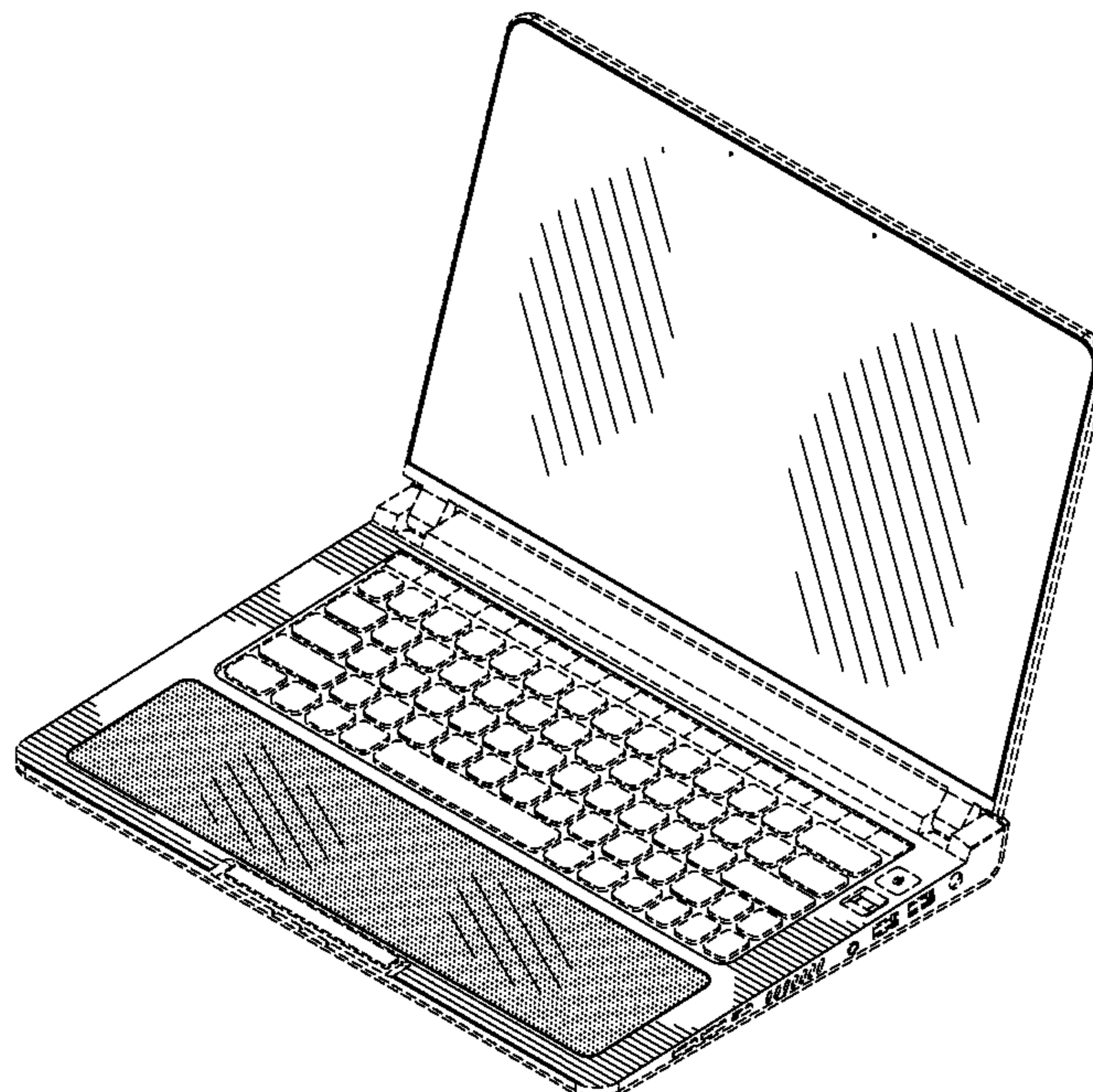
(57) **CLAIM**

The ornamental design for an electronic computer with an at least partially transparent input device, as shown and described.

DESCRIPTION

FIG. 1 is a top perspective view of an electronic computer with partially transparent input device showing our new design the broken lines shown in the drawings illustrate portions of the computer and form no part of the claimed design; FIG. 2 is a front view thereof; FIG. 3 is a back view thereof; FIG. 4 is a side view thereof; FIG. 5 is another side view thereof; FIG. 6 is a top view thereof; FIG. 7 is a bottom view thereof; FIG. 8 is a top perspective view of a second embodiment thereof; FIG. 9 is a front view thereof; FIG. 10 is a back view thereof; FIG. 11 is a side view thereof; FIG. 12 is another side view thereof; FIG. 13 is a top view thereof; and, FIG. 14 is a bottom view thereof. Any broken lines the preceding FIGURES are for illustration only and form no part of the claimed design.

1 Claim, 14 Drawing Sheets



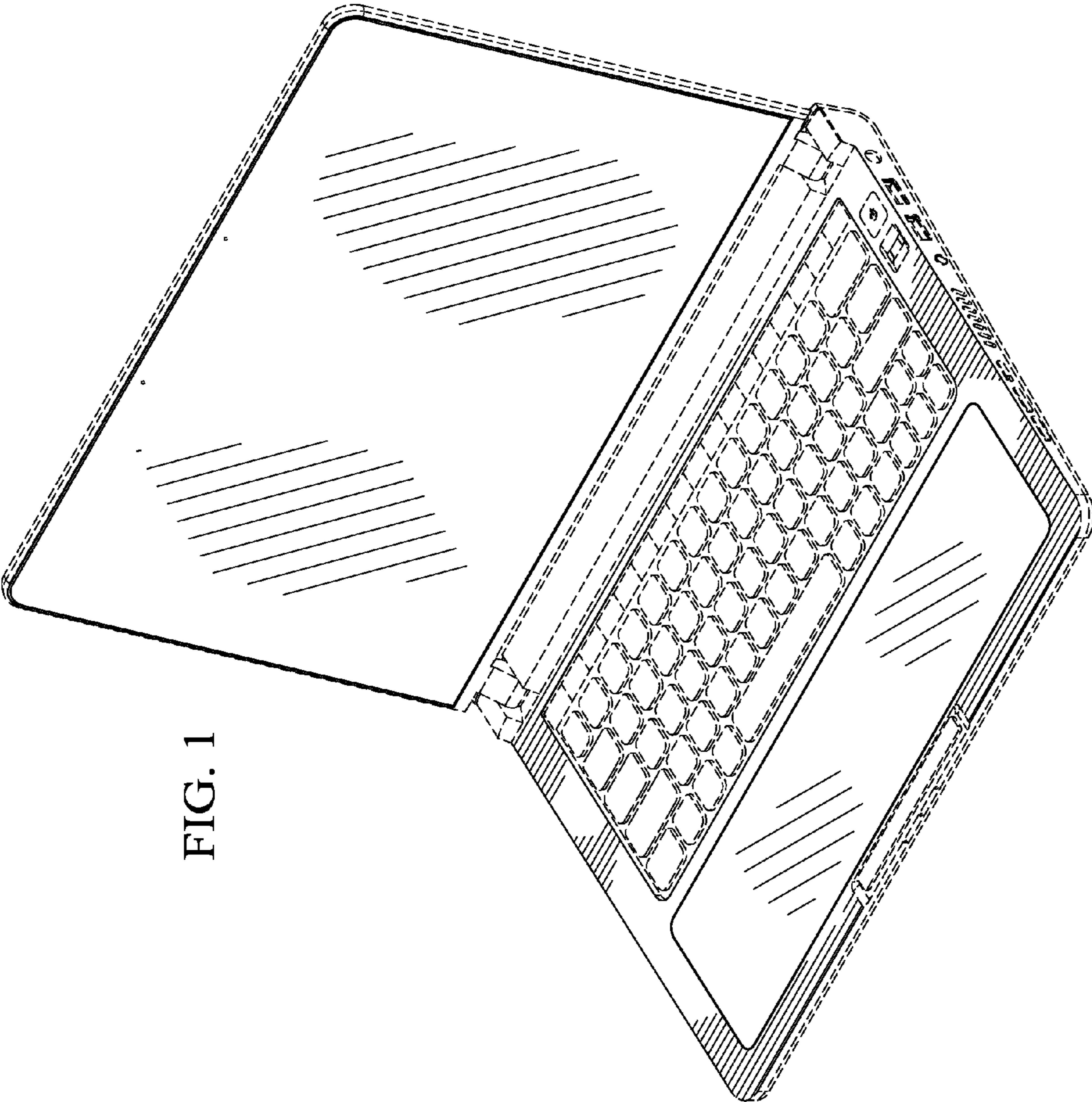


FIG. 1

FIG. 2

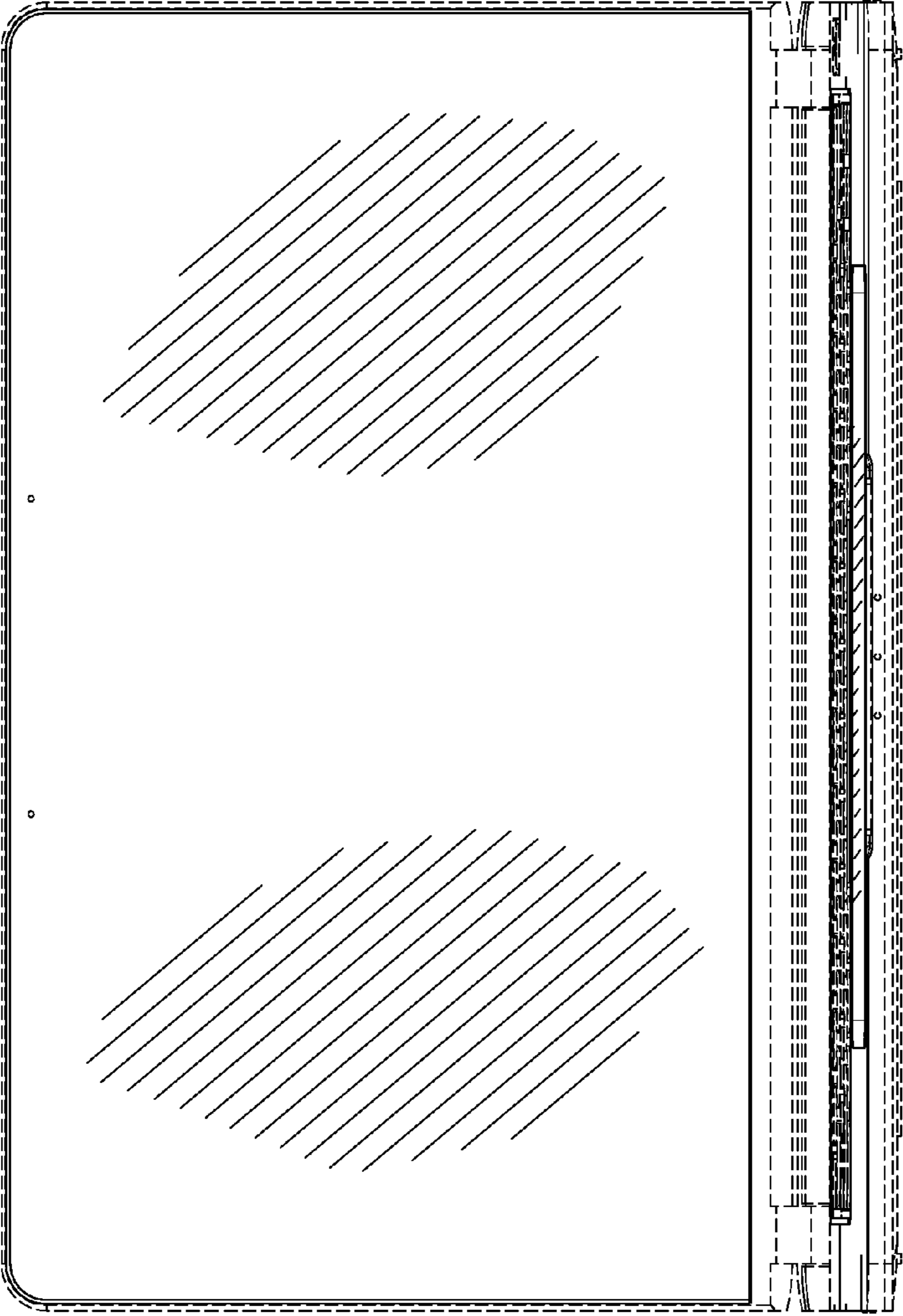
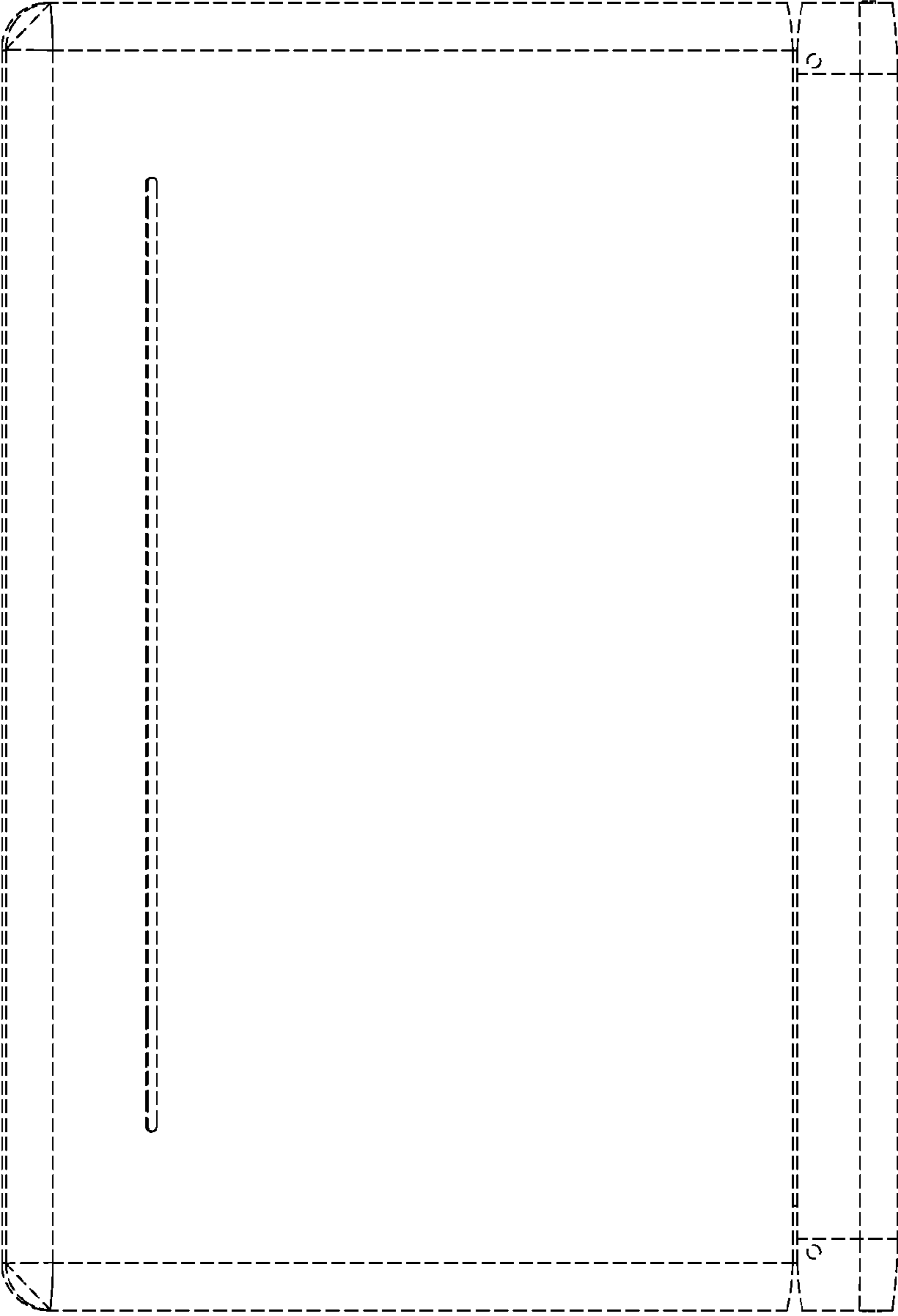


FIG. 3



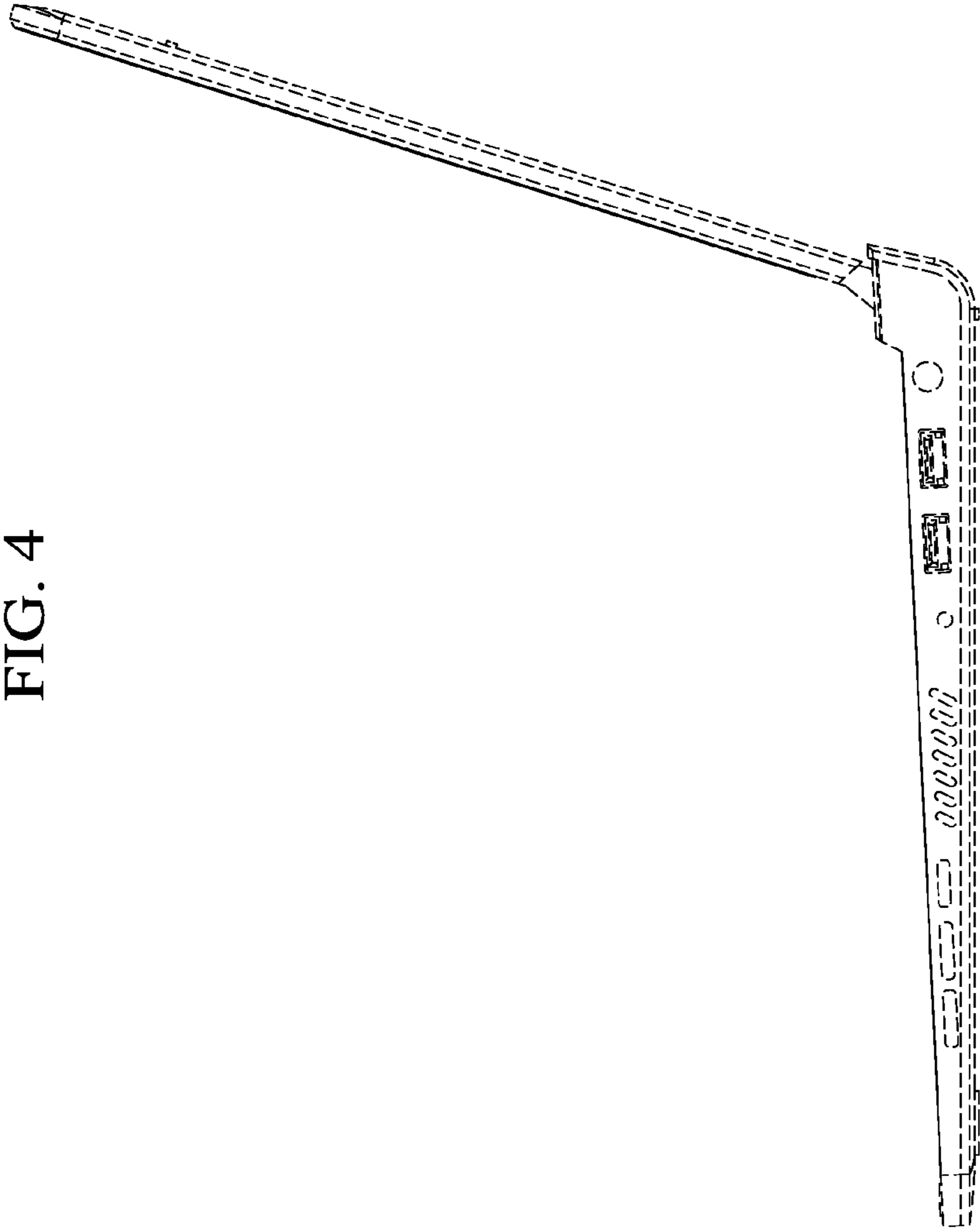


FIG. 4

FIG. 5

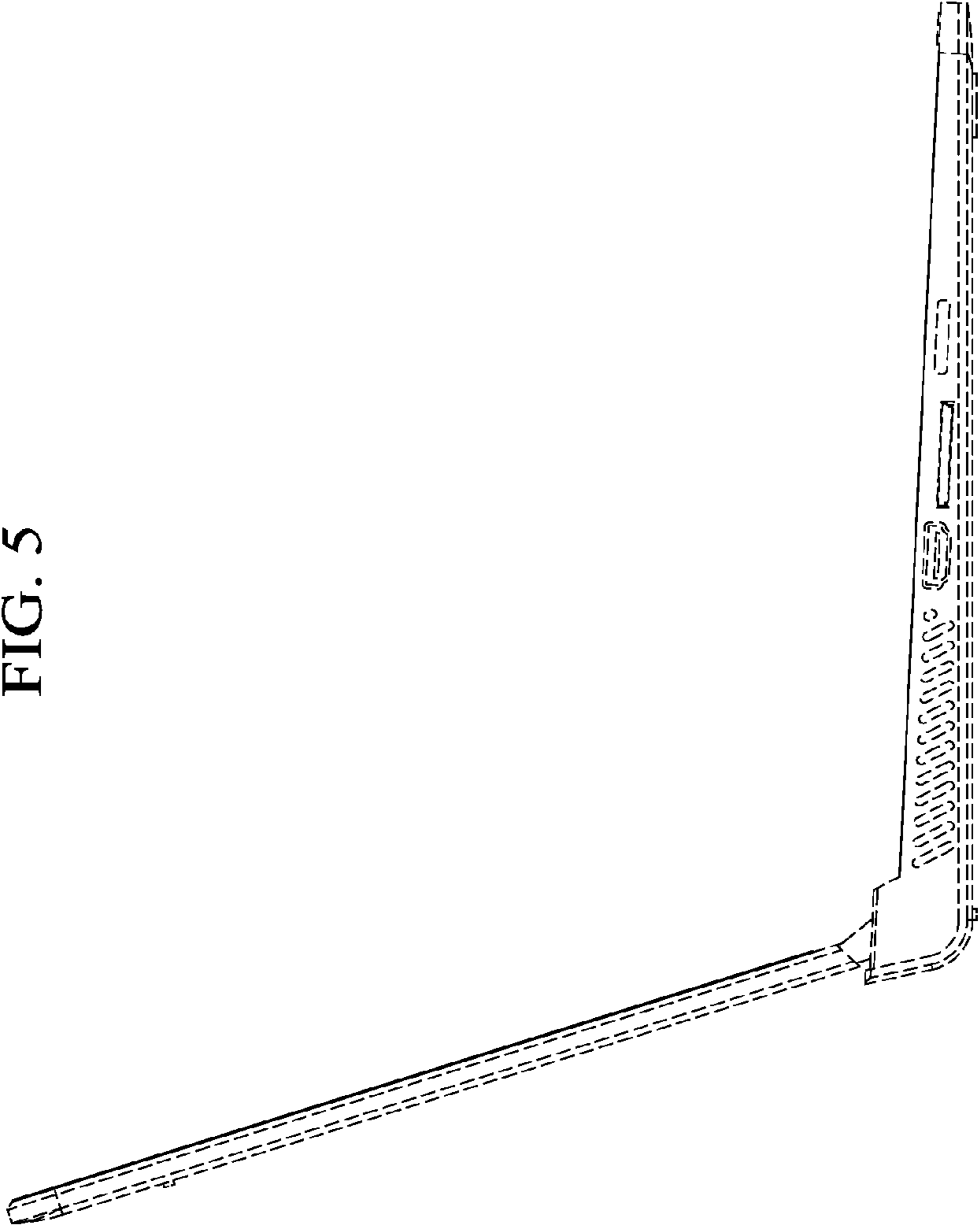


FIG. 6

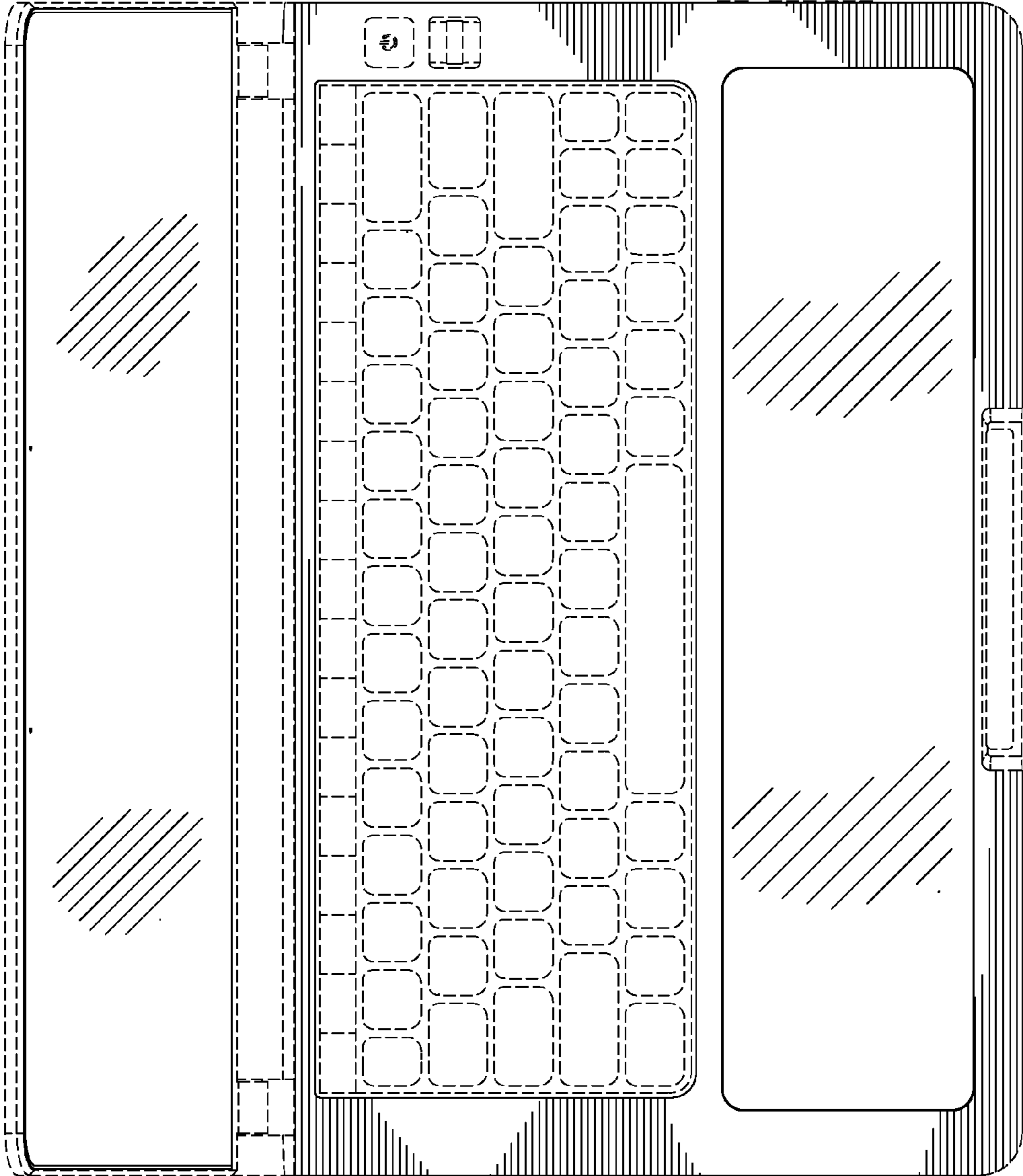
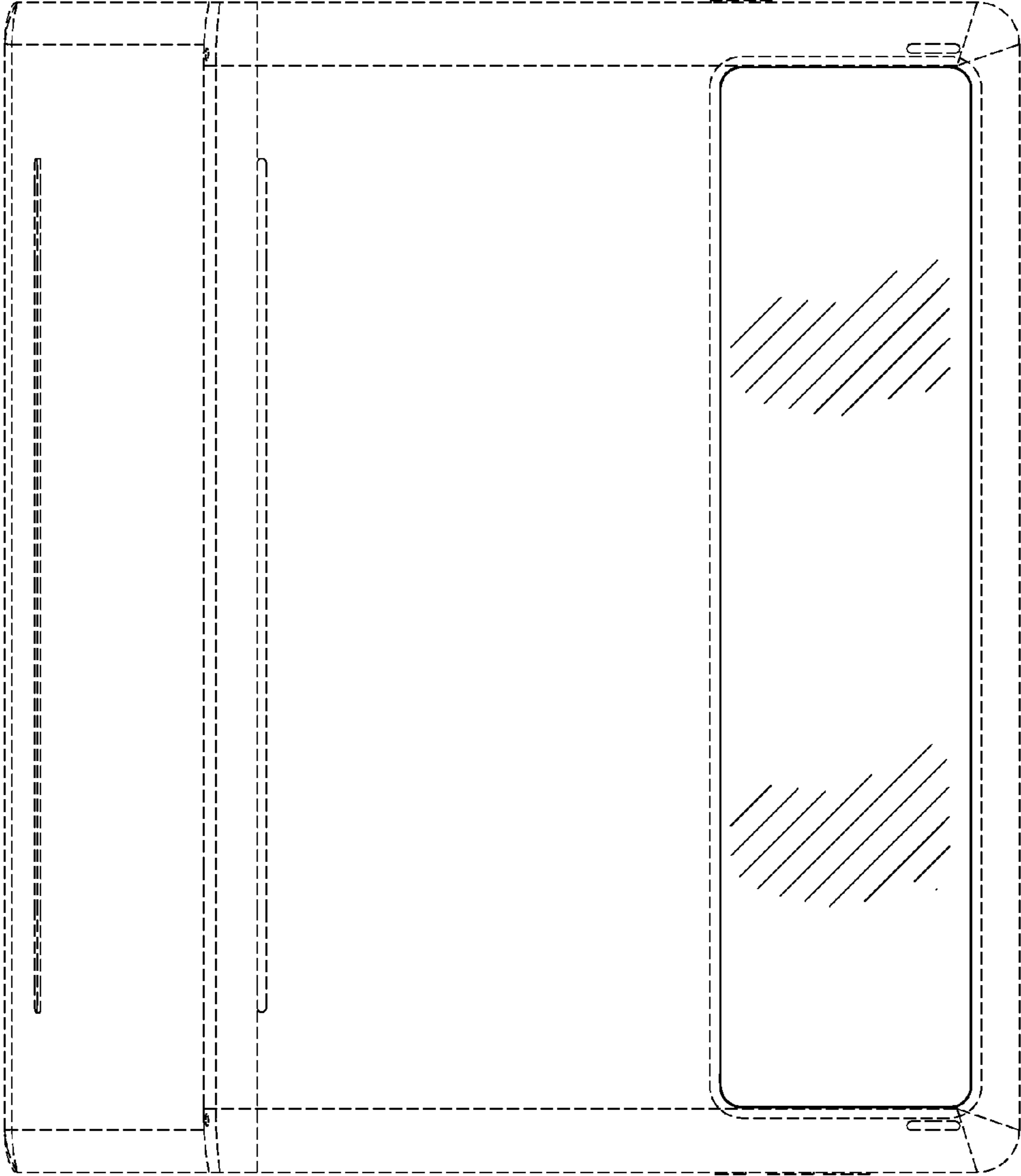


FIG. 7



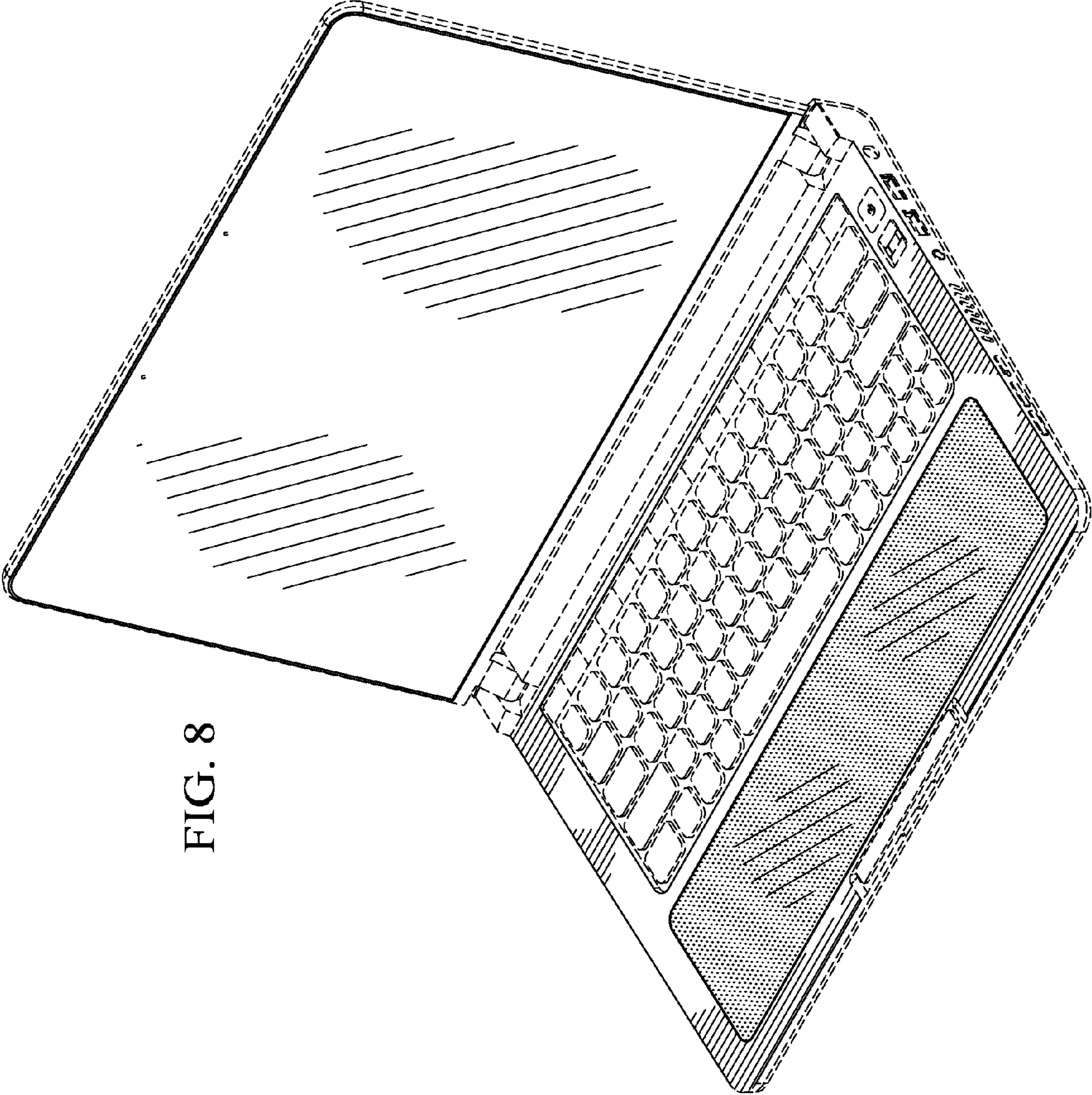


FIG. 8

FIG. 9

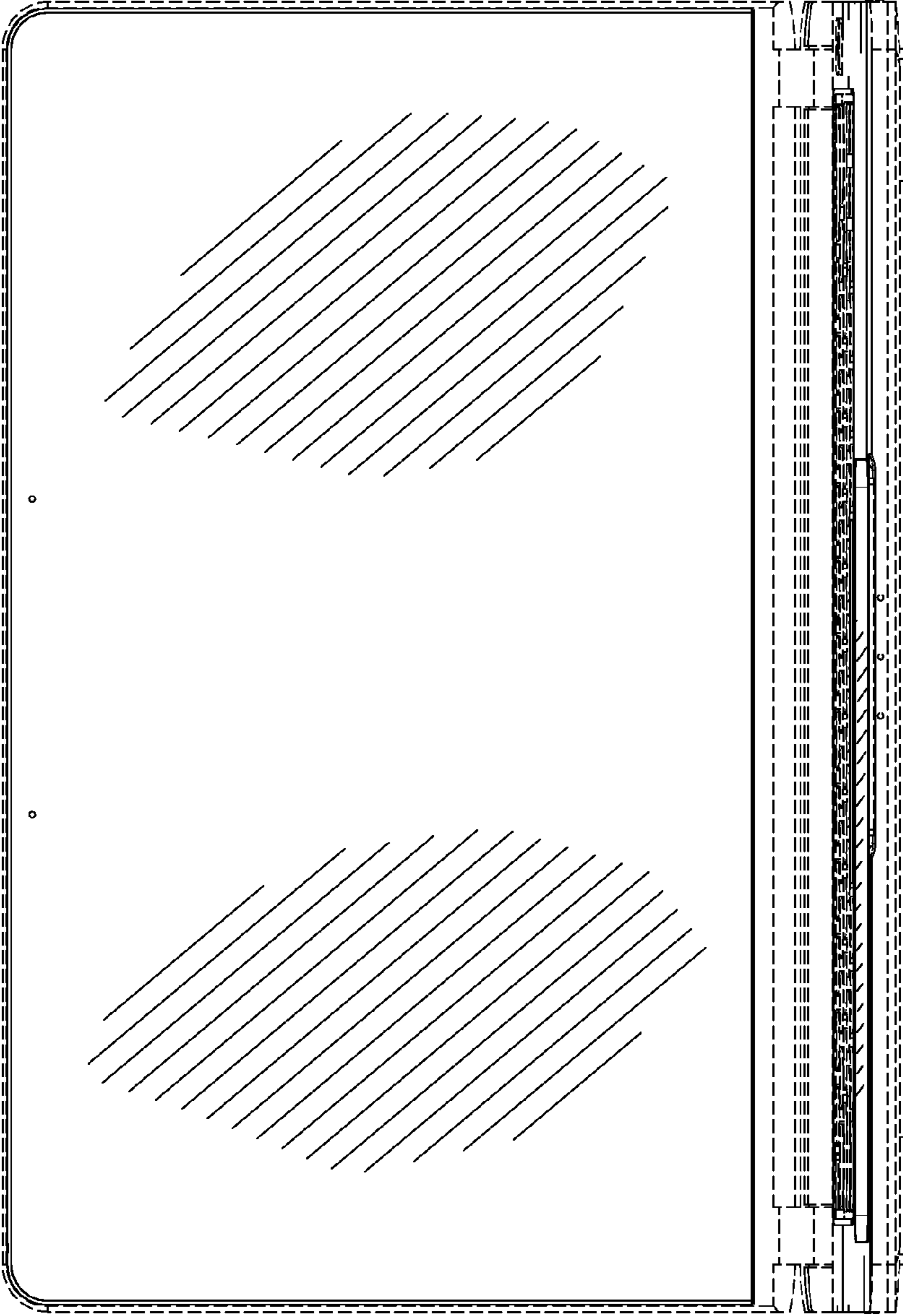
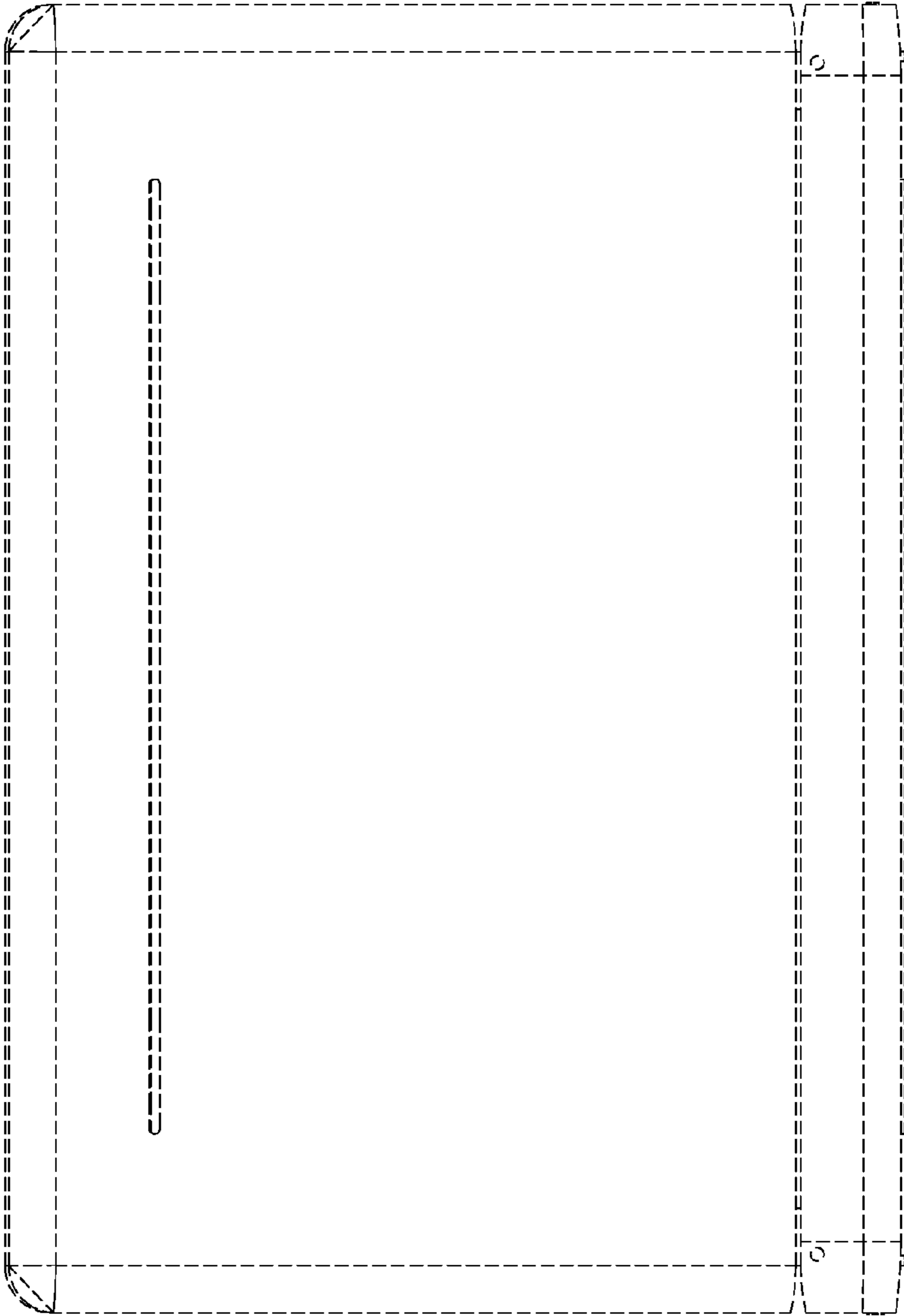


FIG. 10



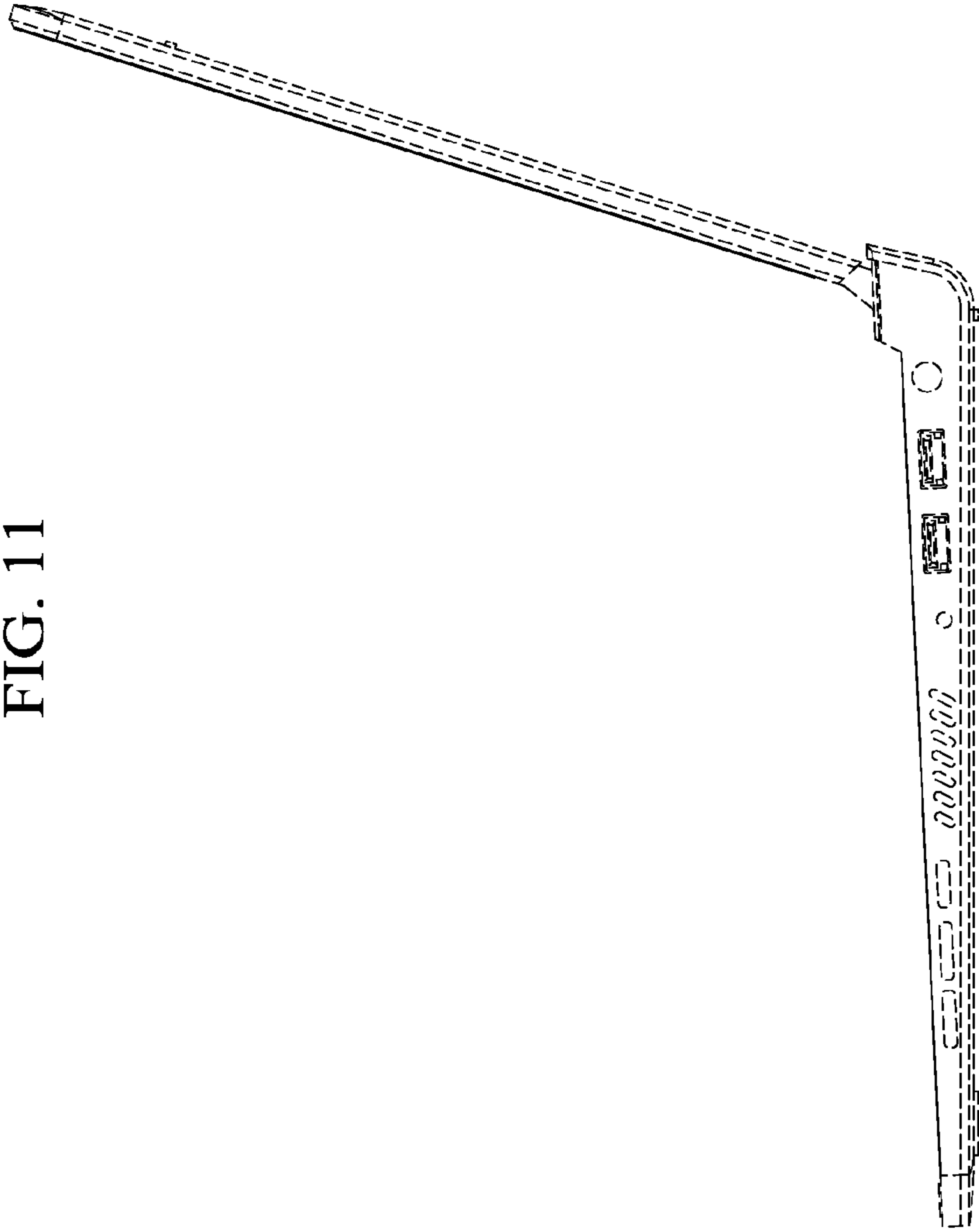


FIG. 11

FIG. 12

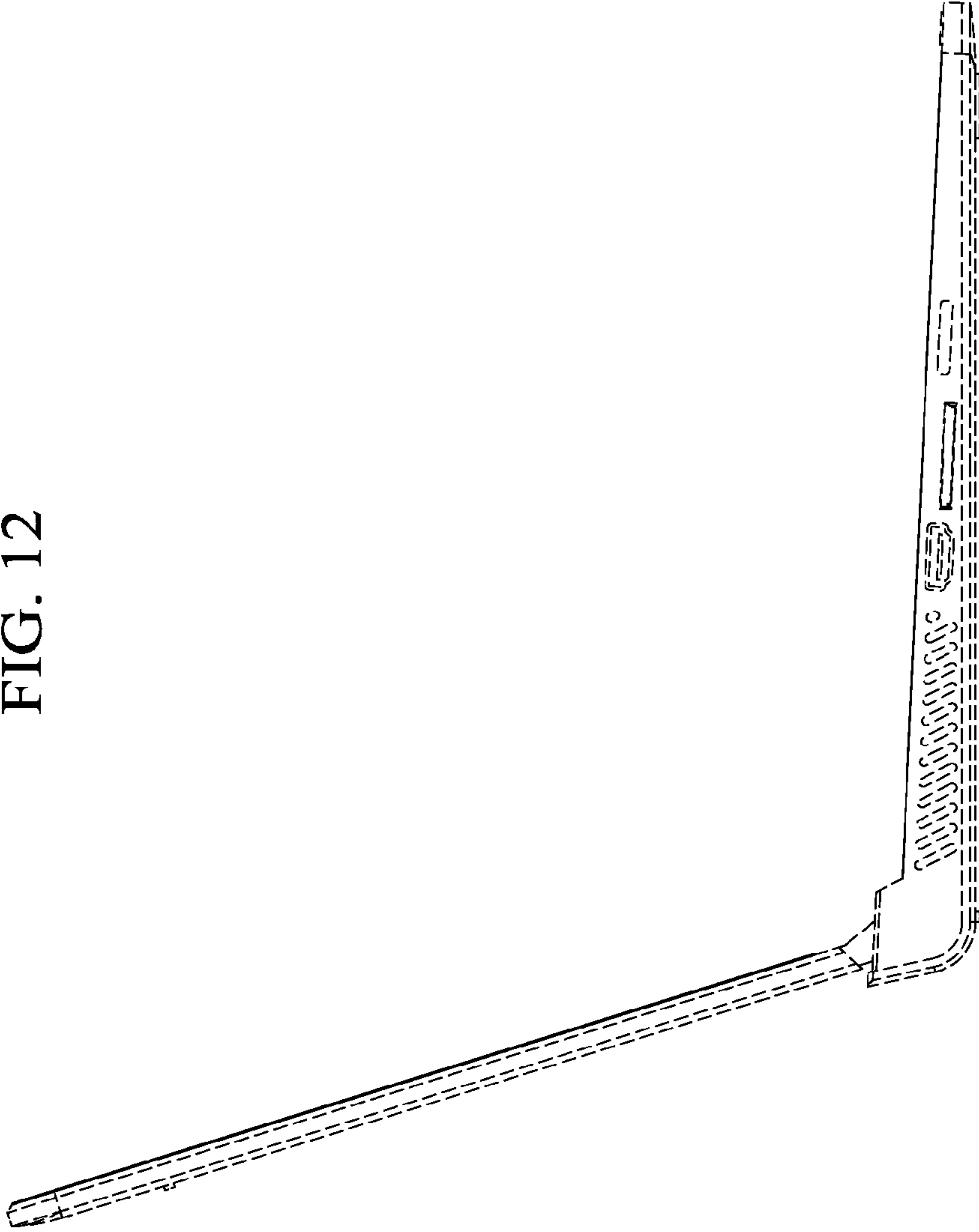


FIG. 13

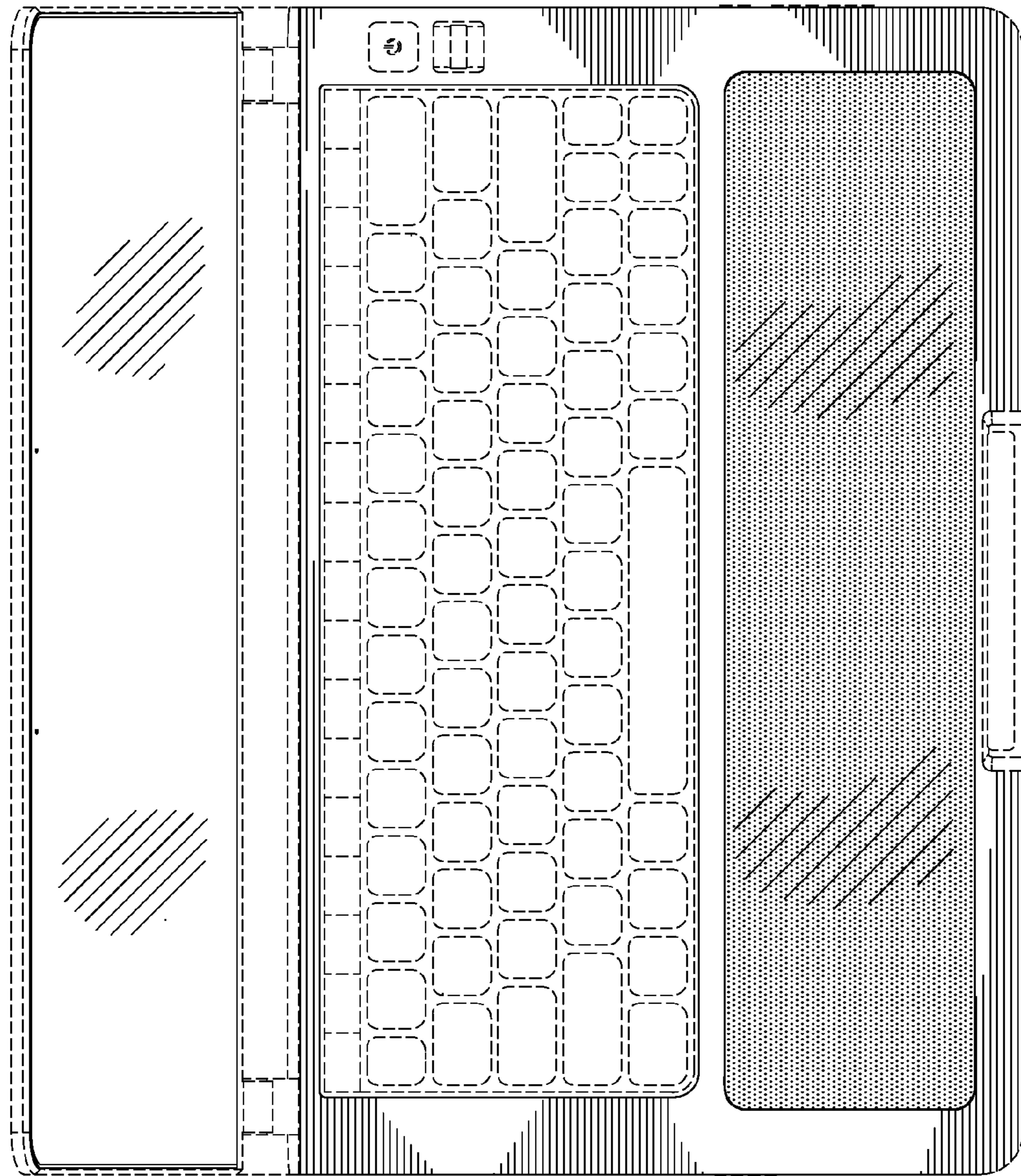
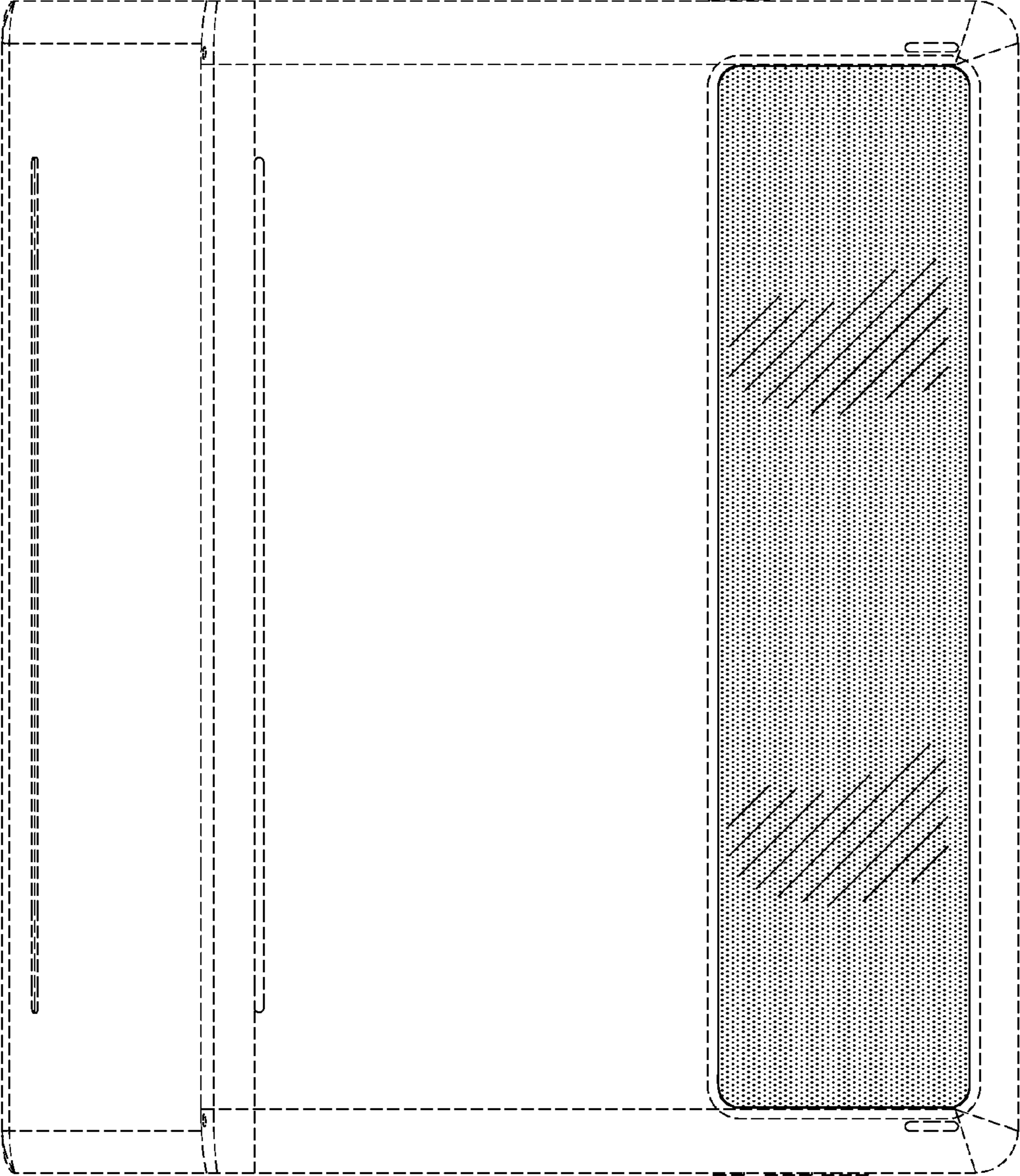


FIG. 14



UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : D691,995 S
APPLICATION NO. : 29/423470
DATED : October 22, 2013
INVENTOR(S) : Peter Adamson et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title Page

Item (54), in column 1, in "Title", line 1, after "WITH" insert -- AN AT LEAST --.

In column 2, under "Description", line 1, after "of" insert -- a first embodiment --.

In column 2, under "Description", line 2, after "with" insert -- an at least --.

In column 2, under "Description", line 3, after "design" insert -- , --.

In column 2, under "Description", line 19, after "lines" insert -- in --.

Signed and Sealed this
Eighteenth Day of October, 2016



Michelle K. Lee
Director of the United States Patent and Trademark Office