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Chu et al.

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(54) **LED CHIP**

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(*) Notice: This patent is subject to a terminal disclaimer.

(**) Term: **14 Years**

(21) Appl. No.: **29/419,110**

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(Under 37 CFR 1.47)

(30) **Foreign Application Priority Data**

Oct. 28, 2011 (TW) 100305815

(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/182**

(58) **Field of Classification Search**
USPC D13/180; D26/1; 257/79, 80, 81,
257/88, 89, 95, 98, 99, 100, E33.058; 313/483,
313/498, 500; 362/555, 800
See application file for complete search history.

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Primary Examiner — Selina Sikder

(74) *Attorney, Agent, or Firm* — WPAT, PC; Justin King

(57) **CLAIM**

The ornamental design for a LED chip, as shown and described in the drawings.

DESCRIPTION

FIG. 1 is a perspective view of a light emitting diode device showing my new design;

FIG. 2 is a top view thereof;

FIG. 3 is a front view thereof;

FIG. 4 is a back view thereof;

FIG. 5 is a left view thereof;

FIG. 6 is a right view thereof;

FIG. 7 is a cross-sectional view thereof, take along line 7-7 in FIG. 2.;

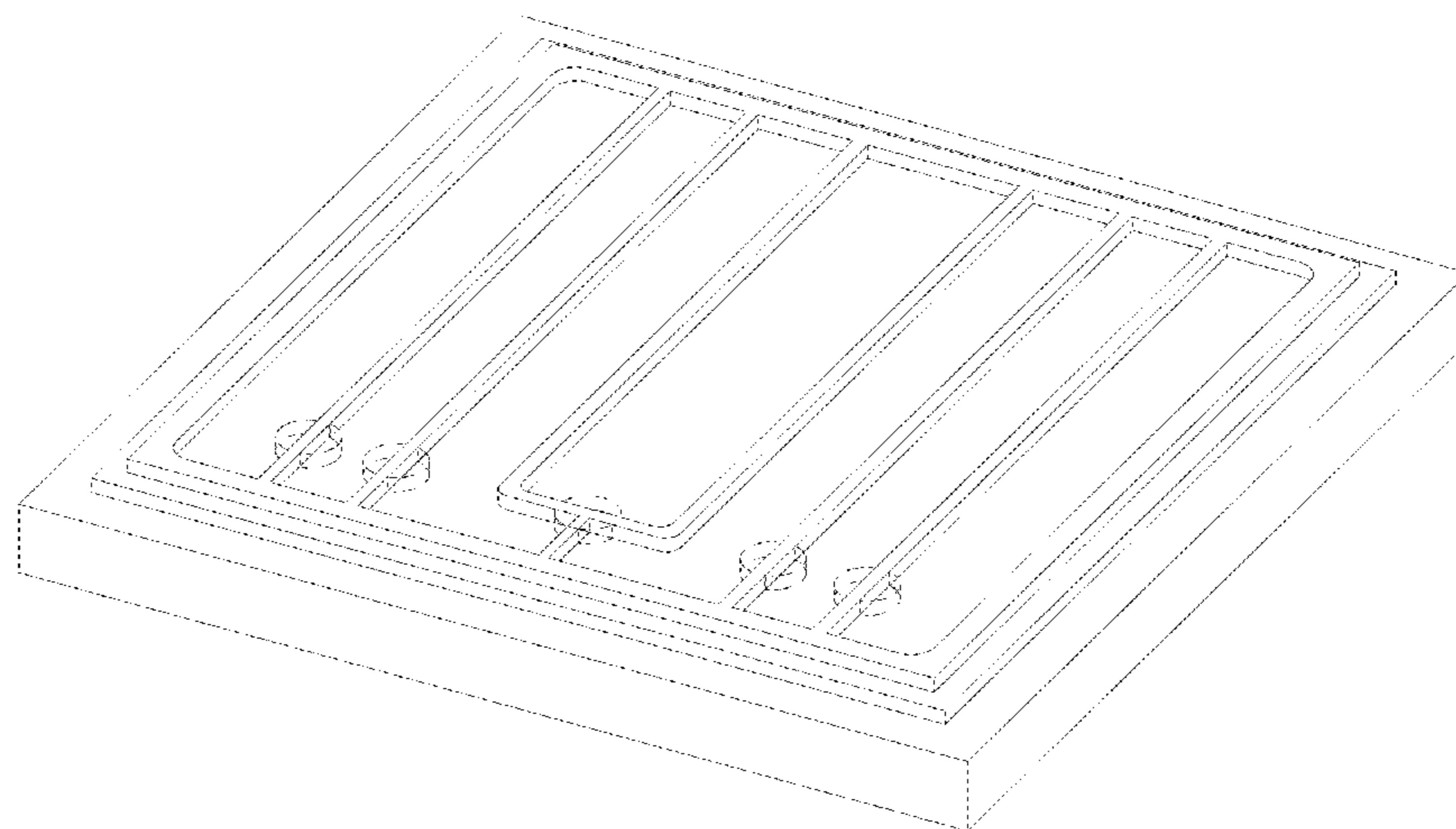
FIG. 8 is a cross-sectional view thereof, take along line 8-8 in FIG. 2.;

FIG. 9 is a right side view of thereof; the left side view is a mirror image of the right side view; and,

FIG. 10 is a front view of thereof; the backside view is a mirror image of the front side view.

It will be understood that the dash lines presented in the drawings are for illustration only, and do not form a part of the claimed design.

1 Claim, 8 Drawing Sheets



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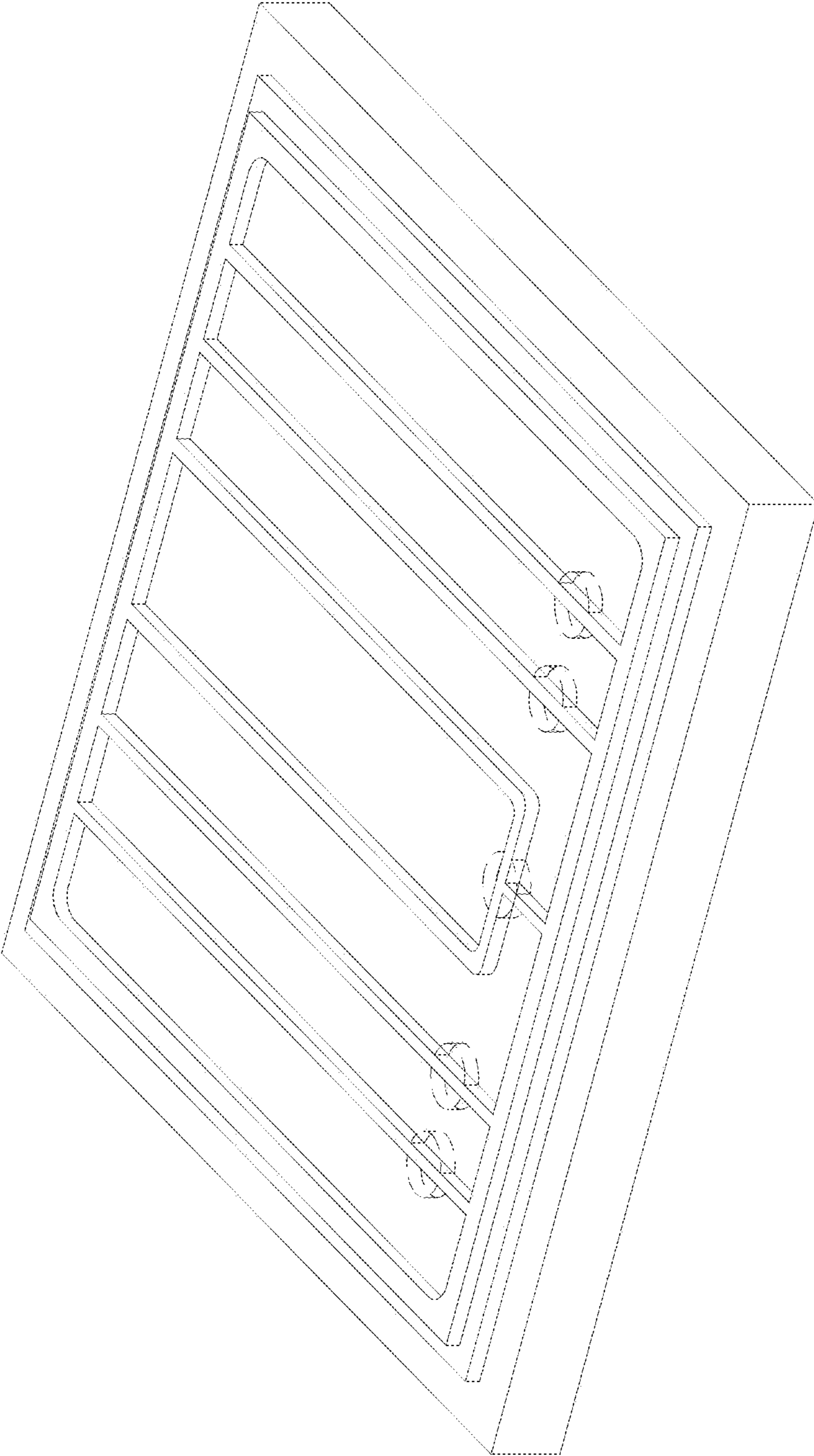


Fig. 1

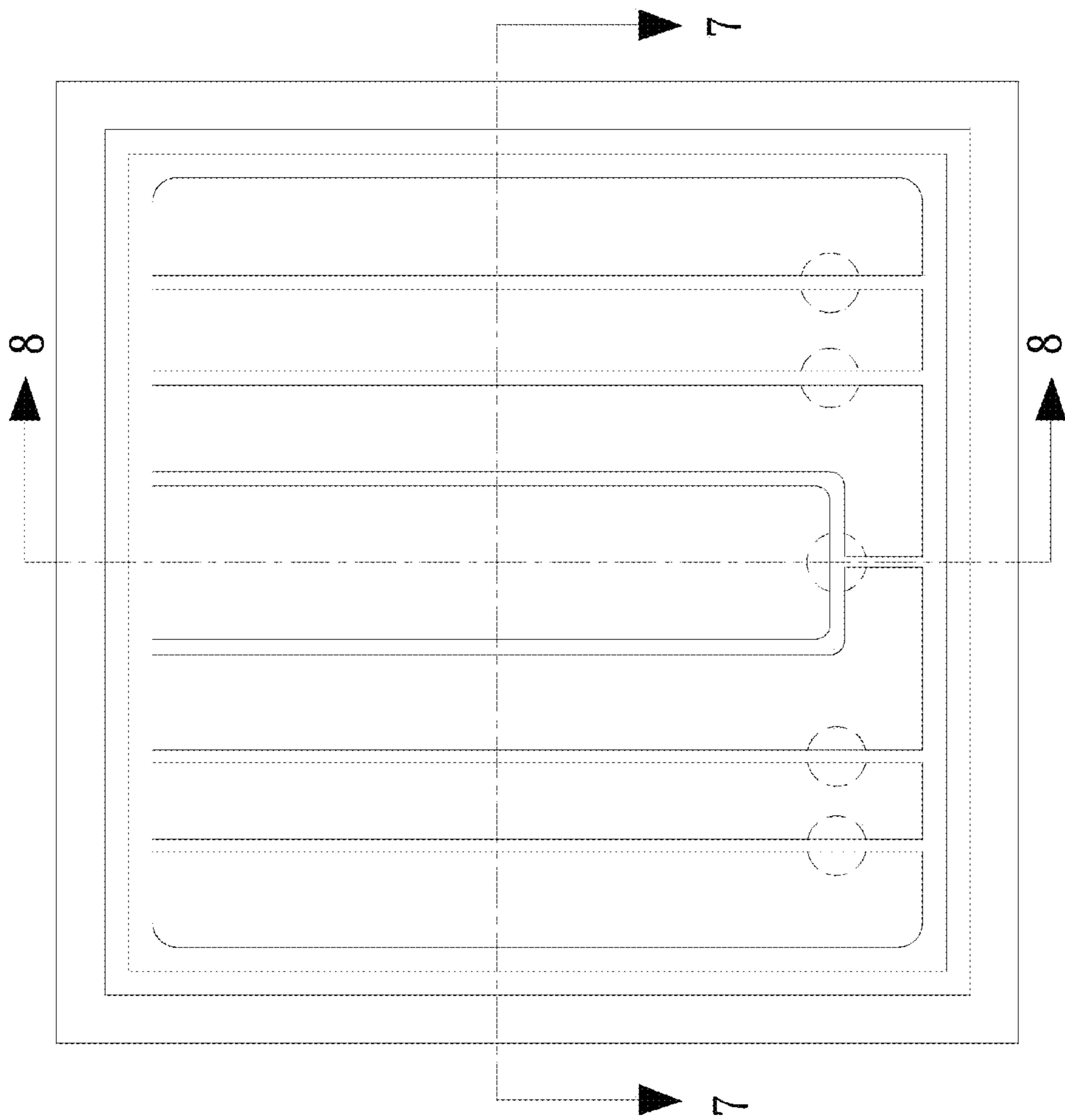


Fig. 2

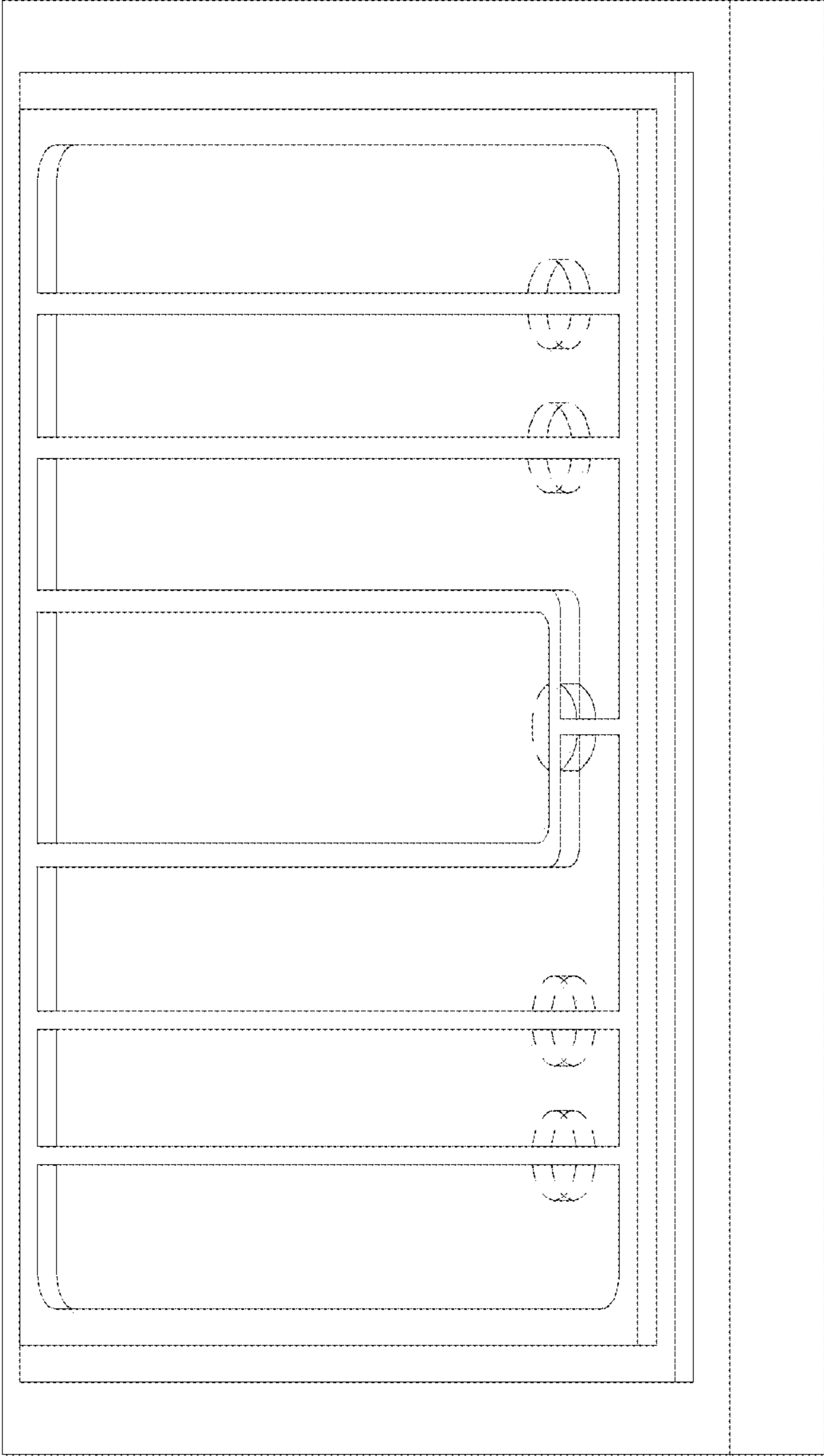


Fig. 3

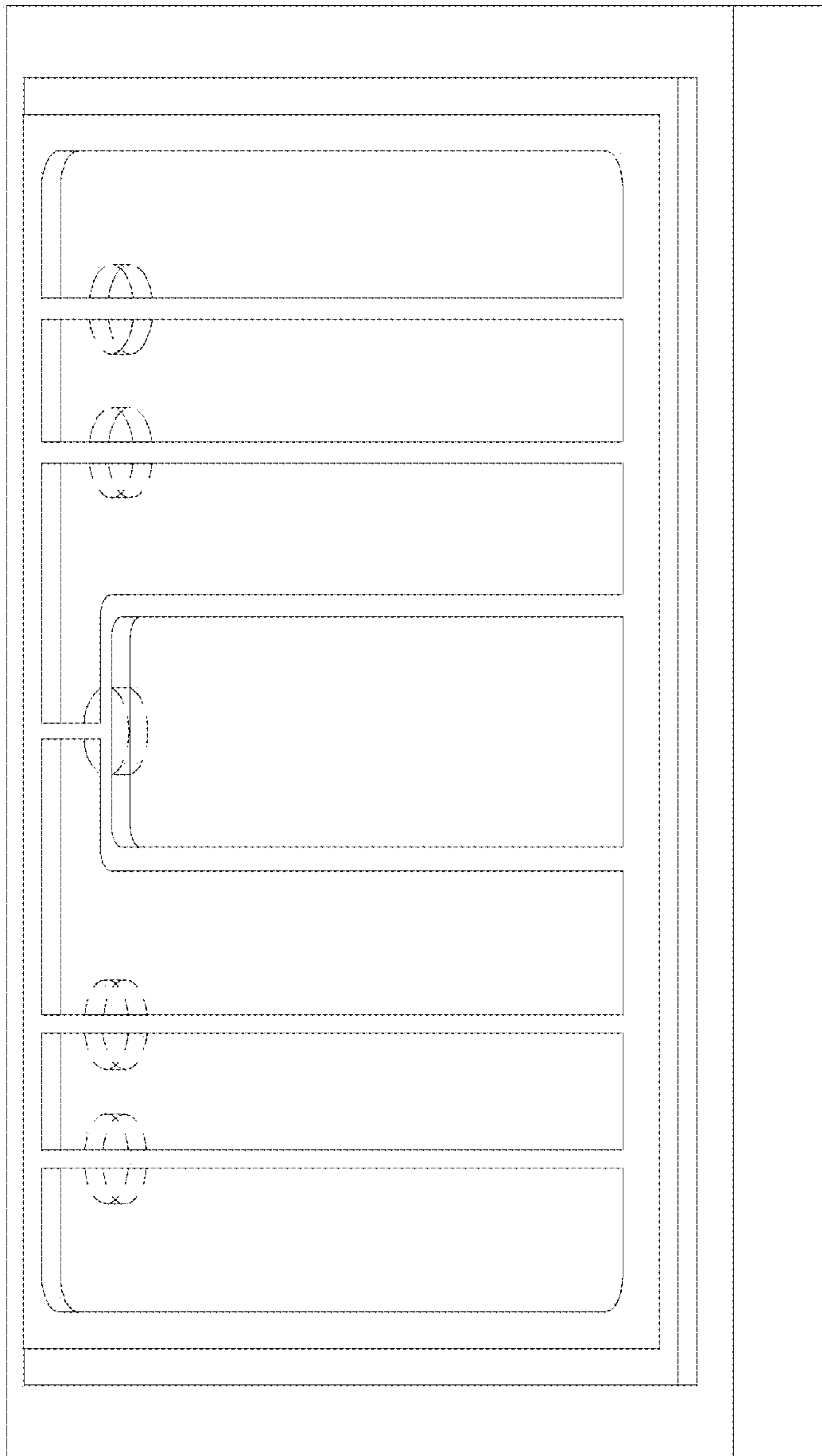


Fig. 4

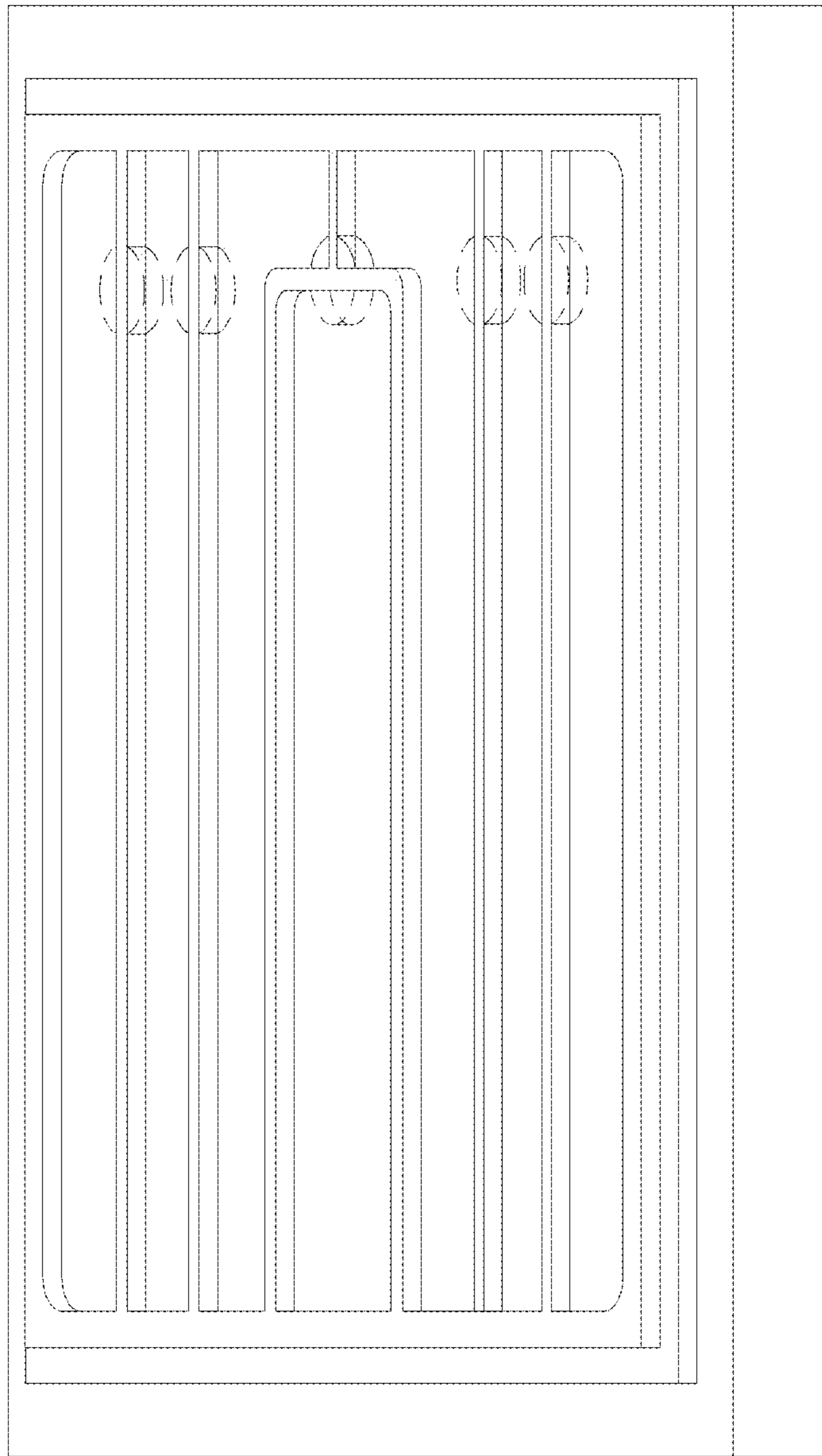


Fig. 5

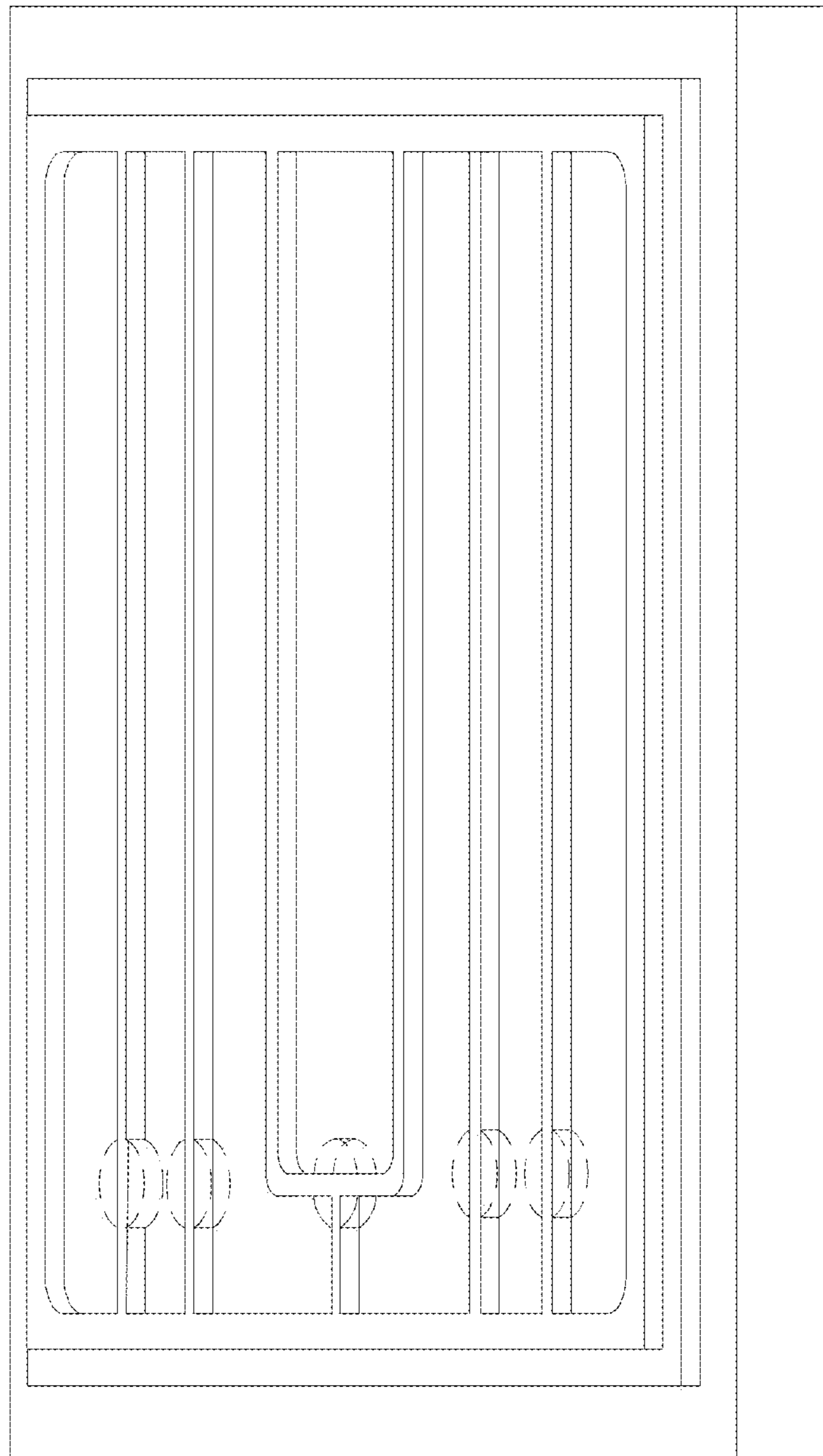


Fig. 6

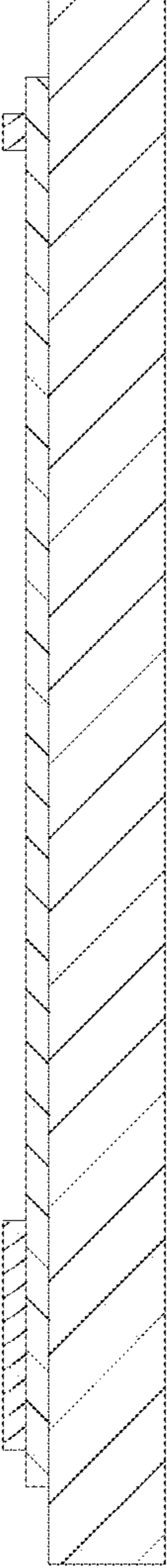


Fig. 7

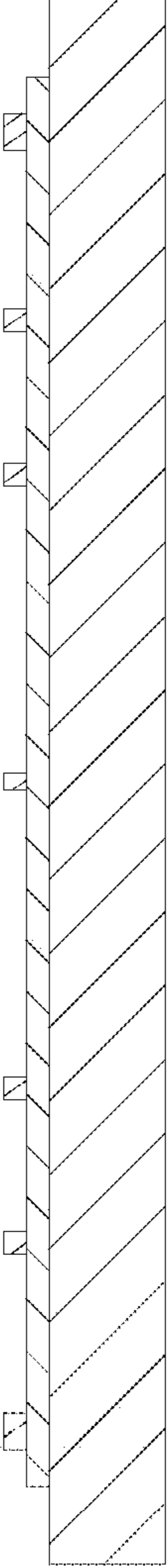


Fig. 8

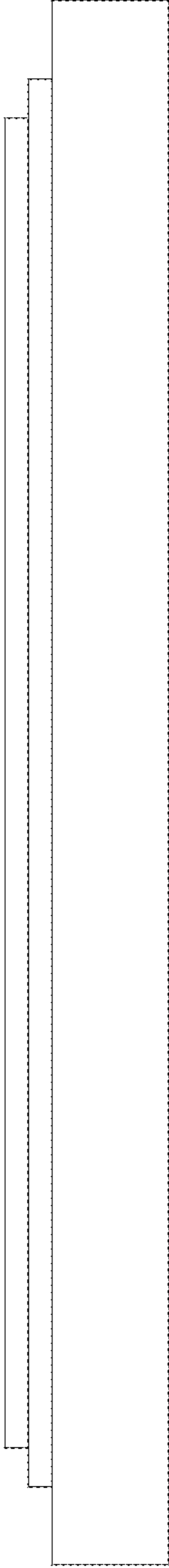


Fig. 9

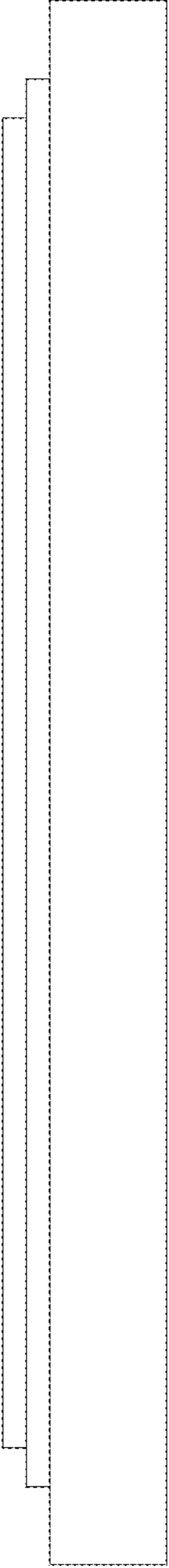


Fig. 10