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(12) **United States Design Patent**
Krishnan et al.

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(54) **KEYED WAFER CARRIER**

(75) Inventors: **Sandeep Krishnan**, Princeton, NJ (US);
Keng Moy, Basking Ridge, NJ (US);
Alexander I. Gurary, Bridgewater, NJ
(US); **Matthew King**, Montville, NJ
(US); **Vadim Boguslavskiy**, Princeton,
NJ (US); **Steven Krommenhoek**,
Annandale, NJ (US)

(73) Assignee: **Veeco Instruments Inc.**, Plainview, NY
(US)

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(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/182**

(58) **Field of Classification Search**
USPC ... D13/182; 118/500, 728, 729; 156/345.53;
279/128; 361/234; 451/287, 288, 289
See application file for complete search history.

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Primary Examiner — Selina Sikder

(74) *Attorney, Agent, or Firm* — Lerner, David, Littenberg,
Krumholz & Mentlik, LLP

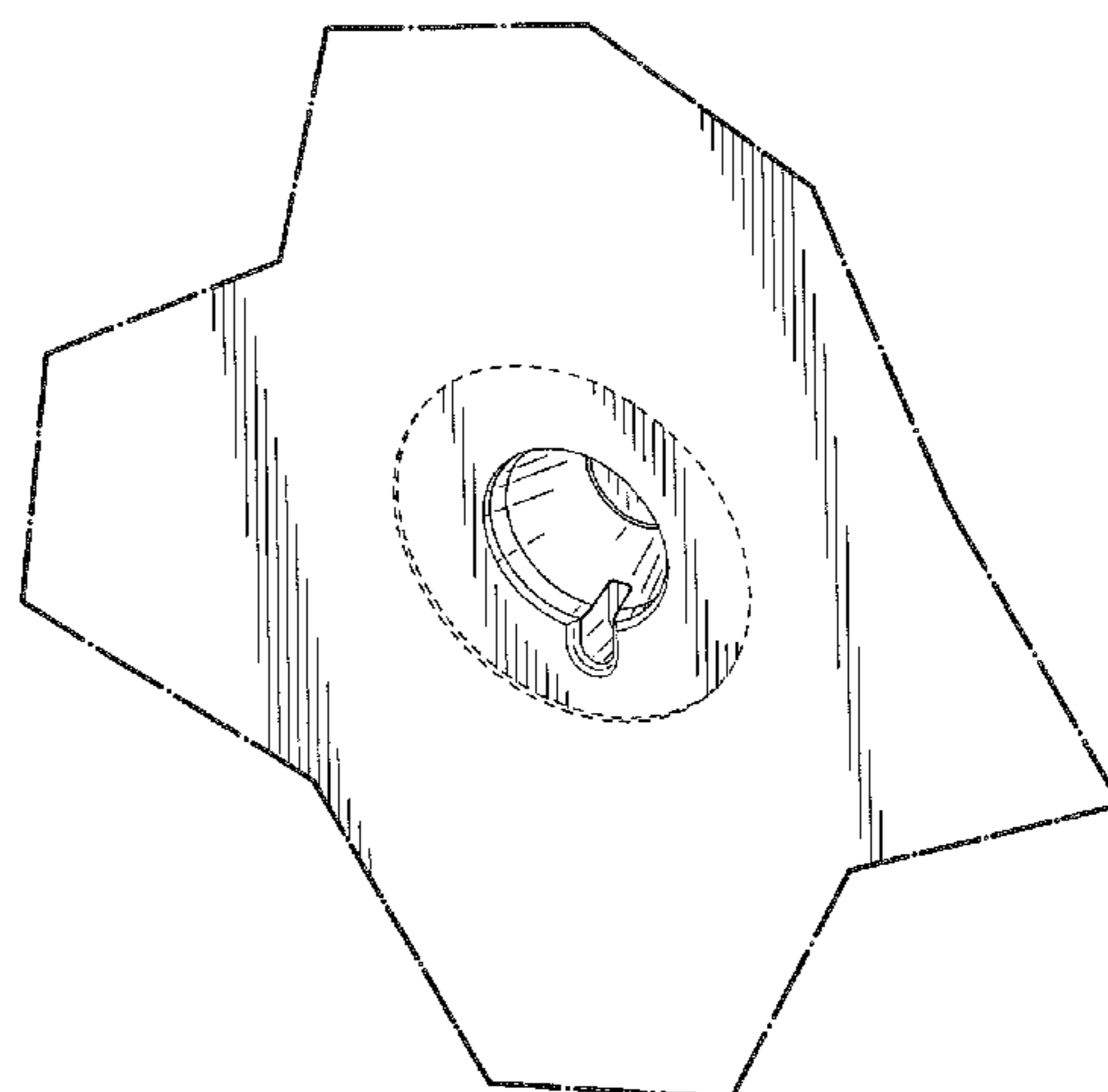
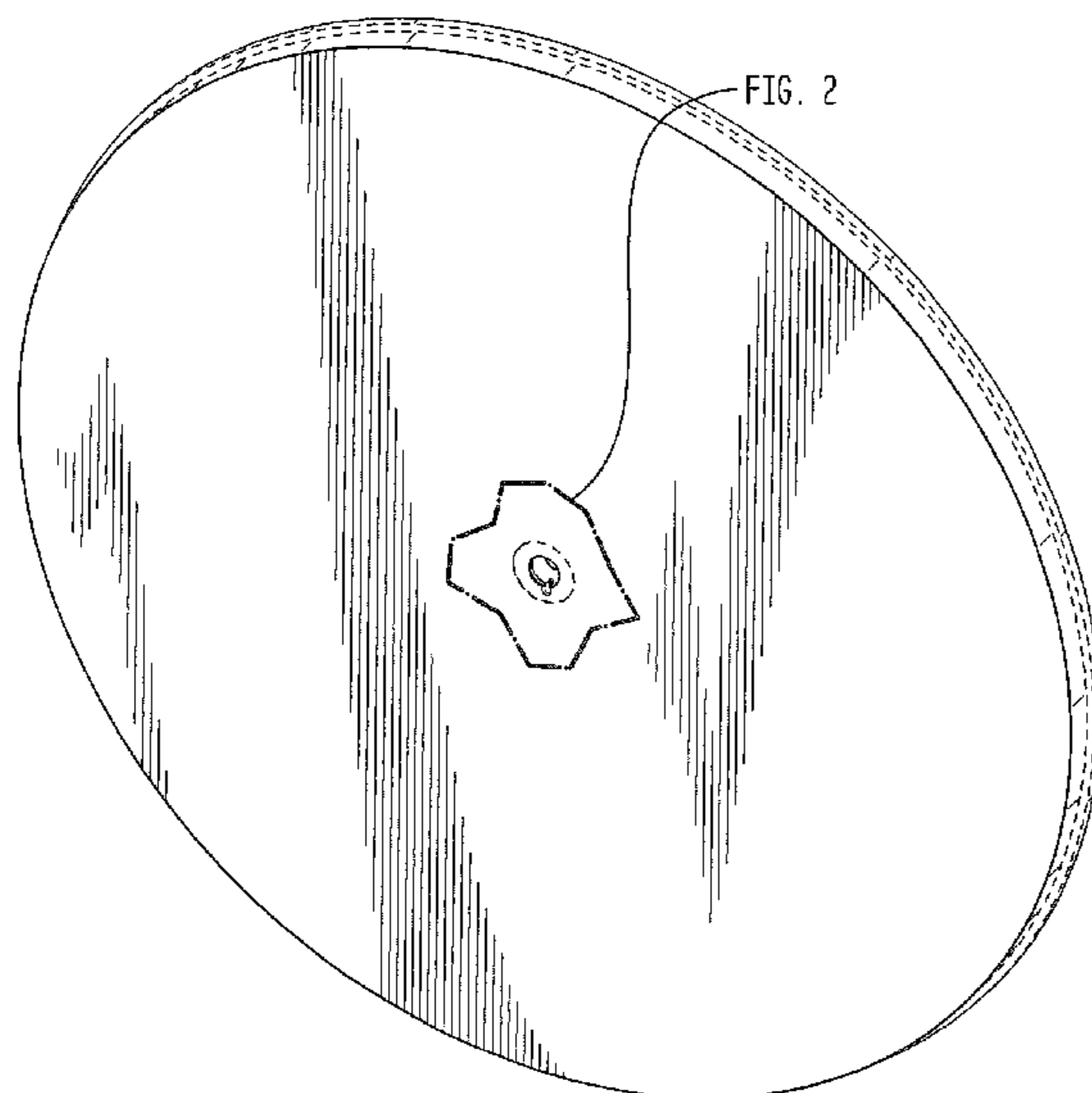
(57) **CLAIM**

The ornamental design for a keyed wafer carrier, as shown
and described.

DESCRIPTION

FIG. 1 is a bottom perspective view of a keyed wafer carrier showing a first embodiment of our new design;
FIG. 2 is an enlarged view of a portion of FIG. 1;
FIG. 3 is a bottom plan view of the first embodiment;
FIG. 4 is an enlarged view of a portion of FIG. 3;
FIG. 5 is a bottom perspective view of a keyed wafer carrier showing a second embodiment of our new design;
FIG. 6 is an enlarged view of a portion of FIG. 5;
FIG. 7 is a bottom plan view of the second embodiment;
FIG. 8 is an enlarged view of a portion of FIG. 7;
FIG. 9 is a top perspective view that can correspond to either the first or second embodiments;
FIG. 10 is a top plan view that can correspond to either the first or second embodiments; and,
FIG. 11 is a right side view that can correspond to either the first or second embodiments, the left side, front, and rear views being the same as the right side view.
The broken lines in the drawings are for illustrative purposes only and form no part of the claimed design.
The dash-dot broken lines shown in the drawings define the area corresponding to the enlarged portion shown in FIGS. 2, 4, 6, and 8 views and form no part of the claimed design.

1 Claim, 10 Drawing Sheets



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FIG. 1

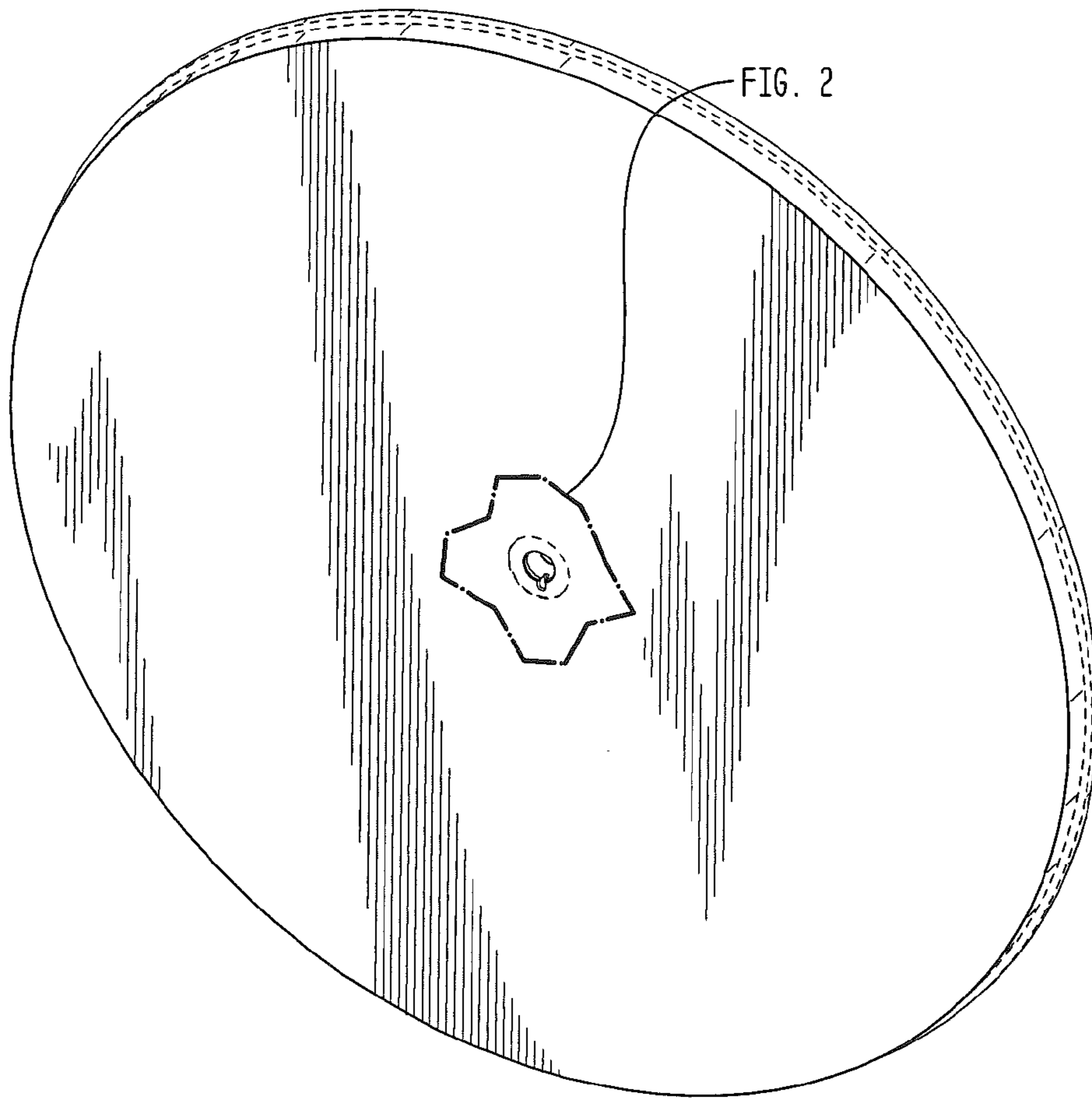


FIG. 2

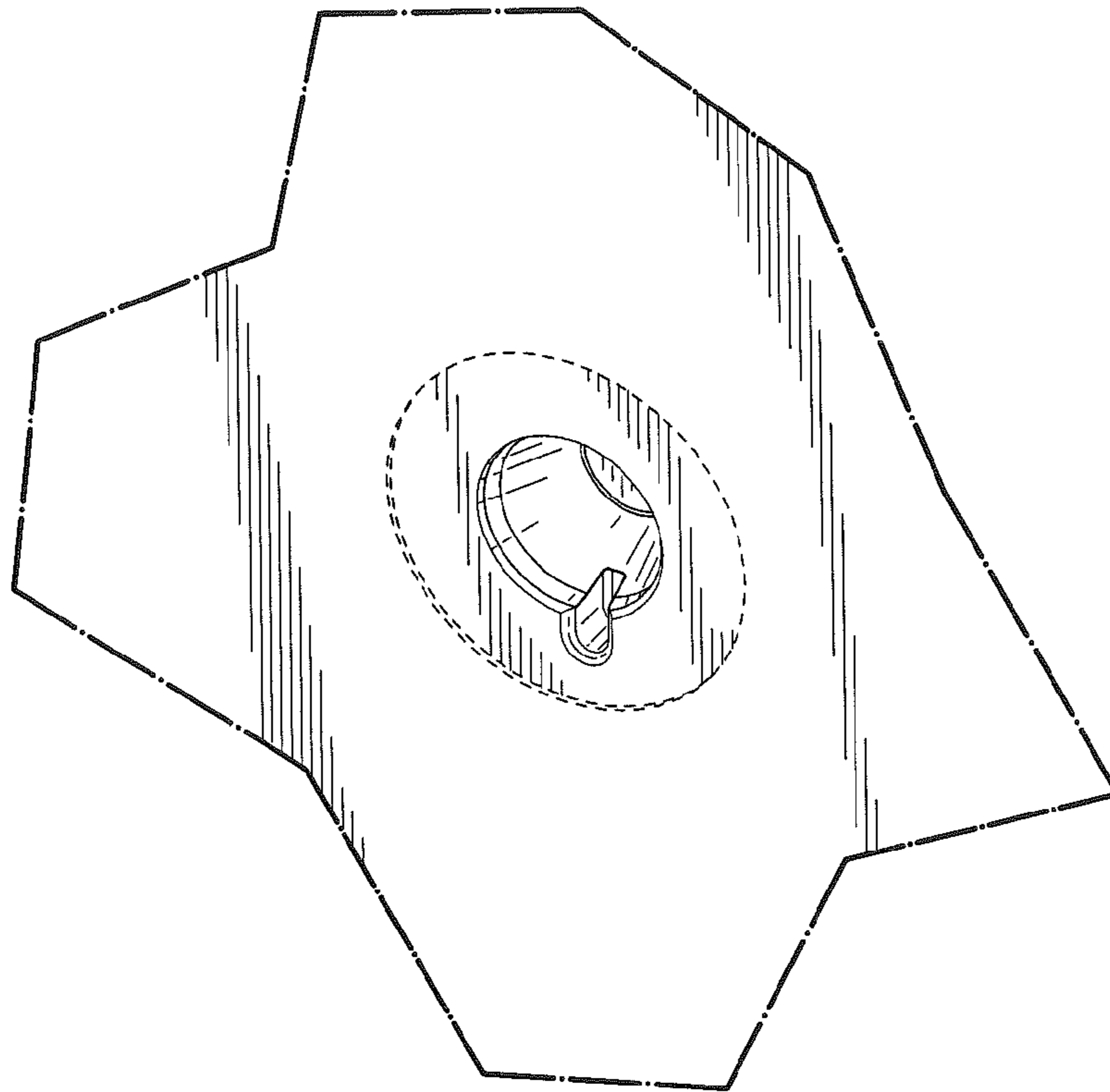


FIG. 3

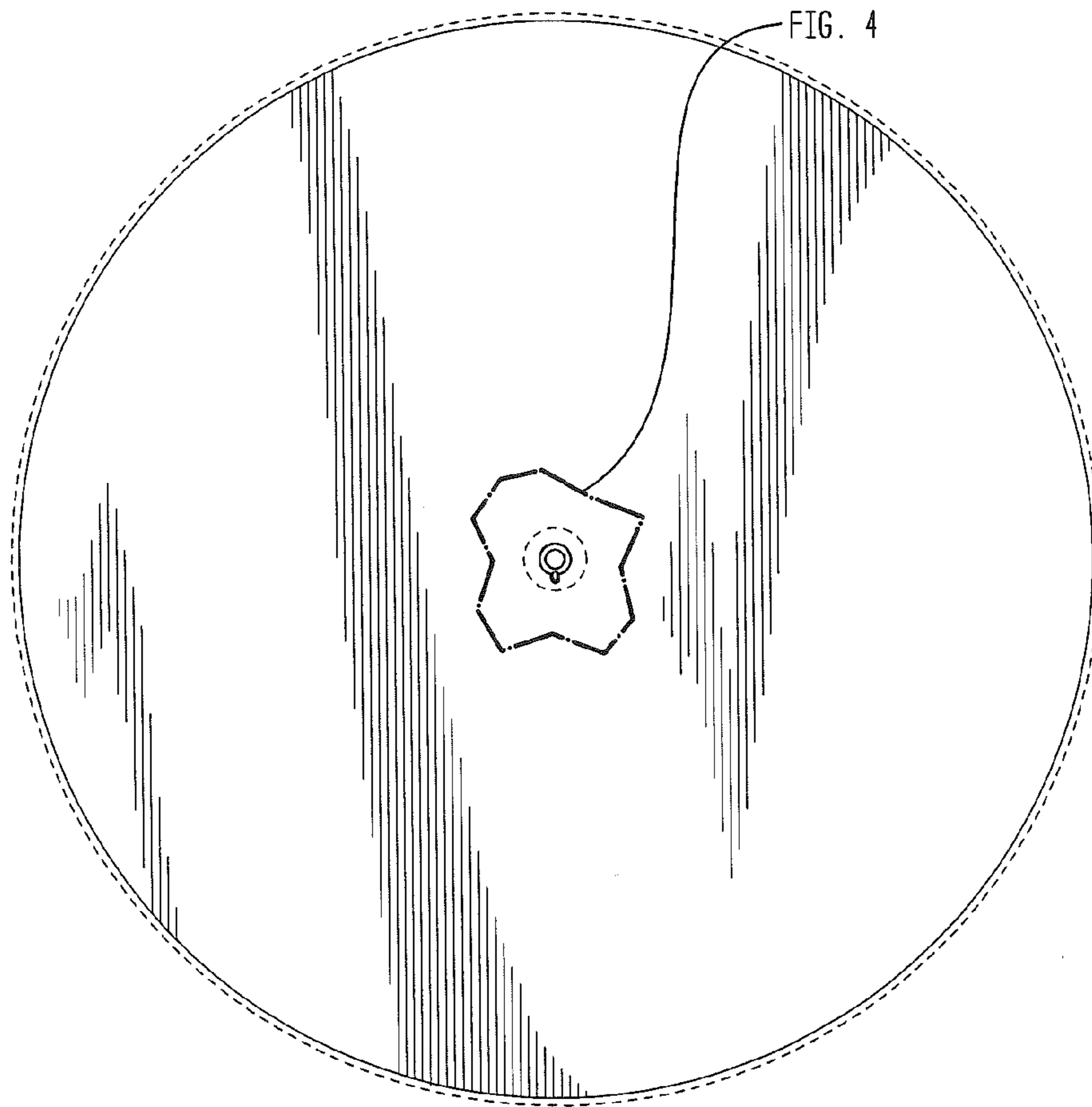


FIG. 4

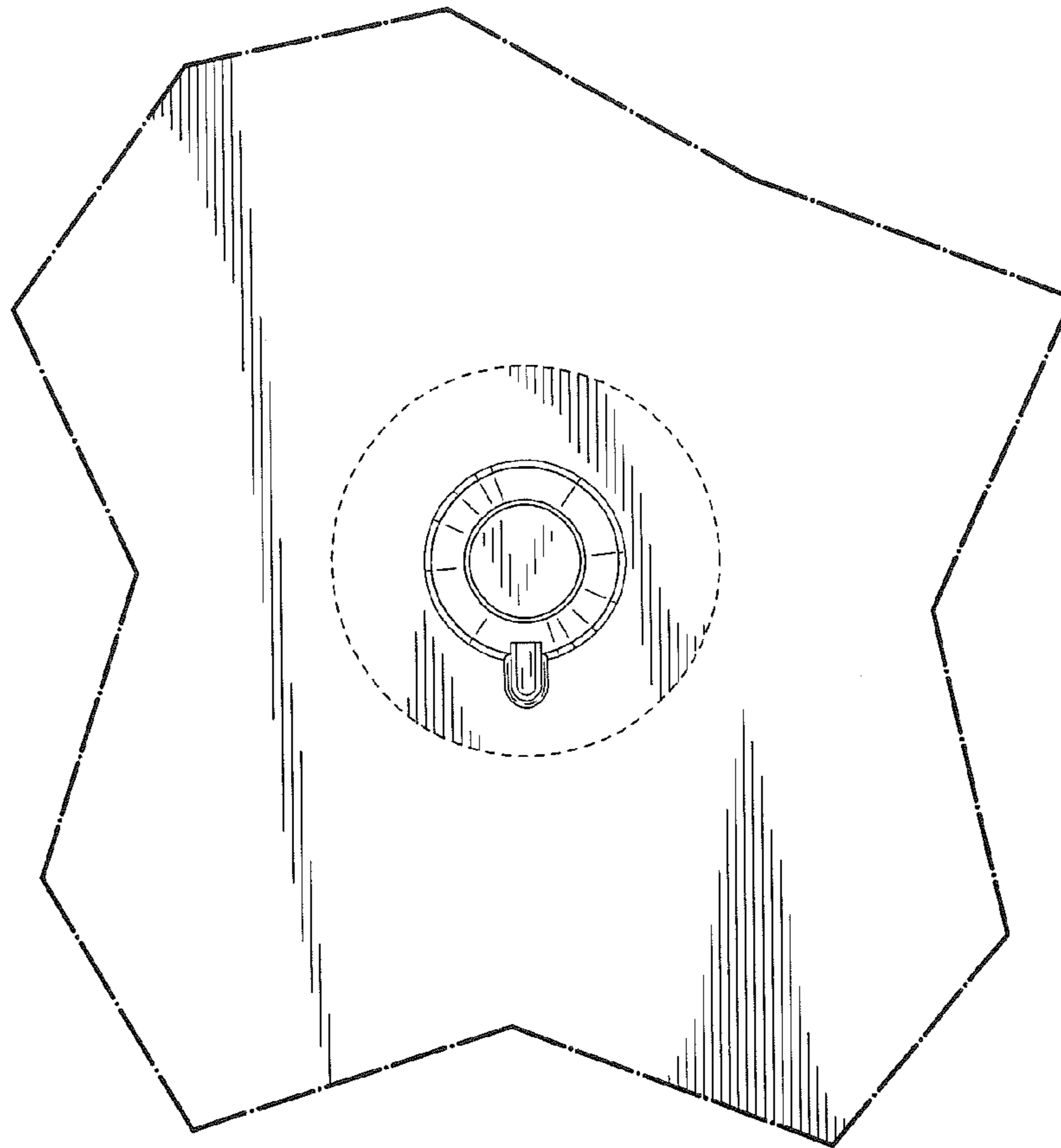


FIG. 5

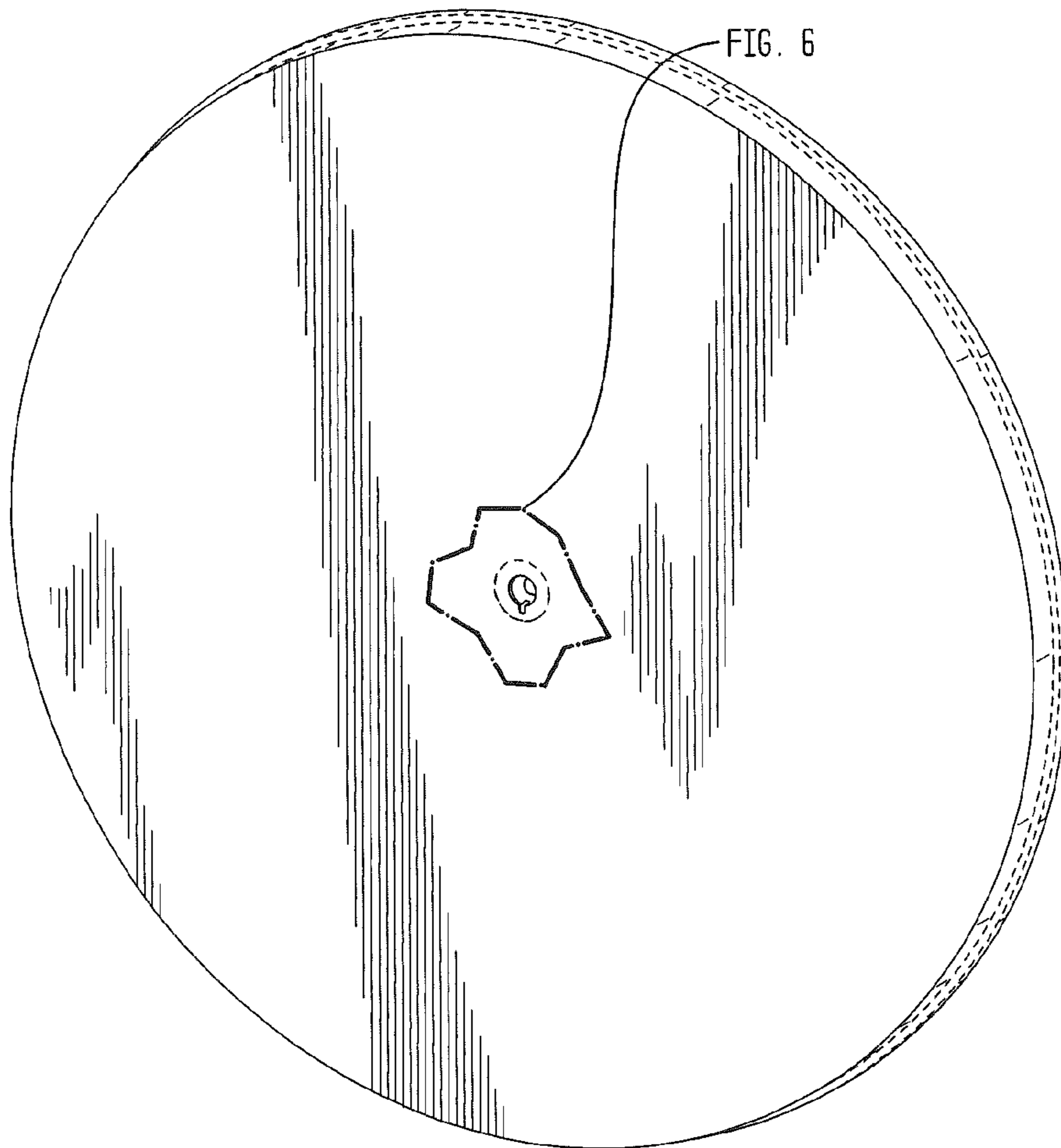


FIG. 6

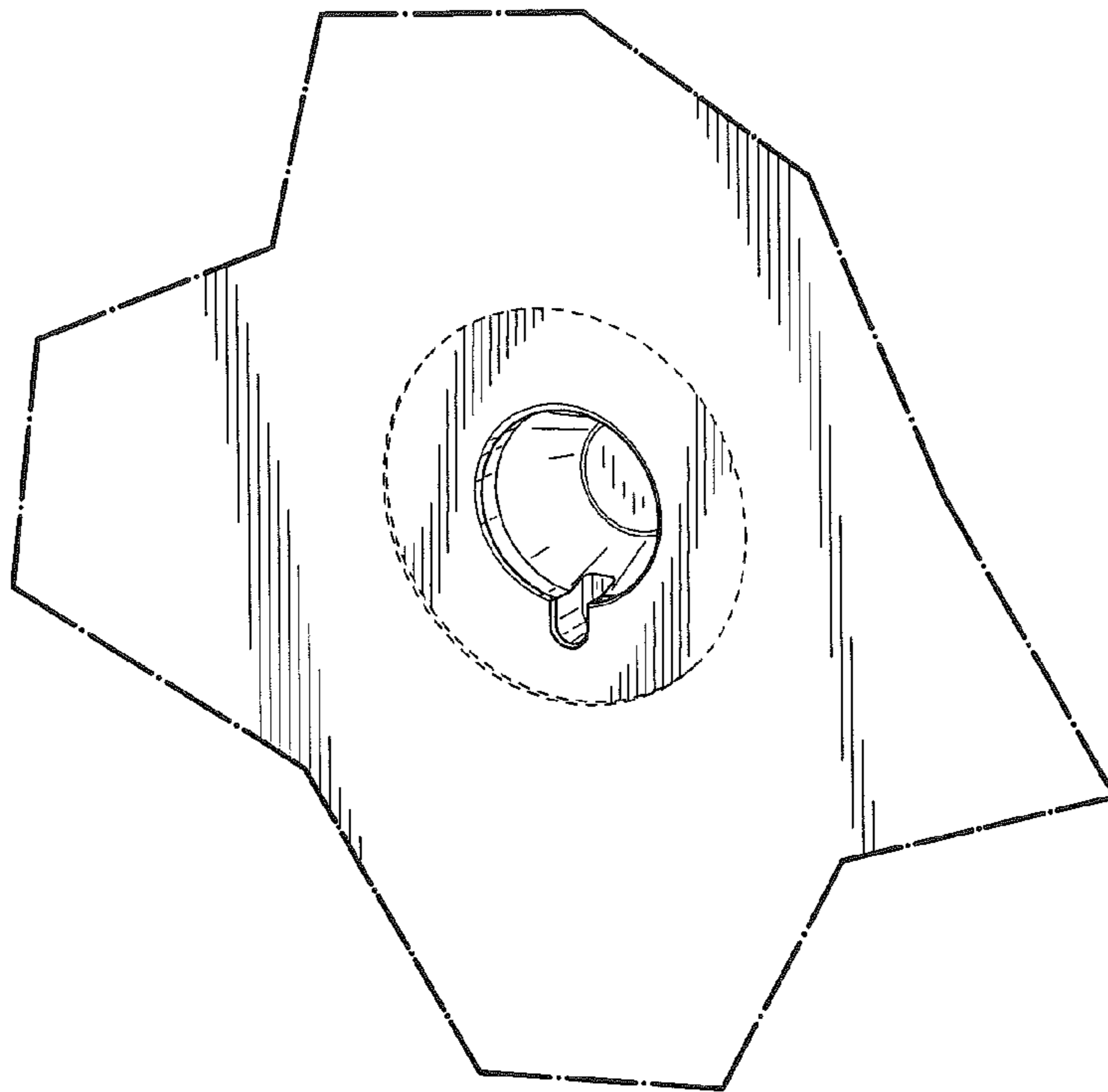


FIG. 7

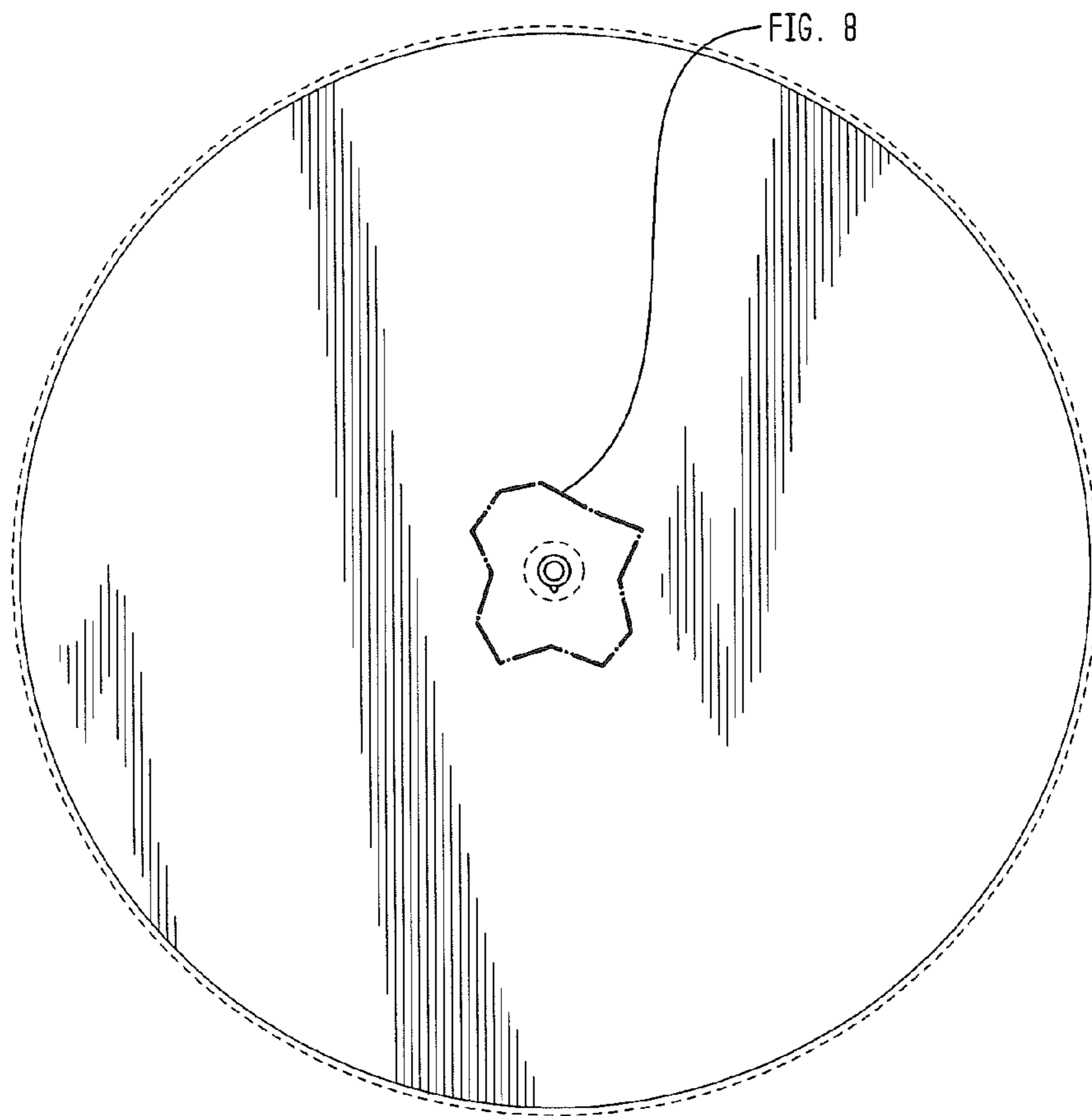


FIG. 8

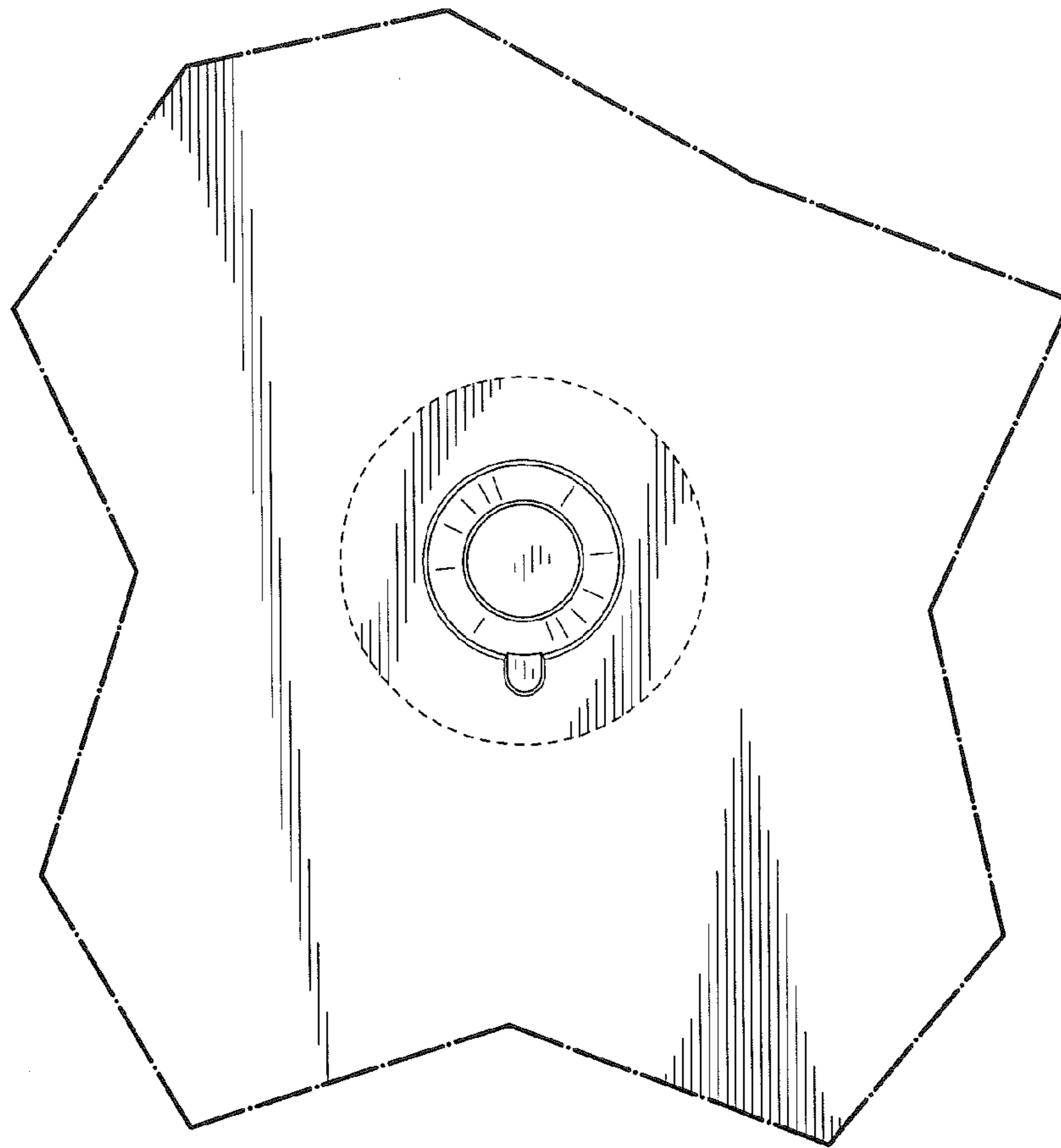


FIG. 9

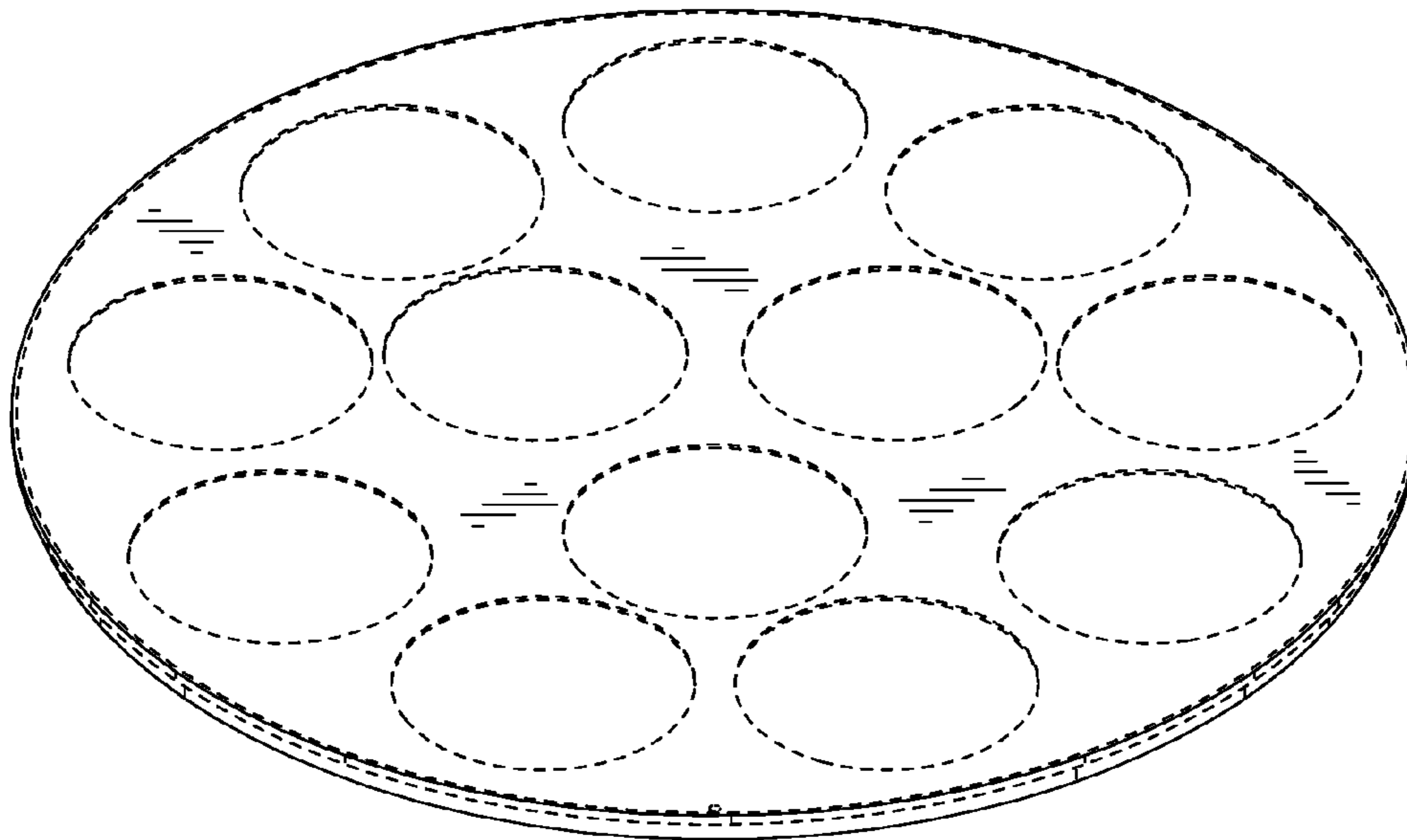


FIG. 10

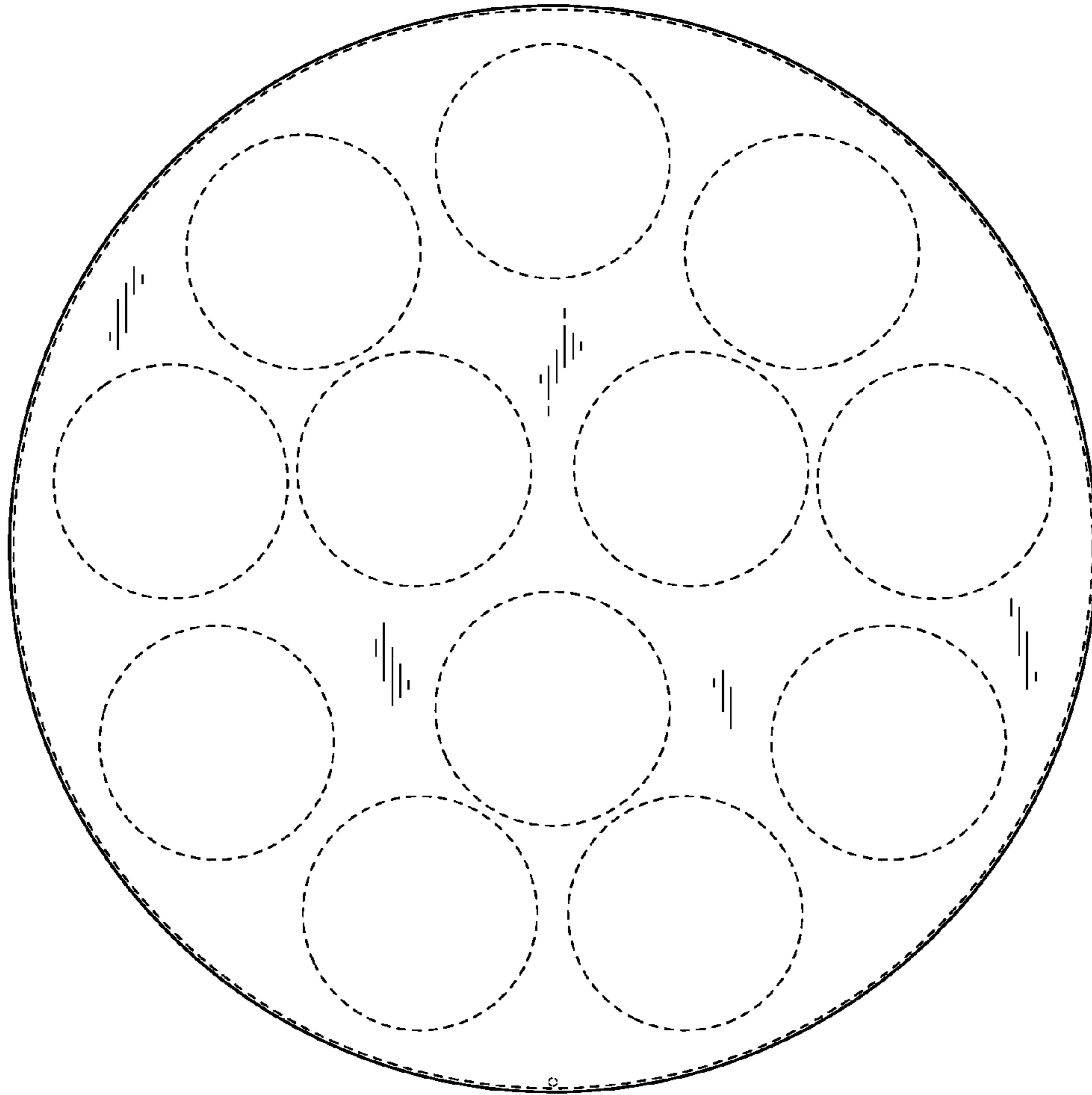


FIG. 11

