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(12) **United States Design Patent**  
**Laskowski et al.**

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(54) **CHIP STATION PLATE WITH LED ASSEMBLY**

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(\*\*) Term: **14 Years**

(21) Appl. No.: **29/385,964**

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(51) **LOC (9) Cl.** ..... **24-01**

(52) **U.S. Cl.**  
USPC ..... **D24/107; D24/216; D24/232**

(58) **Field of Classification Search**  
USPC ..... D24/107, 216–232, 169, 186; D10/81; 422/62–63, 66–67, 68.1, 500, 502, 503; 435/287.1, 287.2, 288.3, 288.7, 305.1  
See application file for complete search history.

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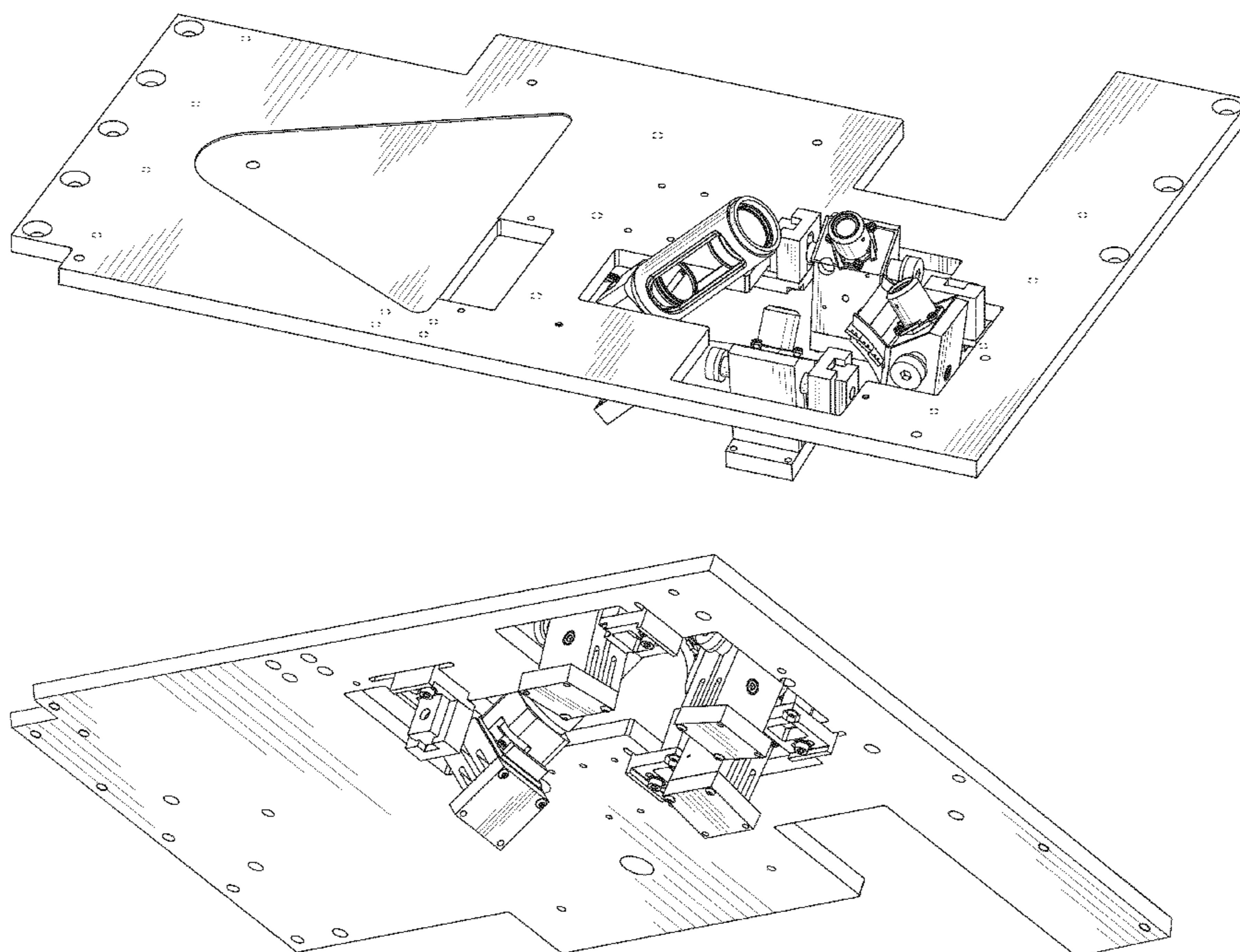
(57) **CLAIM**  
The ornamental design for chip station plate with LED assembly, as shown and described.

**DESCRIPTION**

FIG. 1 is an elevated perspective view of the top, front and right side of the chip station plate with LED assembly; FIG. 2 is an elevated perspective view of the bottom, front and right side of the chip station plate with LED assembly; FIG. 3 is a front view of the chip station plate with LED assembly; FIG. 4 is a rear view of the chip station plate with LED assembly; FIG. 5 is a left side view of the chip station plate with LED assembly; FIG. 6 is a right side view of the chip station plate with LED assembly; FIG. 7 is an enlarged top view of the chip station plate with LED assembly; and, FIG. 8 is an enlarged bottom view of the chip station plate with LED assembly.

The portions of the chip station plate with LED assembly shown in broken lines form no part of the claimed design.

**1 Claim, 8 Drawing Sheets**



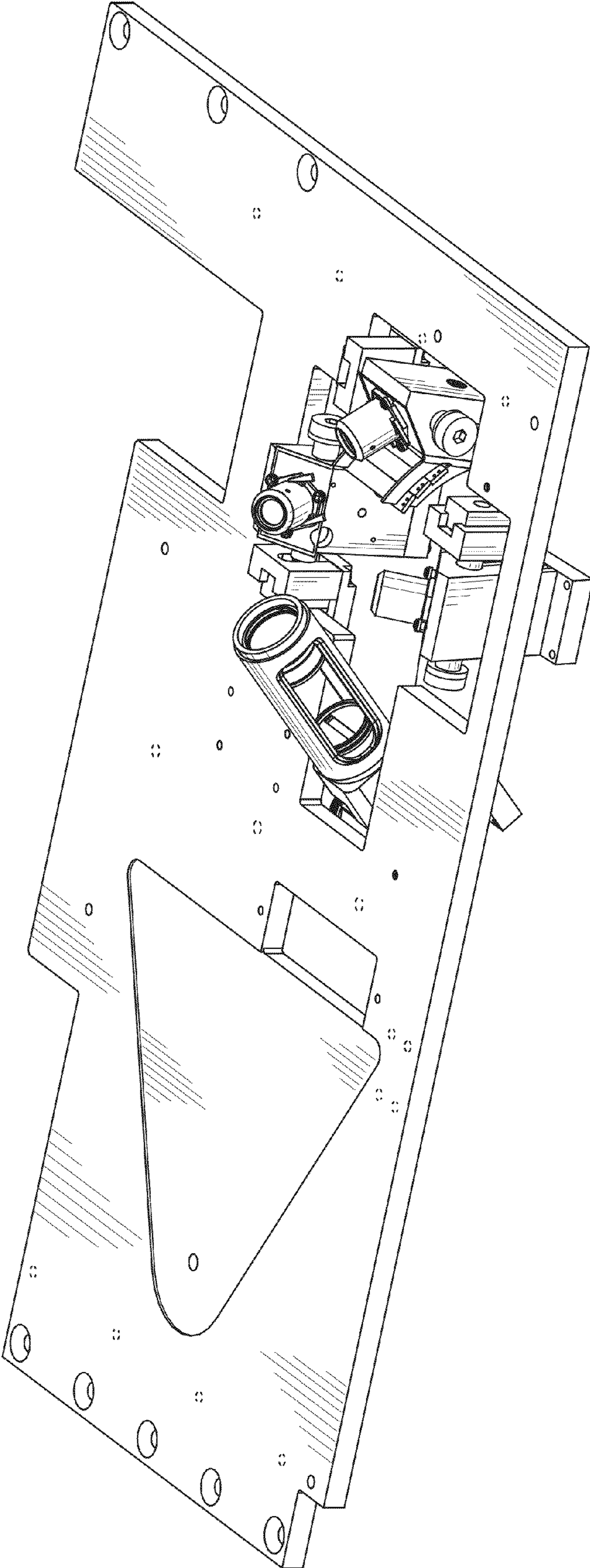


FIG.1

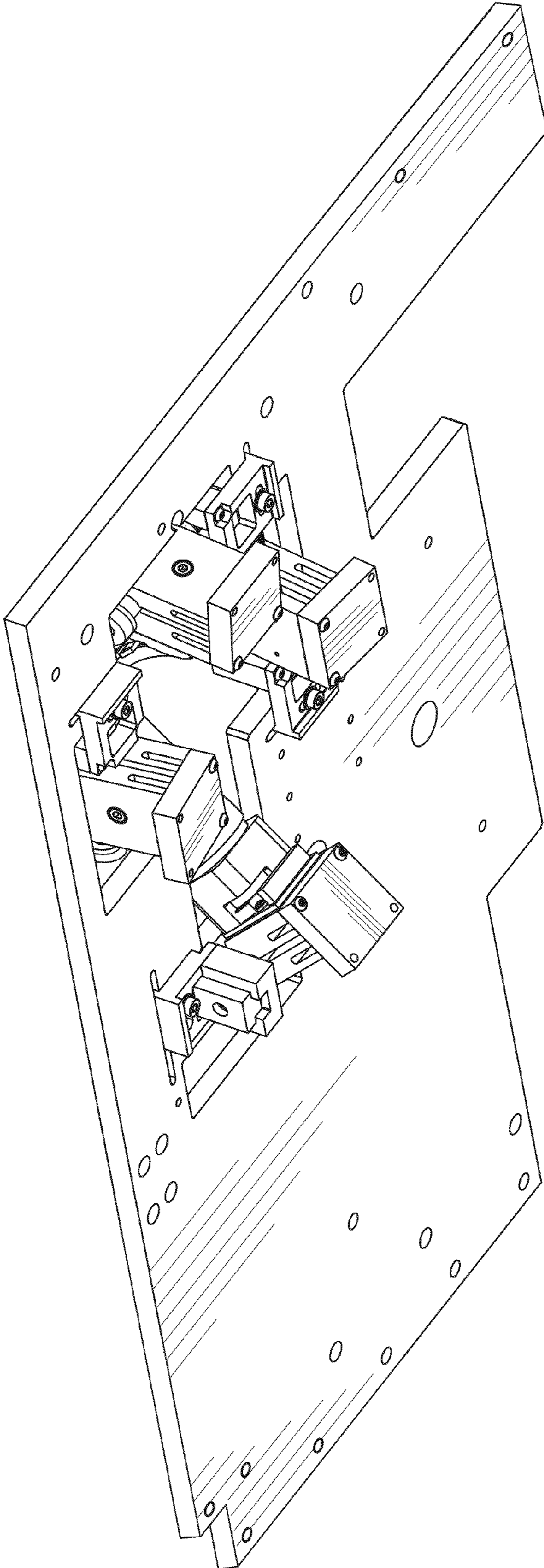


FIG.2

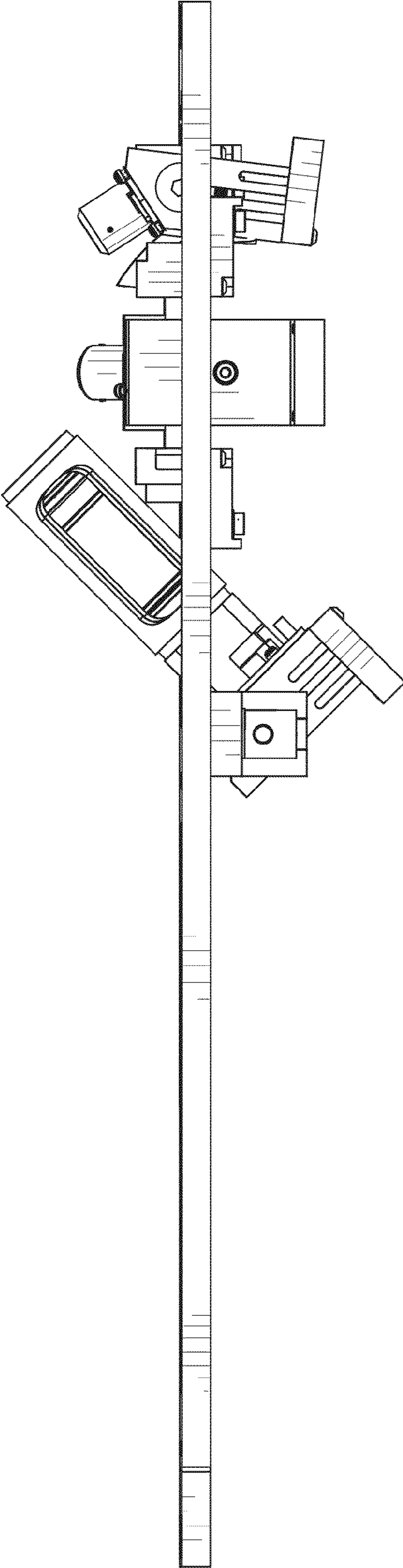


FIG.3

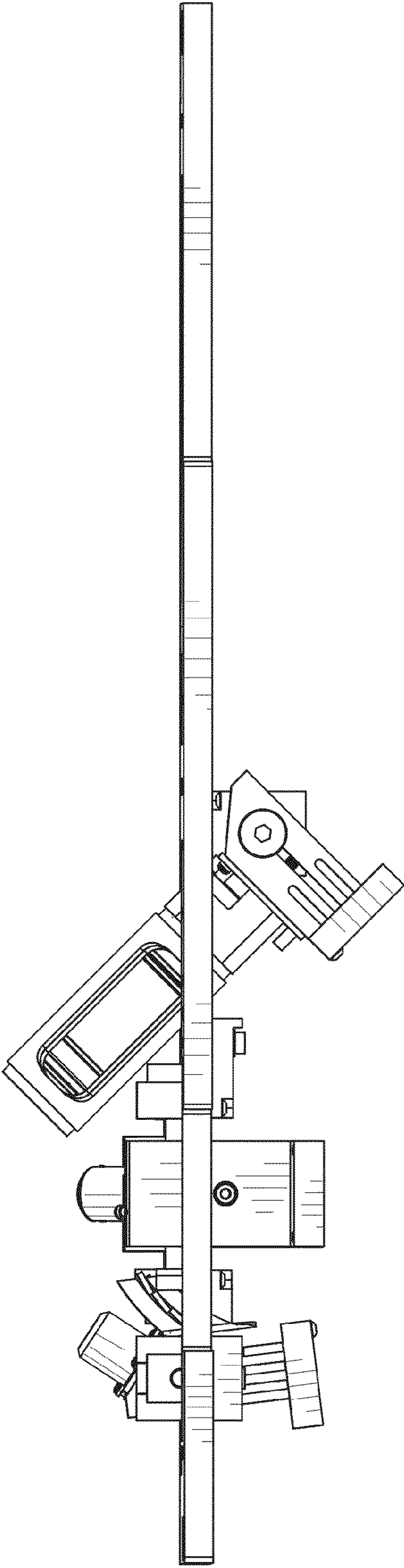


FIG.4

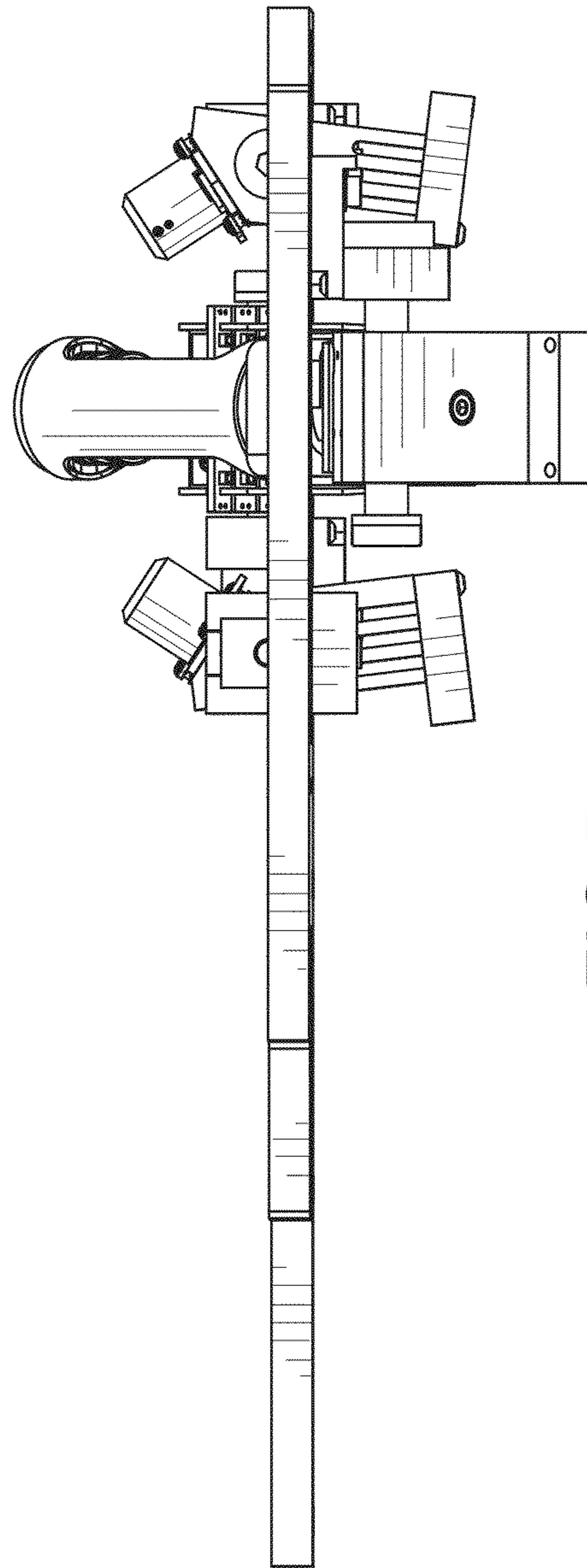


FIG.5

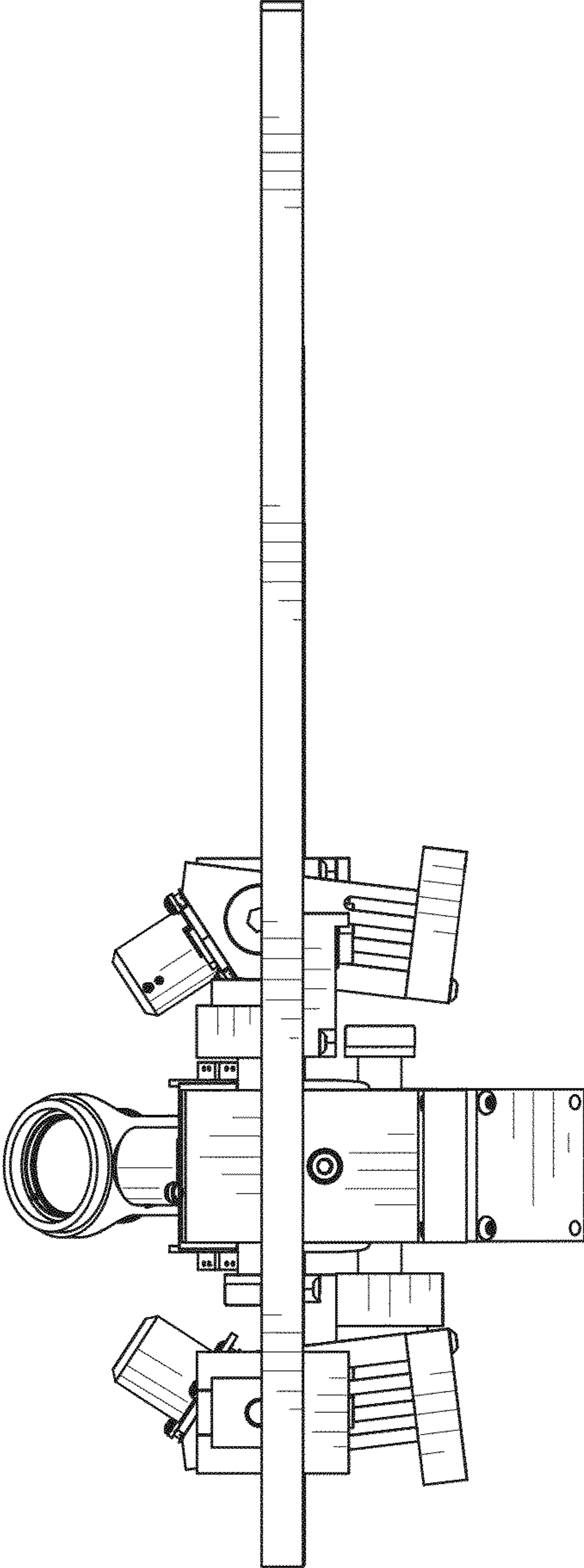


FIG. 6

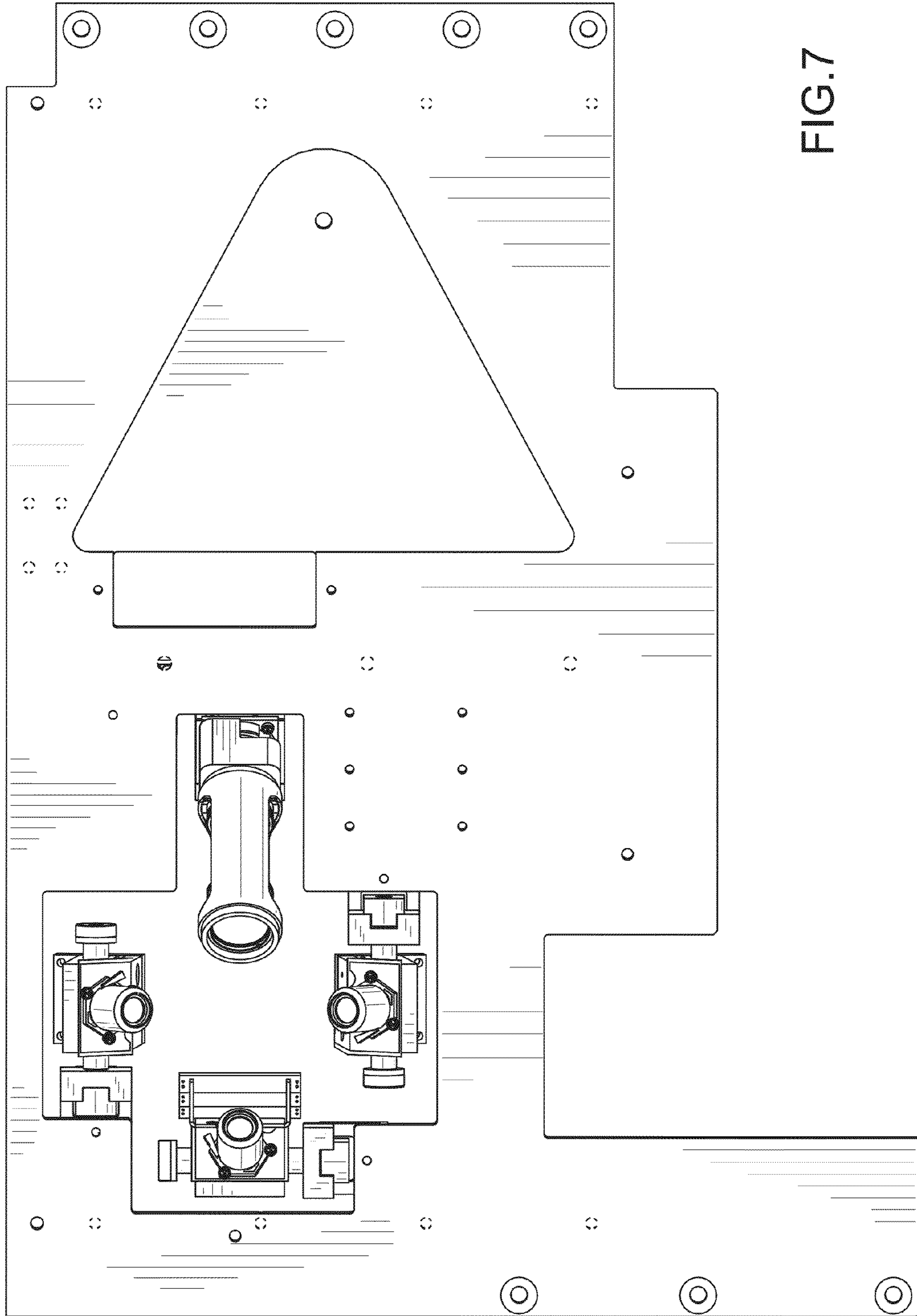


FIG.7



FIG.8

