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(12) **United States Design Patent**
Soyano

(10) **Patent No.:** **US D686,174 S**
(45) **Date of Patent:** **** Jul. 16, 2013**

(54) **SEMICONDUCTOR DEVICE**

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(73) Assignee: **Fuji Electric Co., Ltd**, Kanagawa (JP)
(**) Term: **14 Years**
(21) Appl. No.: **29/408,443**
(22) Filed: **Dec. 13, 2011**

(30) **Foreign Application Priority Data**

Aug. 12, 2011 (JP) 2011-018510

(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/182**

(58) **Field of Classification Search**
USPC D13/110, 182; 257/690; 361/728,
361/736, 775
See application file for complete search history.

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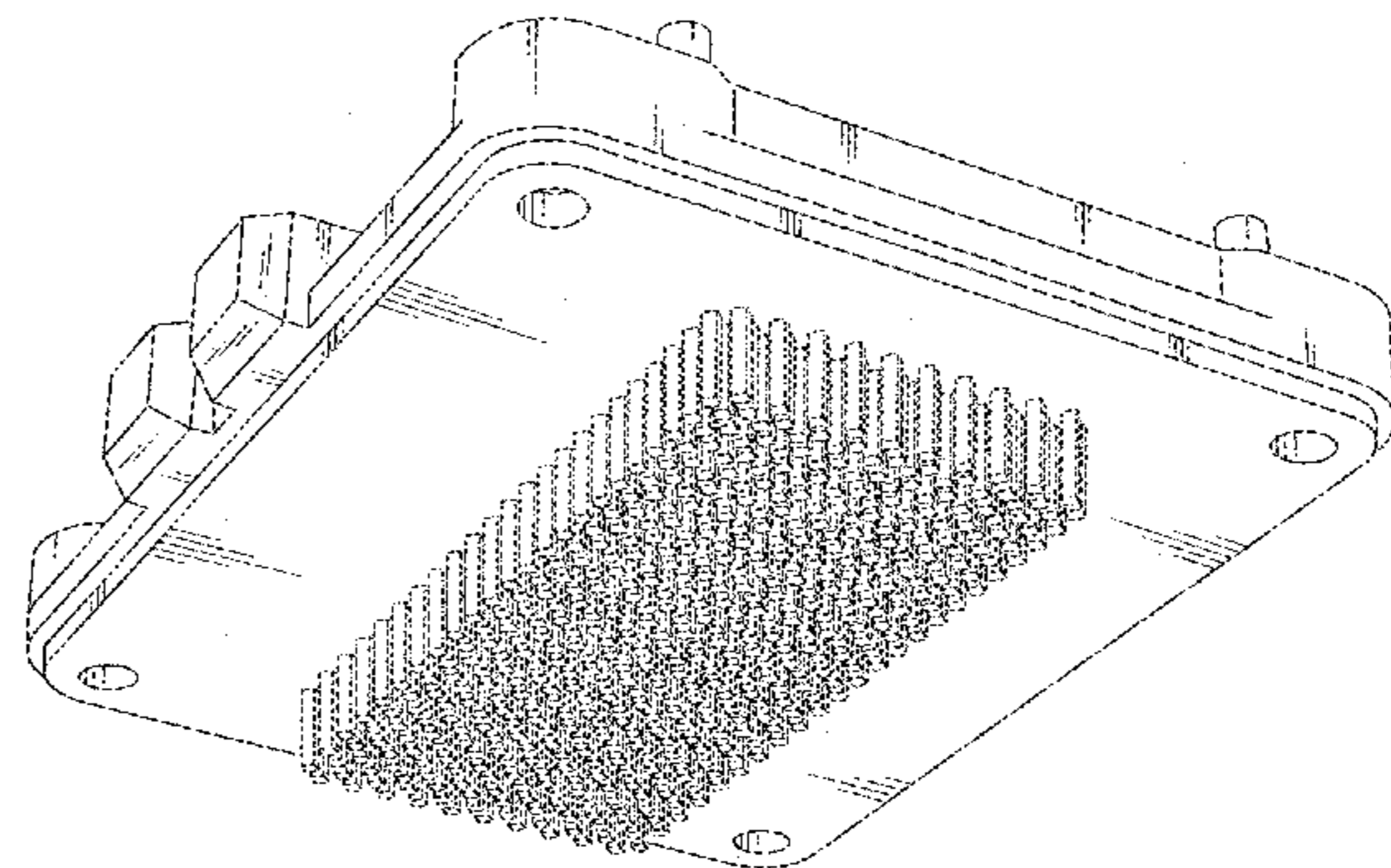
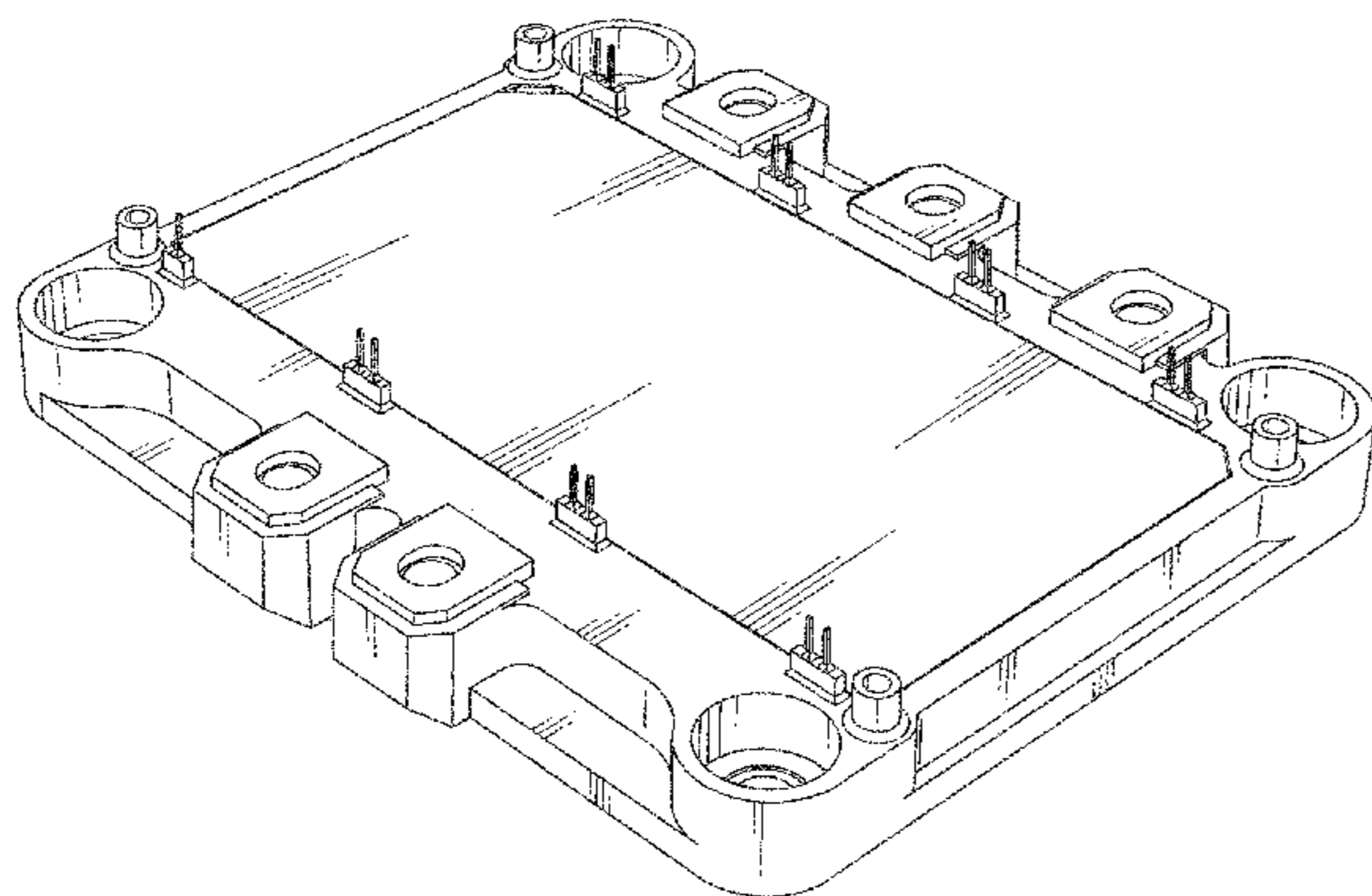
(57) **CLAIM**

The ornamental design for a semiconductor device, as shown and described.

DESCRIPTION

FIG. 1 is a first perspective view of front, bottom and right side of a semiconductor device showing our new design; FIG. 2 is a perspective view of rear, bottom and right side thereof; FIG. 3 is a second perspective view of front, bottom and right side thereof; FIG. 4 is a front view thereof; FIG. 5 is a rear view thereof; FIG. 6 is a left side view thereof; FIG. 7 is a right side view thereof; FIG. 8 is a top plan view thereof; and, FIG. 9 is a bottom view thereof. The portions of the article in broken lines are shown for illustrative purposes only and form no part of the claimed design.

1 Claim, 7 Drawing Sheets



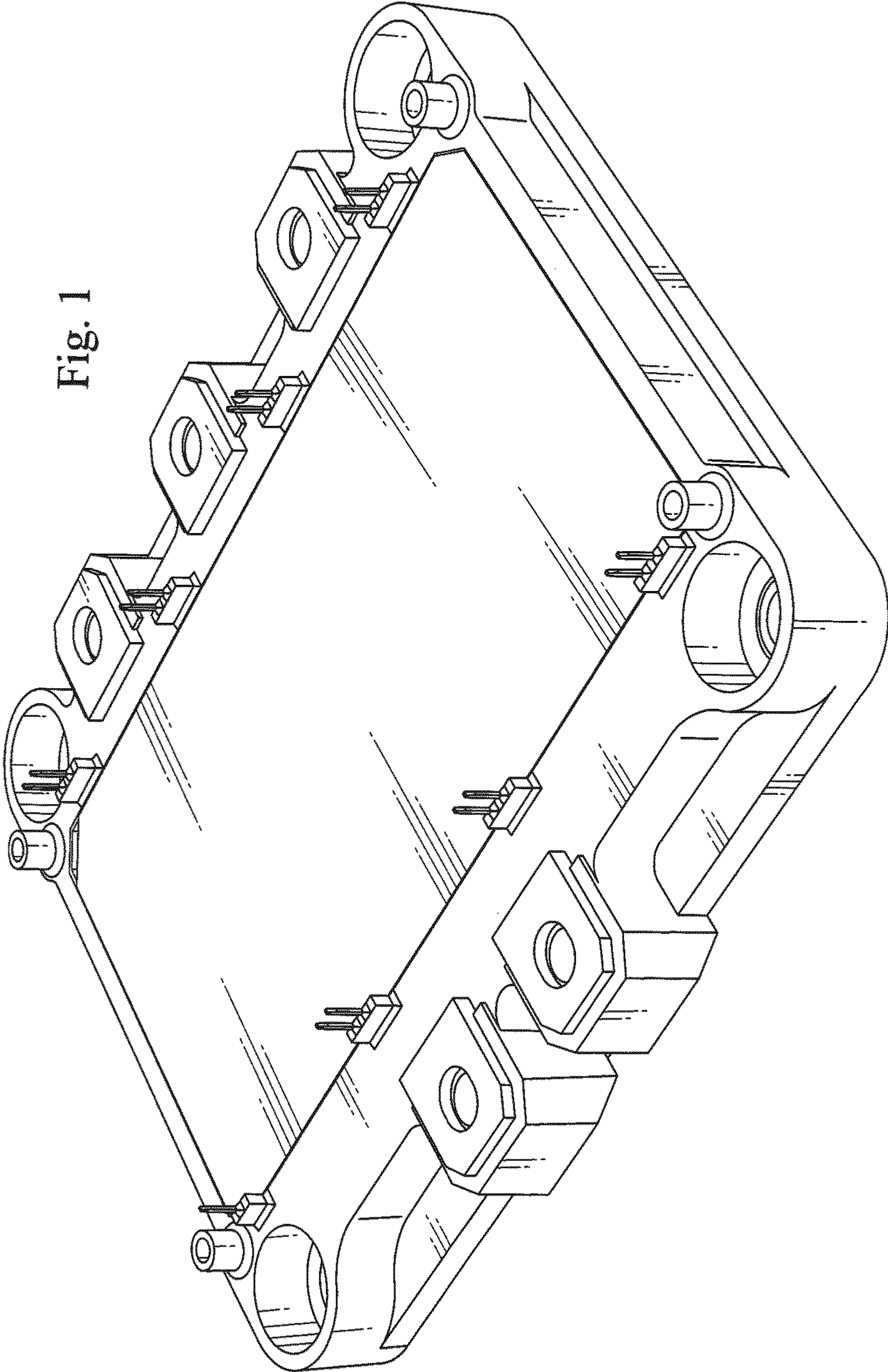


Fig. 2

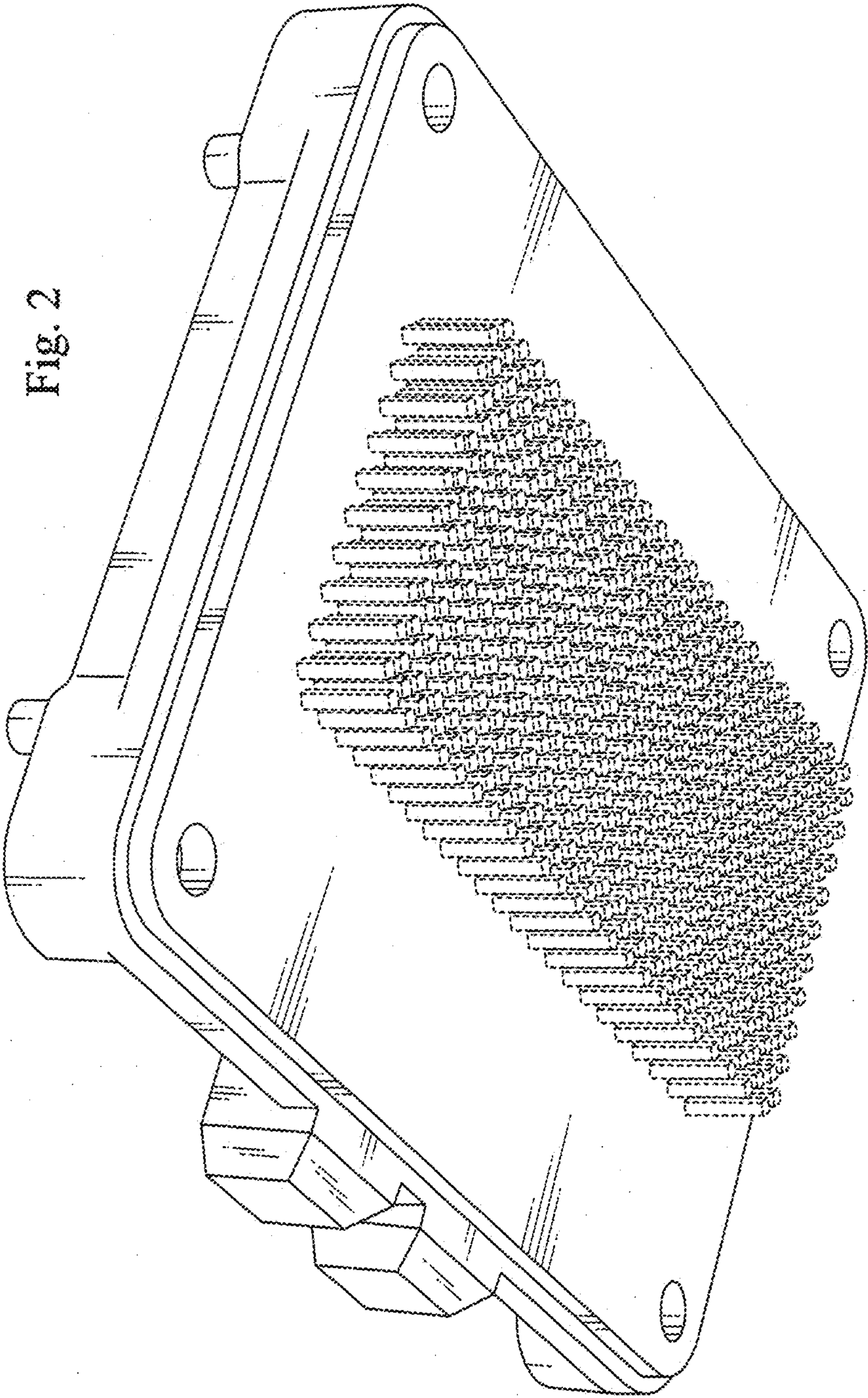


Fig. 3

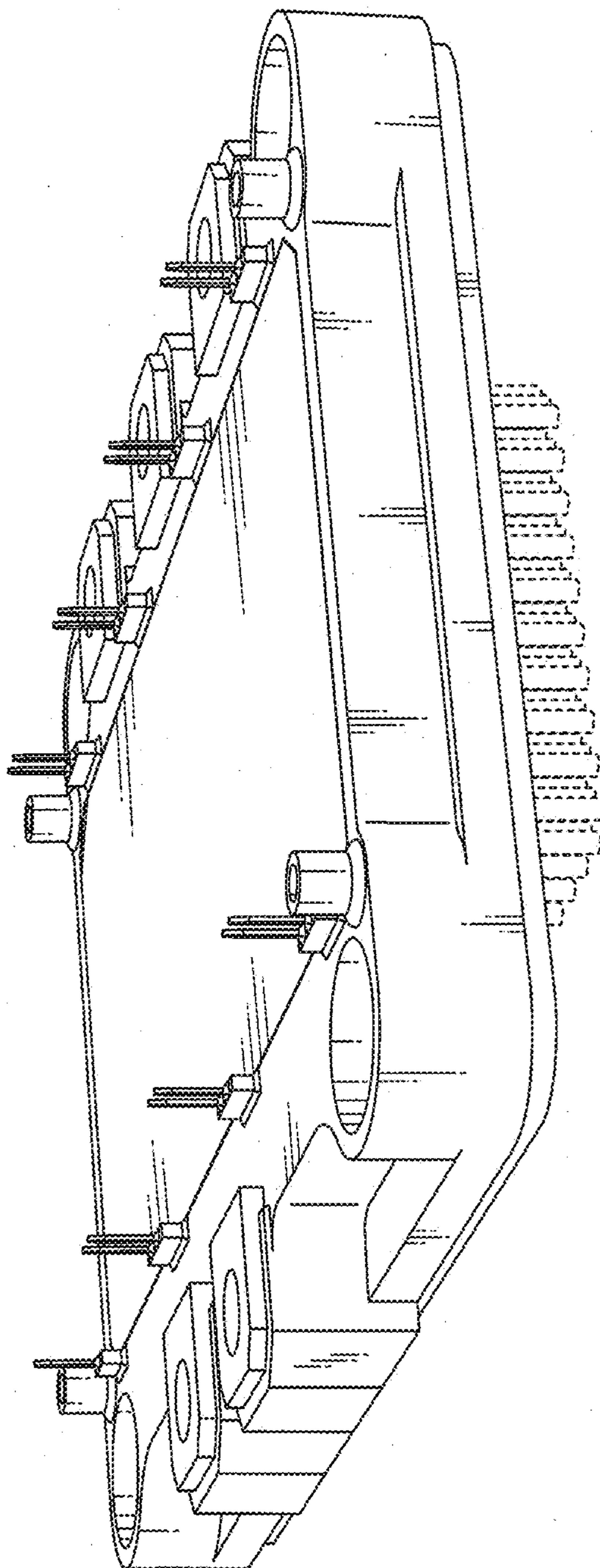


Fig. 4

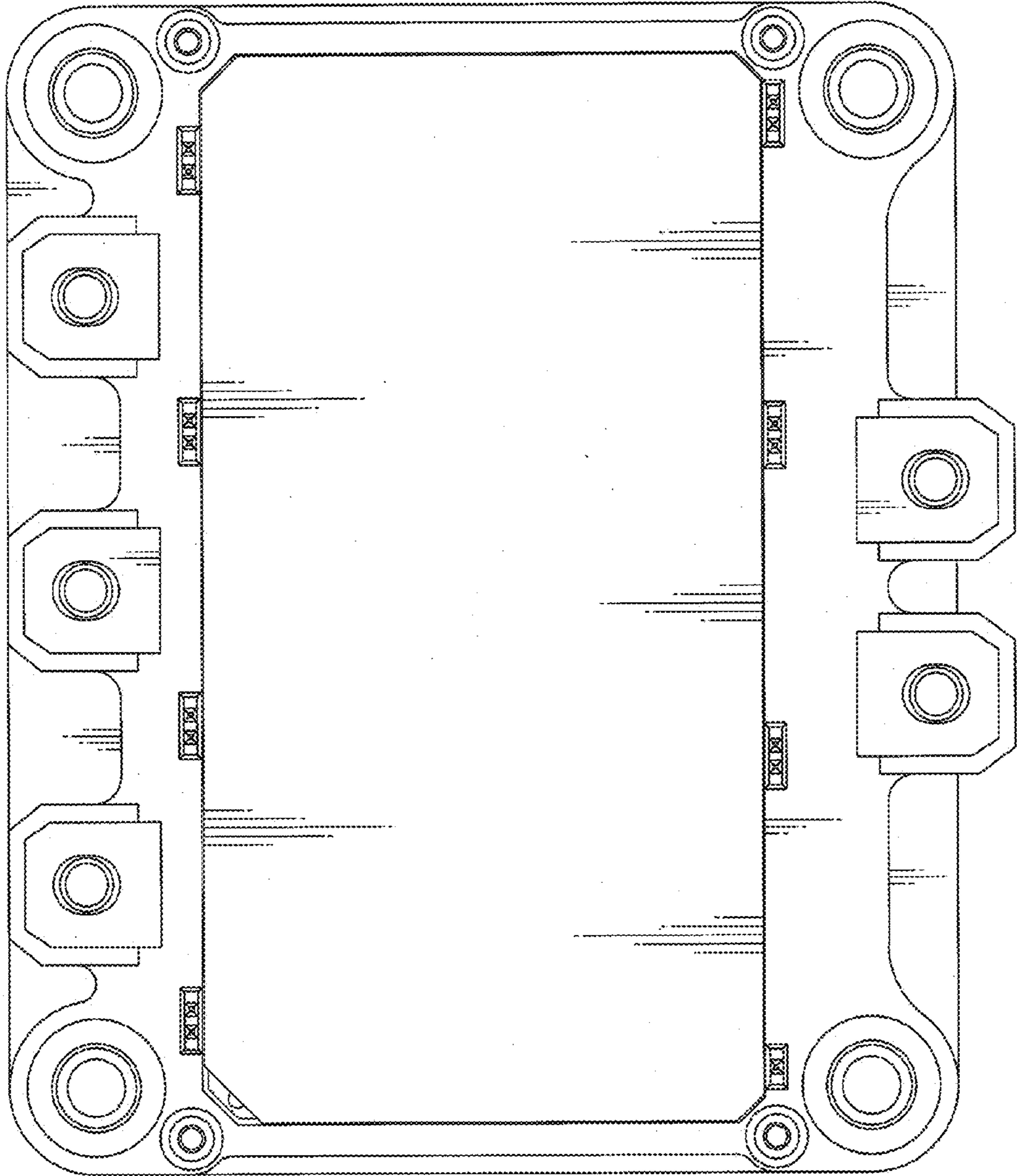


Fig. 5

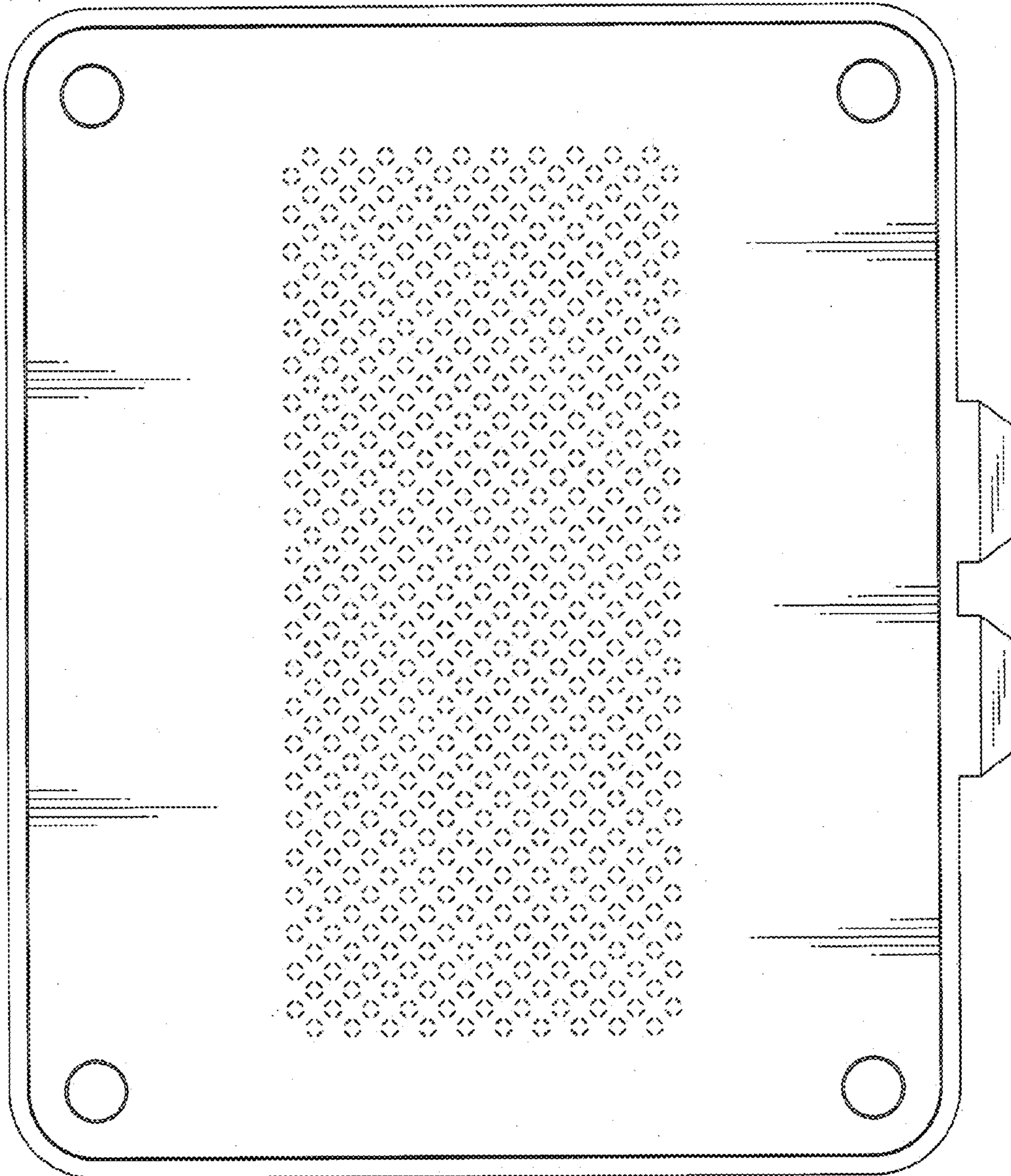


Fig. 7

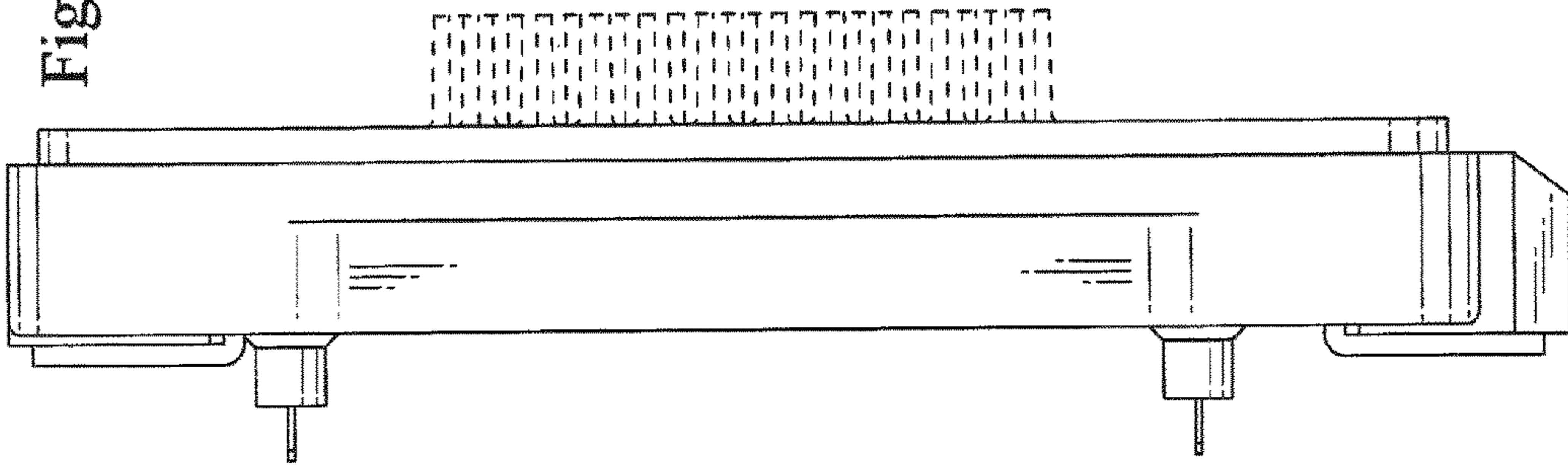


Fig. 6

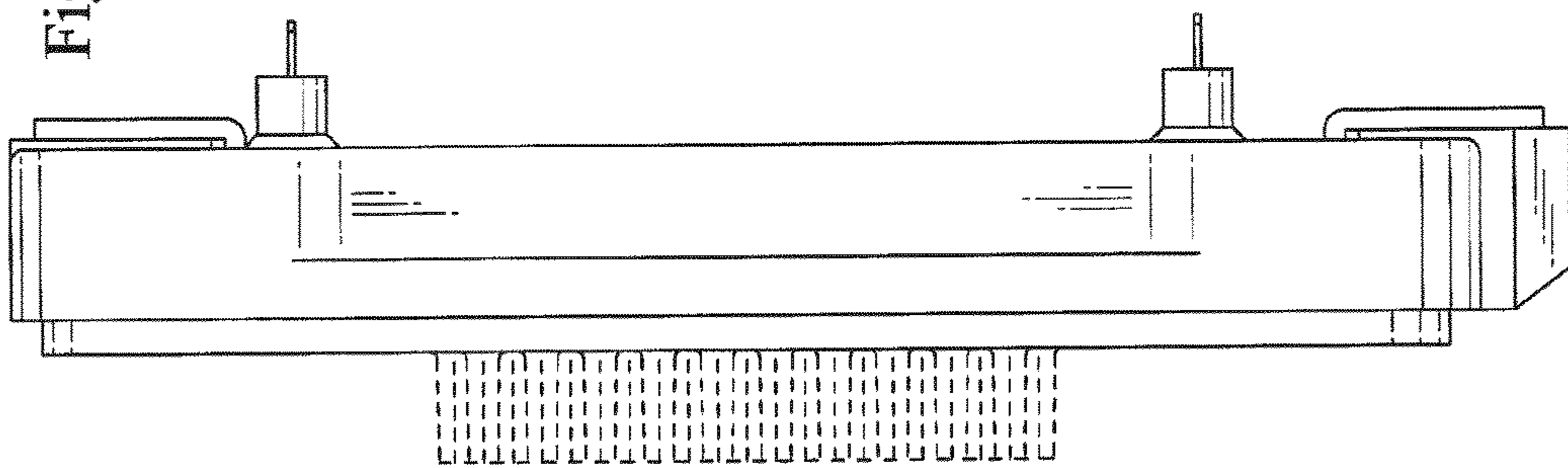


Fig. 8

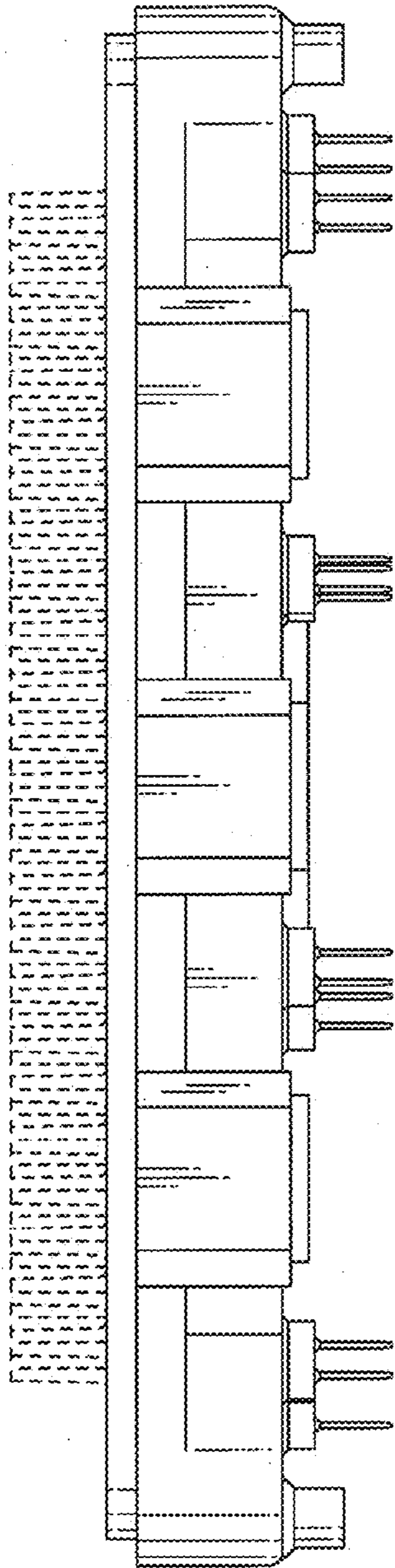


Fig. 9

