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(12) **United States Design Patent**  
**Deck et al.**

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(45) **Date of Patent:** **\*\* Jul. 2, 2013**

(54) **REMOTE INPUT OUTPUT DIGITAL OUTPUT MODULE**

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(73) Assignee: **ABB Technology Ltd.**, Zürich (CH)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/407,595**

(22) Filed: **Nov. 30, 2011**

(30) **Foreign Application Priority Data**

Jun. 10, 2011 (IN) ..... 237210

(51) **LOC (9) Cl.** ..... **14-03**

(52) **U.S. Cl.**  
USPC ..... **D14/240; D13/110**

(58) **Field of Classification Search**  
USPC ..... D14/240, 242, 357, 358, 140-140.9,  
D14/155, 137, 139, 243, 348, 349, 351, 354,  
D14/355; 375/222; 455/557; 725/111  
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a remote input output digital output module, as shown and described.

**DESCRIPTION**

FIG. 1 is a right side perspective view of a remote input output digital output module according to our new design.

FIG. 2 is a left perspective view thereof.

FIG. 3 is a front perspective view thereof.

FIG. 4 is a rear perspective view thereof.

FIG. 5 is a top perspective view thereof.

FIG. 6 is a bottom perspective view thereof.

FIG. 7 is a front elevational view thereof.

FIG. 8 is a rear elevational view thereof.

FIG. 9 is a top plan view thereof.

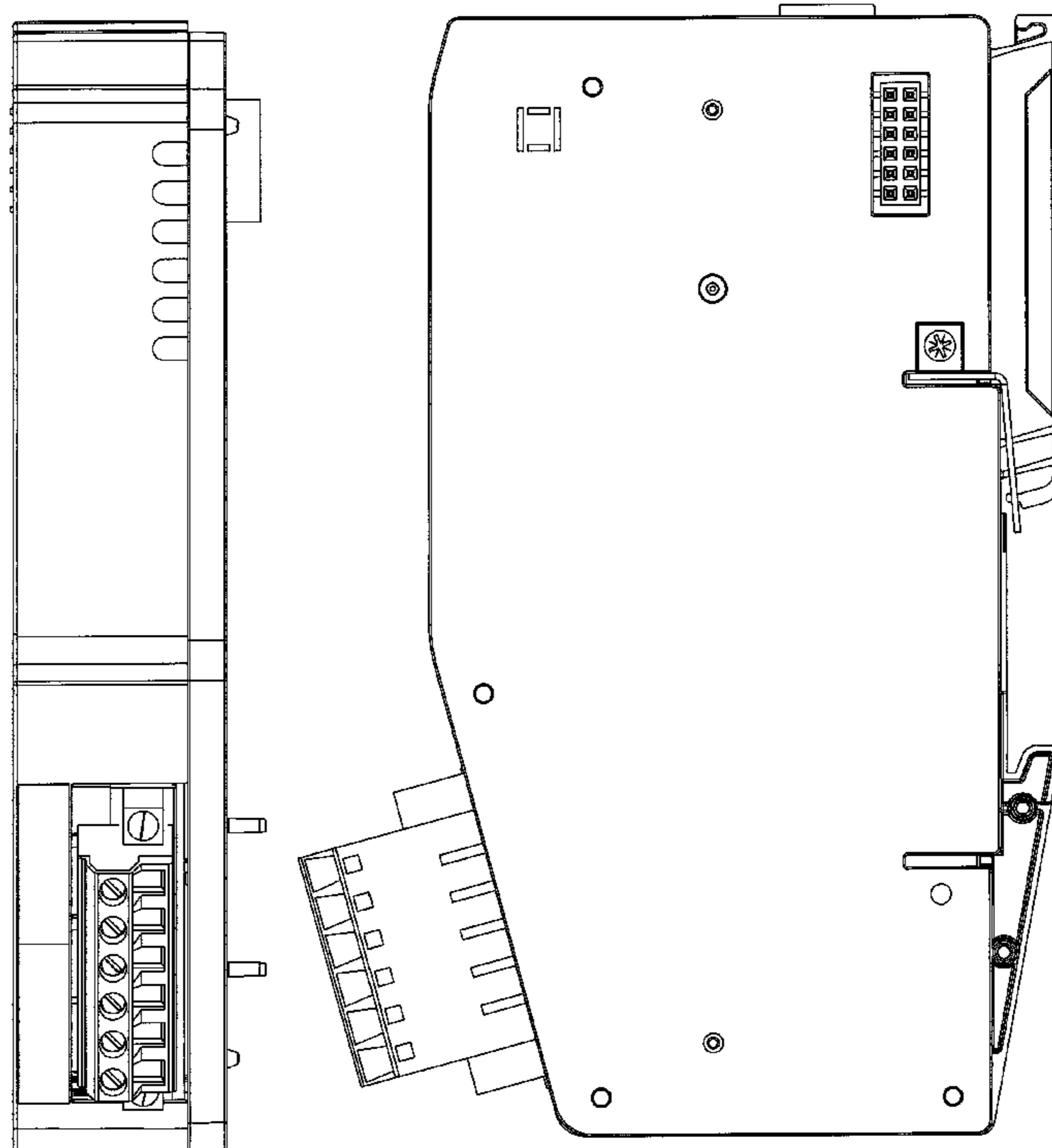
FIG. 10 is a bottom plan view thereof.

FIG. 11 a left side elevational view thereof; and,

FIG. 12 is a right side elevational view thereof.

The wireframe lines shown throughout the views are intended to indicate surface contour and are not intended to be surface ornamentation.

**1 Claim, 12 Drawing Sheets**



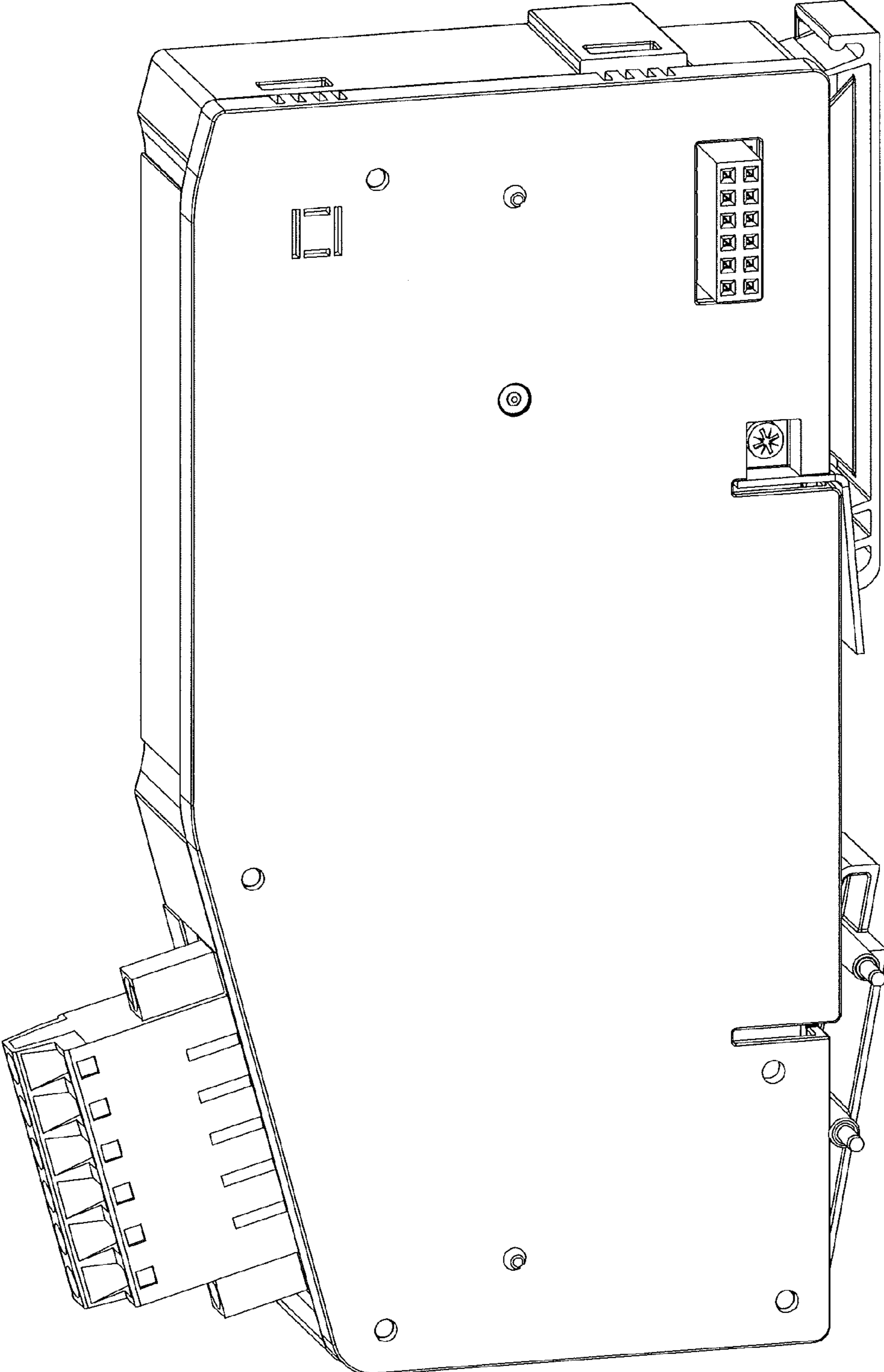


FIG. 1

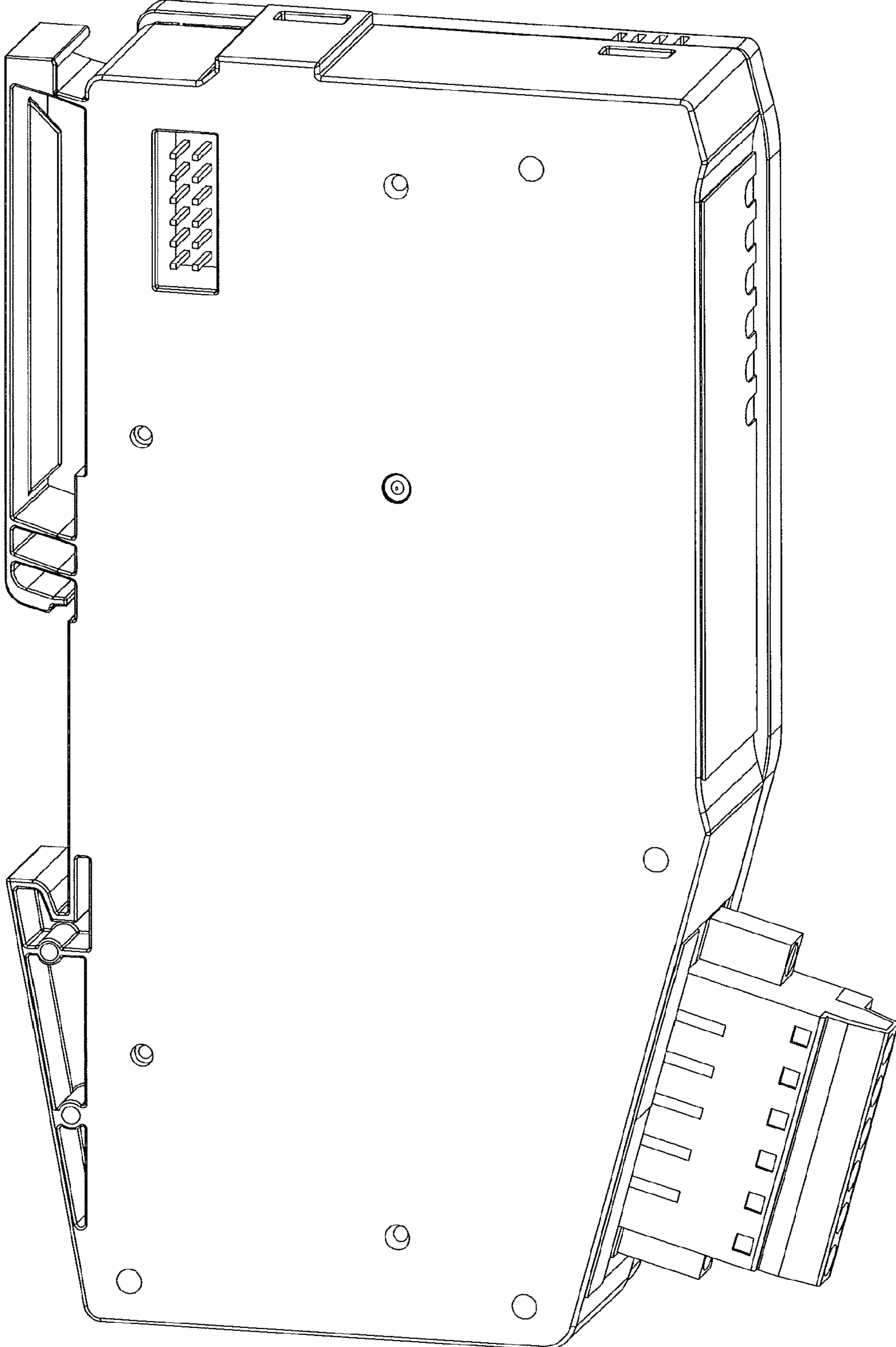


FIG. 2

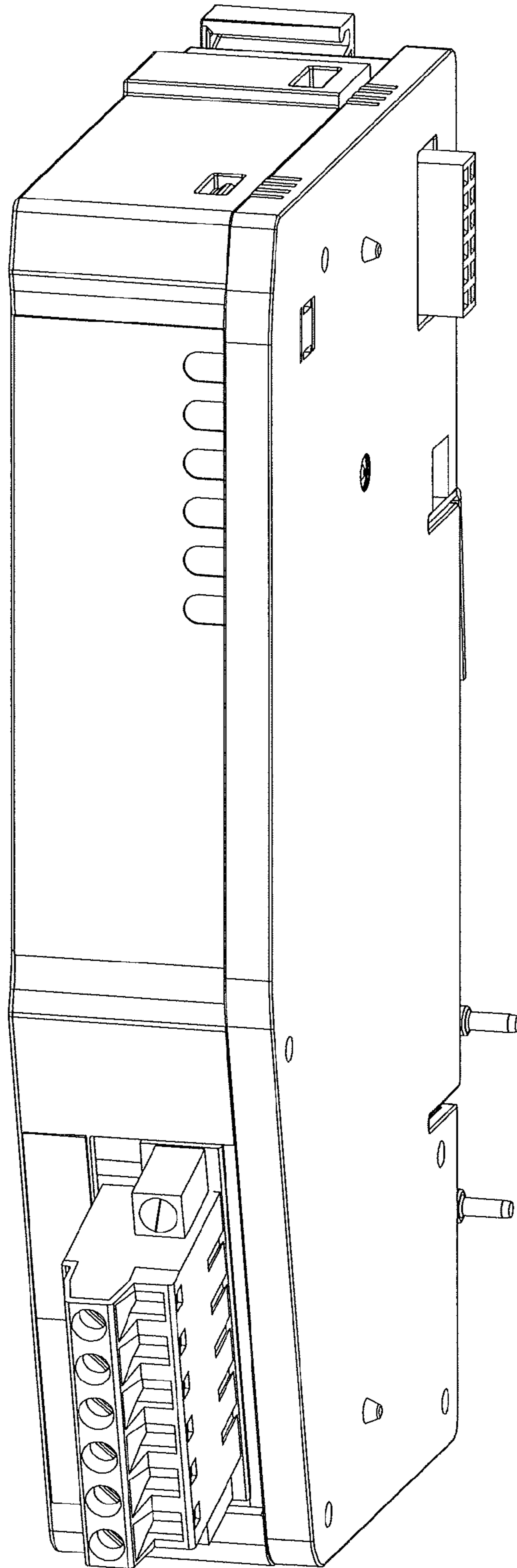


FIG. 3

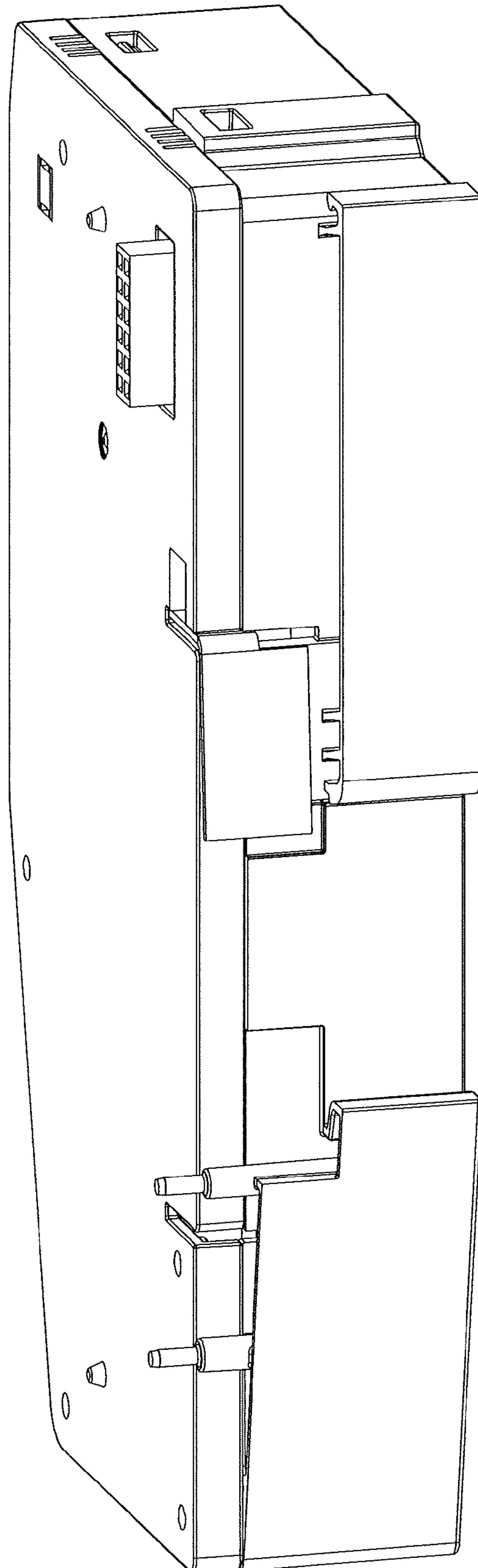


FIG. 4

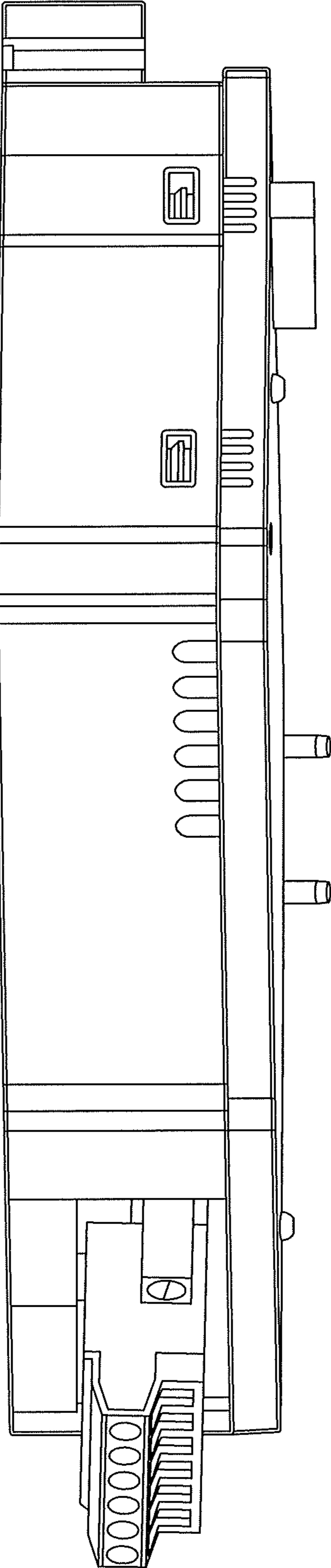


FIG. 5

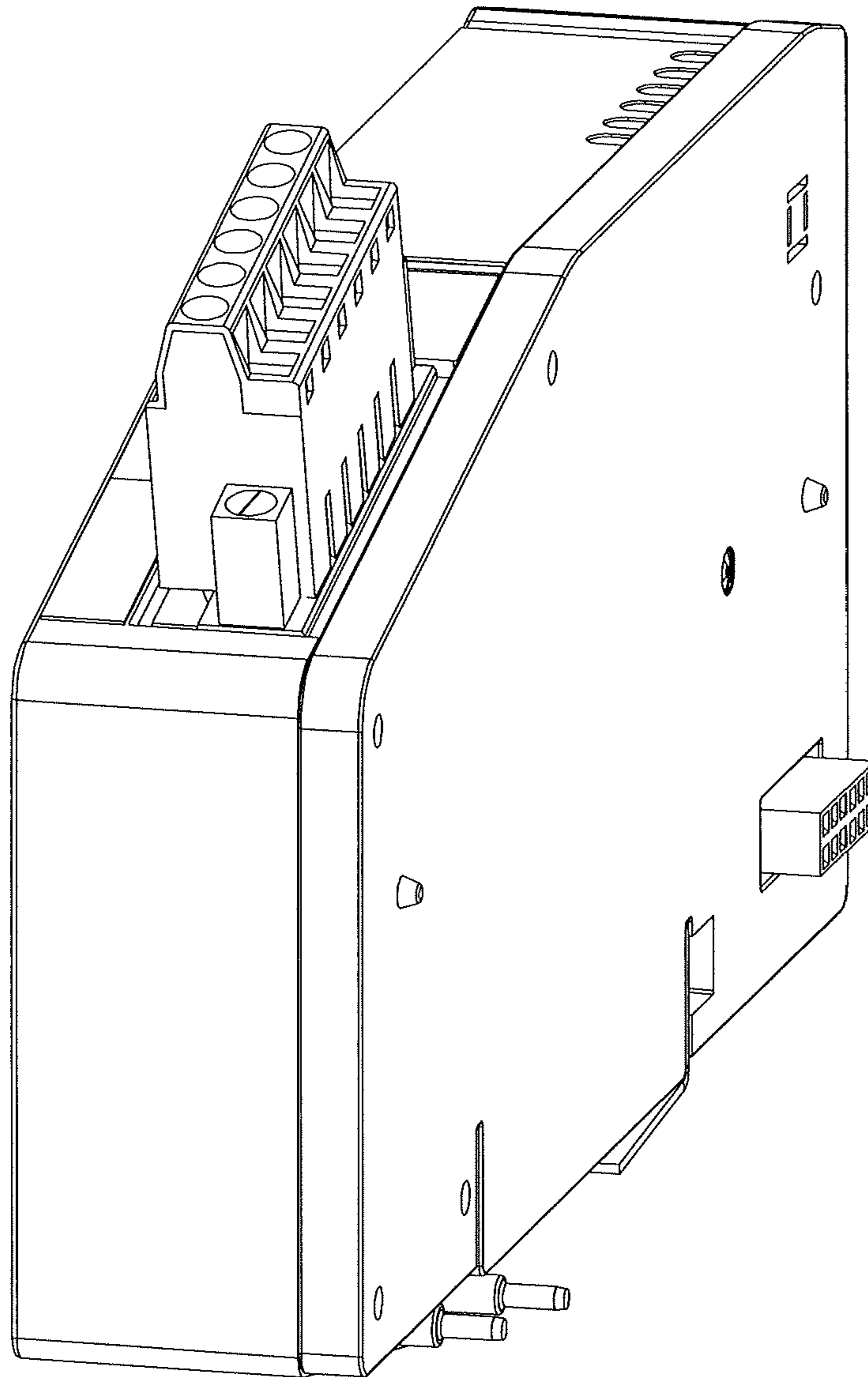


FIG. 6

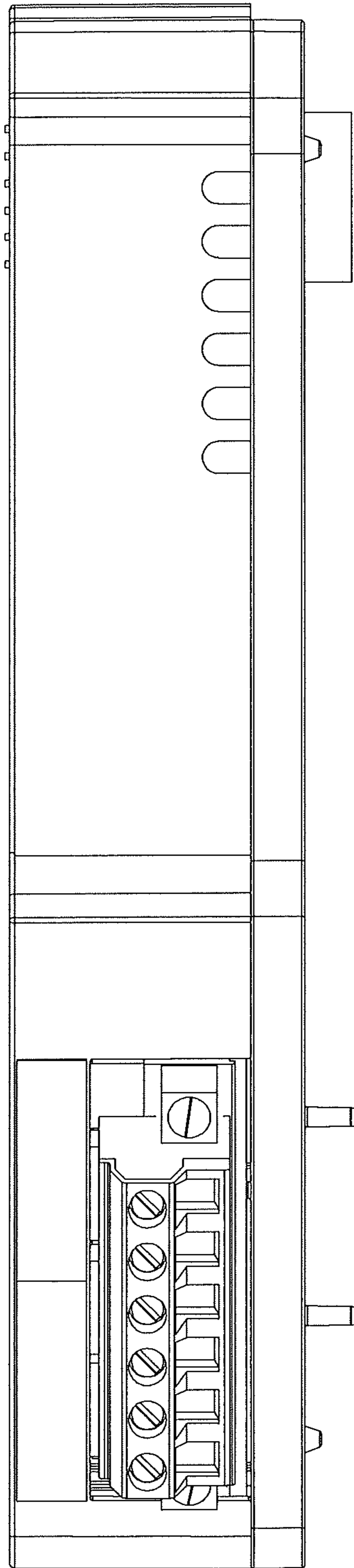


FIG. 7



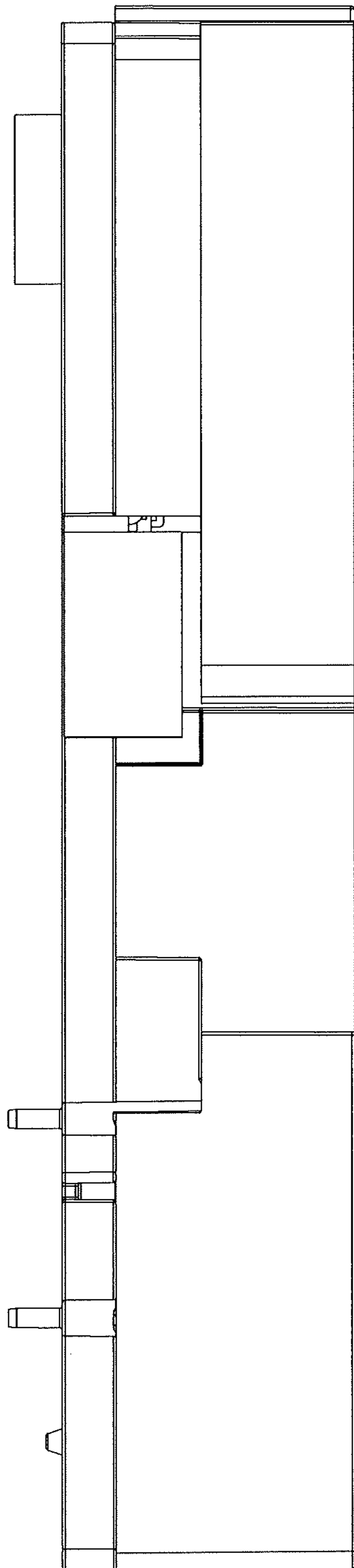


FIG. 8

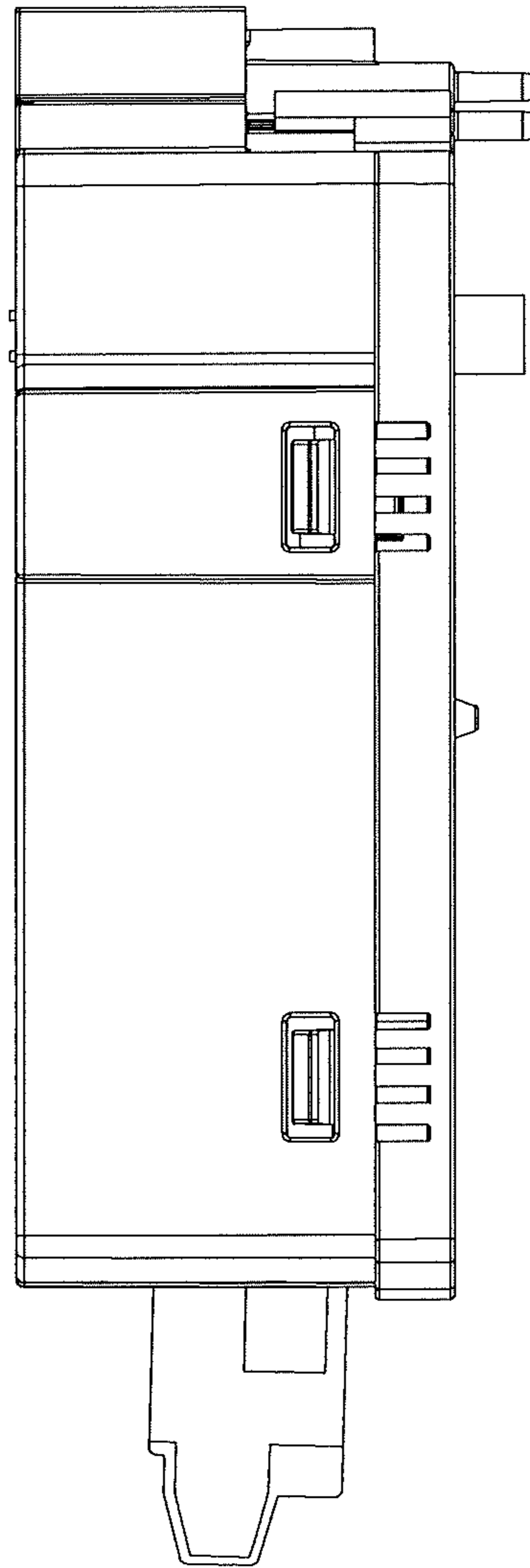


FIG. 9

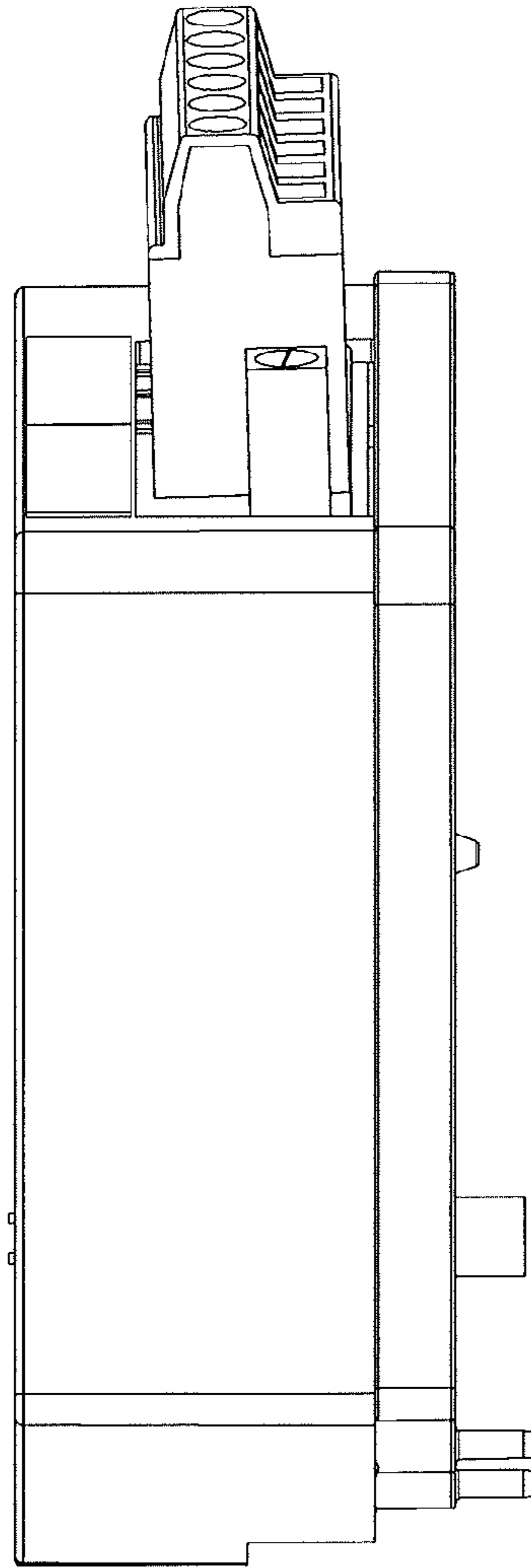


FIG. 10

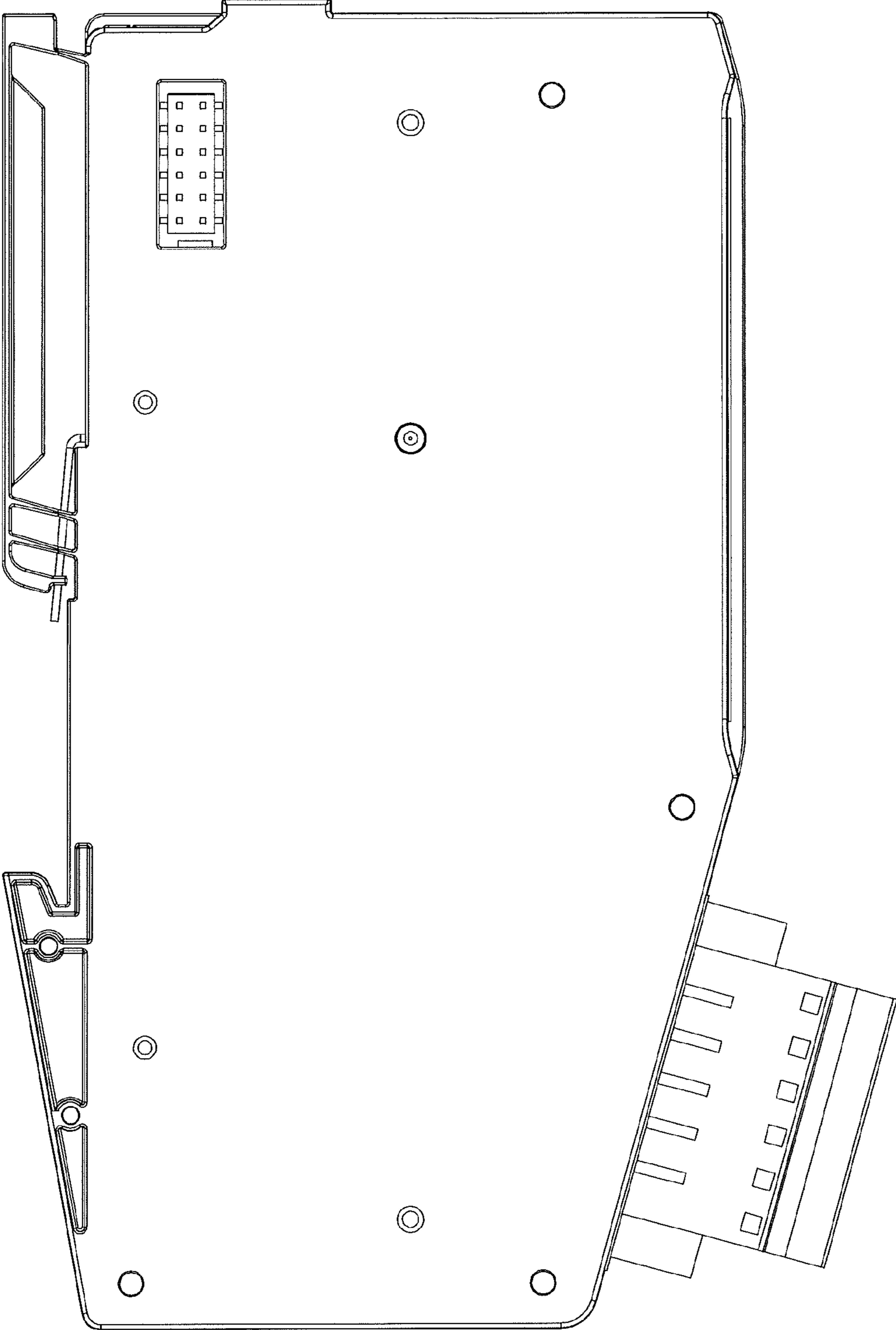


FIG. 11

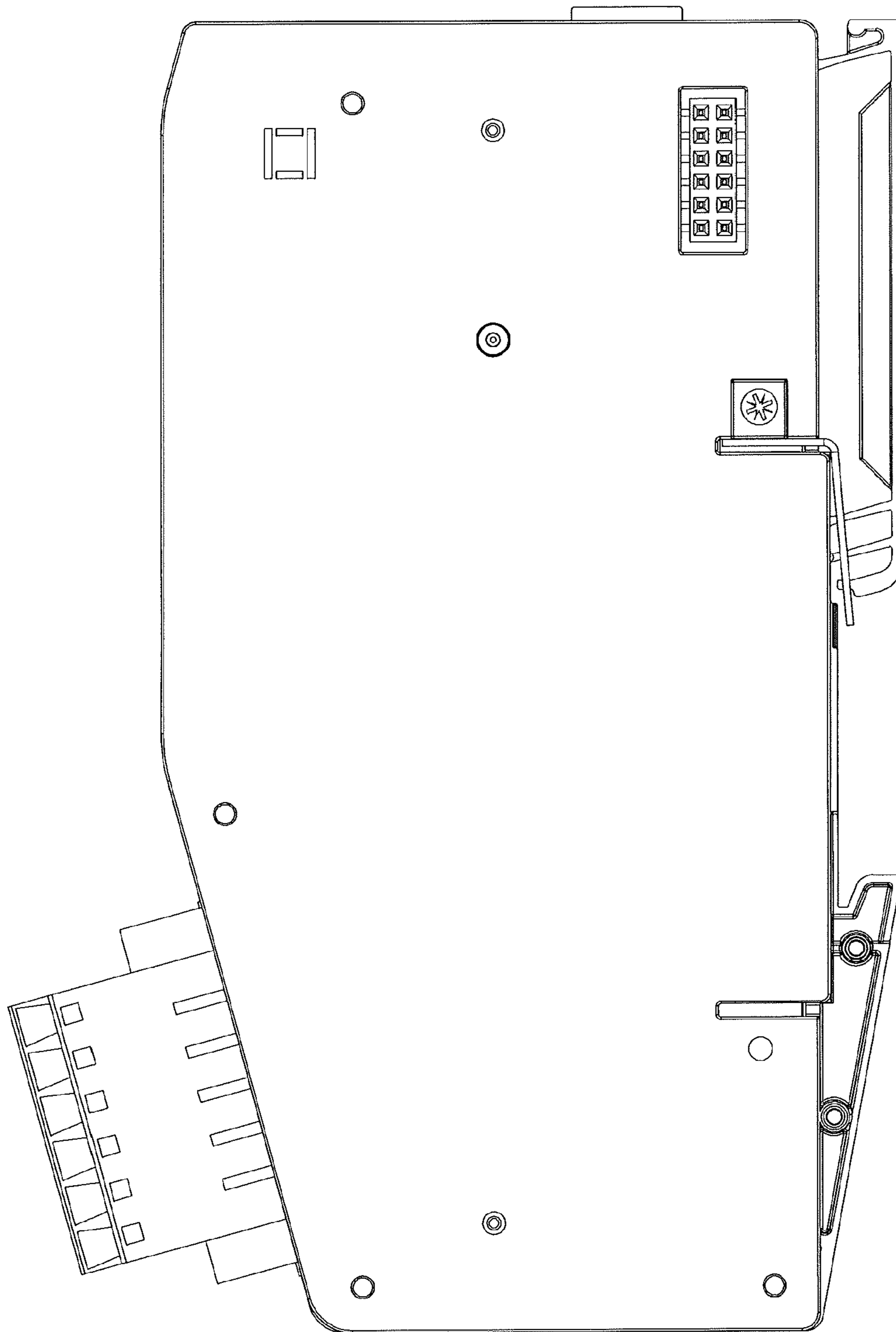


FIG. 12