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(12) **United States Design Patent**  
**Yeh et al.**

(10) **Patent No.:** **US D681,569 S**  
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(54) **LIGHT-EMITTING DIODE ARRAY**  
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(73) Assignee: **Epistar Corporation**, Hsinchu (TW)

(\*\*) Term: **14 Years**

\* cited by examiner

(21) Appl. No.: **29/425,030**

*Primary Examiner* — Selina Sikder

(22) Filed: **Jun. 19, 2012**

(74) *Attorney, Agent, or Firm* — Muncy, Geissler, Olds & Lowe, PLLC

(30) **Foreign Application Priority Data**

(57) **CLAIM**

Feb. 3, 2012 (TW) ..... 101300486

The ornamental design for a light-emitting diode array, as shown and described.

(51) **LOC (9) Cl.** ..... **13-03**

**DESCRIPTION**

(52) **U.S. Cl.**  
USPC ..... **D13/180**

(58) **Field of Classification Search** ..... D13/180;  
D26/1; 257/79, 80, 81, 88, 89, 95, 98, 99,  
257/100, E33.058; 313/483, 498, 500; 362/555,  
362/800

FIG. 1 is a front perspective view of a light-emitting diode array showing our new design;  
FIG. 2 is a front elevational view thereof;  
FIG. 3 is a rear elevational view thereof;  
FIG. 4 is a left side view thereof;  
FIG. 5 is a right side view thereof;  
FIG. 6 is a top plan view thereof;  
FIG. 7 is a bottom plan view thereof; and,  
FIG. 8 is another front perspective view.

See application file for complete search history.

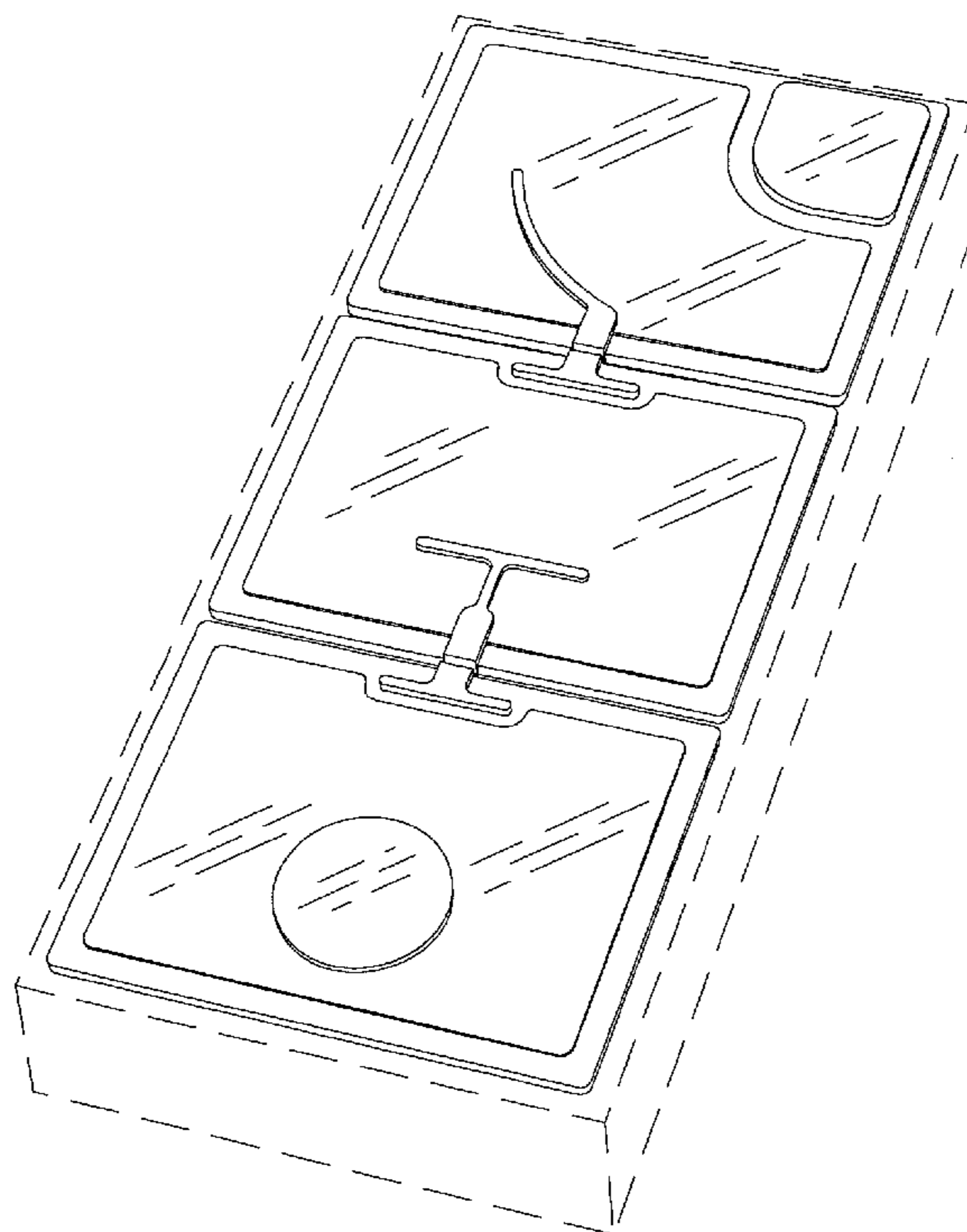
The broken line showing of the light-emitting diode array is for illustrative purposes only and forms no part of the claimed design.

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**1 Claim, 6 Drawing Sheets**



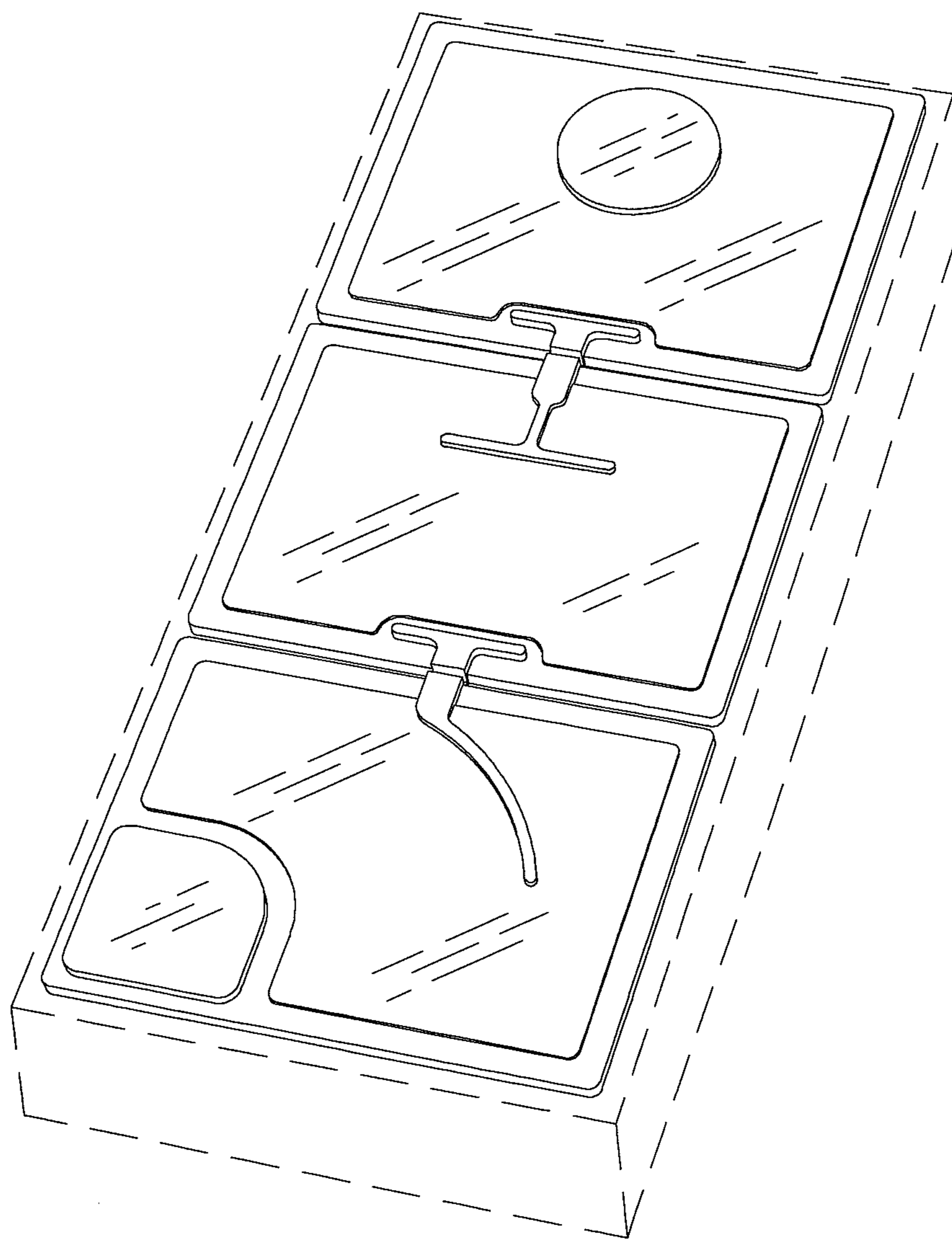


FIG.1

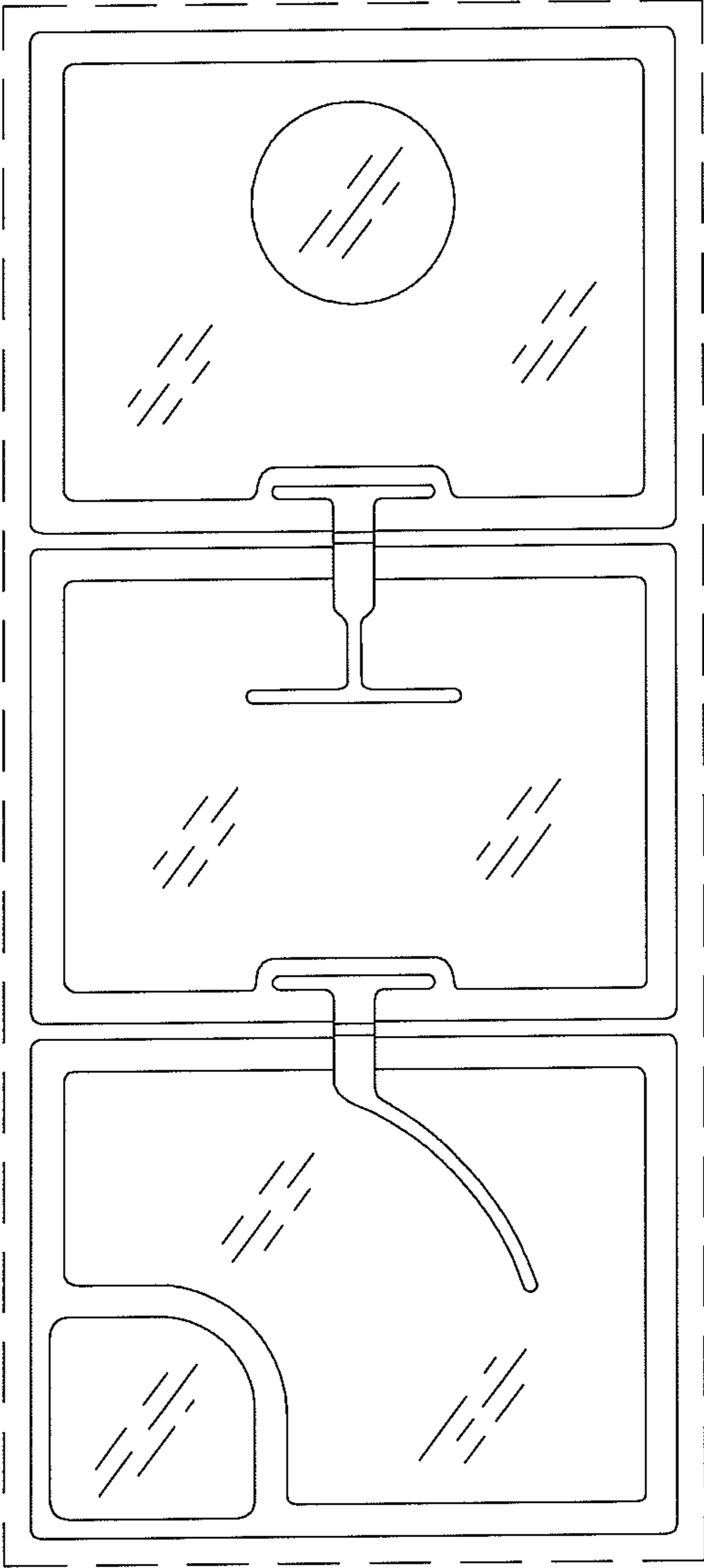
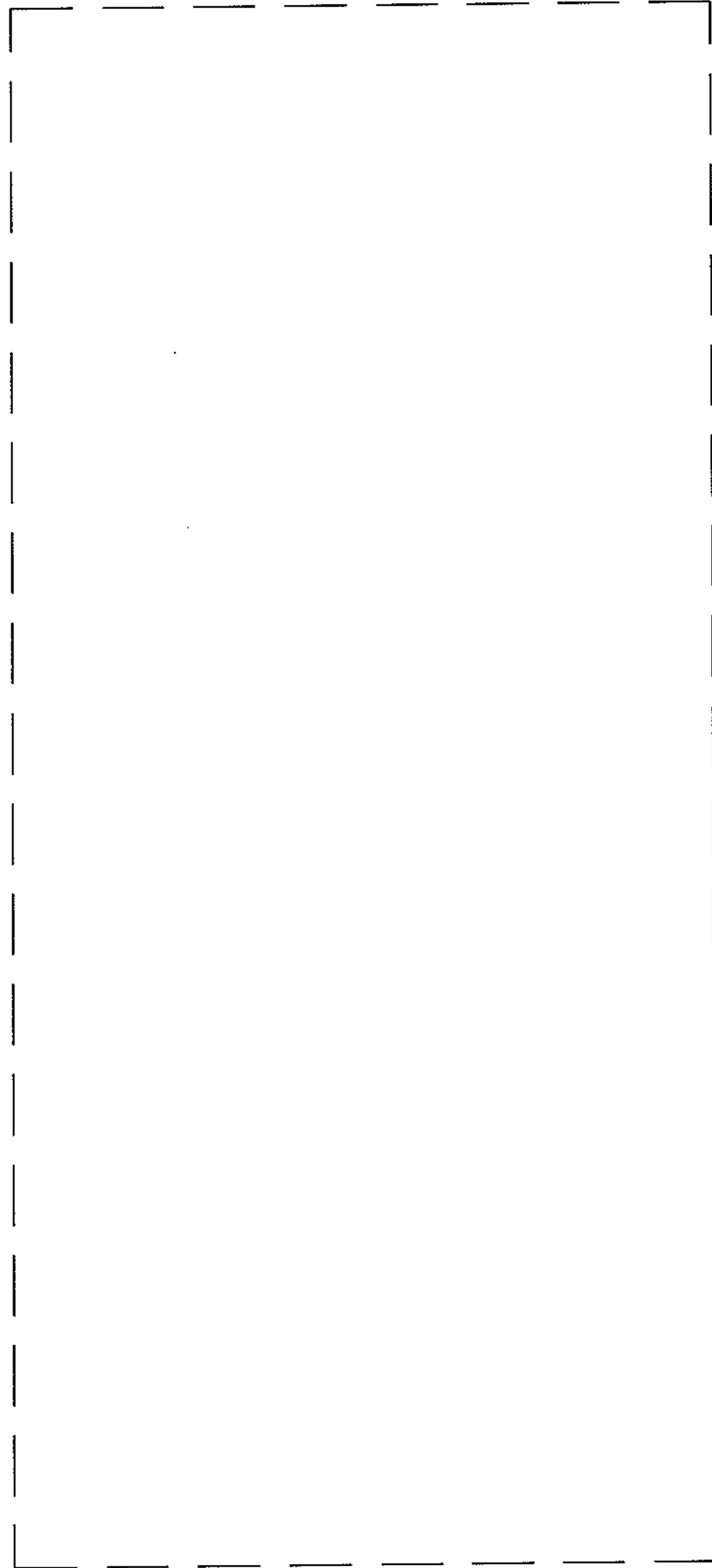


FIG.2



**FIG.3**

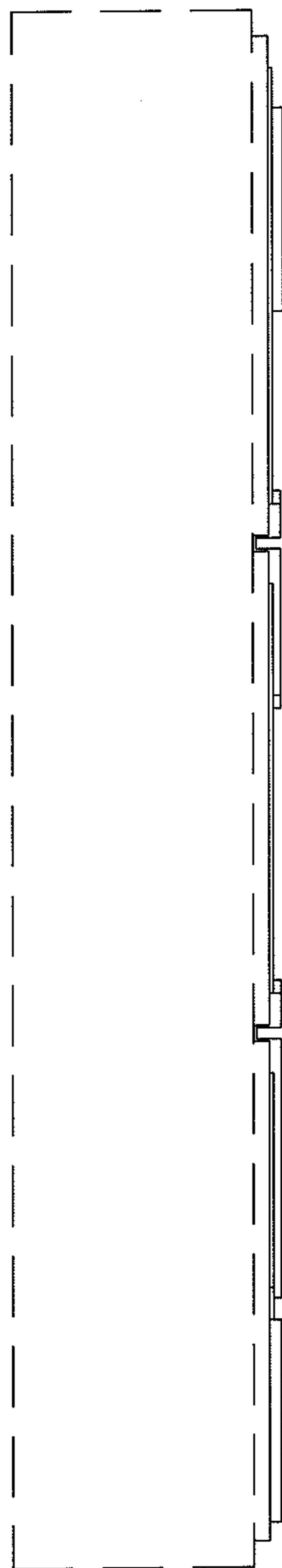


FIG.4

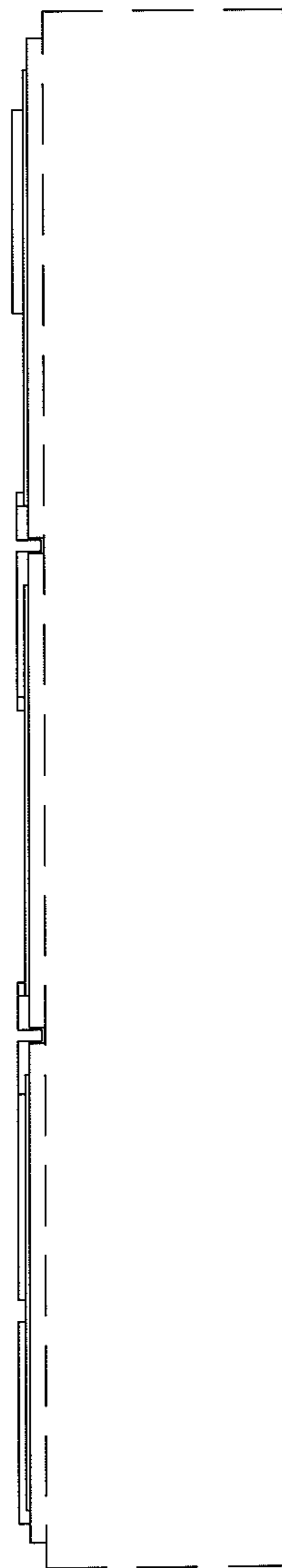


FIG.5

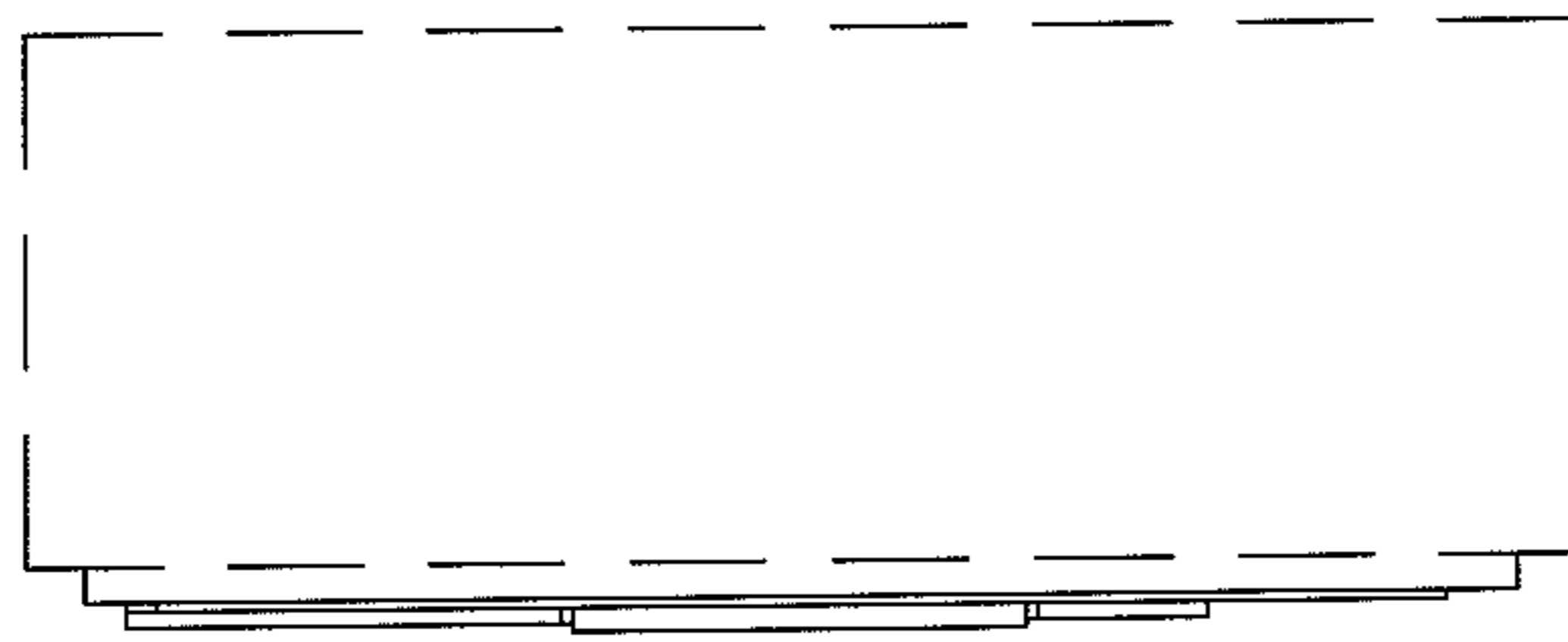


FIG. 6

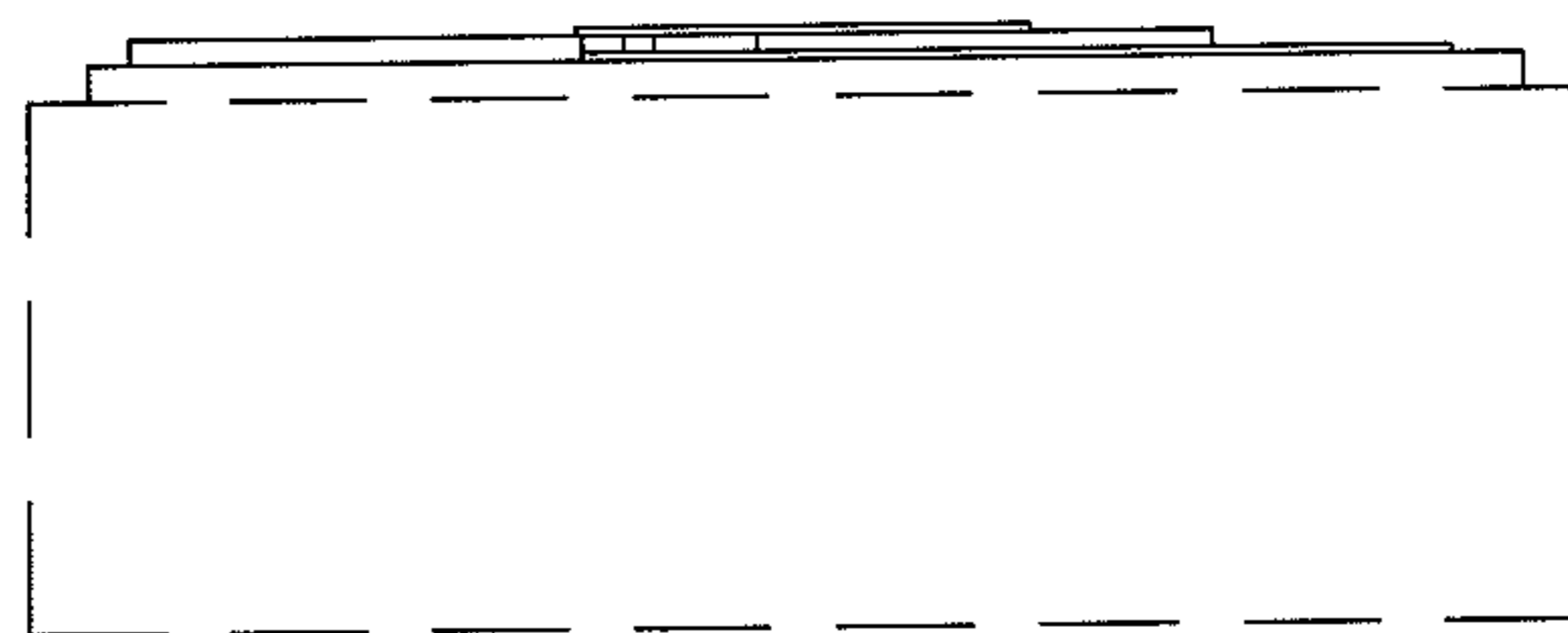


FIG. 7

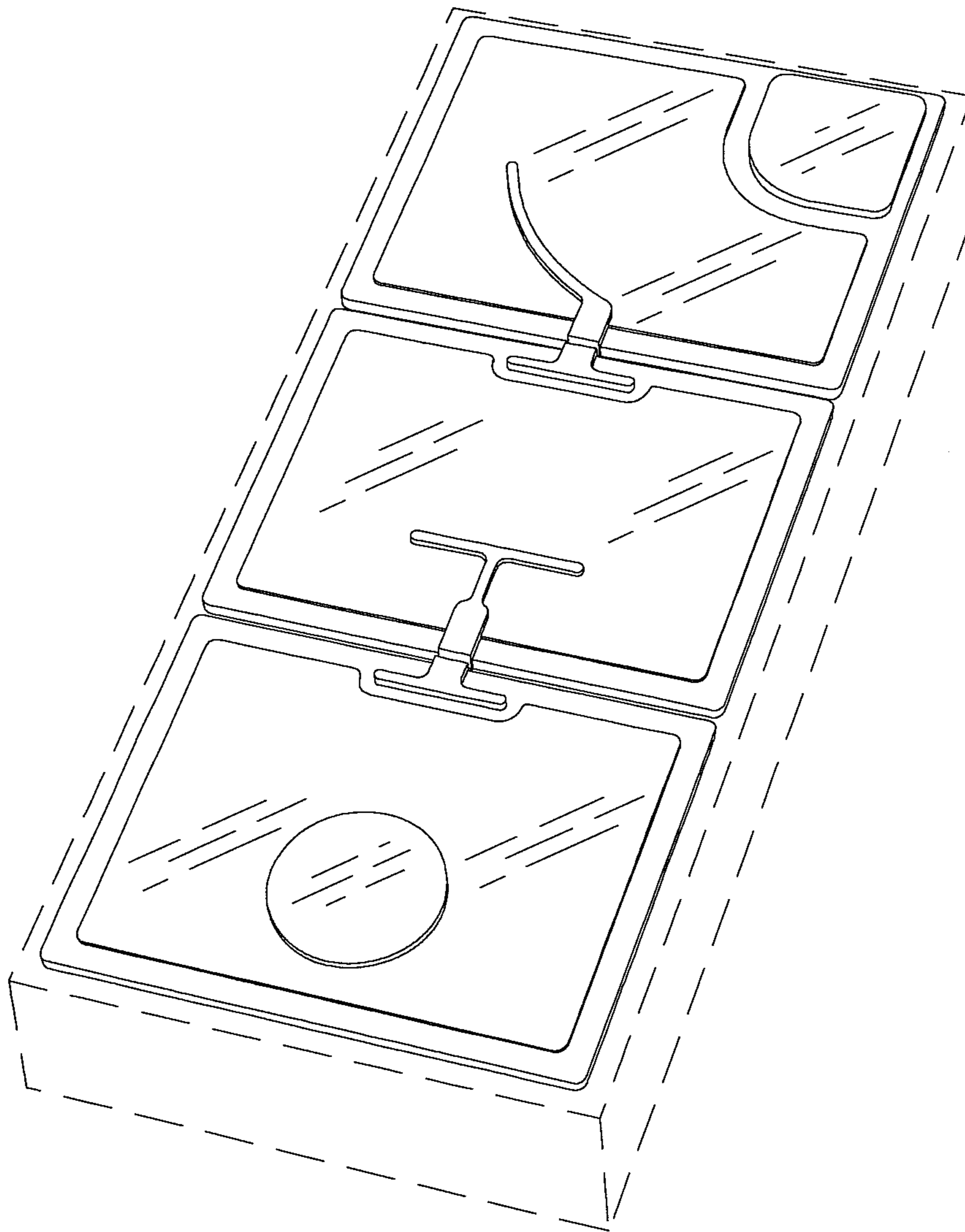


FIG.8