



US00D681560S

(12) **United States Design Patent**  
**Shen et al.**

(10) **Patent No.:** **US D681,560 S**  
(45) **Date of Patent:** **\*\* May 7, 2013**

(54) **LIGHT-EMITTING DIODE ARRAY**

(75) Inventors: **Chien-Fu Shen**, Hsinchu (TW);  
**Tsun-Kai Ko**, Hsinchu (TW);  
**Hui-Chun Yeh**, Hsinchu (TW)

(73) Assignee: **Epistar Corporation**, Hsinchu (TW)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/418,970**

(22) Filed: **Apr. 24, 2012**

(30) **Foreign Application Priority Data**

Oct. 24, 2011 (TW) ..... 100305707

(51) **LOC (9) Cl.** ..... **13-03**

(52) **U.S. Cl.**  
USPC ..... **D13/180**

(58) **Field of Classification Search** ..... D13/180;  
D26/1; 257/79, 80, 81, 88, 89, 95, 98, 99,  
257/100, E33.058; 313/483, 498, 500; 362/555,  
362/800

See application file for complete search history.

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

6,914,261	B2 *	7/2005	Ho	.....	257/79
D578,536	S *	10/2008	Lei et al.	.....	D14/435
D582,865	S *	12/2008	Edmond et al.	.....	D13/180
7,786,498	B2 *	8/2010	Choi et al.	.....	257/98
7,795,610	B2 *	9/2010	Kim et al.	.....	257/35
D644,187	S *	8/2011	Shen et al.	.....	D13/180

D644,188	S *	8/2011	Shen et al.	.....	D13/180
D646,234	S *	10/2011	Yao et al.	.....	D13/180
8,076,688	B2 *	12/2011	Kim et al.	.....	257/98
8,188,493	B2 *	5/2012	Li et al.	.....	257/91
D662,068	S *	6/2012	Yao et al.	.....	D13/180
8,198,648	B2 *	6/2012	Bougrov et al.	.....	257/98
2005/0052878	A1 *	3/2005	Yamada et al.	.....	362/460
2011/0204392	A1 *	8/2011	Weekamp et al.	.....	257/88
2012/0138964	A1 *	6/2012	Hsu	.....	257/88
2012/0241808	A1 *	9/2012	Akiyama et al.	.....	257/99

\* cited by examiner

*Primary Examiner* — Selina Sikder

(74) *Attorney, Agent, or Firm* — Muncy, Geissler, Olds & Lowe, PLLC

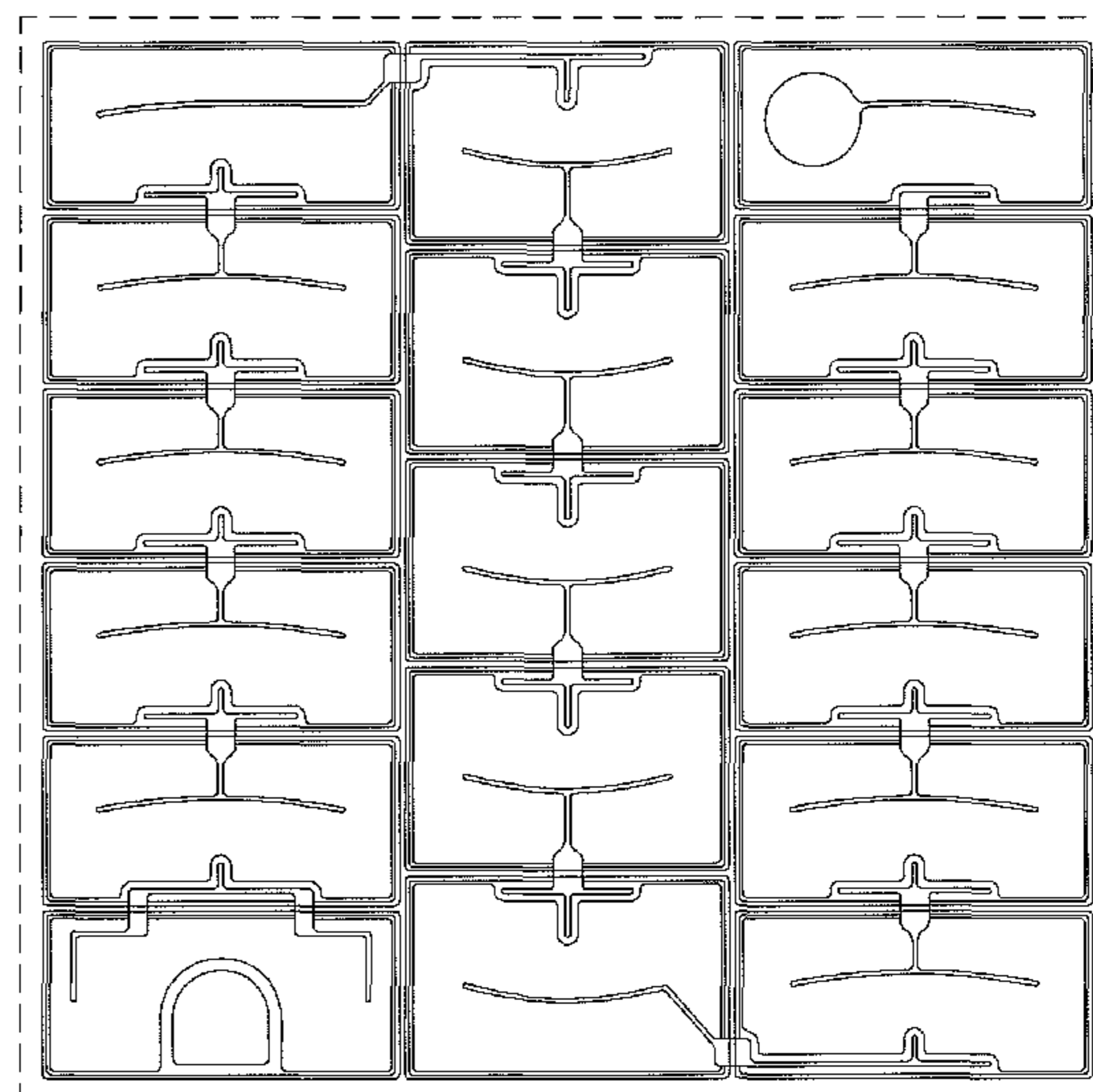
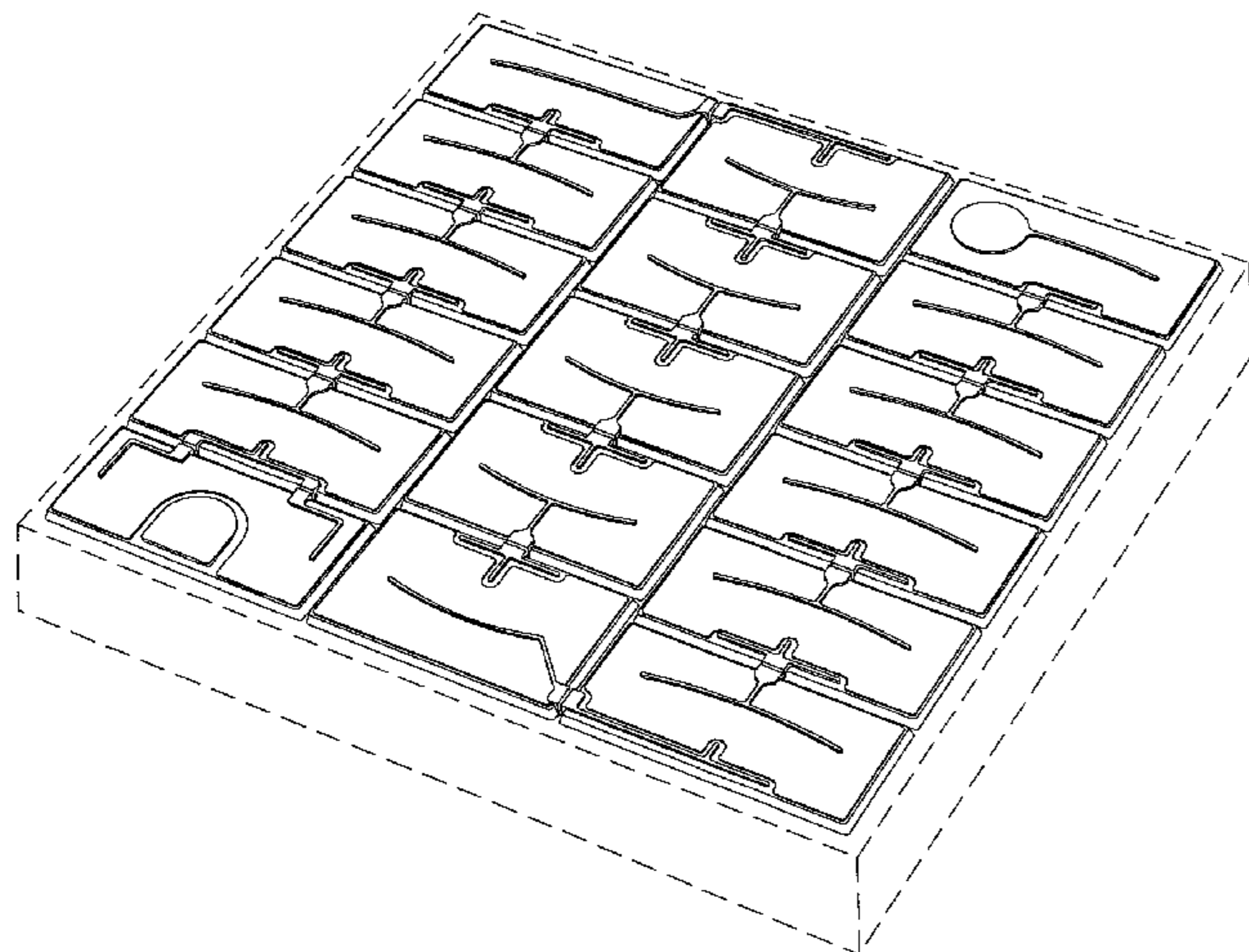
(57) **CLAIM**

The ornamental design for a light-emitting diode array, as shown and described.

**DESCRIPTION**

FIG. 1 is a perspective view of a light-emitting diode array showing our new design;  
 FIG. 2 is a front elevational view of the light-emitting diode array;  
 FIG. 3 is a rear elevational view of the light-emitting diode array;  
 FIG. 4 is a left side view of the light-emitting diode array;  
 FIG. 5 is a right side view of the light-emitting diode array;  
 FIG. 6 is a top plan view of the light-emitting diode array; and,  
 FIG. 7 is a bottom plan view of the light-emitting diode array.  
 The broken line showing is for the purpose of illustrating environmental structure only and forms no part of the claimed design.

**1 Claim, 5 Drawing Sheets**



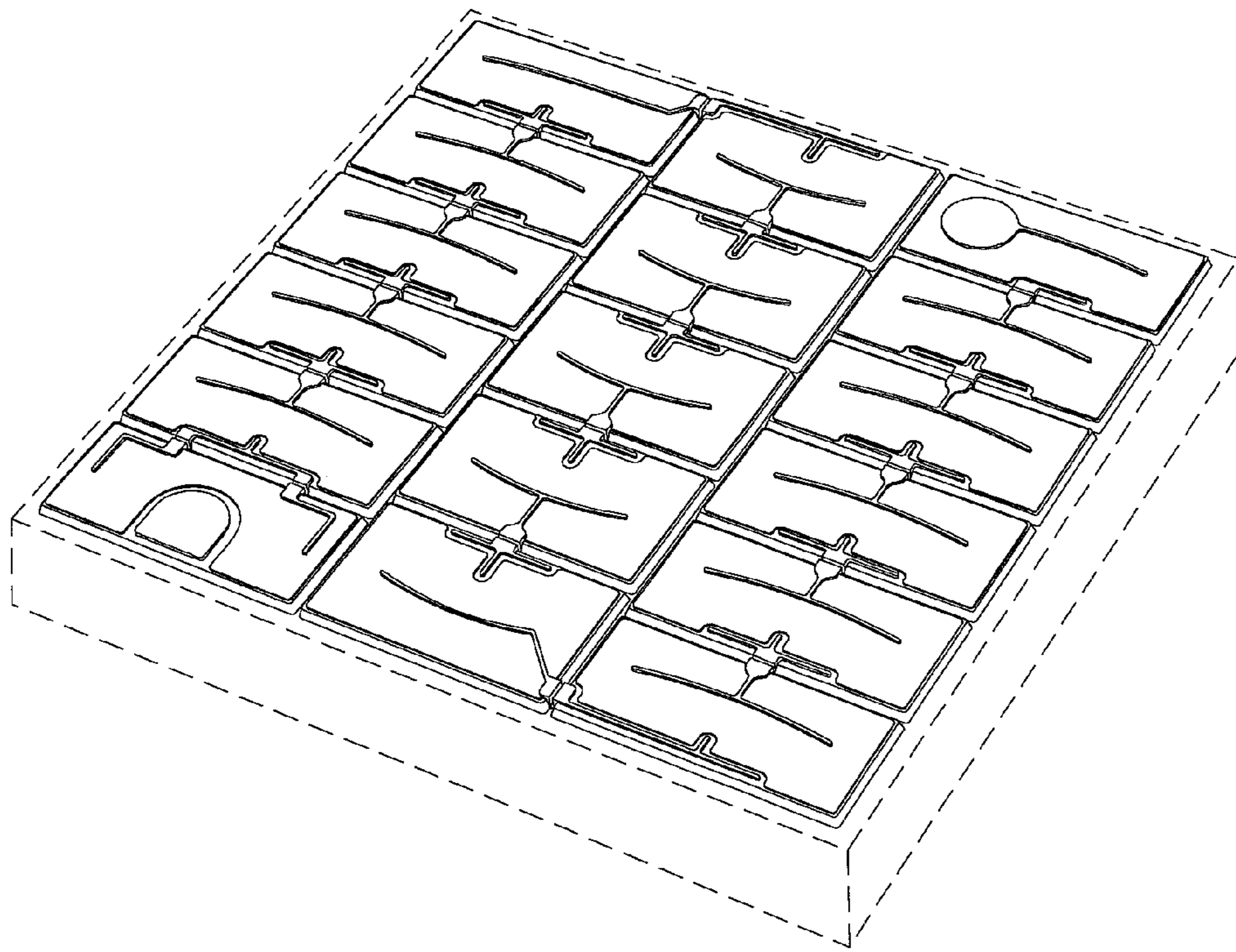


FIG.1

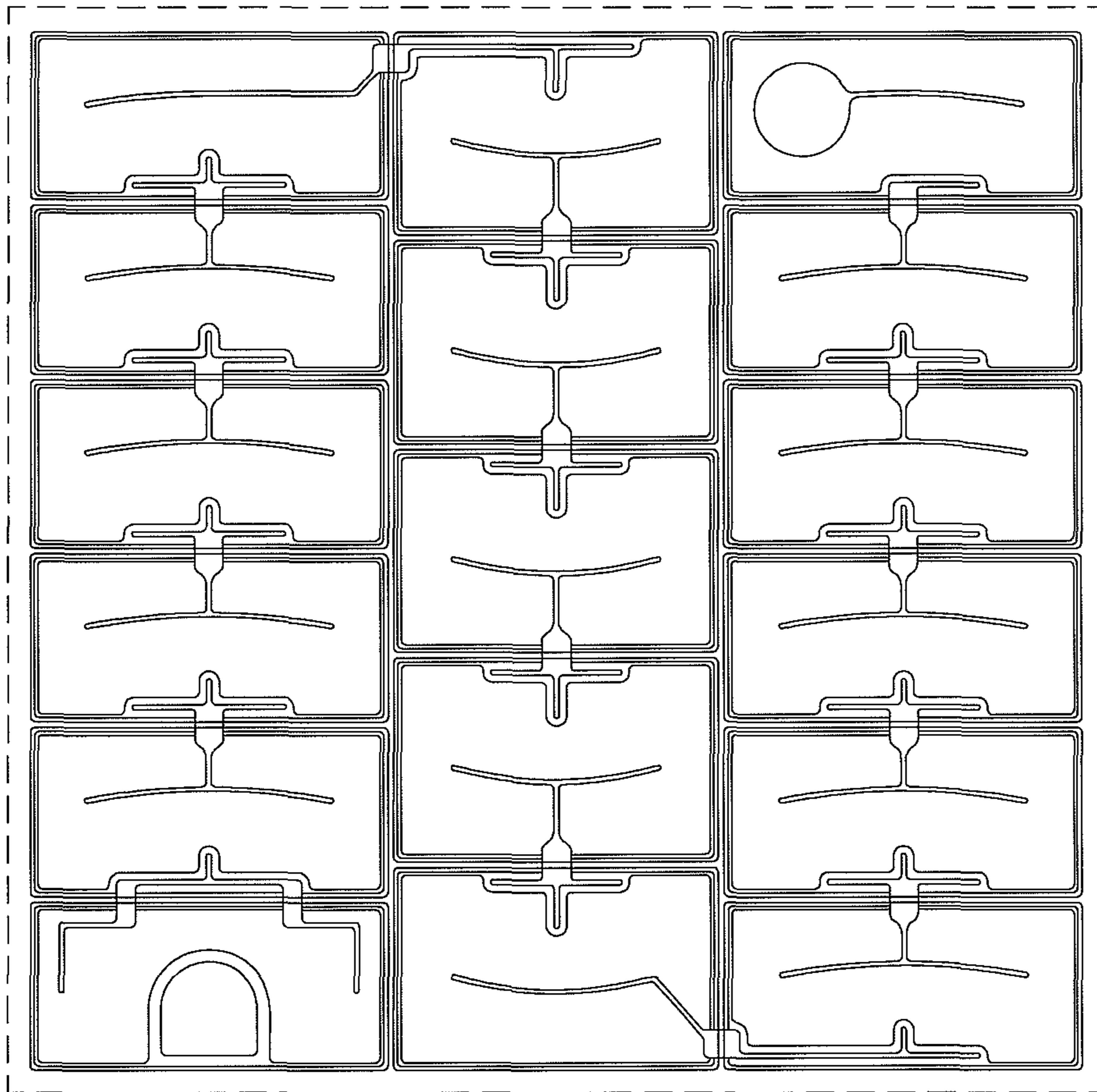
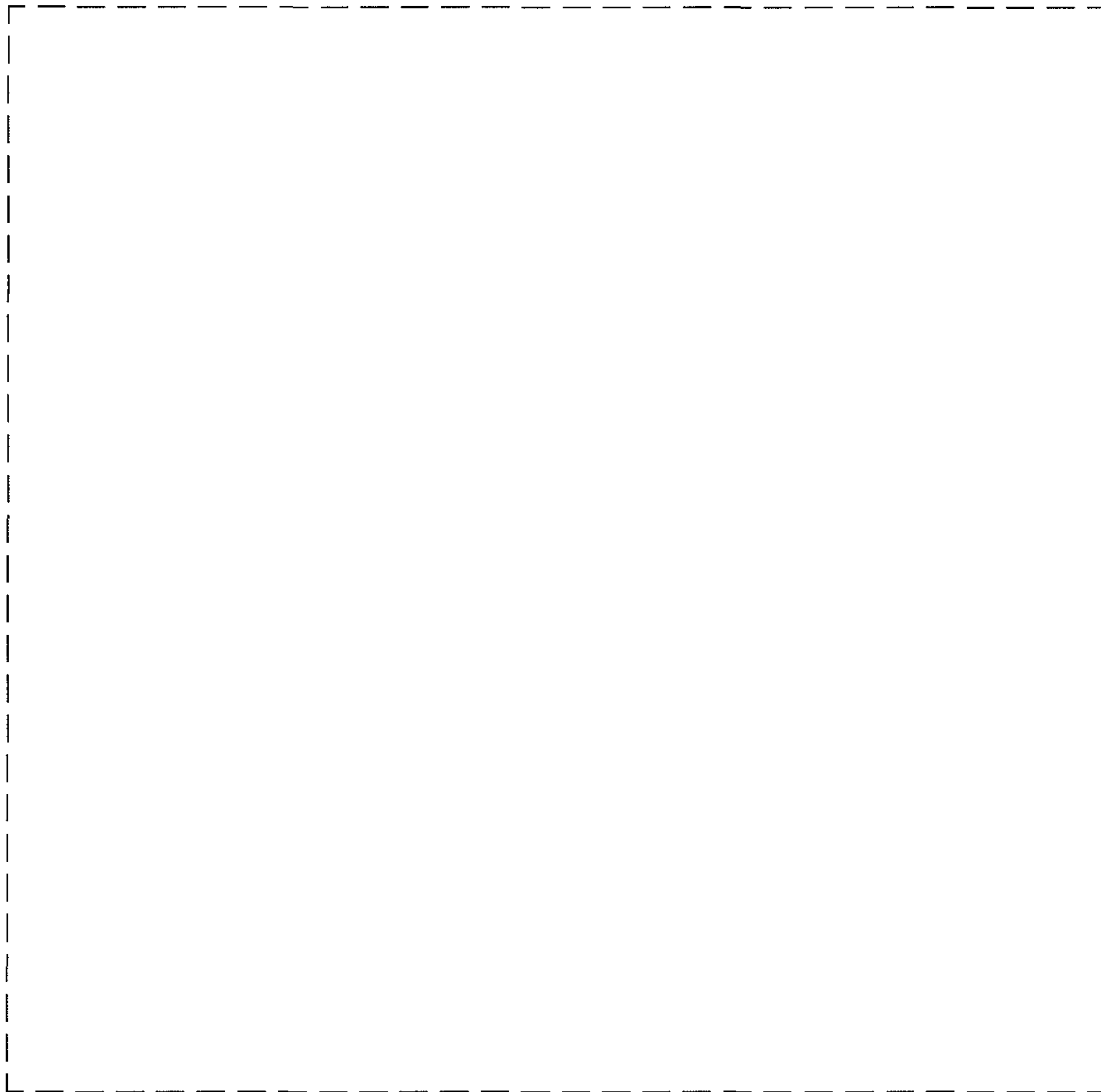


FIG.2



**FIG.3**

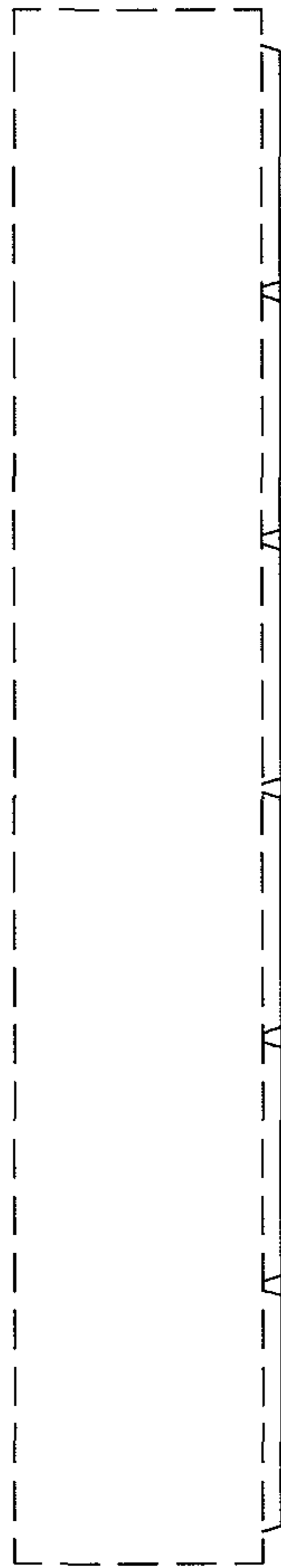


FIG. 4

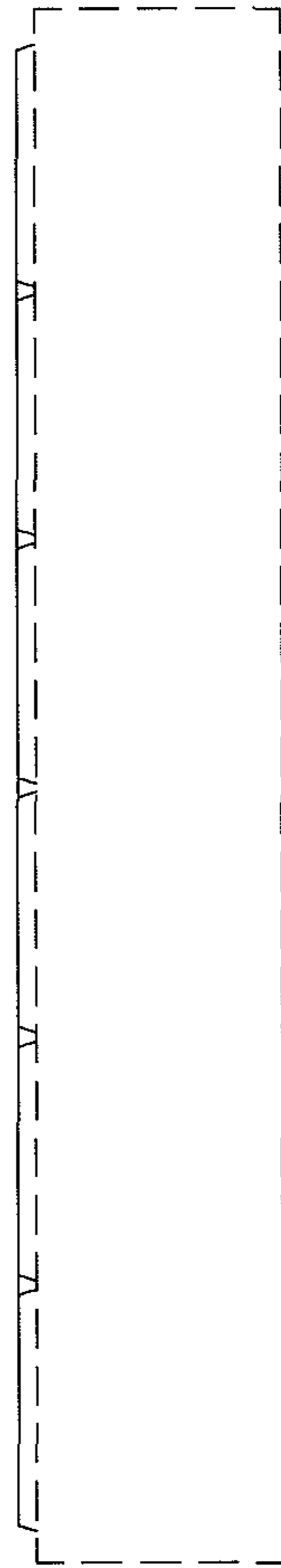


FIG. 5



FIG.6

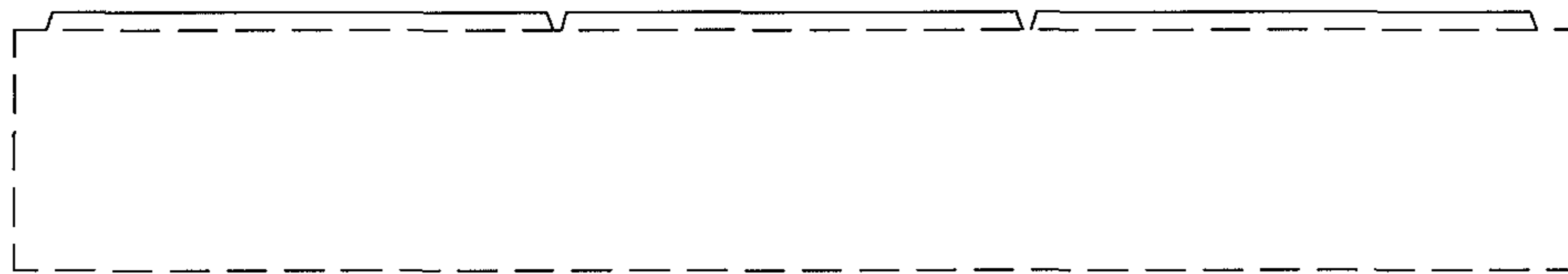


FIG.7