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(12) **United States Design Patent**
Alves et al.

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(54) **ELECTRONIC CIRCUIT CABINET**

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(73) Assignee: **Whirlpool S.A.**, Sao Paulo (BR)

(**) Term: **14 Years**

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(30) **Foreign Application Priority Data**

Mar. 17, 2010 (BR) DI7000925-2

(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/152**

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D13/156, 155, 154, 153, 152, 147, 139.1–139.4,
D13/137.1–137.4, 133; 600/166; 439/675,
439/668, 623, 578, 502, 106; 385/77; 174/151,
174/138 F, 113 R

See application file for complete search history.

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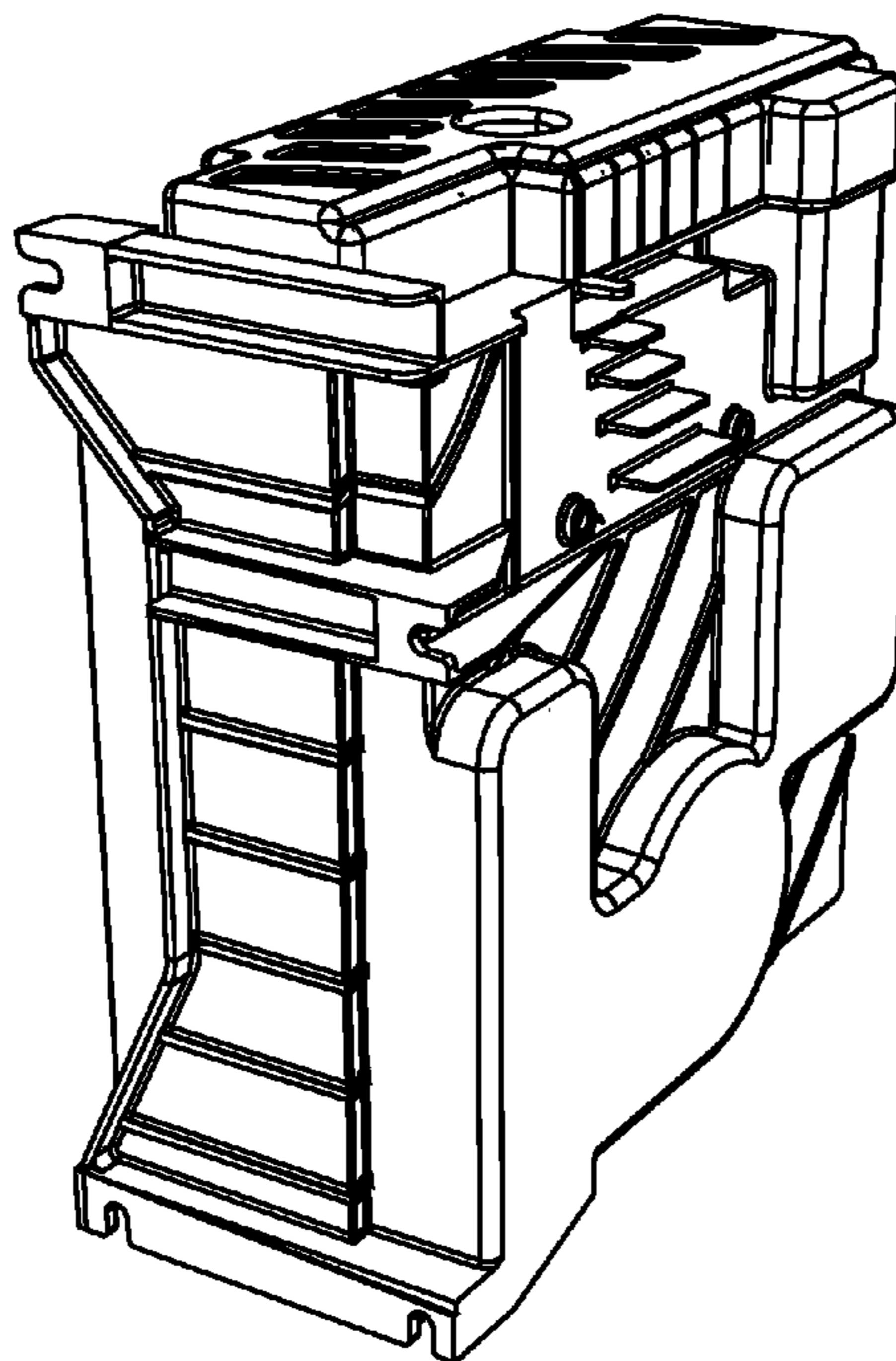
(57) **CLAIM**

The ornamental design for an electronic circuit cabinet, as shown and described.

DESCRIPTION

FIG. 1 is a front perspective view of an electronic circuit cabinet showing our new design; FIG. 2 is a front view of the electronic circuit cabinet of FIG. 1; FIG. 3 is a rear view of the electronic circuit cabinet of FIG. 1; FIG. 4 is a top view of the electronic circuit cabinet of FIG. 1; FIG. 5 is bottom view of the electronic circuit cabinet of FIG. 1; FIG. 6 is a left side view of the electronic circuit cabinet of FIG. 1; and, FIG. 7 is a right side view of the electronic circuit cabinet of FIG. 1.

1 Claim, 4 Drawing Sheets



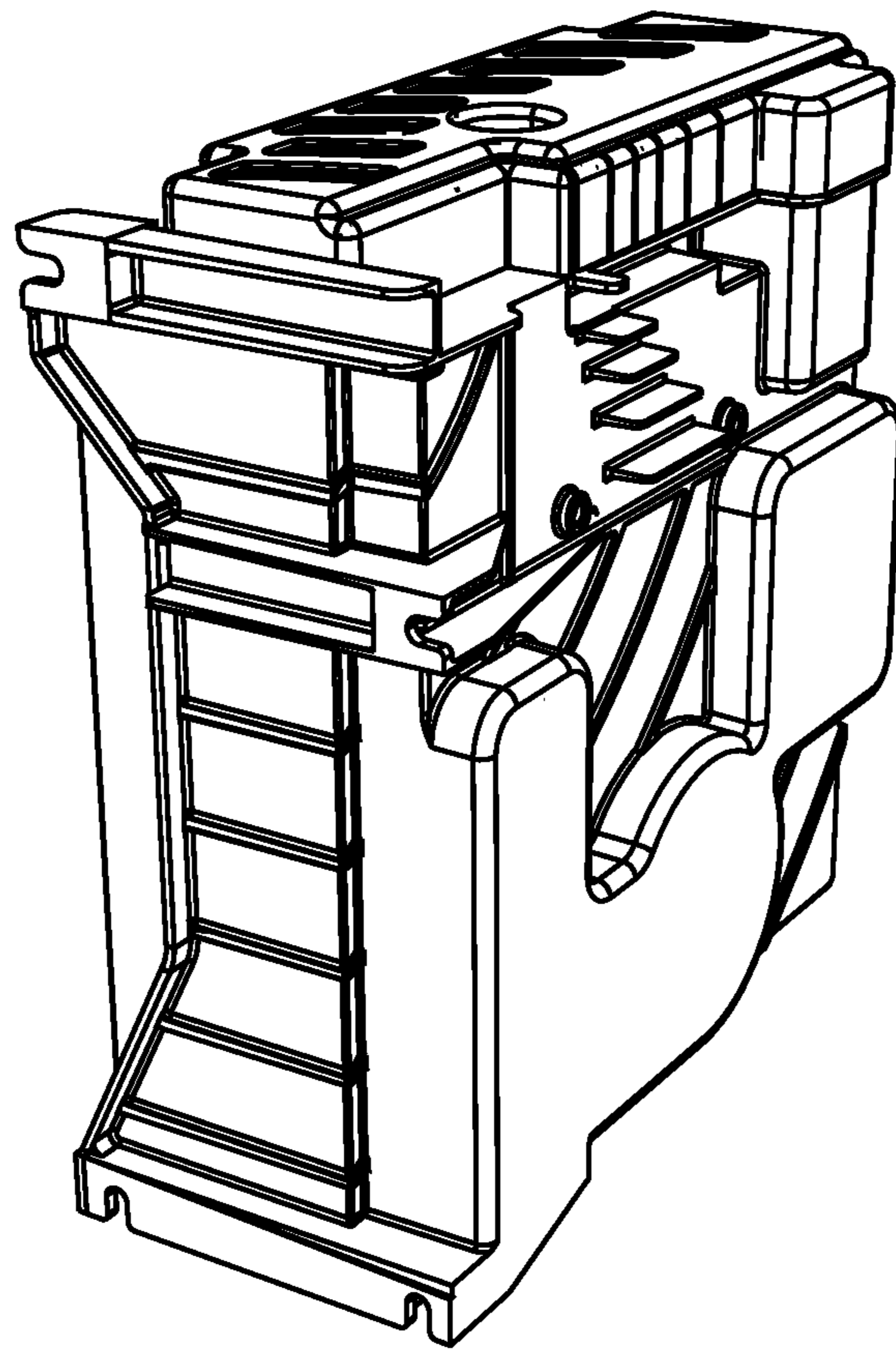


FIG. 1

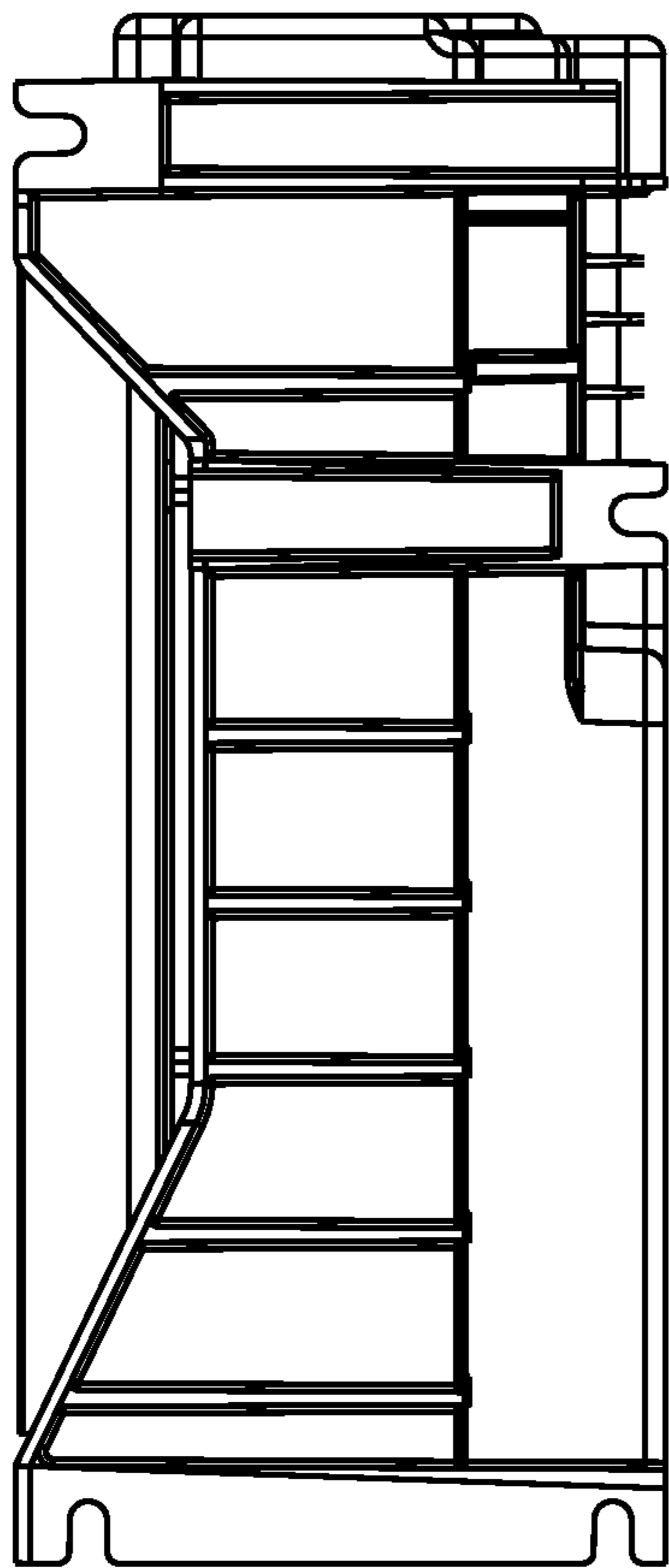


FIG. 2

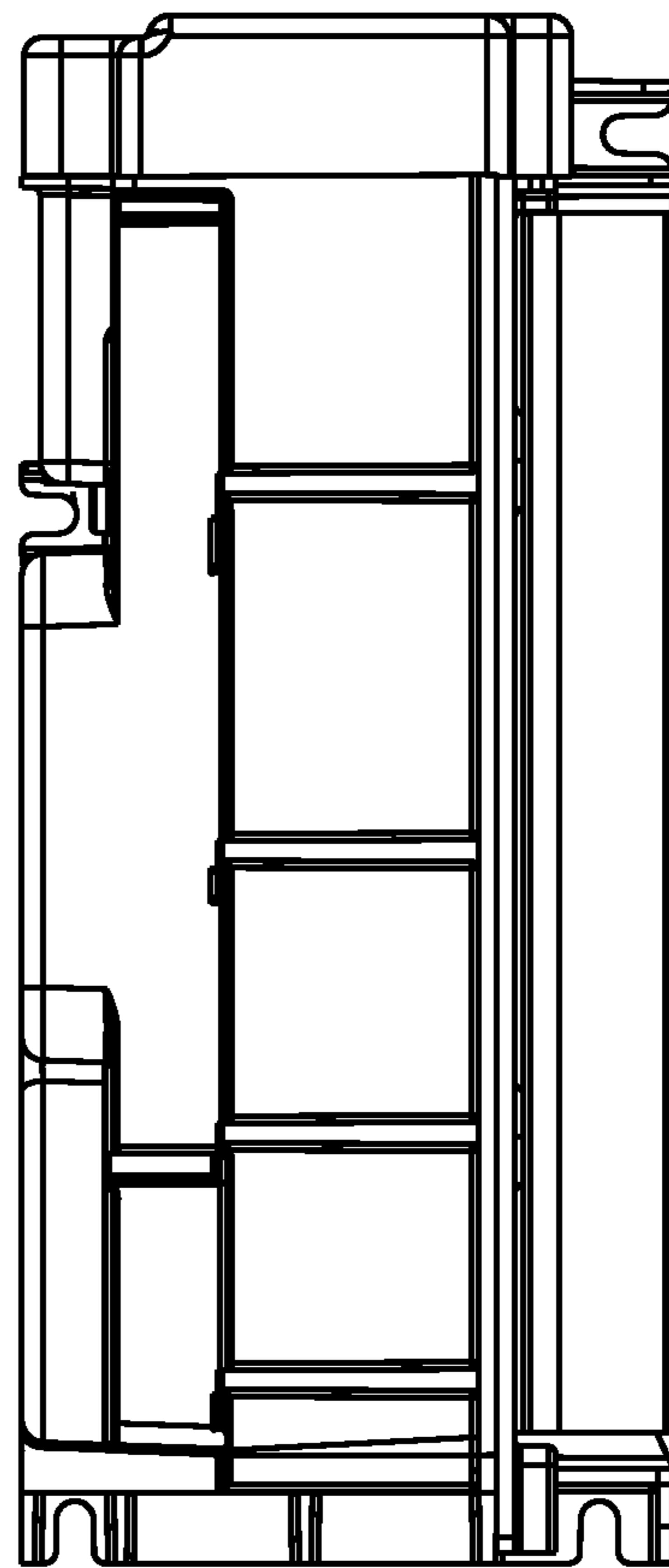


FIG. 3

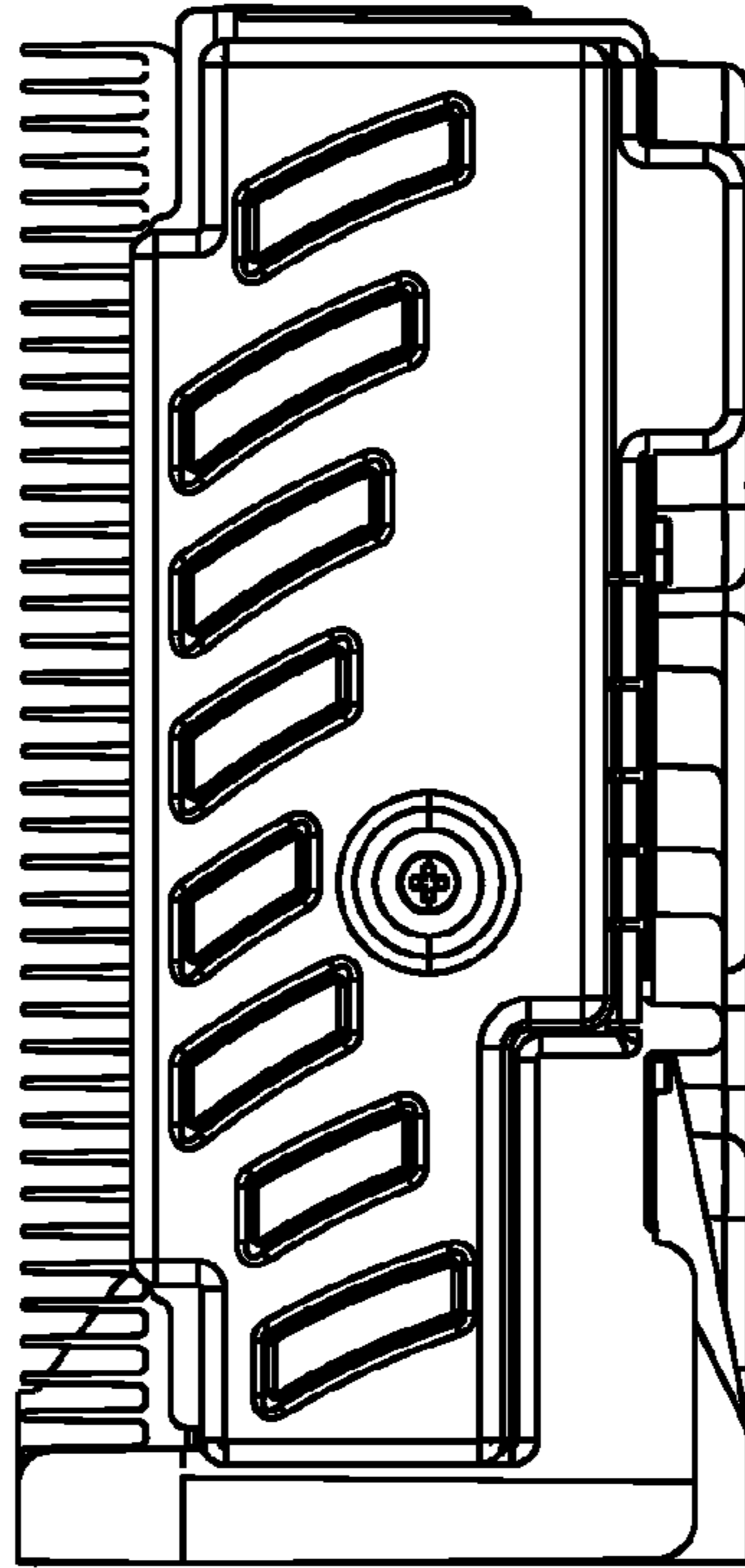


FIG. 4

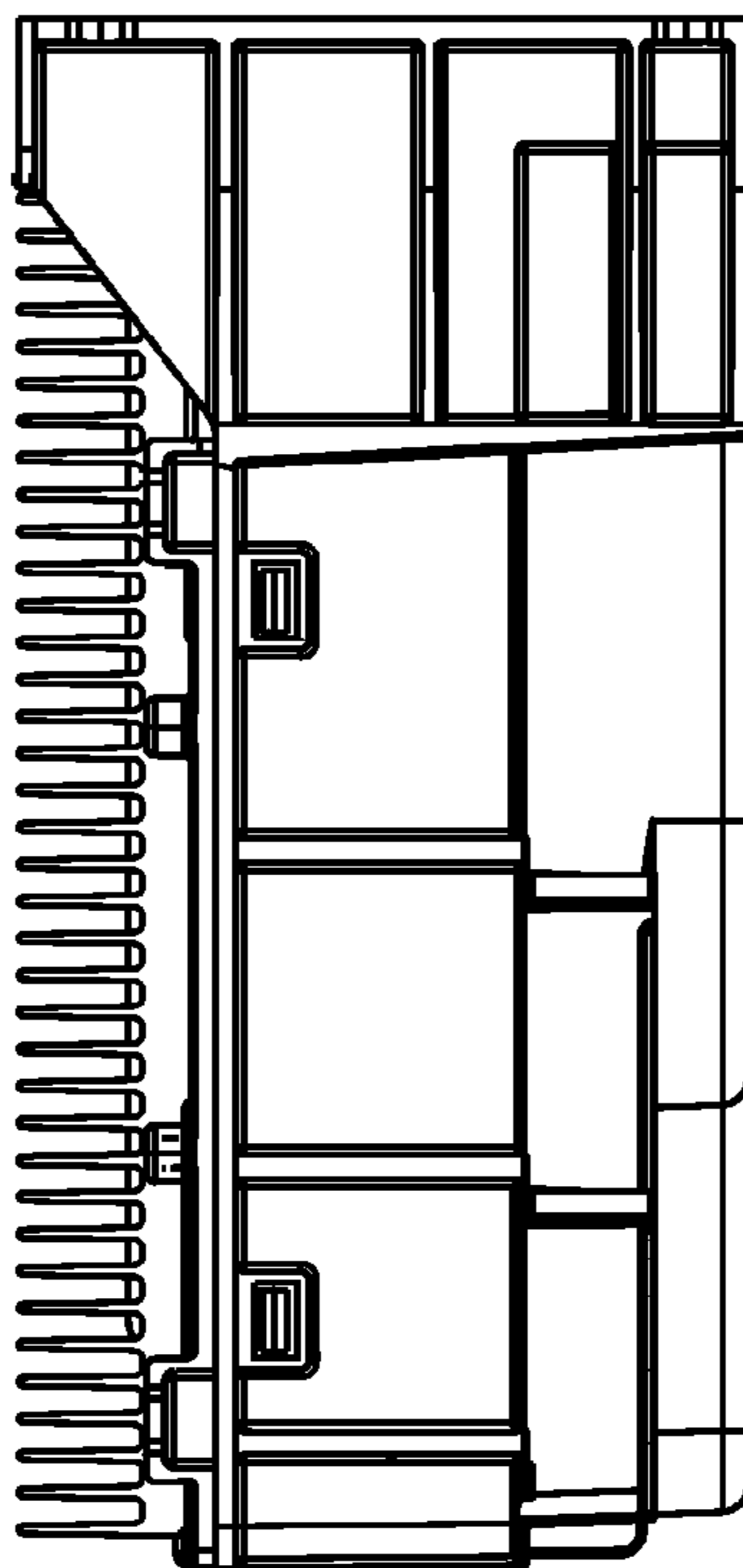


FIG. 5

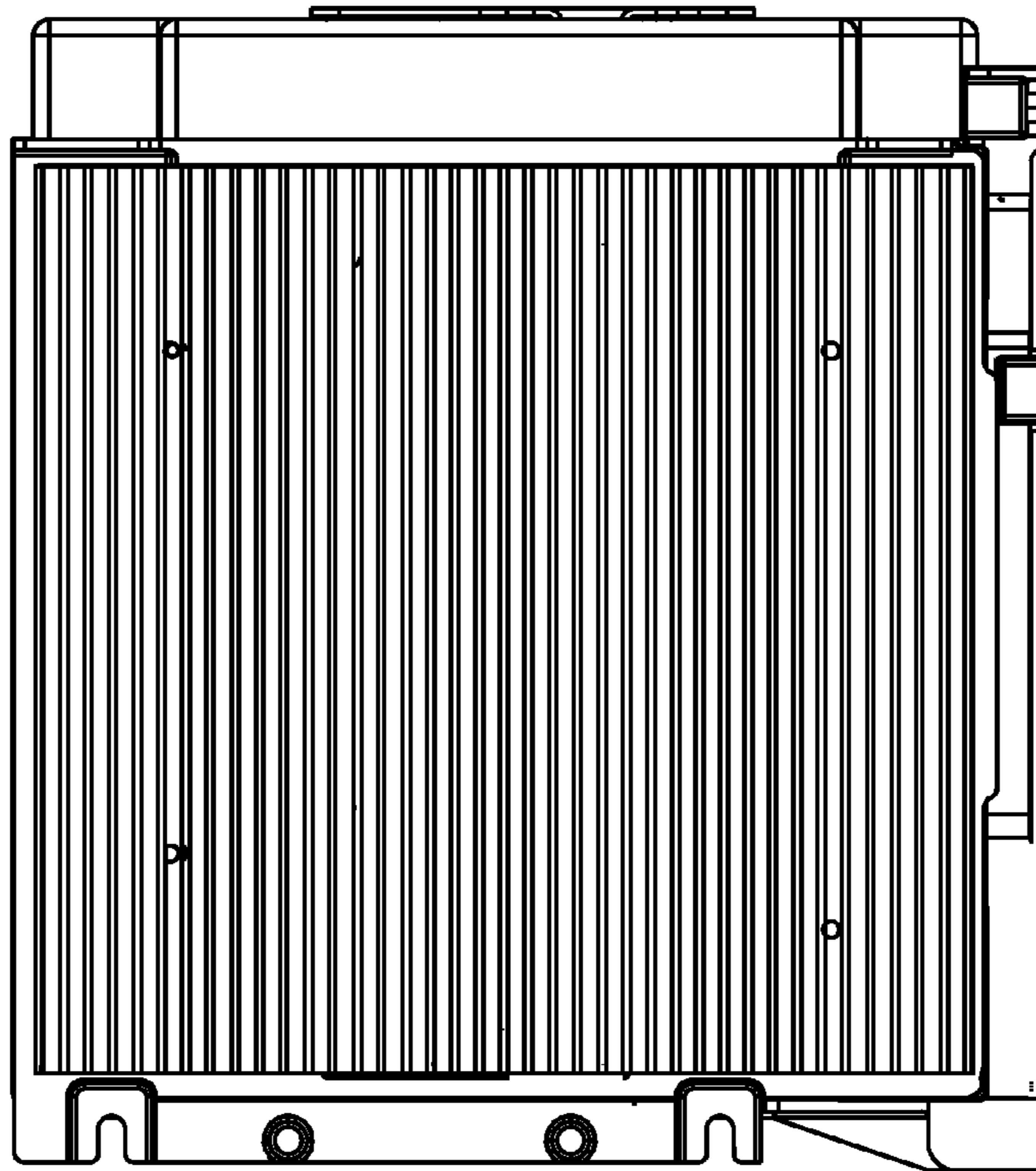


FIG. 6

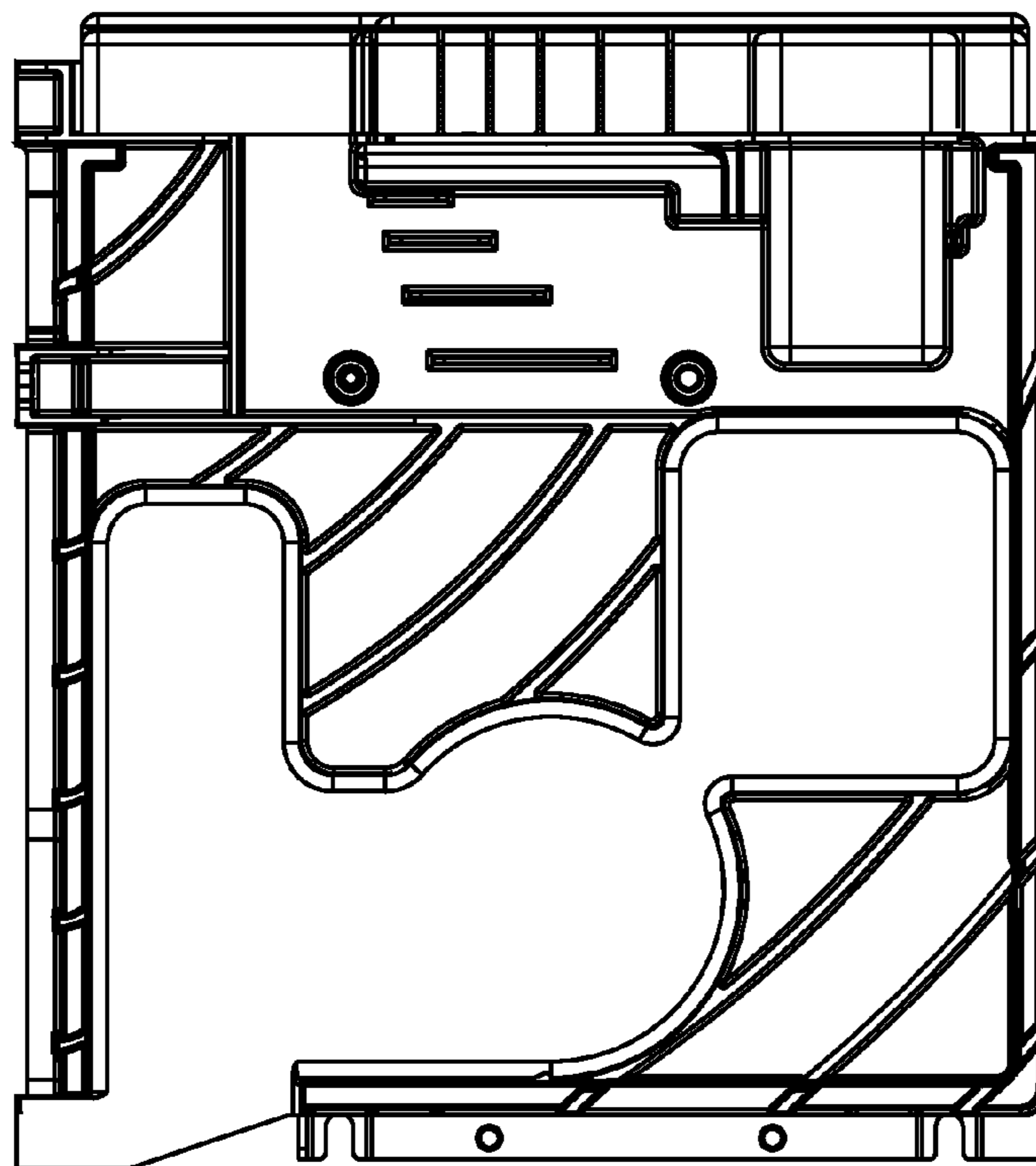


FIG. 7