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(12) **United States Design Patent**
Taniguchi et al.

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(54) **ADHESIVE TAPE FOR SEMICONDUCTOR MANUFACTURING**

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(**) Term: **14 Years**

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(30) **Foreign Application Priority Data**

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(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/182; D19/69**

(58) **Field of Classification Search** D10/71;
D19/69; D24/189; D5/99; 33/494, 758,
33/562, 759; 101/288; 206/390; 40/638,
40/641; 428/43; D7/585; D20/22, 42; D13/182
See application file for complete search history.

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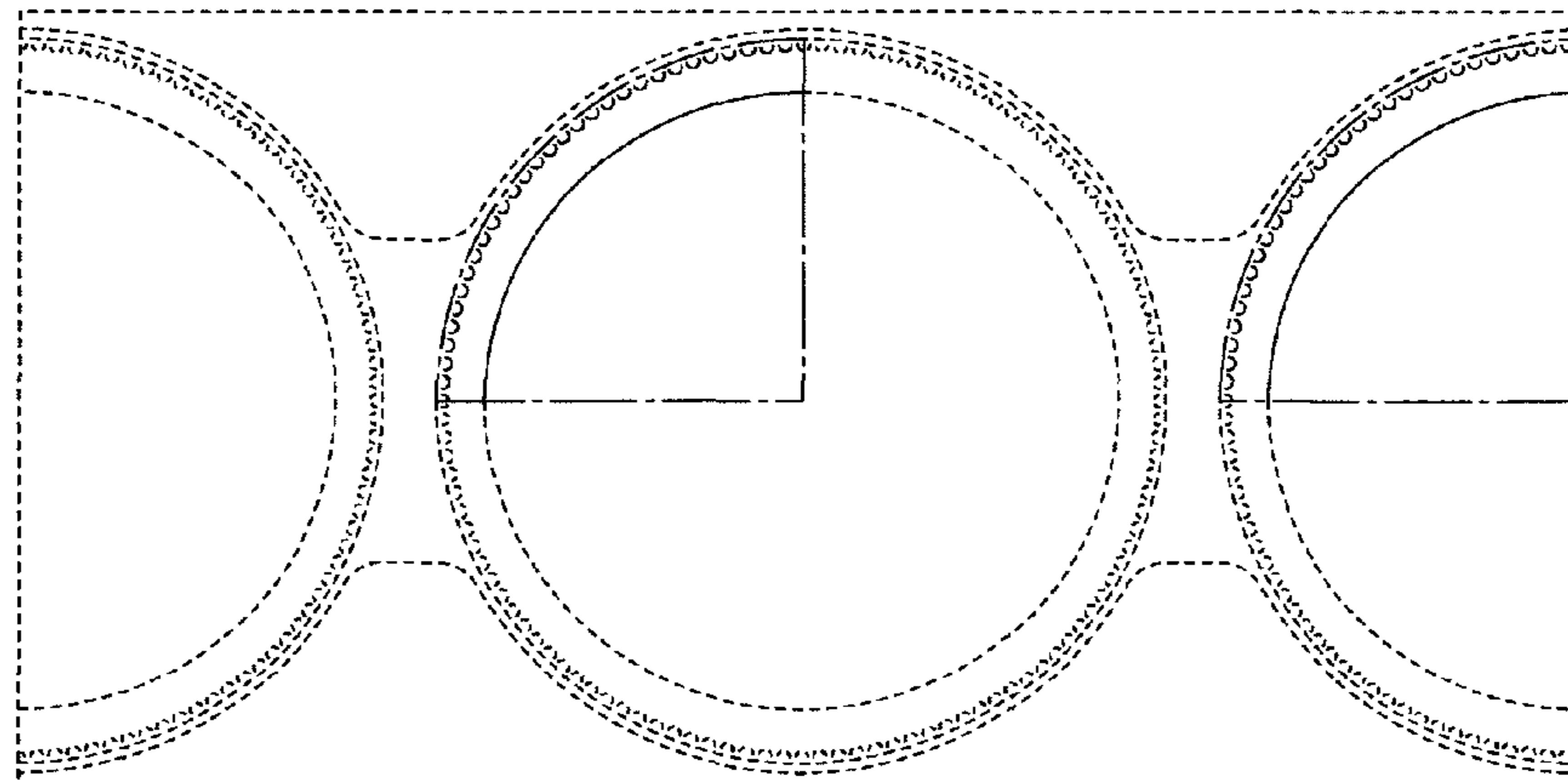
(57) **CLAIM**

The ornamental design for an adhesive tape for semiconductor manufacturing, as shown and described.

DESCRIPTION

FIG. 1 is a top plan view of an adhesive tape for semiconductor manufacturing incorporating a first embodiment of our new design;
FIG. 2 is a bottom plan view of the adhesive tape of FIG. 1;
FIG. 3 is a rear side view of the adhesive tape of FIG. 1;
FIG. 4 is a front side view of the adhesive tape of FIG. 1;
FIG. 5 is a right side view of the adhesive tape of FIG. 1;
FIG. 6 is a left side view of the adhesive tape of FIG. 1;
FIG. 7 is a cross-sectional view taken at line VII-VII in FIG. 1;
FIG. 8 is a cross-sectional view taken at line VIII-VIII in FIG. 1;
FIG. 9 is an enlarged view of portion IX-IX in FIG. 7; and,
FIG. 10 is an enlarged view of portions X-X, XI-XI in FIG. 1.
The dashed line showing of the adhesive tape is for purposes of illustrating portions of the adhesive tape and forms no part of the claimed design.
The broken lines having alternating long and short segments define bounds of the claimed design and form no part thereof.

1 Claim, 10 Drawing Sheets



US D680,505 S

Page 2

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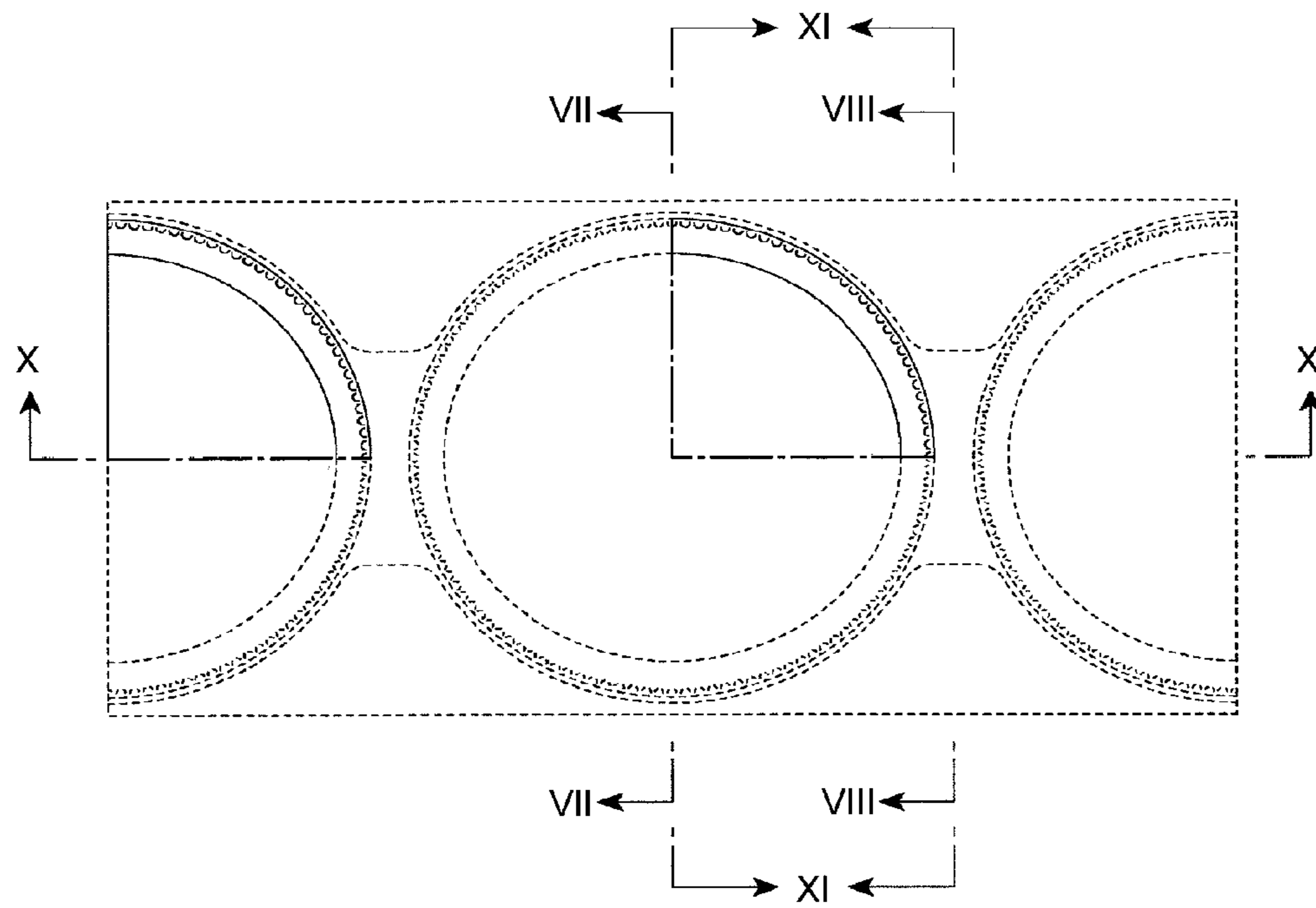


FIG. 1

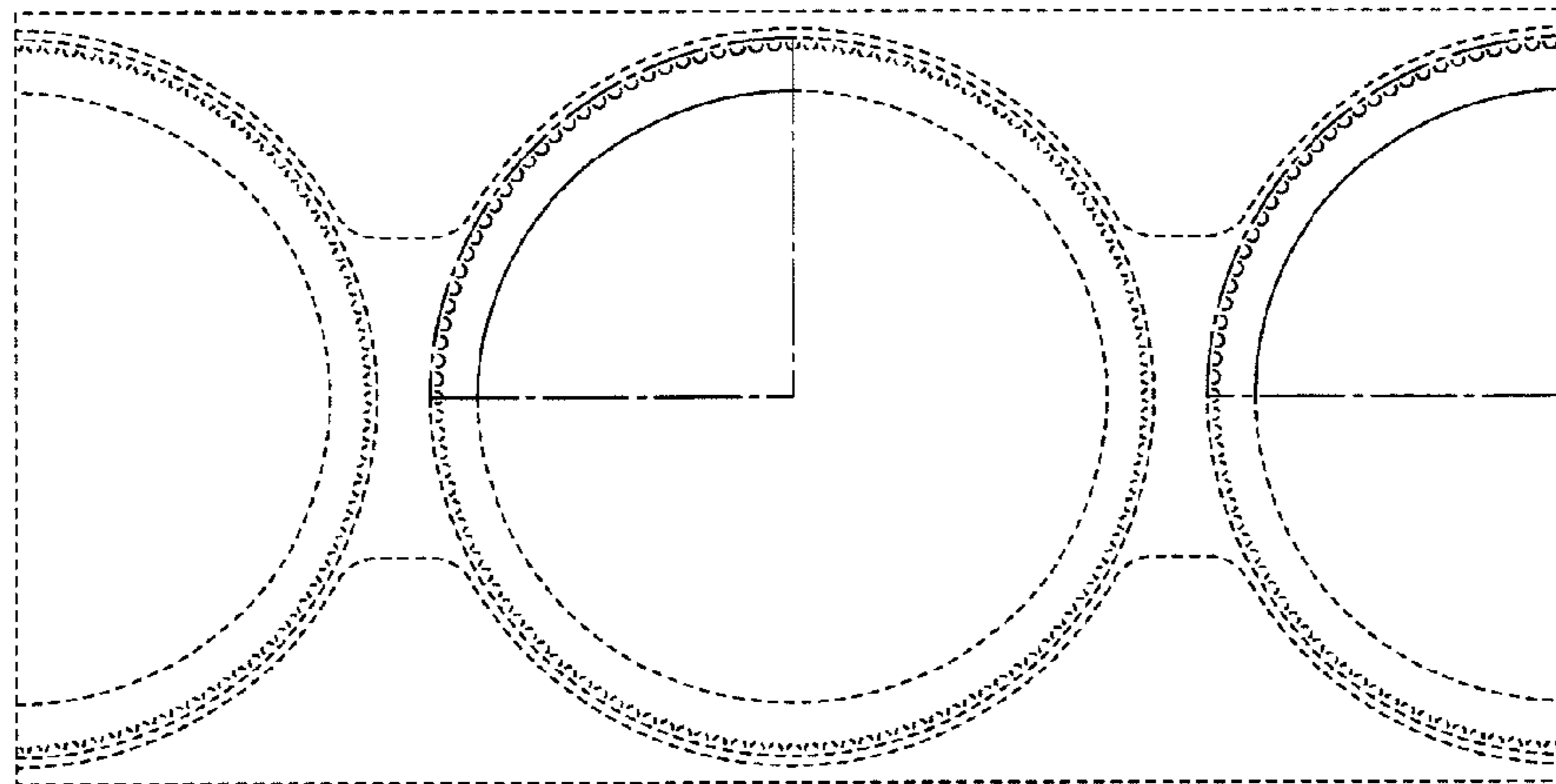


FIG. 2

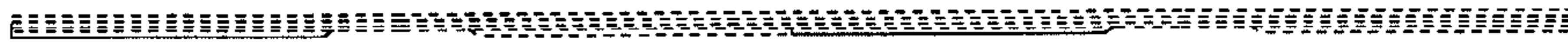


FIG. 3

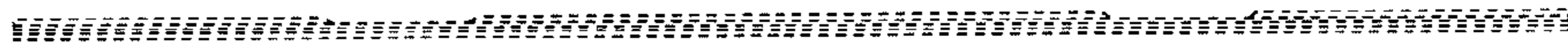


FIG. 4

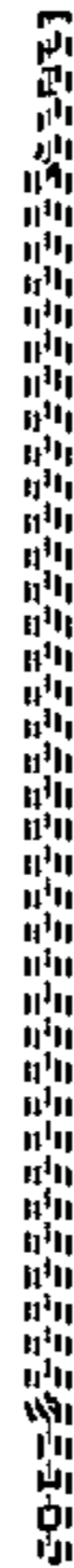


FIG. 5



FIG. 6

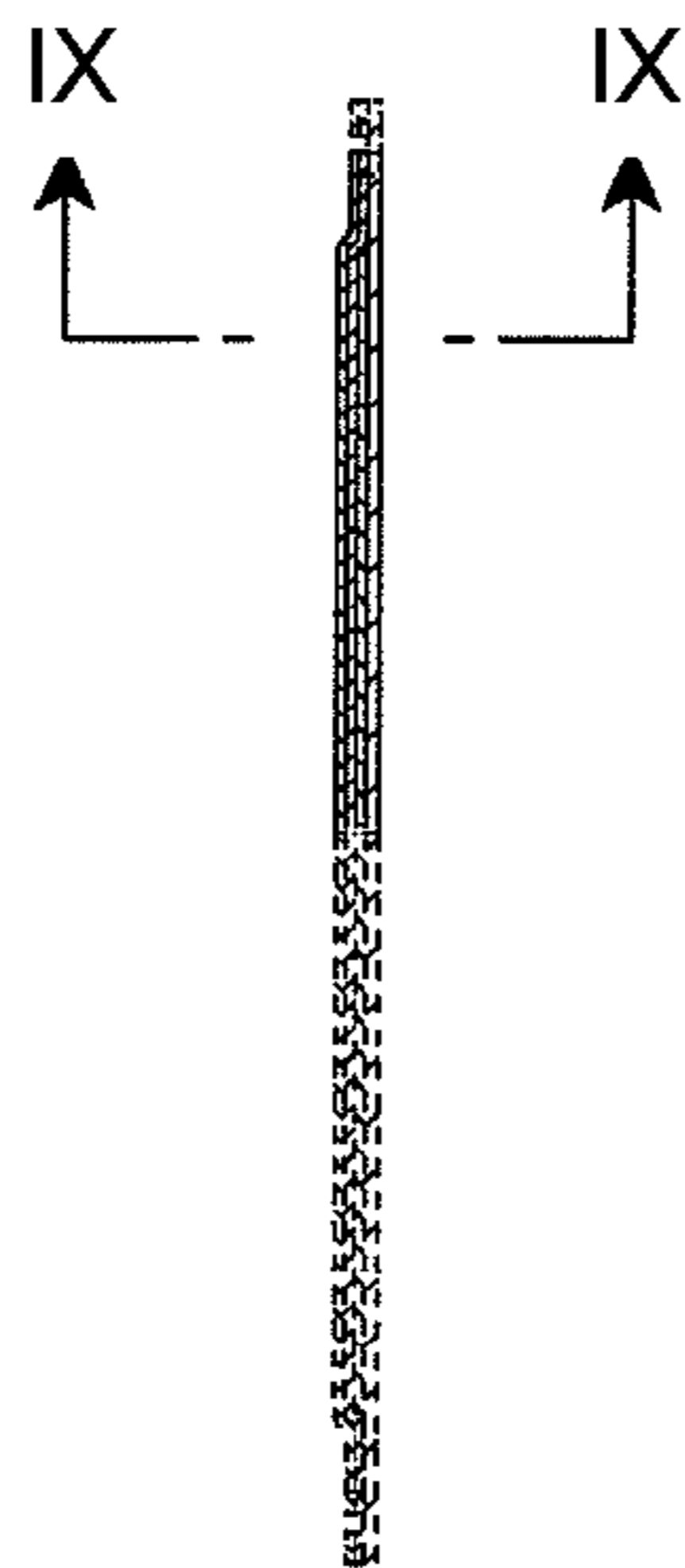


FIG. 7

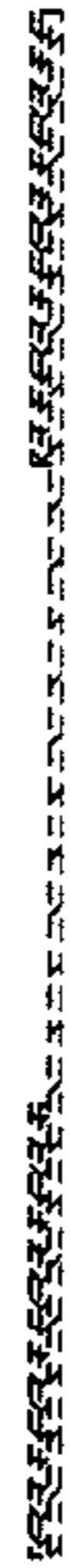


FIG. 8

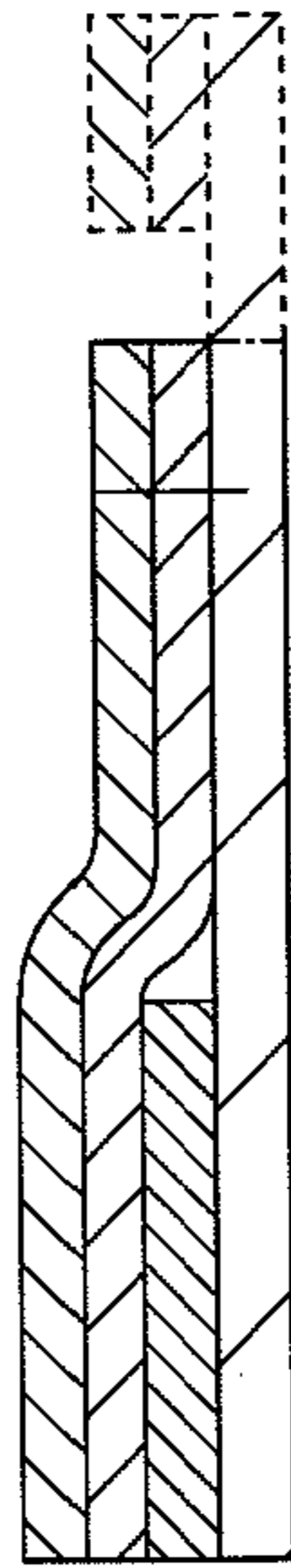


FIG. 9

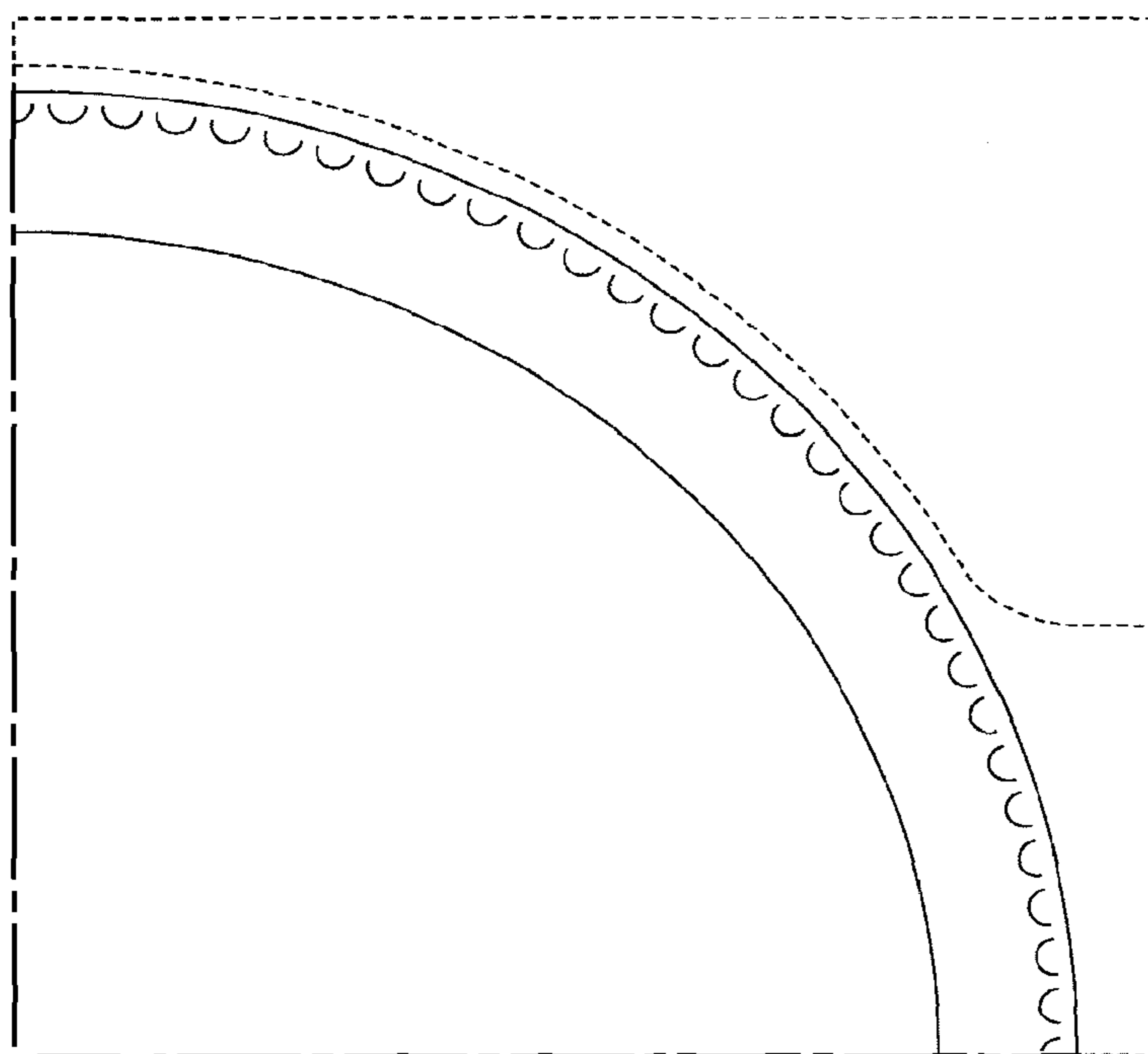


FIG. 10