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(12) **United States Design Patent**
Sekizawa et al.

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(54) **MICRO FLOW CHANNEL CHIP**

DESCRIPTION

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(**) Term: **14 Years**
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D24/219, 222–226, 227, 231, 232, 107, 169,
D24/186; D10/81; 422/400–404, 408, 430,
422/68.1, 69, 500, 502, 503; 435/287.1,
435/287.3, 287.4, 288.1, 288.3, 288.7
See application file for complete search history.

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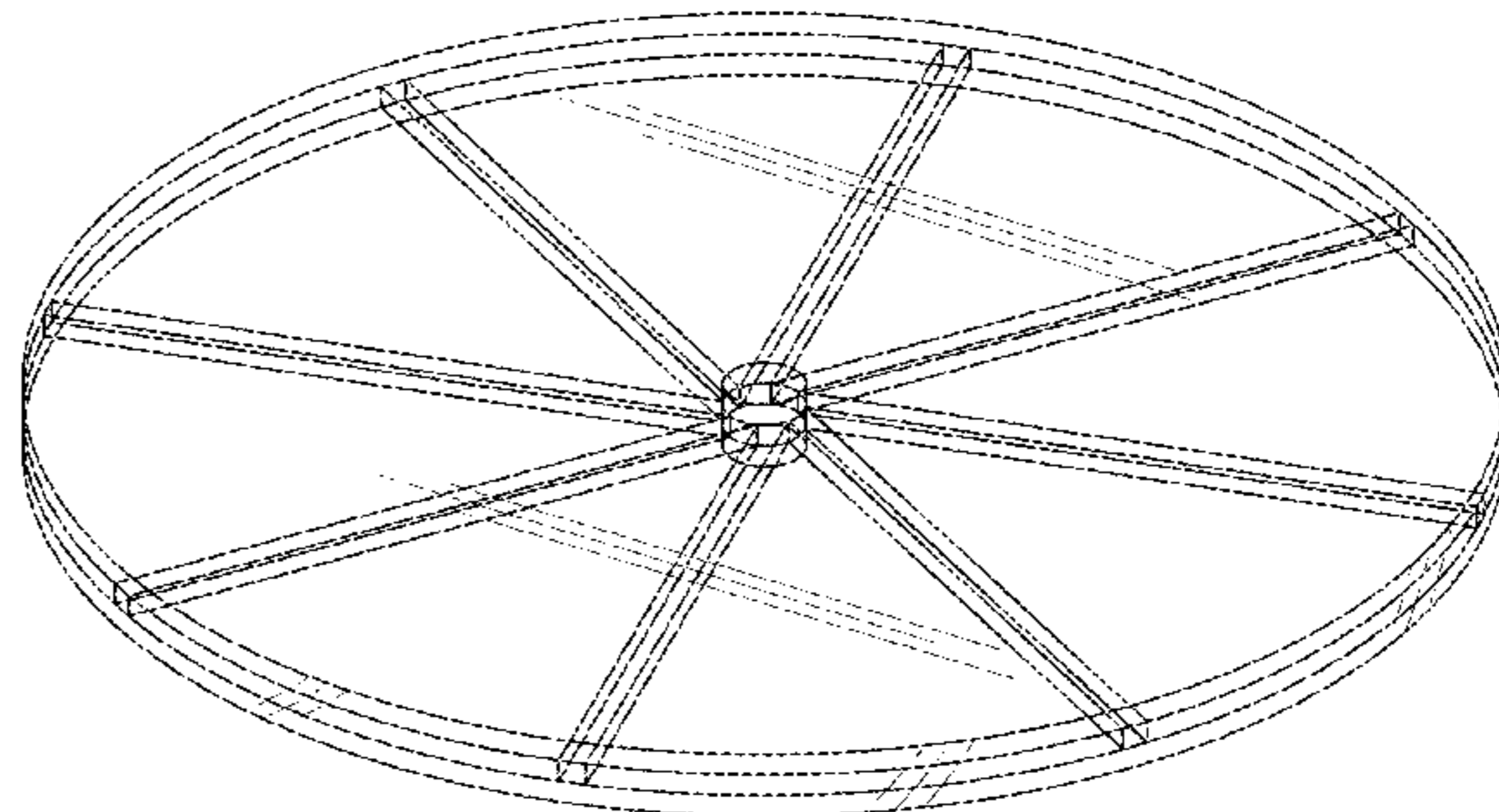
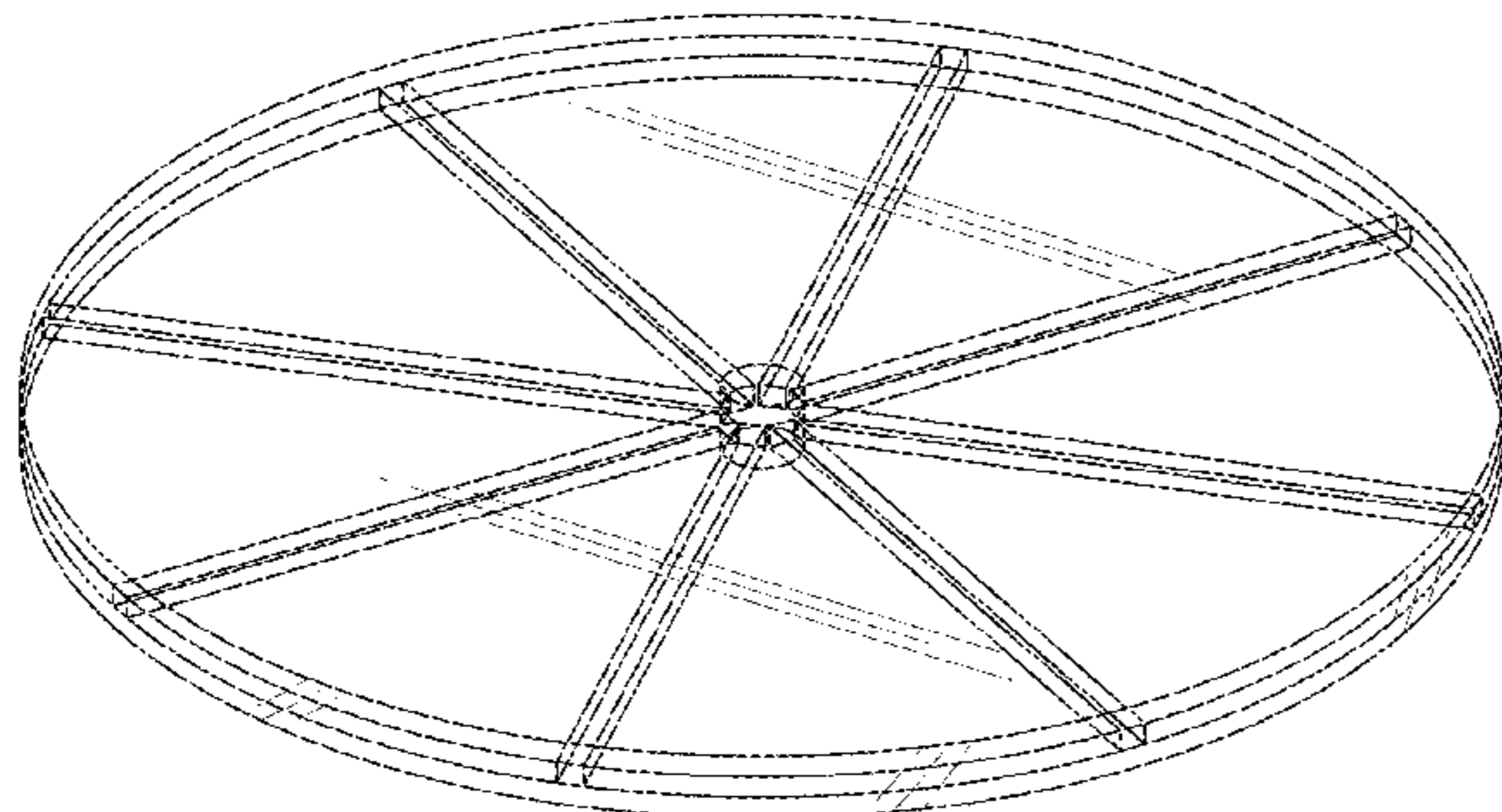
Primary Examiner — Anhdao Doan

(57) **CLAIM**

The ornamental design for a micro flow channel chip, as shown and described.

FIG. 1 is a front elevational view of a first embodiment of a micro flow channel chip showing our new design, with the rear elevational view being a mirror image thereof;
FIG. 2 is a top plan view of the micro flow channel chip of FIG. 1, with the bottom plan view being a mirror image thereof;
FIG. 3 is a left side elevational view of the micro flow channel chip of FIG. 1, with the right side elevational view being a mirror image of the micro flow channel chip of FIG. 1;
FIG. 4 is a front-bottom-left perspective view of the micro flow channel chip of FIG. 1;
FIG. 5 is a partially enlarged perspective view of the micro flow channel chip shown in FIG. 4;
FIG. 6 is a front elevational view of a second embodiment of a micro flow channel chip showing our new design, with the rear elevational view being a mirror image thereof;
FIG. 7 is a top plan view of the micro flow channel chip of FIG. 6, with the bottom plan view being a mirror image thereof;
FIG. 8 is a left side elevational view of the micro flow channel chip of FIG. 6, with the right side elevational view being a mirror image thereof;
FIG. 9 is a front-bottom-right perspective view of the micro flow channel chip of FIG. 6;
FIG. 10 is a partially enlarged perspective view of the micro flow channel chip shown in FIG. 9;
FIG. 11 is a front elevational view of a third embodiment of a micro flow channel chip showing our new design, with the rear elevational view being a mirror image thereof;
FIG. 12 is a top plan view of the micro flow channel chip of FIG. 11, with the bottom plan view being a mirror image thereof;
FIG. 13 is a left side elevational view of the micro flow channel chip of FIG. 11, with the right side elevational view being a mirror image thereof;
FIG. 14 is a front-bottom-left perspective view of the micro flow channel chip of FIG. 11; and,
FIG. 15 is a partially enlarged perspective view of the micro flow channel chip shown in FIG. 14.

1 Claim, 15 Drawing Sheets



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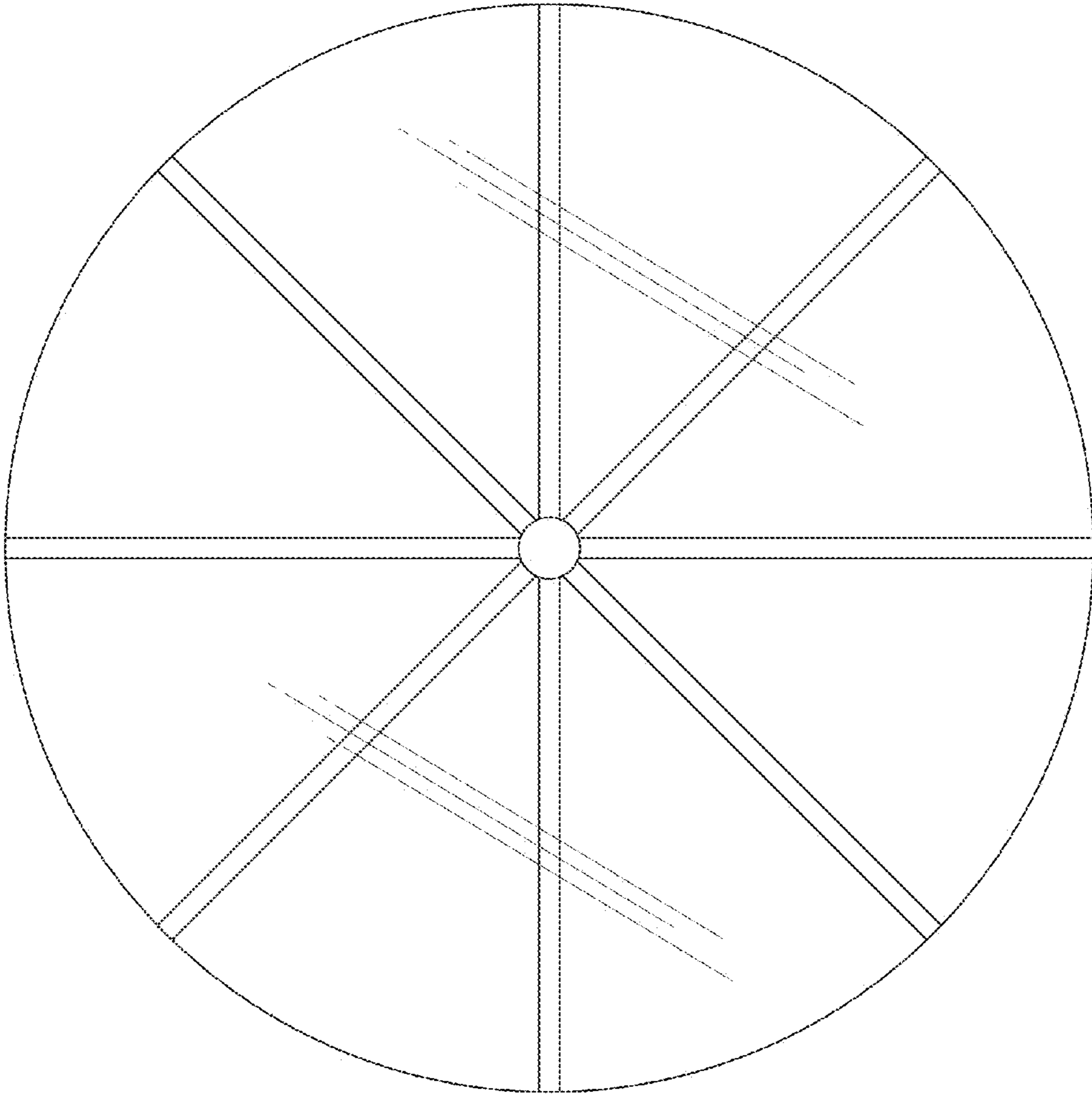


Fig.1

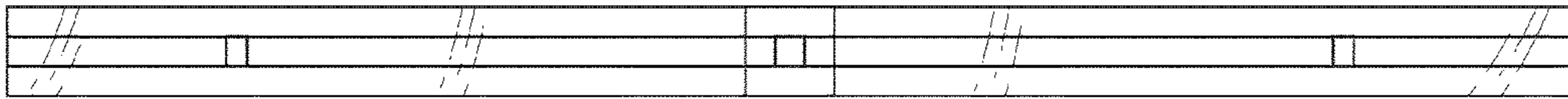


Fig.2

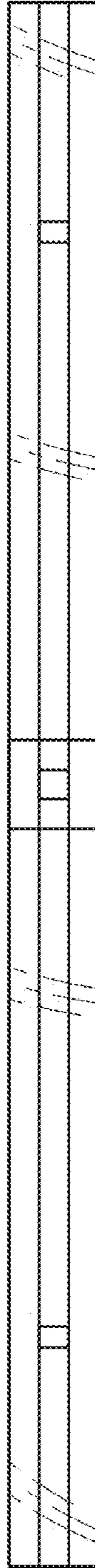


Fig.3

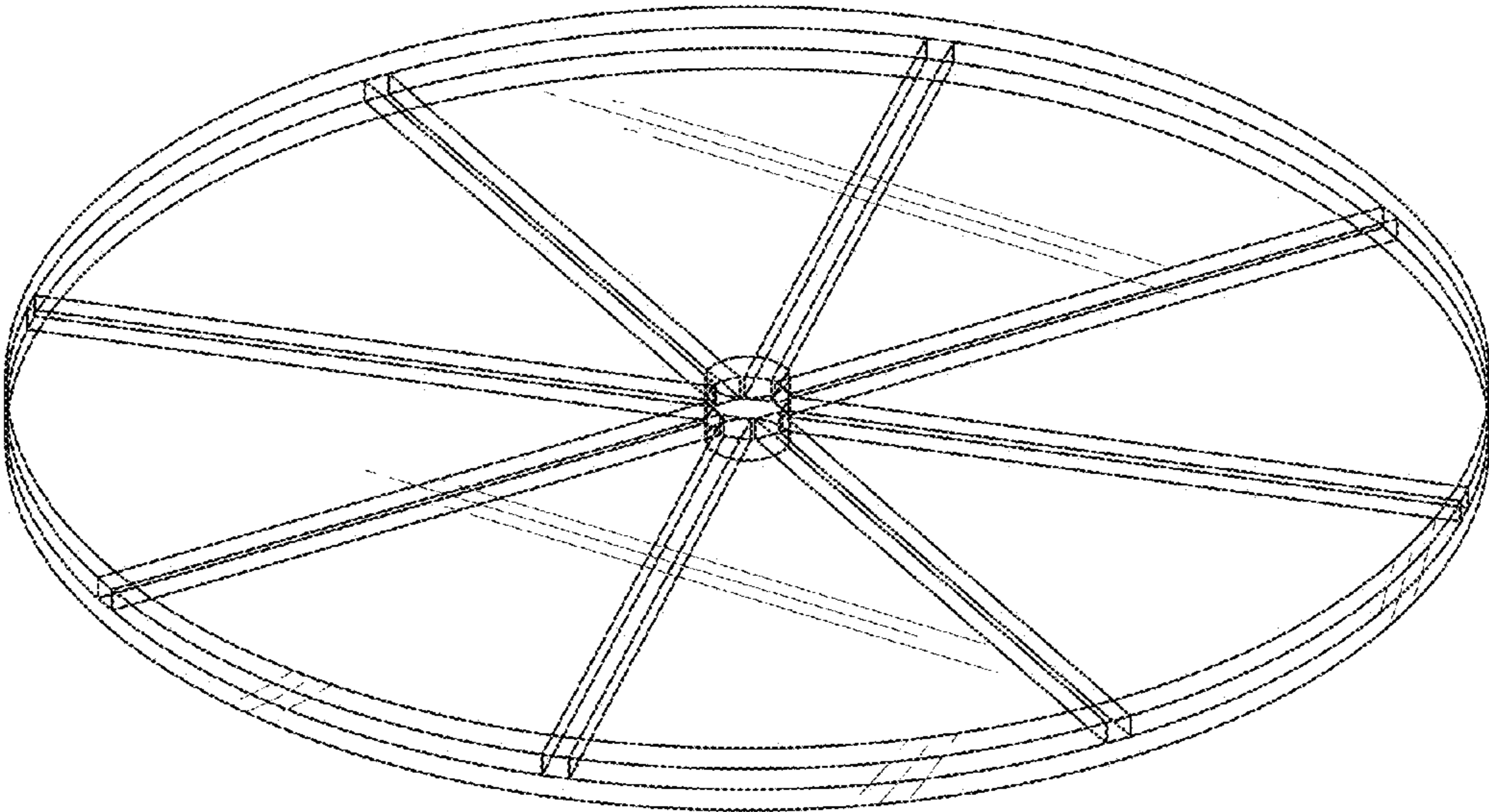


Fig.4

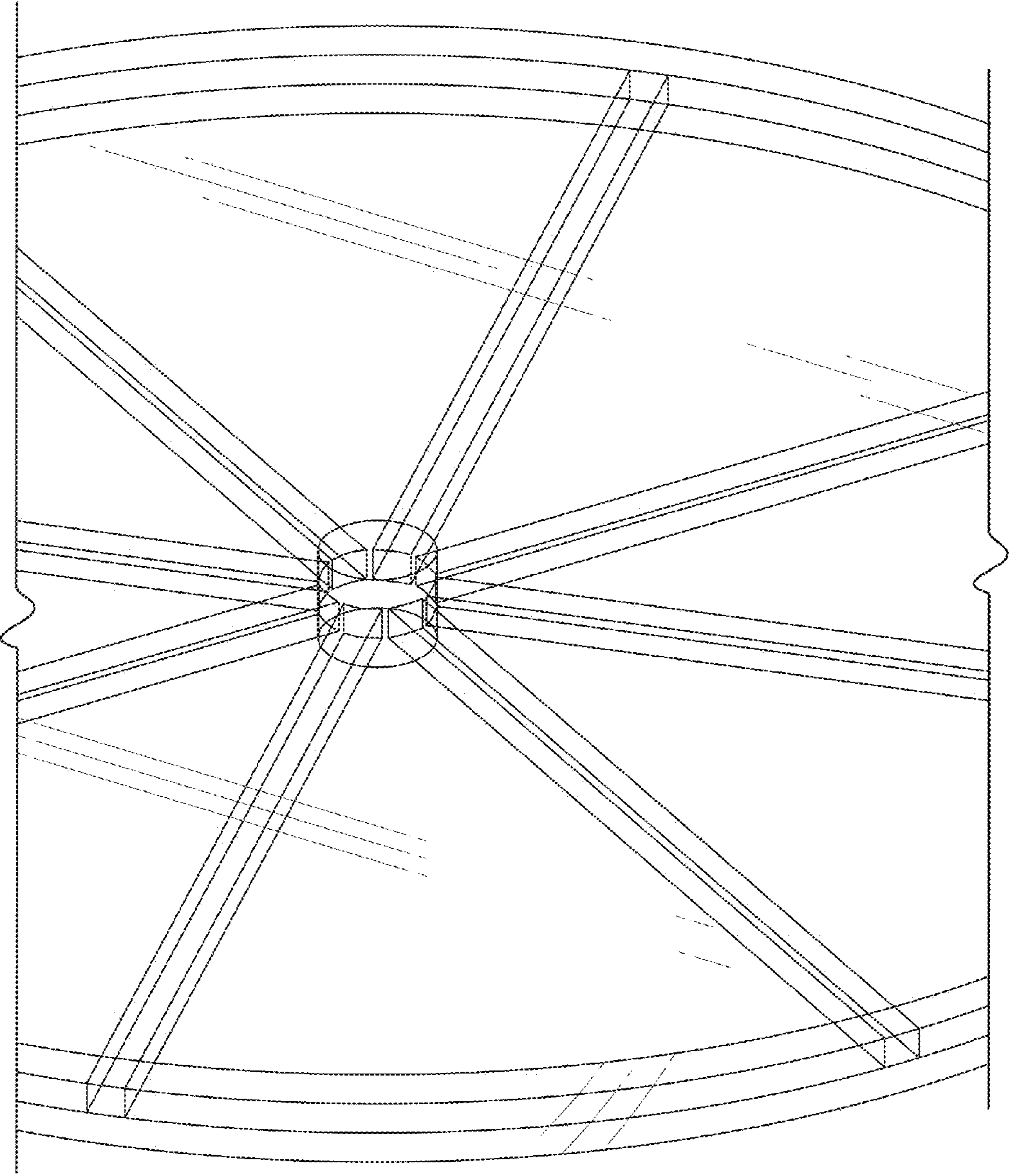


Fig.5

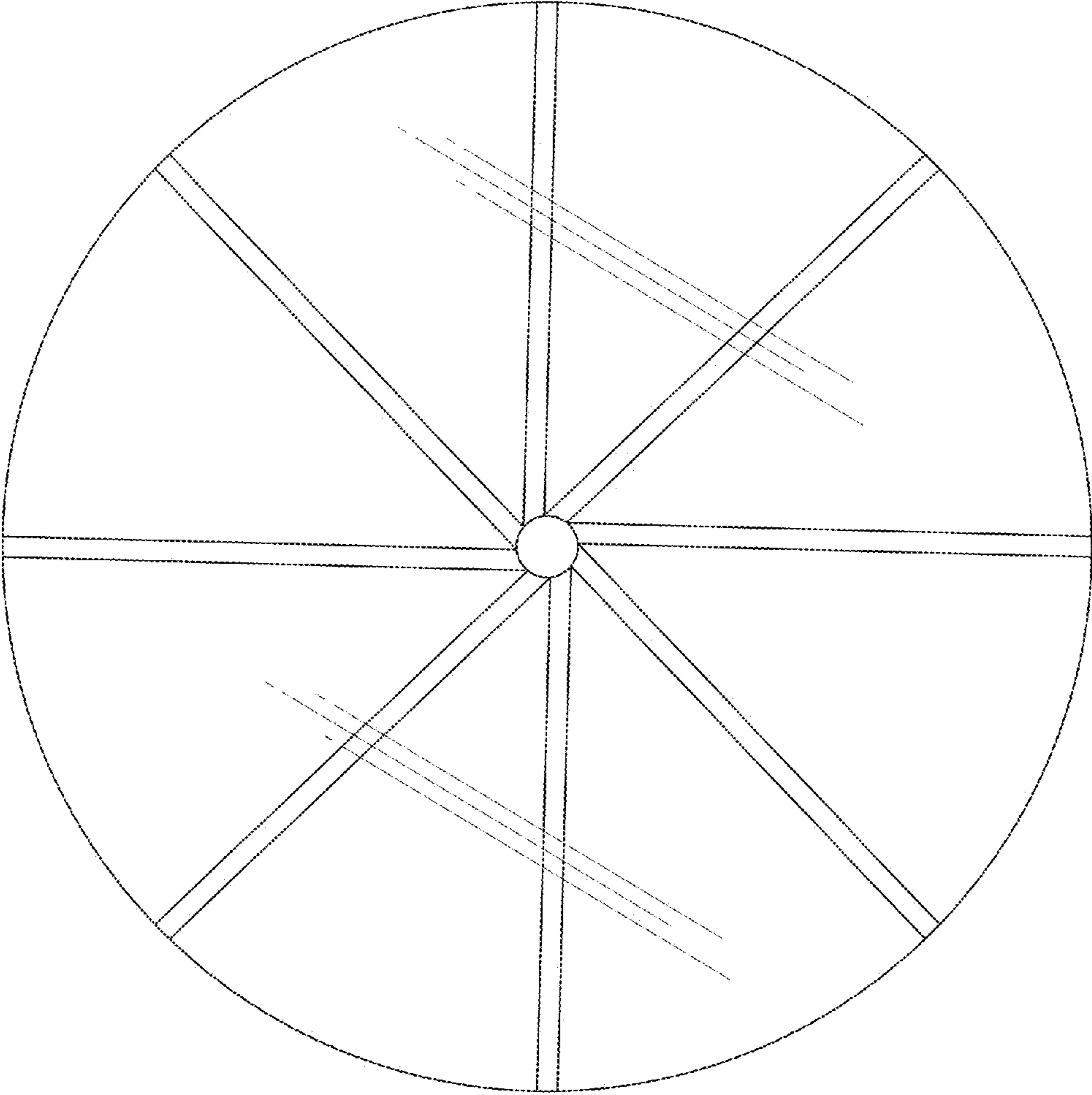


Fig.6

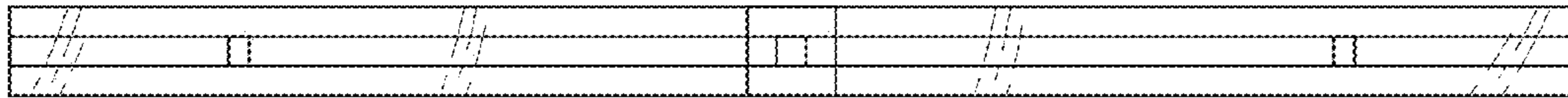


Fig.7

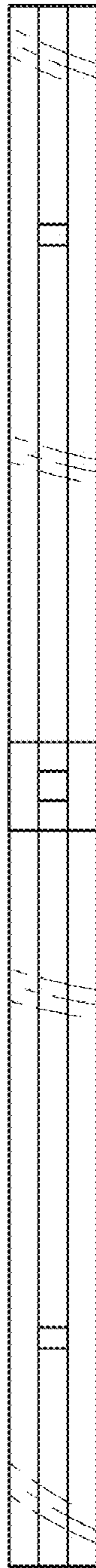


Fig.8

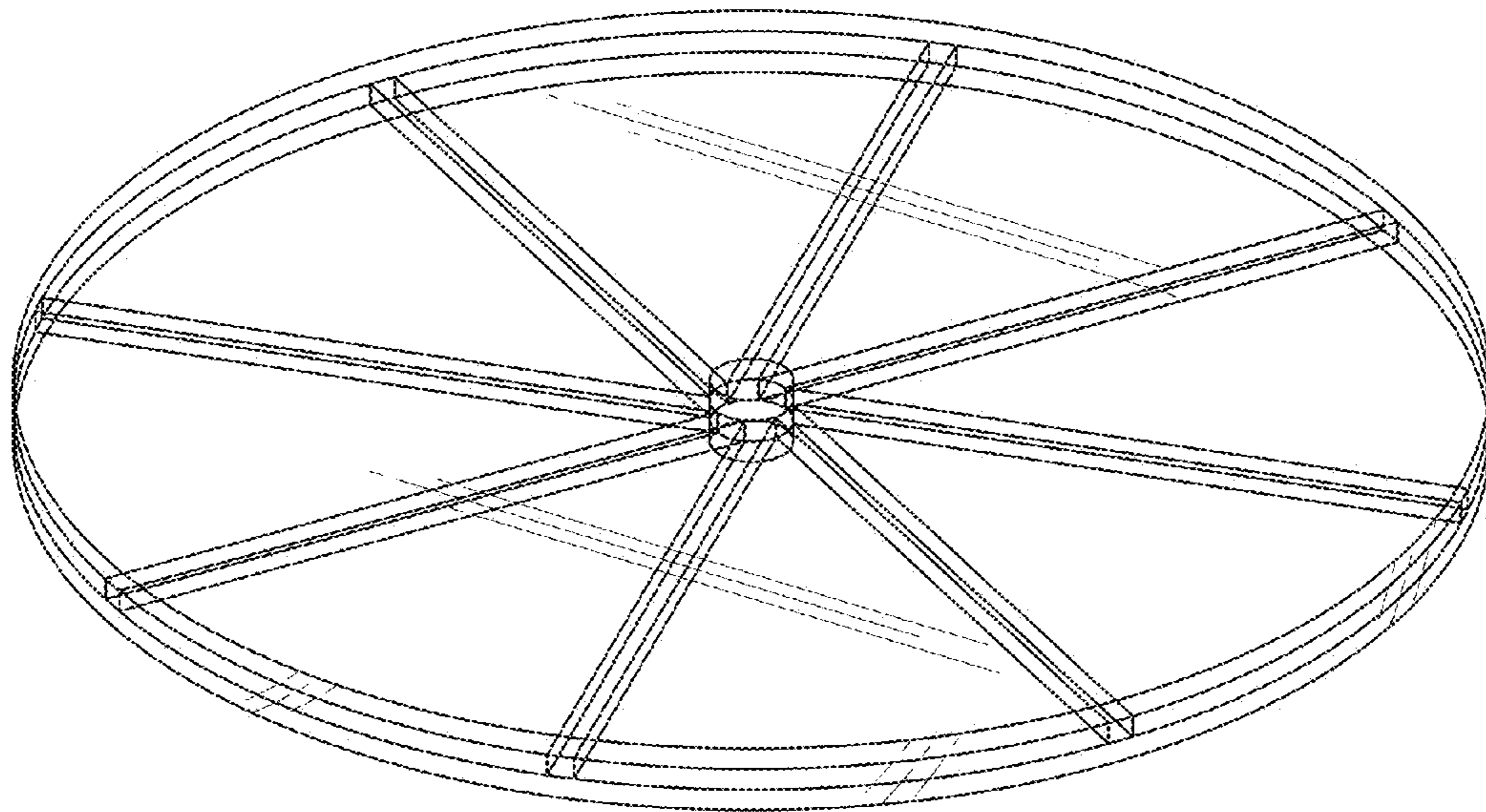


Fig.9

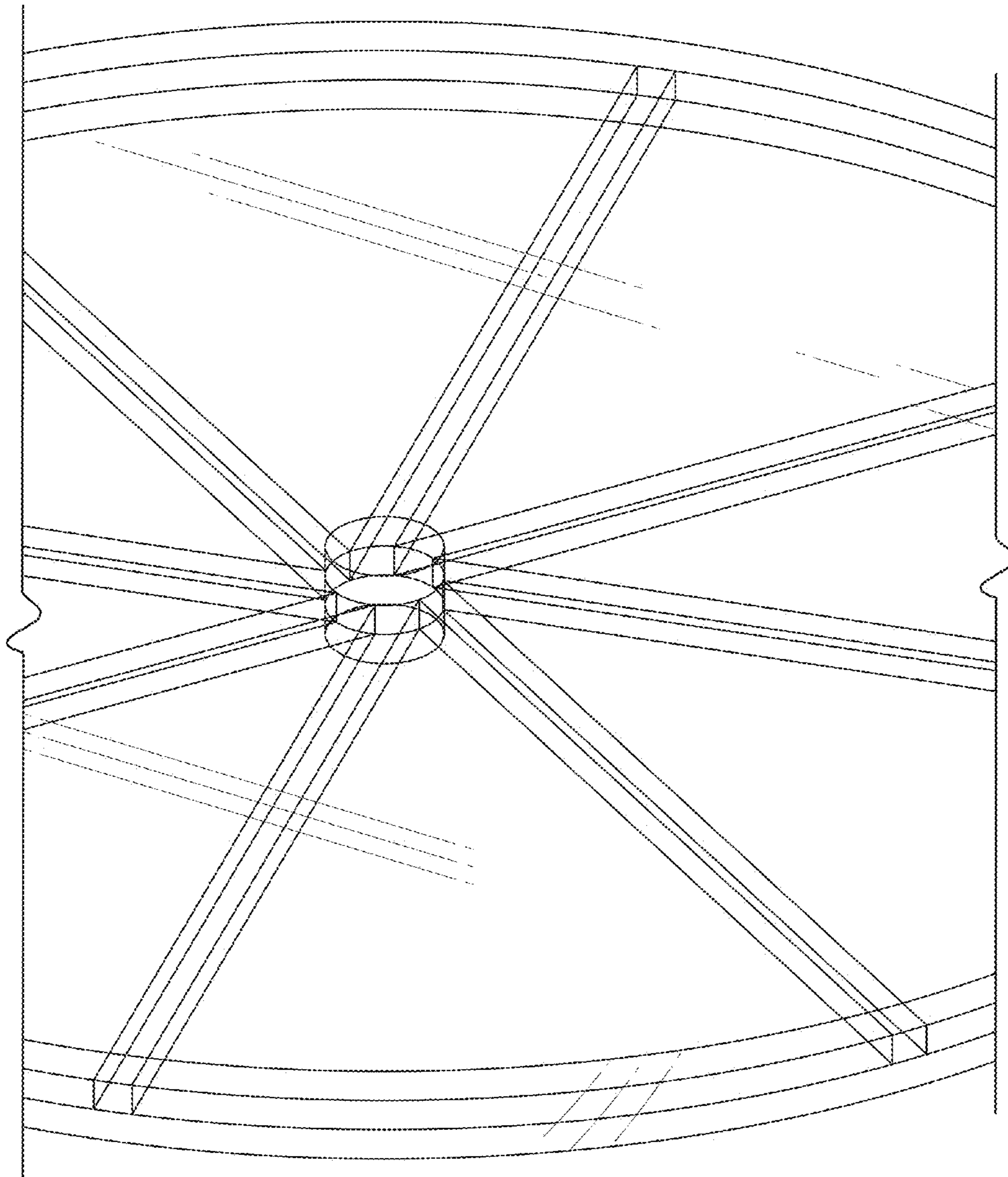


Fig.10

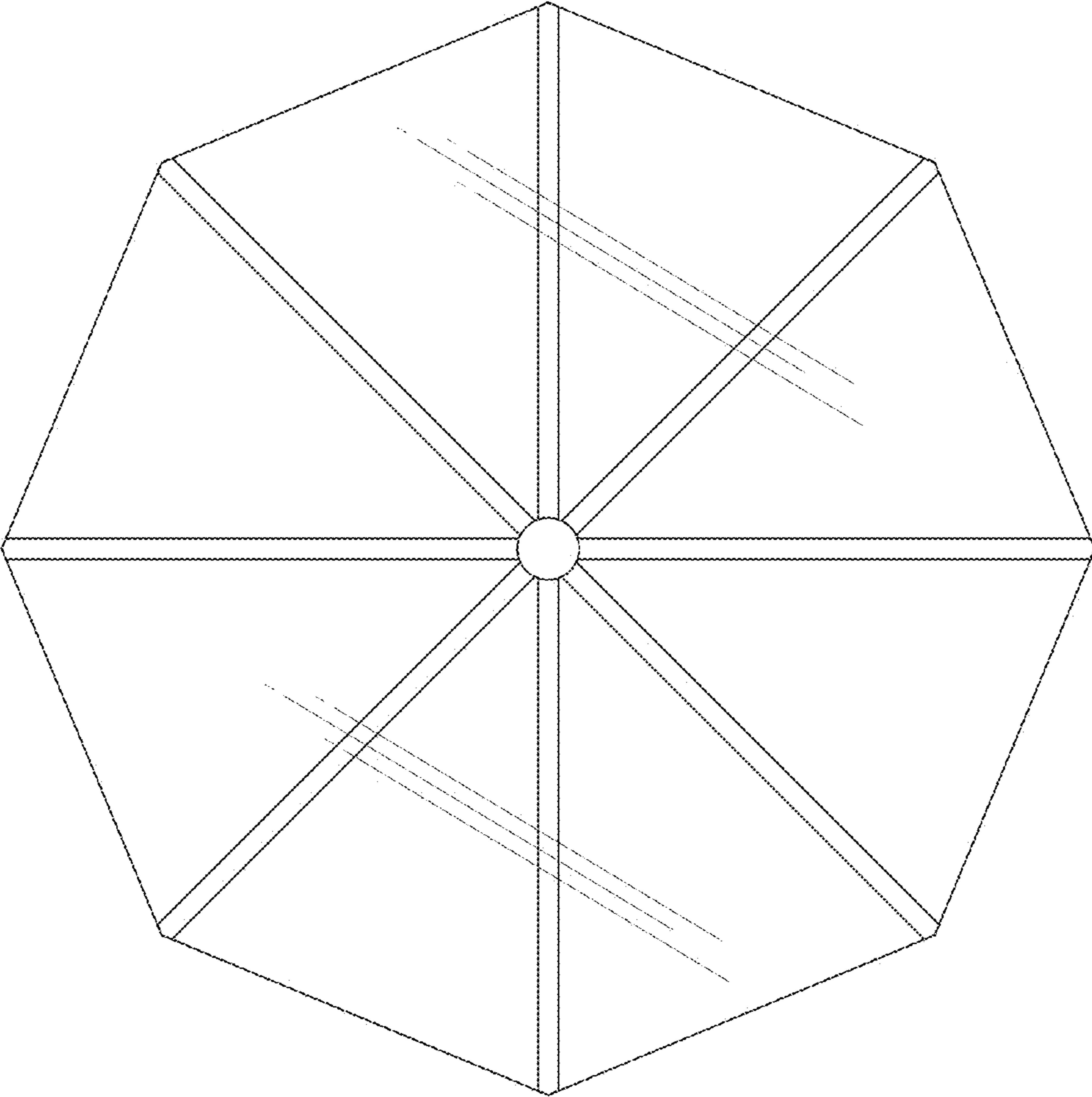


Fig.11

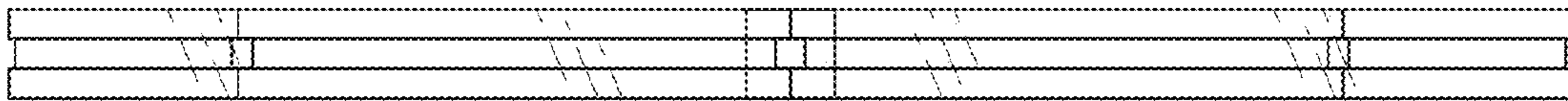


Fig.12

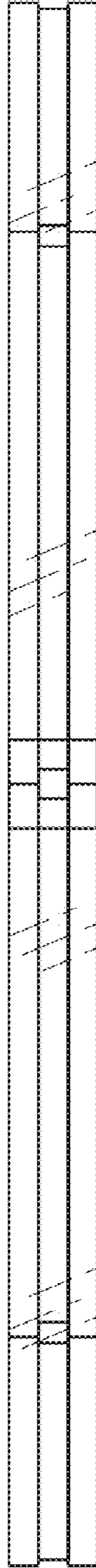


Fig.13

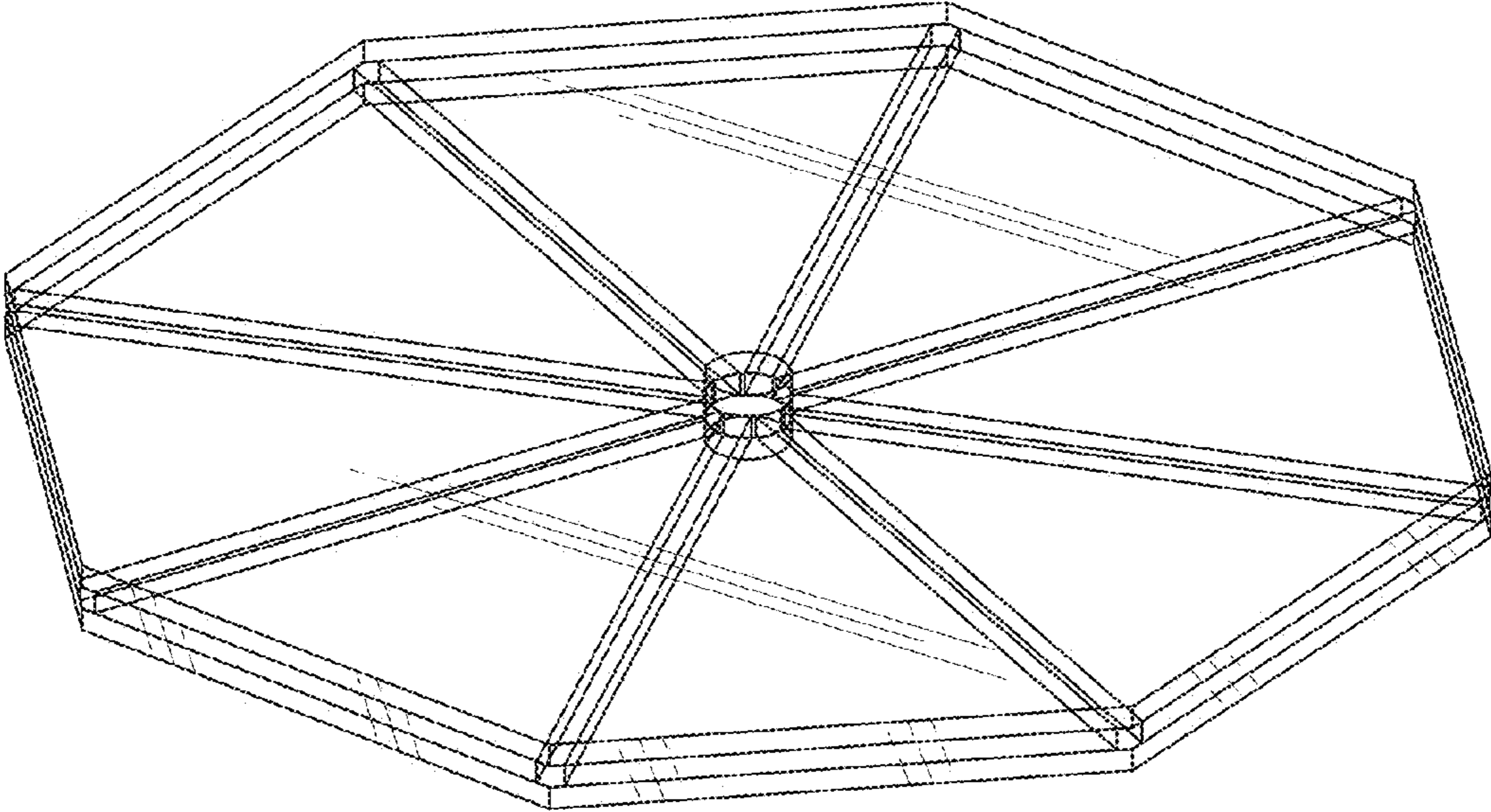


Fig.14

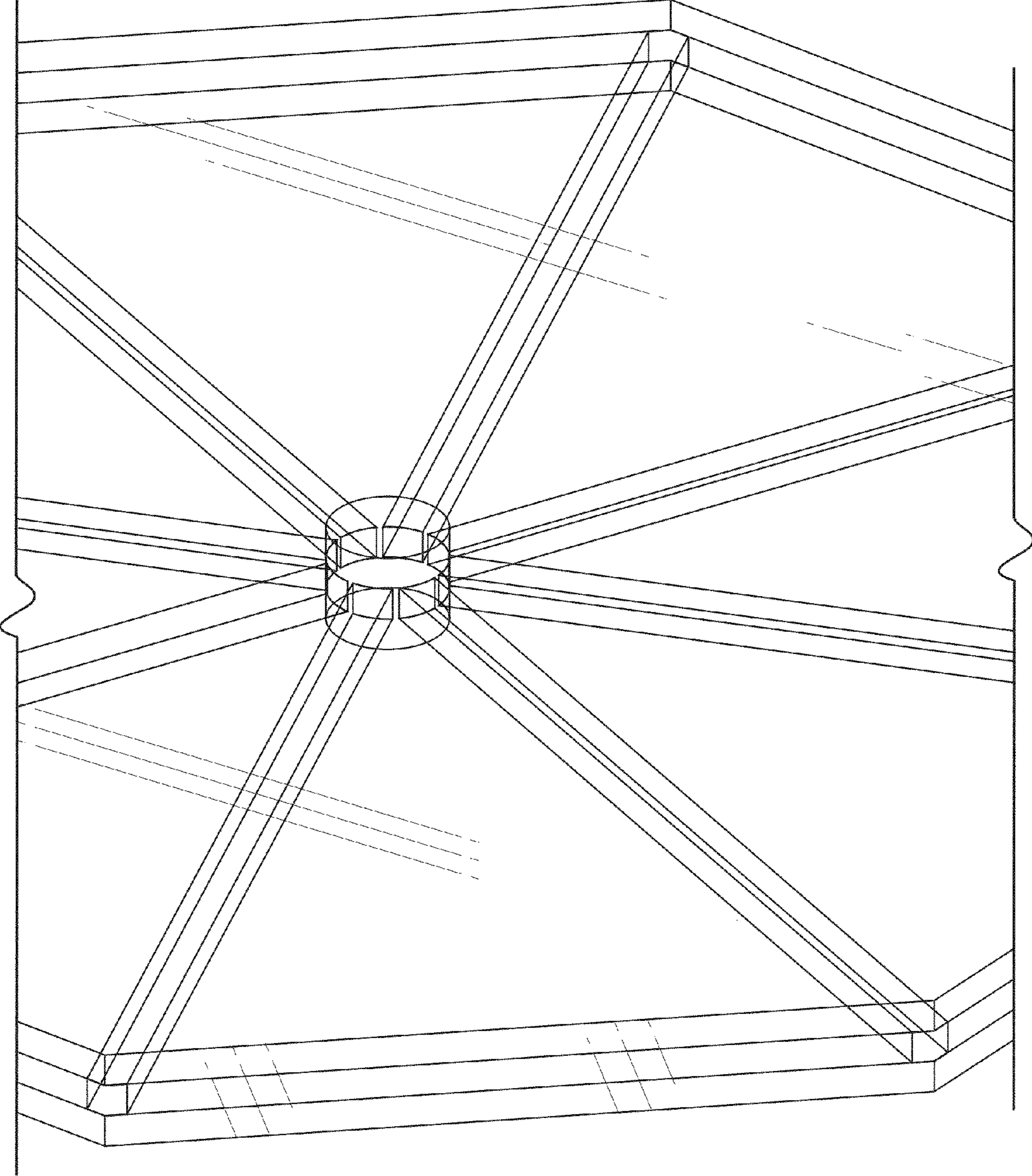


Fig.15